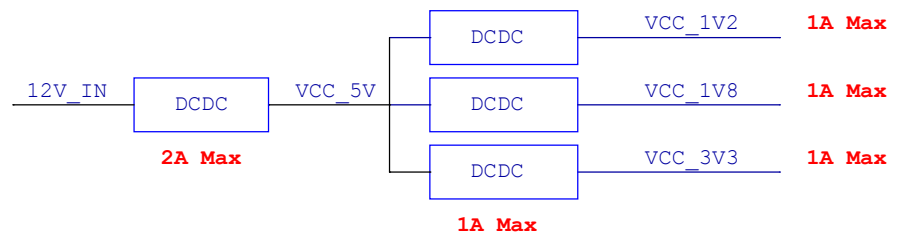
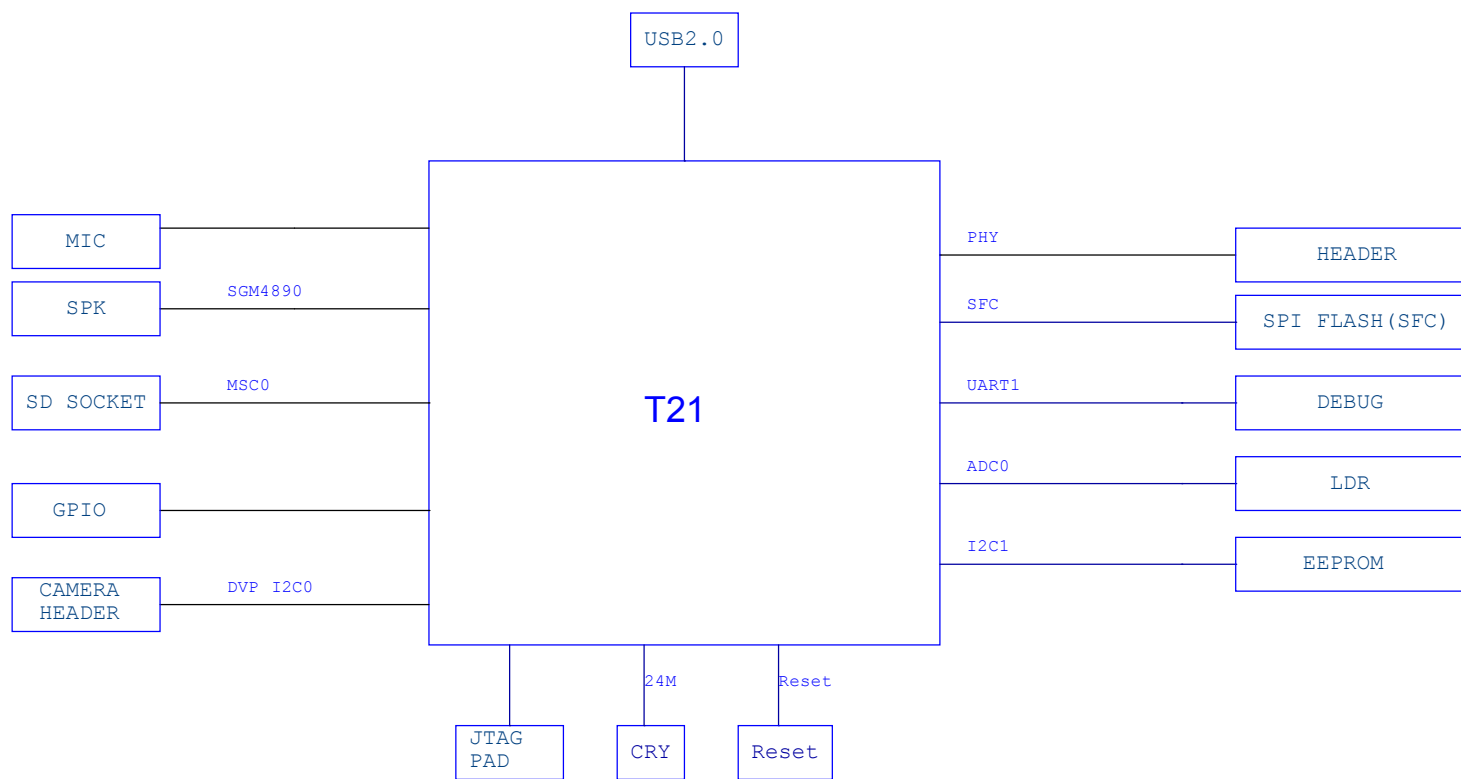




RD_T21_DONKEY

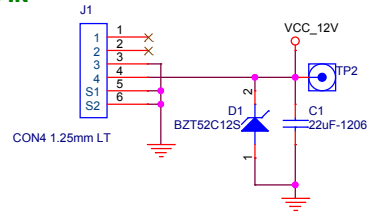
Schematic Revision 2.1

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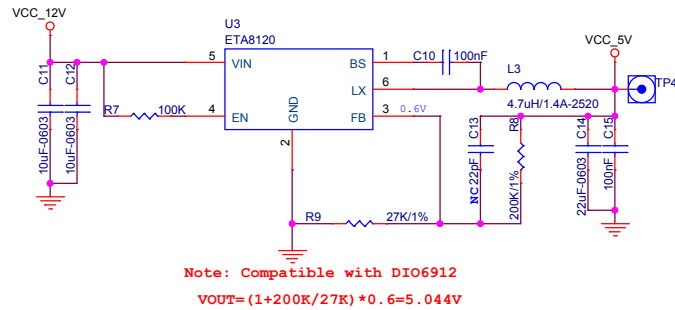


POWER SUPPLY

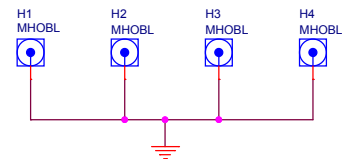
12V DC-IN



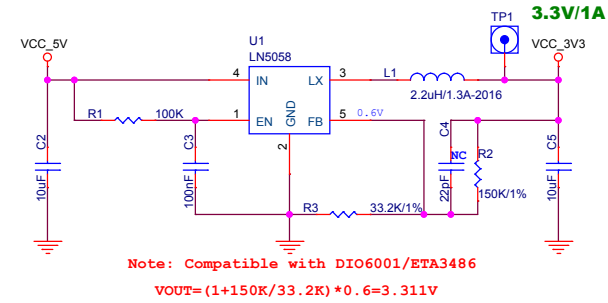
5V/2A



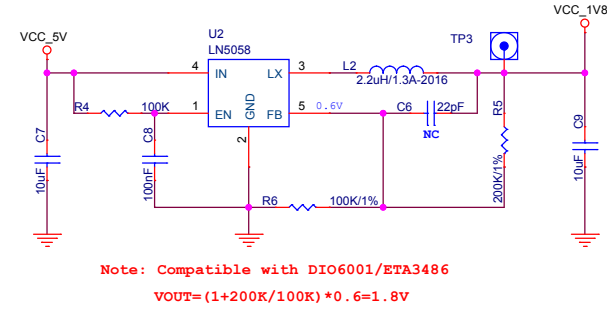
POWER LED



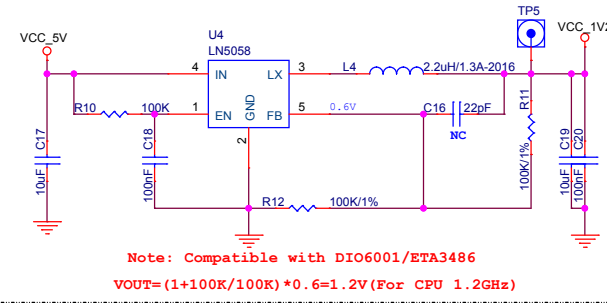
Mechanical Hole



1.8V/1A

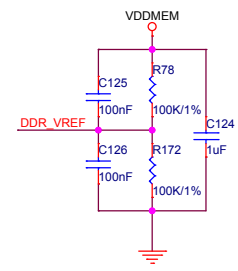
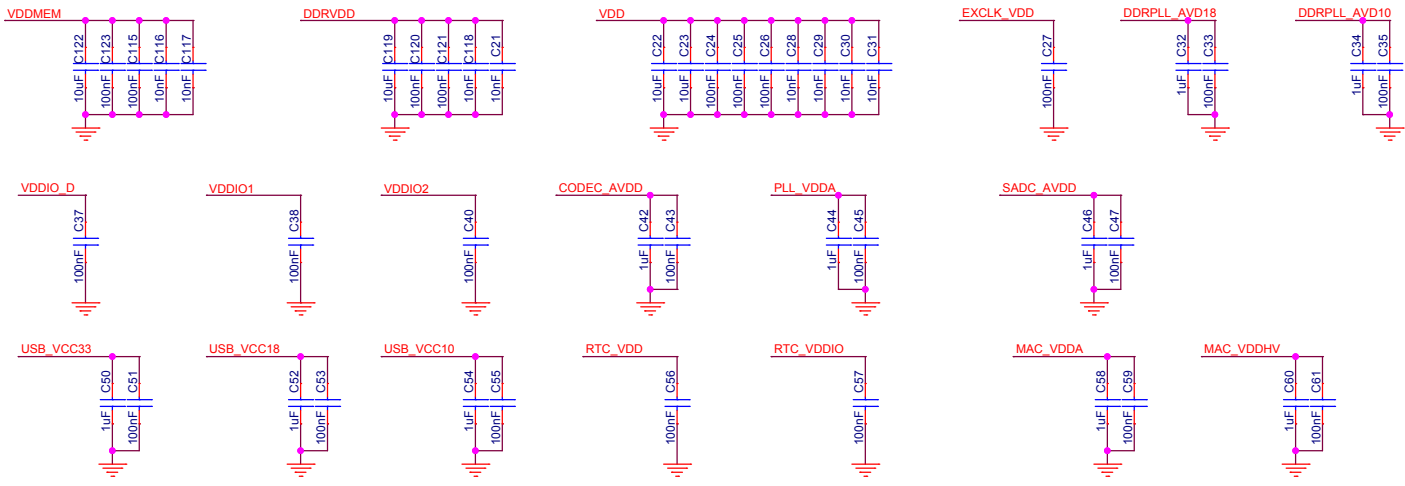
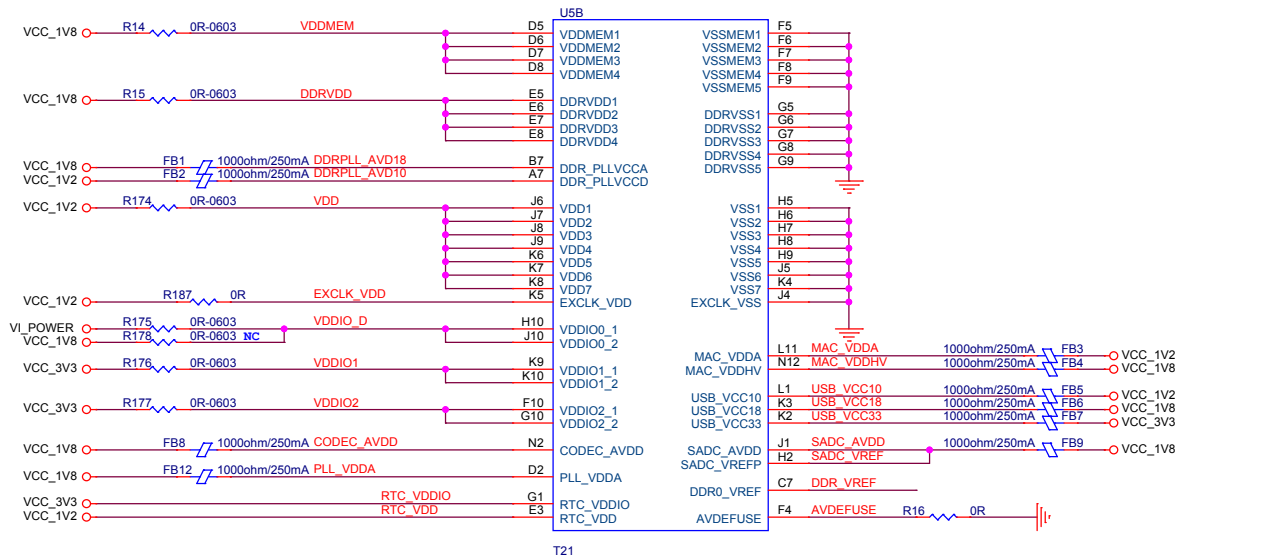


1.2V/1A

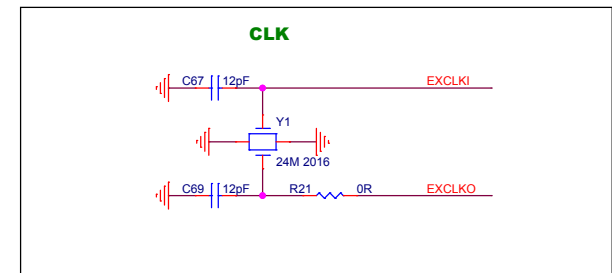
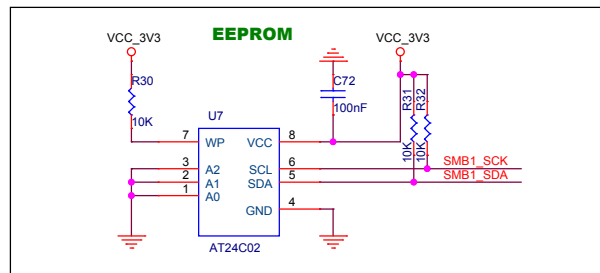
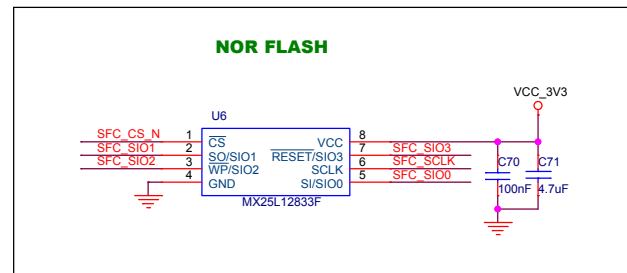
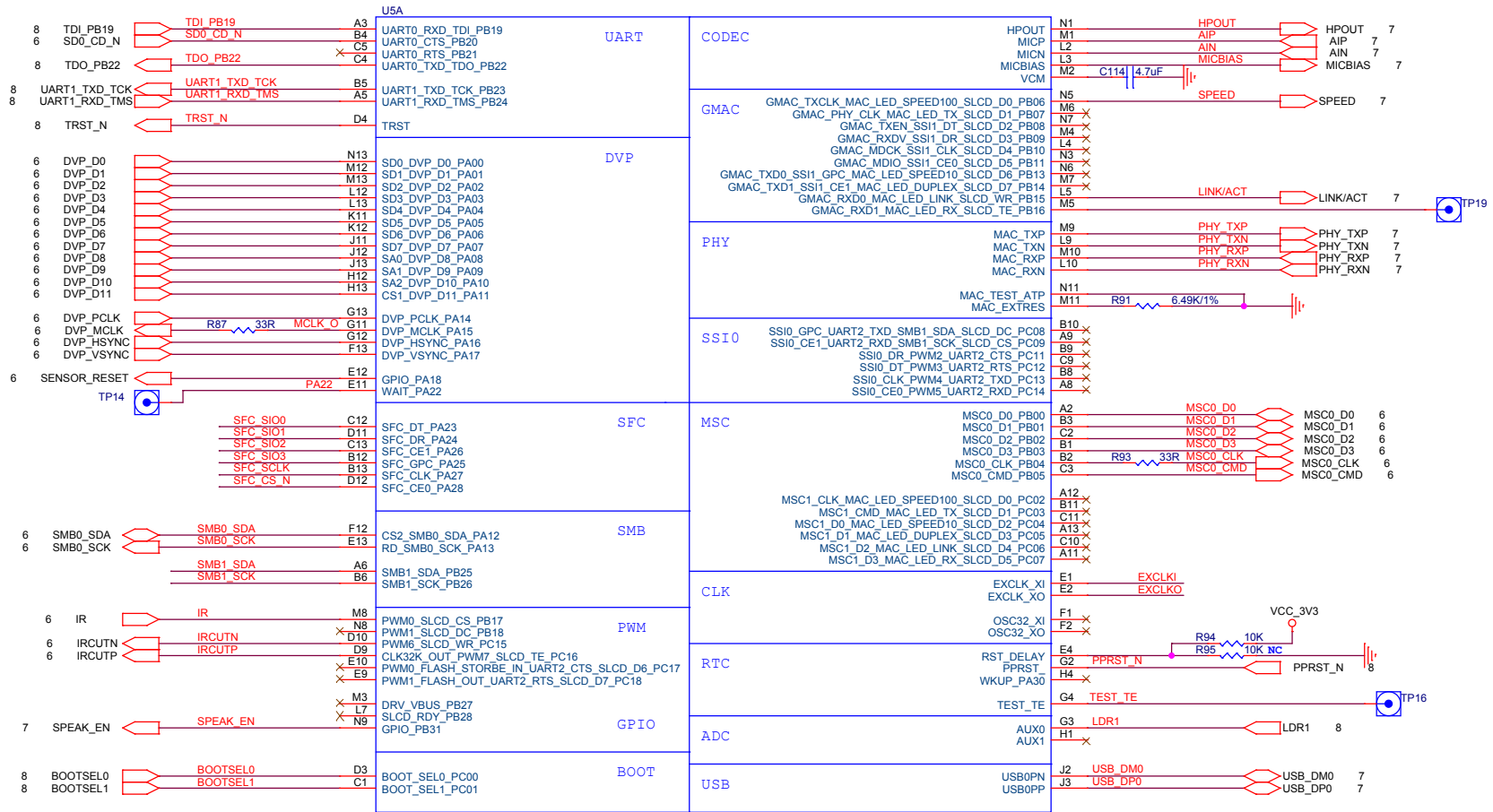


INGENIC SEMICONDUCTOR CO.,LTD			
Title	RD_T21_DONKEY		
Size	Page Name	Rev	
A3	POWER_SUPPLY	2.1	
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T21_POWER

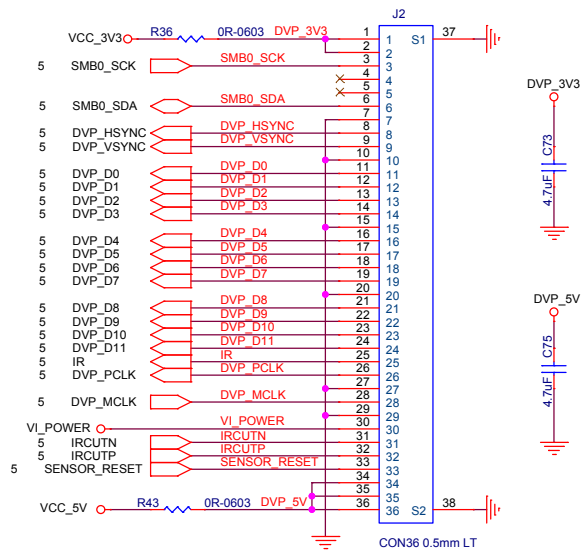


T21_IO/NOR FLASH/EEPROM

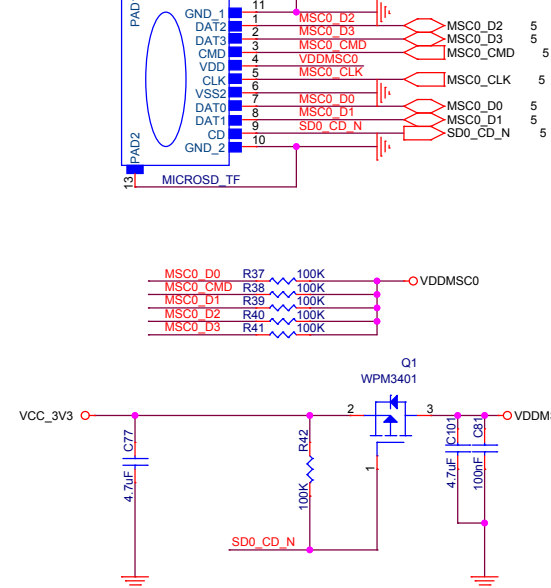


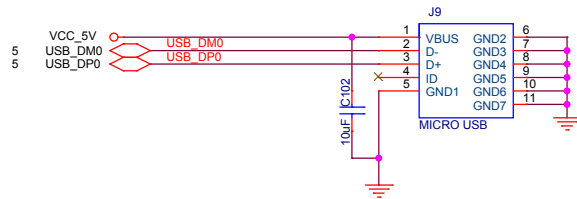
DVP_CONNECTOR/SD_CARD

DVP

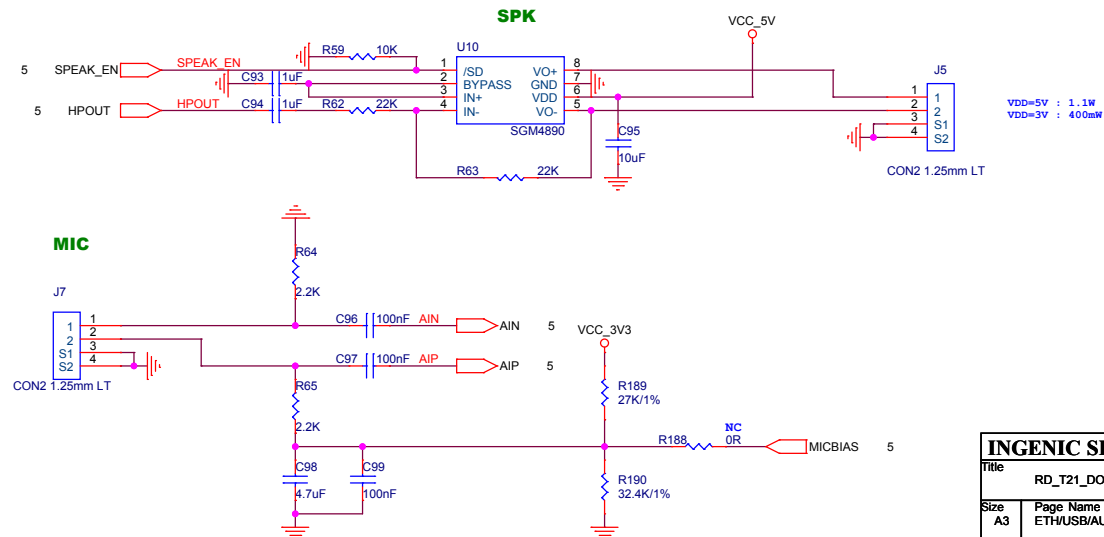


SD CARD



[illegible]

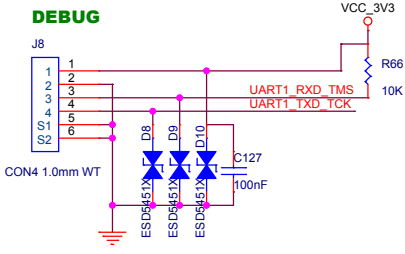
AUDIO



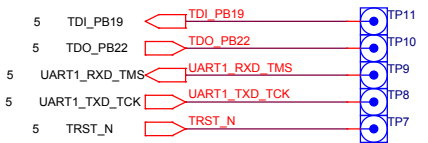
INGENIC SEMICONDUCTOR CO.,LTD			
Title RD_T21_DONKEY			
Size A3	Page Name ETH/USB/AUDIO		Rev 2.1
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RST/BOOT/LDR/DEBUG/JTAG

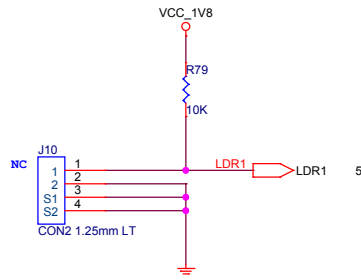
DEBUG



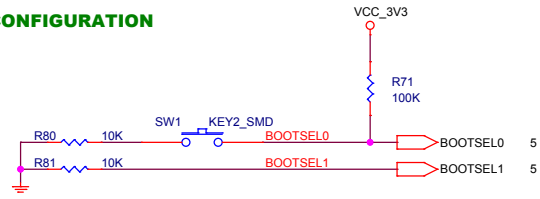
JTAG



LDR

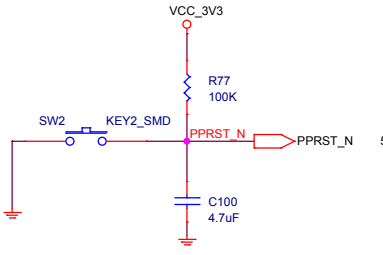


BOOT CONFIGURATION



BOOT_SEL1	BOOT_SEL0	BOOT FROM
0	0	SD card : MSC0
0	1	SFC : SPi0/SSI_CEO

RESET CIRCUITS



Date	Revision	Change
Jul 02 2018	Rev1.0	1. First Revision
Aug 02 2018	Rev1.1	1. Correct the ETH matching circuit 2. Add ETH protective circuit
Sep 18 2018	Rev2.0	1. Change U1 K5 PIN Number to EXCLK_VDD 2. Change U1 J4 PIN Number to EXCLK_VSS 3. Add FB13/D8/D9/D10/C127 4. Change J8 3PIN to 4PIN 5. Delete D5/D7/R20, Add D11
Mar 25 2019	Rev2.1	1. Change VDDCore 1.0V to 1.2V(CPU For 1.2GHz) 2. Modified MIC&HPOUT Cap value 3. Compatible with external 1.8V Micbias