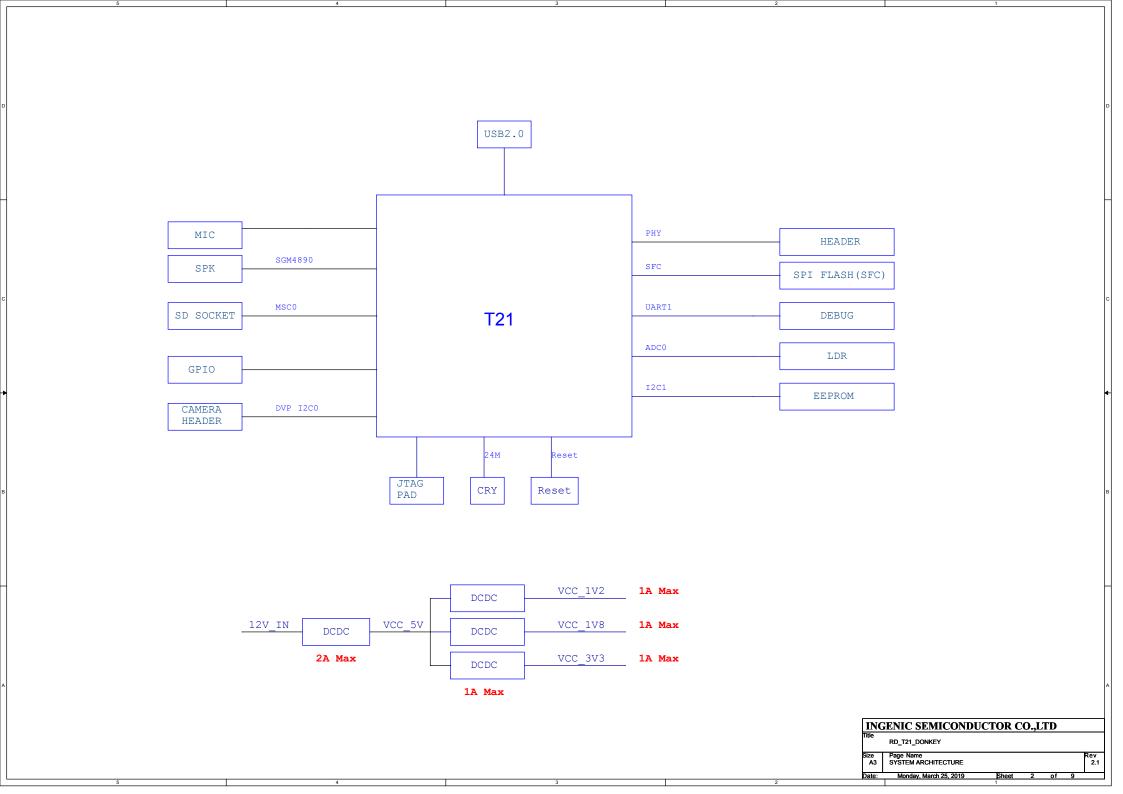


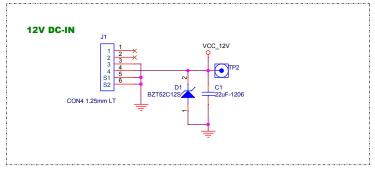
RD_T21_DONKEY Schematic Revision 2.1

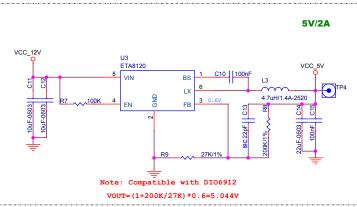
COVER SHEET 1 SYSTEM ARCHITECTURE 2 POWER_SUPPLY 3 T21_POWER 4 T21_IO/NOR FLASH/EEPROM 5 DVP_CONNECTOR/SD_CARD 6 ETH/USB/AUDIO 7	Title	Page
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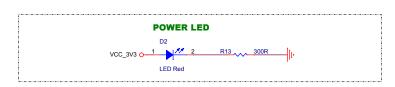
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Size A3	Page Name COVER SHEET					Rev 2.		
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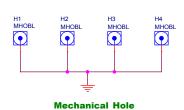


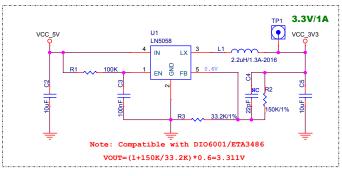
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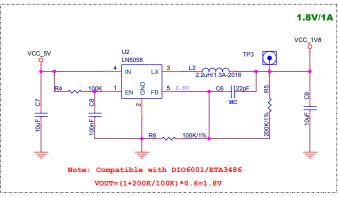


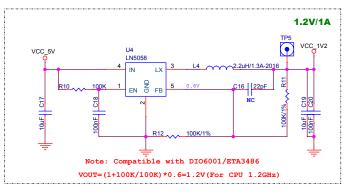






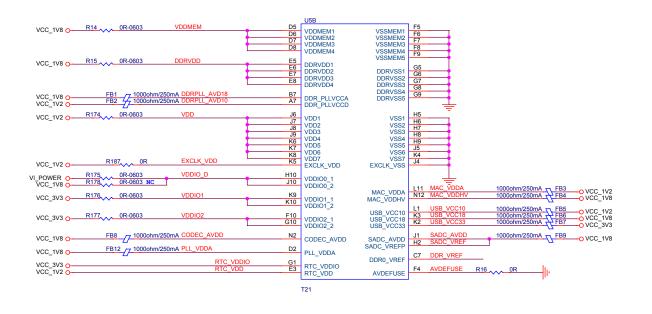


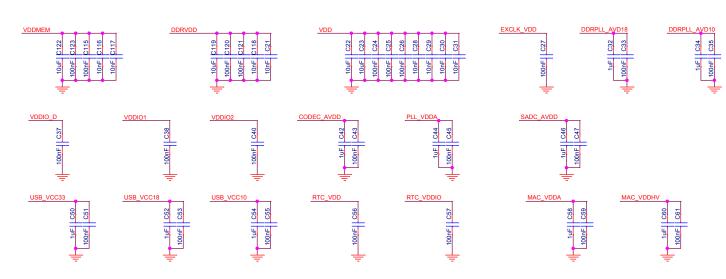


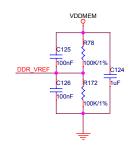


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T21 POWER

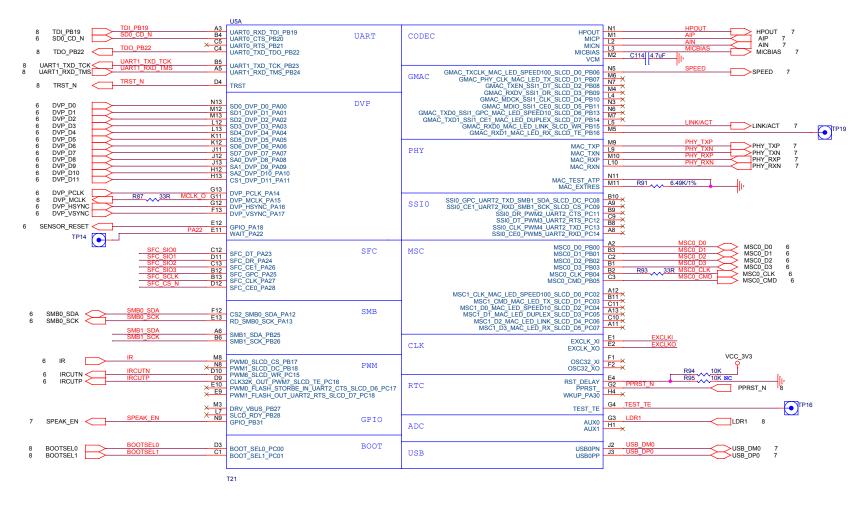


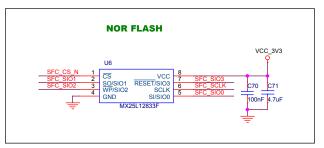


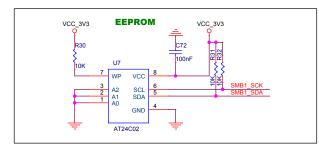


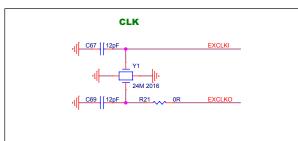
| INGENIC SEMICONDUCTOR CO.,LTD | Title | RD_T21_DONKEY | Size | Page Name | T21_POWER | 2.1 | Date: | Monday, March 25, 2019 | Sheet | 4 | of | 9

T21_IO/NOR FLASH/EEPROM



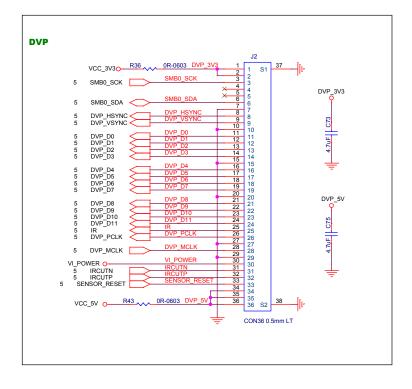


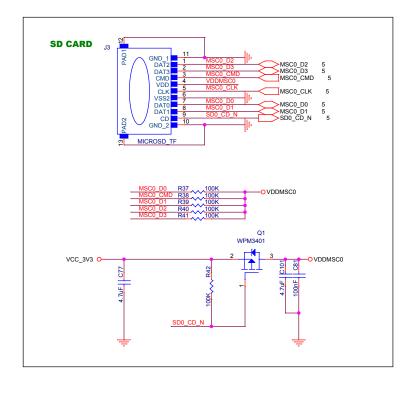




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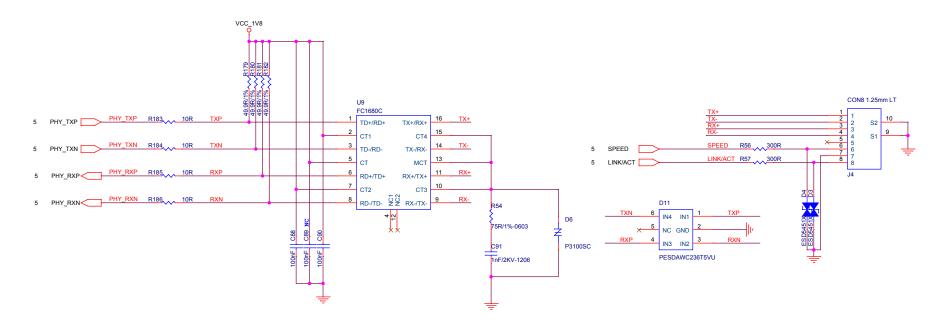
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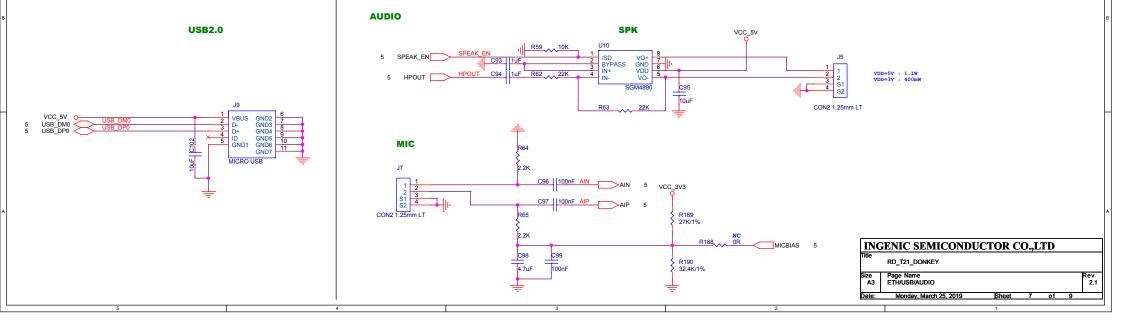




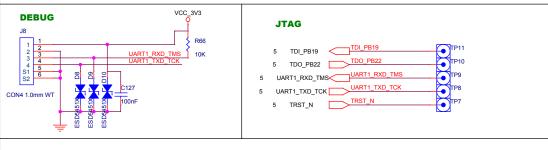
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ETH/USB/AUDIO

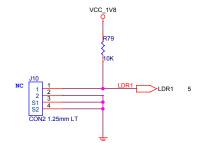


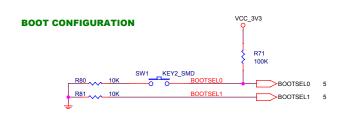


RST/BOOT/LDR/DEBUG/JTAG



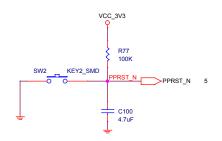
LDR





T_SEL1	BOOT_SEL0	BOOT FROM
0	0	SD card : MSCO
0	1	SEC : SPIO/SSI CEO

RESET CIRCUITS



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Date	Revision	Change
Jul 02 2018	Rev1.0	1. First Revision
Aug 02 2018	Rev1.1	Correct the ETH matching circuit Add ETH protective circuit
Sep 18 2018	Rev2.0	1. Change U1 K5 PIN Number to EXCLK_VDD 2. Change U1 J4 PIN Number to EXCLK_VSS 3. Add FB13/D8/D9/D10/C127 4. Change J8 3PIN to 4PIN 5. Delete D5/D7/R20, Add D11
Mar 25 2019	Rev2.1	1. Change VDDCore 1.0V to 1.2V(CPU For 1.2GHz) 2. Modified MIC&HPOUT Cap value 3. Compatible with external 1.8V Micbias

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