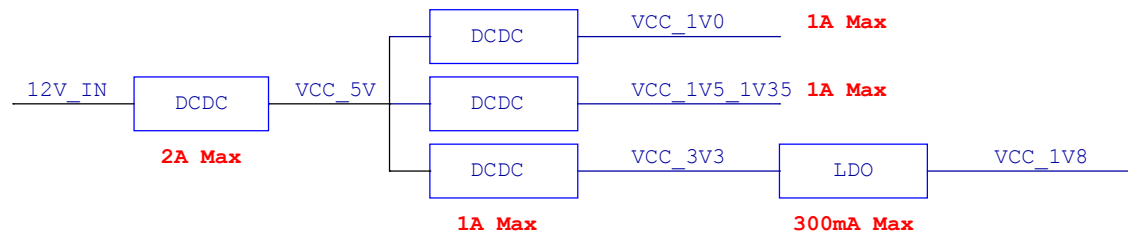
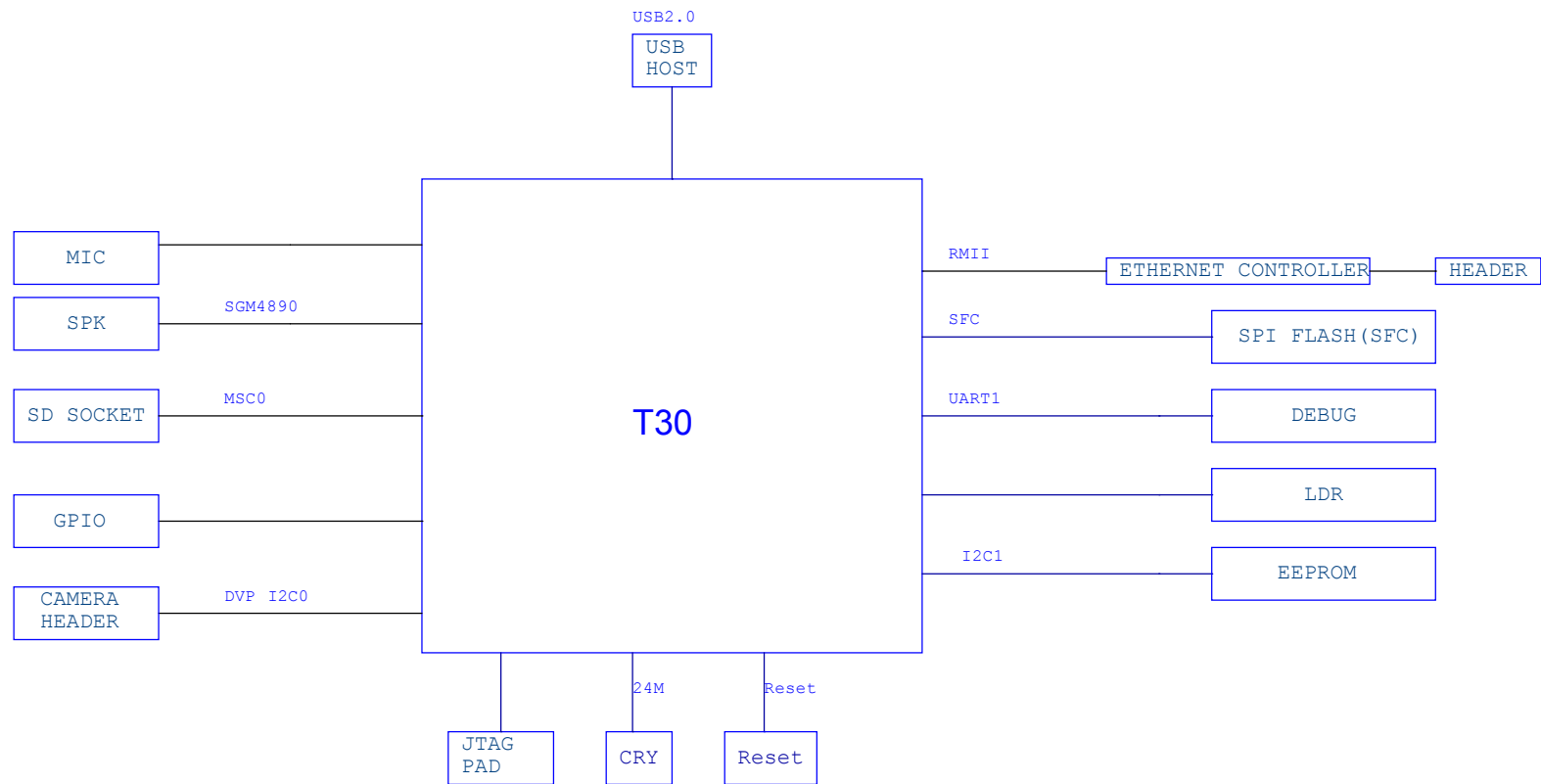




RD_T30_KOALA_D

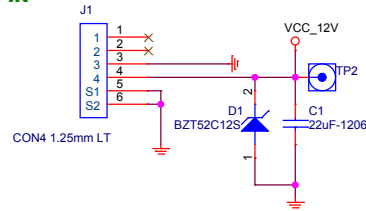
Schematic Revision 1.1

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T30_POWER	4
T30_IO/NOR FLASH/EEPROM	5
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ETH/USB/AUDIO	7
RST/BOOT/LDR/DEBUG/JTAG	8
REVISION HISTORY	9

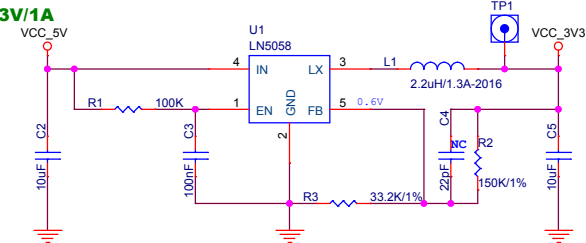


POWER SUPPLY

12V DC-IN



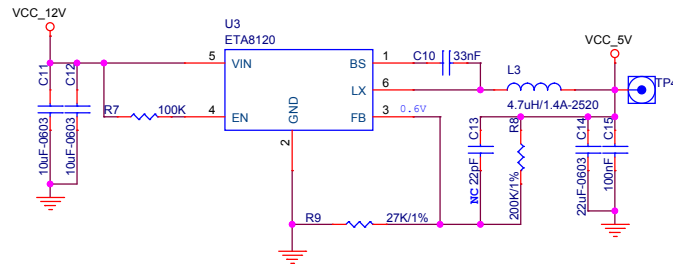
3.3V/1A



Note: Compatible with DIO6001/ETA3486

$$VOUT = (1 + 150K / 33.2K) * 0.6 = 3.311V$$

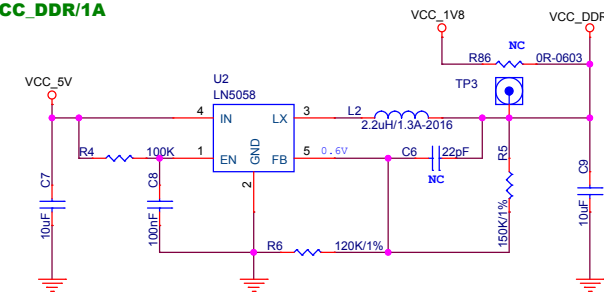
5V/2A



Note: Compatible with DIO6912

$$VOUT = (1 + 200K / 27K) * 0.6 = 5.044V$$

VCC_DDR/1A



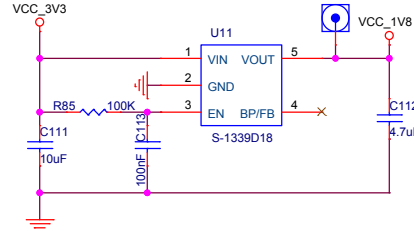
Note: Compatible with DIO6001/ETA3486

$$VCC_DDR = (1 + 200K / 100K) * 0.6 = 1.8V (R5 = 200K / 1\%) \quad \text{FOR T30X/L/N}$$

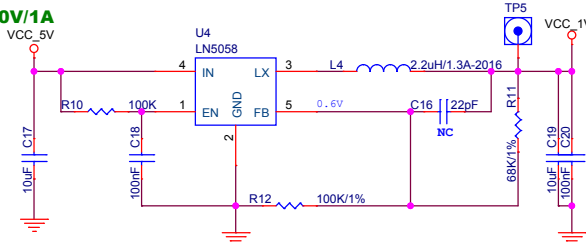
$$VCC_DDR = (1 + 150K / 100K) * 0.6 = 1.5V (R5 = 150K / 1\%) \quad \text{FOR T30Z}$$

$$VCC_DDR = (1 + 150K / 120K) * 0.6 = 1.35V (R5 = 150K / 1\%) \quad \text{FOR T30A} \checkmark$$

1.8V/300mA



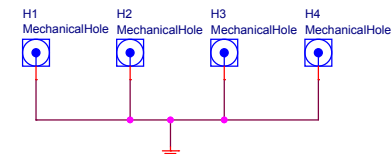
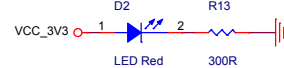
1.0V/1A



Note: Compatible with DIO6001/ETA3486

$$VOUT = (1 + 68K / 100K) * 0.6 = 1.0V$$

Indicator LED

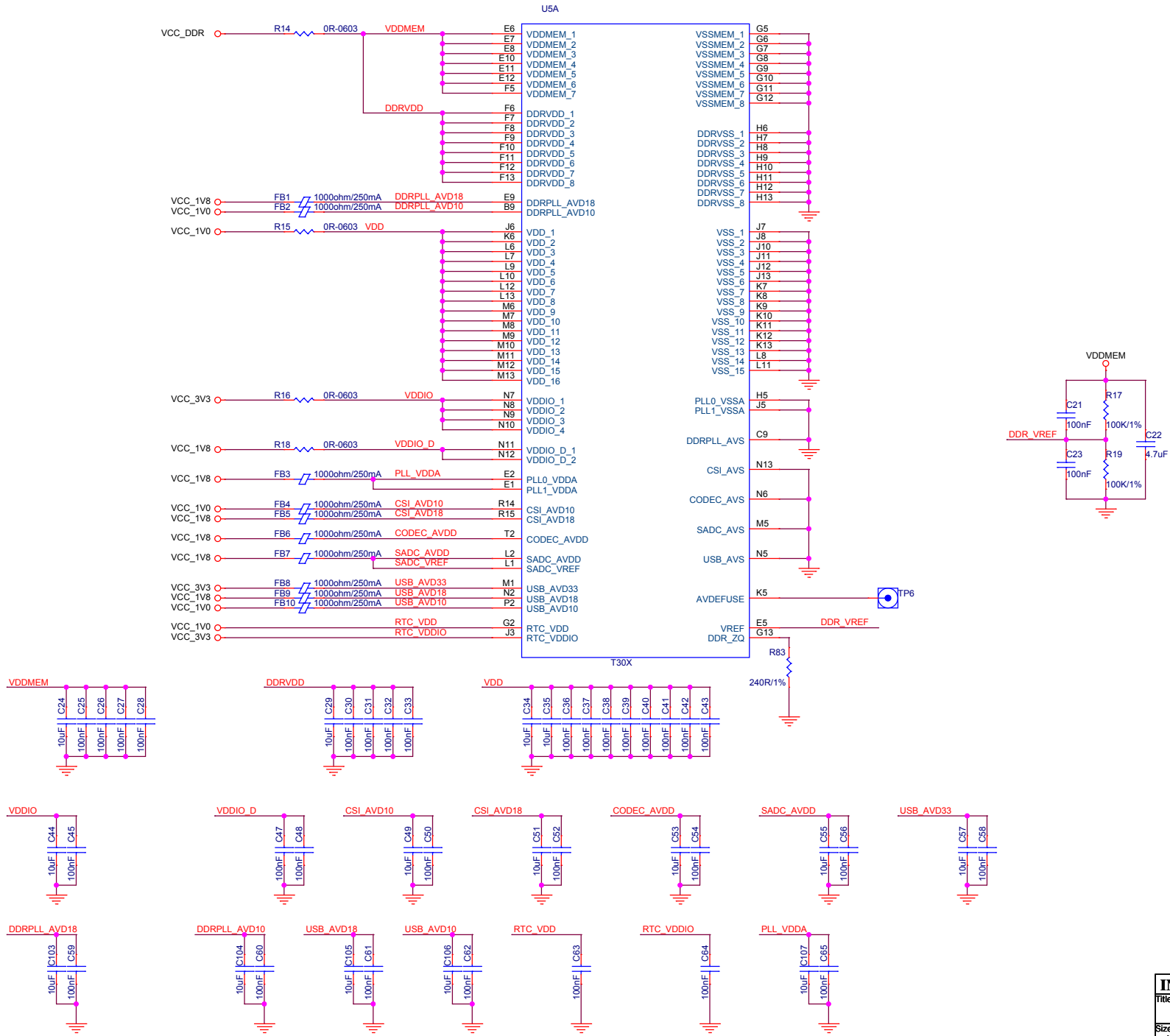


Mechanical Hole

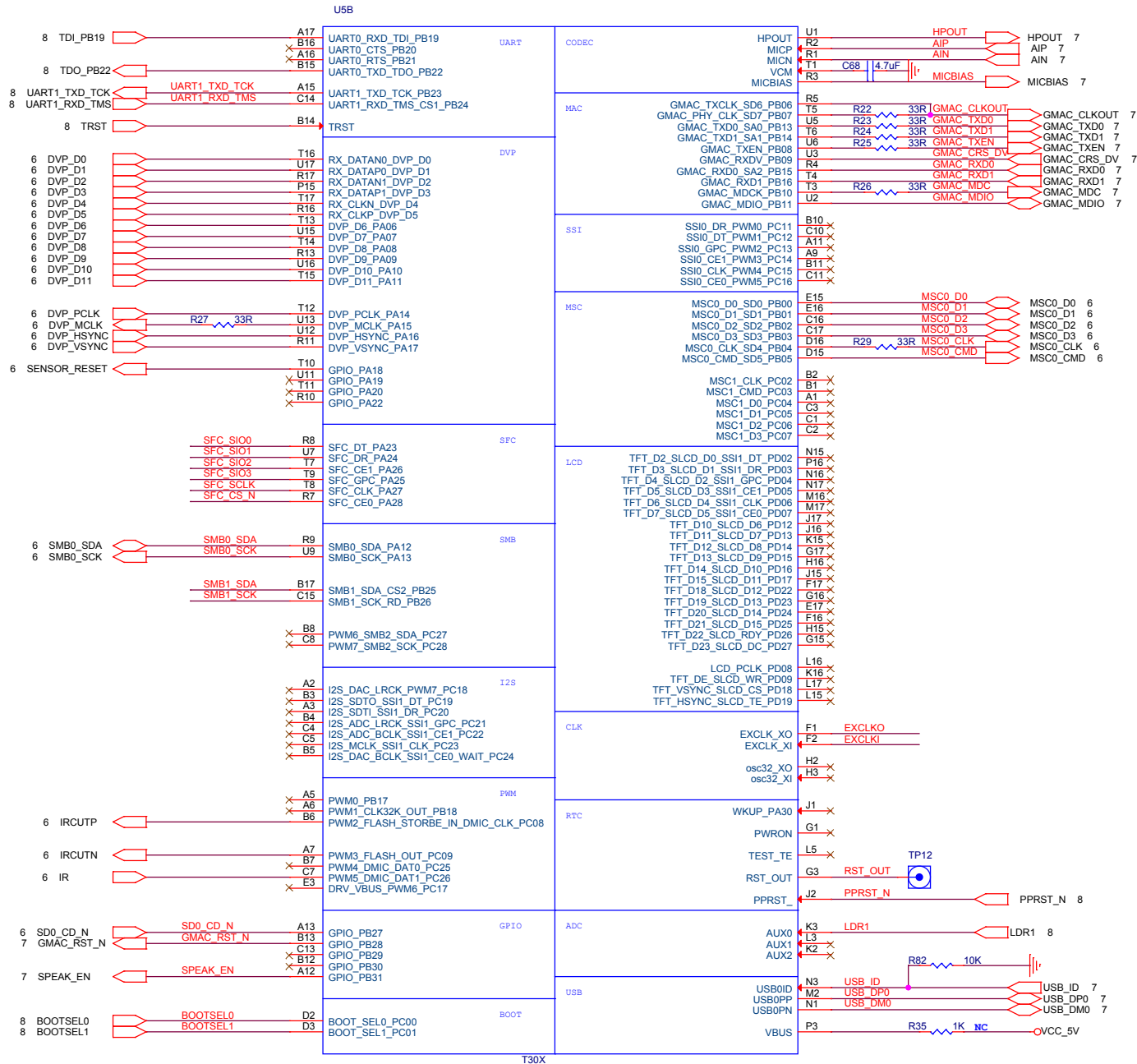
INGENIC SEMICONDUCTOR CO.,LTD

Title		RD_T30_KOALA_D
Size	Page Name	POWER_SUPPLY
A3		Rev 1.1
Date:	Wednesday, April 17, 2019	Sheet 3 of 9

T30-POWER

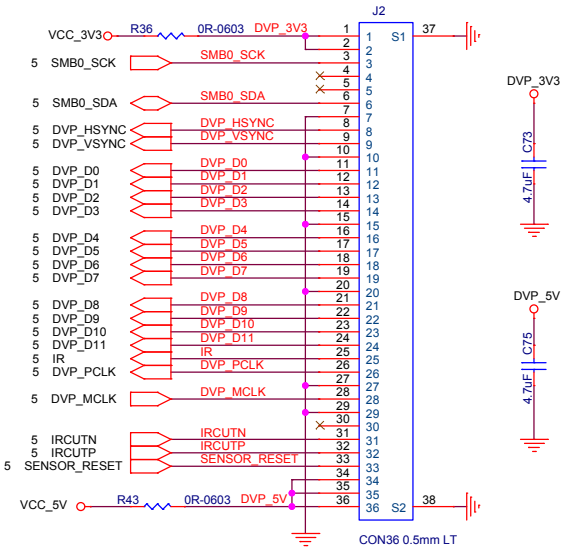


T30_IO/NOR FLASH/EEPROM

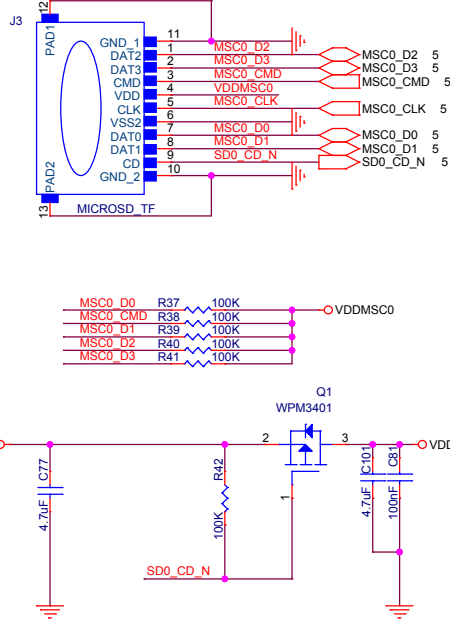


DVP_CONNECTOR/SD_CARD

DVP

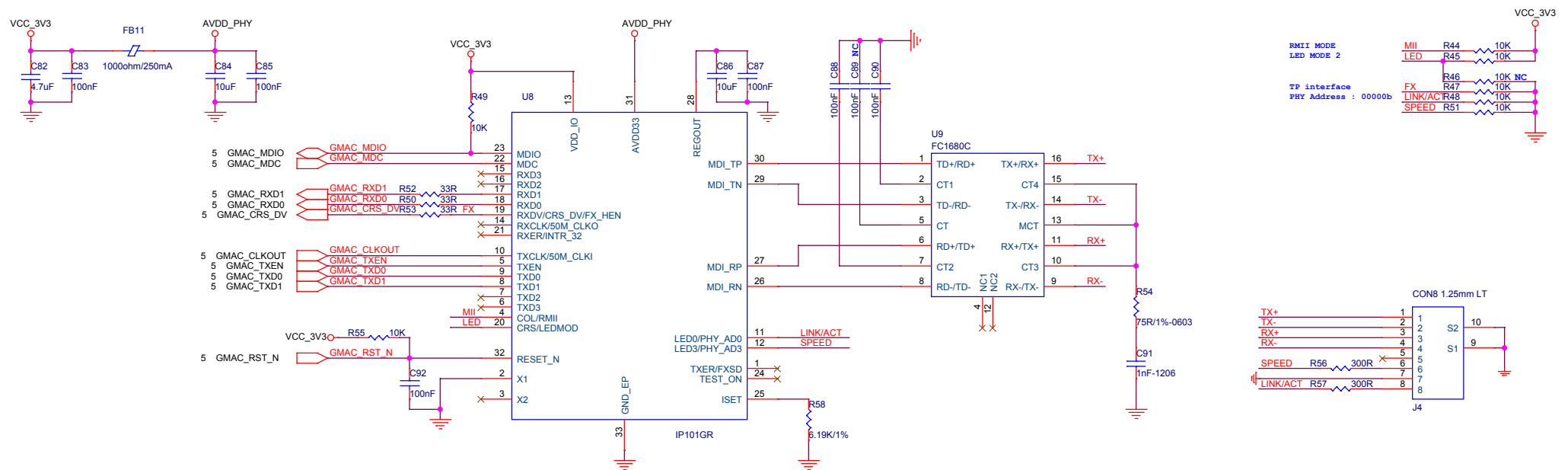


SD CARD

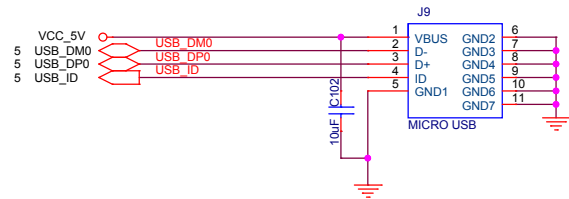


ETH_RMII/USB/AUDIO

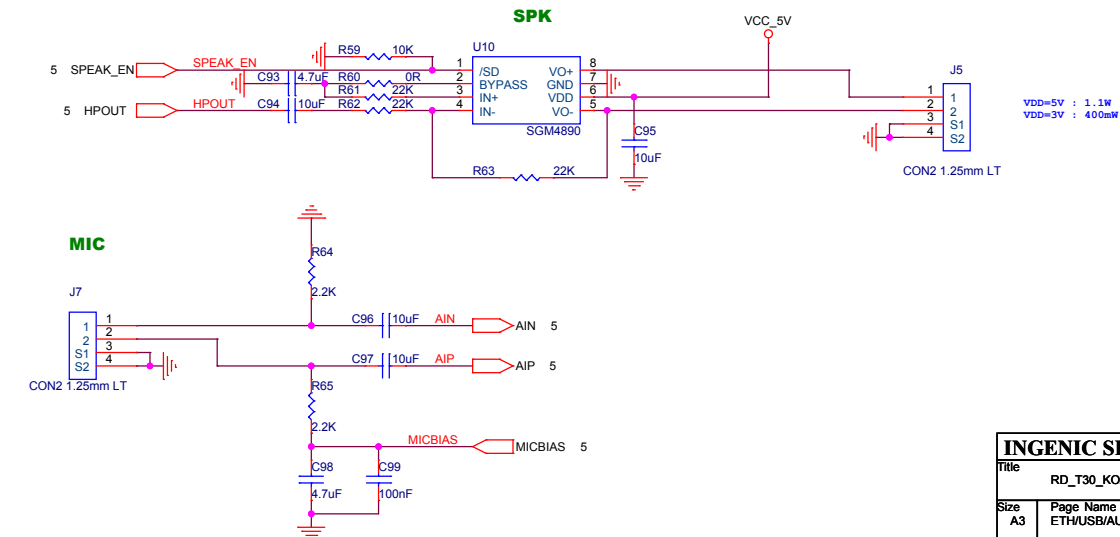
ETH_RMII



USB2.0_HOST

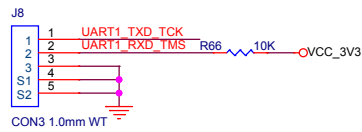


AUDIO

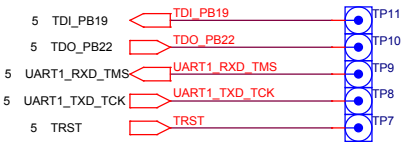


RST/BOOT/LDR/DEBUG/JTAG

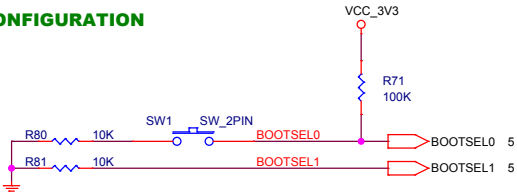
DEBUG



JTAG

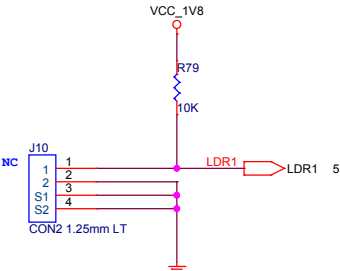


BOOT CONFIGURATION

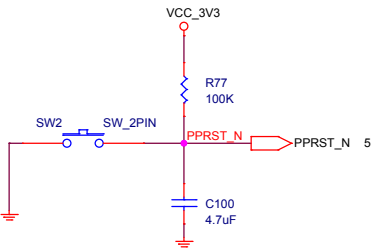


BOOT_SEL1	BOOT_SEL0	BOOT FROM
0	0	SD card : MSC0
0	1	SFC : SP10/SSI_CEO

LDR



RESET CIRCUITS



Date	Revision	Change
Nov 09 2017	Rev1.0	1. First Revision
Apr 16 2019	Rev1.1	1. Optimize DDR Voltage Comments 2. Add DCDC notes 3. Change 3.3V(LDR pull-up resistor power) to 1.8V