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# **Project Proposal**

**FPGA-Based Machine Learning on a Drone**

**ELEC 491 Capstone Team 109**  
**University of British Columbia**

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## Revision History

Revision history written here.

Version #	Initials	Release Date	Changes Made
0.0	MH	2019-09-30	Initial skeleton of the document.
1.0	MW	2019-10-08	First draft.
1.0.1	MH	2019-10-09	Revised "Project Plan" section.

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## **Terms and Abbreviations**

Technical terms and abbreviations dictionary go here.

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# 1 About This Document

Briefing for this section.

## 1.1 Purpose

FIXME (requires editing)

The purpose of this document is to solidify the project outline, objective and requirements. It is meant to

## 1.2 Intended Audience

The intended audience is the client so that the client can confirm the requirements of the project is as they expected. It is also intended for the instructors and the TAs of our capstone team, such that they may aid us accordingly.

Furthermore, the document is also intended for our own teammates as a reference throughout the duration the milestones of the project.

## 1.3 Reading Guide

How to read this document.

# 2 Background

## 2.1 Business Background

The client of this project is Professor Mieszko Lis, a professor in the Electrical and Computer Engineering department at the University of British Columbia (UBC). A particular research focus of Dr. Lis is the acceleration of (traditionally software-based) machine-learning (ML) models through the use of dedicated hardware.

The fabrication of custom-designed hardware is often prohibitively expensive - the manufacture of a single application-specific integrated controller (ASIC) can easily cost millions of dollars and take several years to complete. This is inherently uncondusive to the quick and iterative prototyping required by small-scale research projects like those run by Dr. Lis. To mitigate this constraint, Dr. Lis would like to use Field Programmable Gate Arrays (FPGAs) - effectively programmable analogs of a real circuit - to prototype his ML hardware designs. Actual hardware designs can be (rapidly) modeled on an FPGA, which acts exactly like a traditional circuit, allowing for rapid iterative prototyping. Once a finalized design has emerged from the prototyping process, it can then be manufactured into an actual hardware chip, if desired.

## 2.2 Project Context

In late 2020, Dr. Lis will undertake a research project assessing the feasibility of converting software-based machine-learning models into efficient hardware designs through the use of High-Level Synthesis (HLS) tools.

Dr. Lis would like to obtain a drone-mounted, FPGA-based demonstration platform in order to demonstrate the mobility and applications of his research.

In addition to constructing the demonstration platform, Dr. Lis would like an initial machine-learning application implemented on the device. This initial application will use a drone-mounted video camera to identify pedestrians through the use of machine learning, wirelessly transmitting the video and extracted pedestrian information to an external device. Dr. Lis will use this developed application as a starting point for further modifications/improvements stemming from his research. Additionally, the application could be used for a wide variety of purposes as-is, including disaster response, wildlife management, and demographic studies.

The student team selected to undertake this project is comprised of five students in the Capstone Program offered by the Department of Electrical and Computer Engineering at the University of British Columbia.

### 3 Objectives, Constraints, & Goals

This section elaborates the objectives to be pursued in the project, constraints that limit the project scope, and the goals to be achieved.

The definition that the student team will use will be as follows:

- **Objectives:** Also referred sometimes as *requirements*. These are a list of *yes-or-no* qualities that the student team would like to achieve in order to consider the project as “successful”.
- **Constraints:** The constraints are limiting factors that affect the variability of our success, and to what extent our goals can be achieved.
- **Goals:** Goals are quantitative specifications that would be ideal to achieve; they are the target specification the student team is aiming for, but is not necessary for the project to be considered successful.

#### 3.1 Objectives

The main objectives have been separated into three categories.

##### Machine Learning Implementation on the FPGA

- The FPGA and its connected hardware receives usable video data.
- Adequately implemented datapath and or processor to facilitate data-flow.
- Adequately implemented machine learning model such as CNN, RNN, or YOLO in hardware.
- Video can be processed in real time with help of reduced frame rates and or resolution.

##### Integration of Electronics with the Drone

- The total package of the electronics and hardware (that is, equipment used to gather, process, and transmit video data) is reasonably compact and deployable on a drone.
- Total power draw is reasonable.
- Total power output (heat) is reasonable.

##### Video Transmission to Ground Station

- Video stream is able to be received from the ground station along with processed ML data.

- Transmission in the chosen frequency bands is legal (no further actions required if 2.4GHz or 5.2GHz is used).

## 3.2 Constraints

In pursuit of the objectives listed above, the main constraints relating each main objective is as follows:

The most important constraints are the non-technical ones. The project described here is considered very large in scope. The limited 8-months period is extremely limiting and thus will affect design decisions. The budget is a significant constraint which will affect the components and parts the student team chooses.

The FPGA is a highly adaptive device that can deliver almost-ASIC level speed but a large number of logic elements are required to implement a large model such as a machine learning - computer vision - model. The inherit constraints with FPGA RTL designs such as timing, area, and power constraints will ultimately limit the processing throughput of the video data. Bottleneck for limiting processing resolution and frequency (frame rate).

Common machine learning models for computer vision is designed for GPU to maximize parallel computing. The FPGA cannot match GPU data throughput, thus the limited amount of logic elements would also constrain our architecture design.

The physical form factor of the payload (FPGA, electronics, camera, and transmitter) is an important constraint. Since the purpose of the project is to validate the viability, the payload should be light and compact such that it can be carried on a drone for a reasonable flight time to carry out applications.

The bandwidth of video transmission is also limited by transmission frequency. The range of transmission is constrained by transmission power which is limited by power draw and heat.

## 3.3 Goals

The main goal is by the end of the project, have a working implementation of a ML computer vision model, capable of object detection and spacial object tracking implemented on a commercial FPGA. The number of logic elements required to implement video processing, video transmission, and other data tasks are within the hardware limit allowed by the budget. The implementation is capable of being mounted on a drone and perform ML tasks continuously and autonomously. Both the unprocessed (raw) and processed data are to be transmitted wirelessly using 2.4GHz WiFi to a ground station, which could be a laptop computer, or a mobile phone. The transmitted video data should have reasonable quality of at least 640x480 resolution and at a reasonable frame rate – at least 10 fps. The total take-off weight of the drone along with the processing hardware should not exceed 25 kilograms (as specified by Transport Canada, a pilot with *Basic Operations* certificate or *Advanced Operations* certificate cannot operate a drone heavier than 25kg). Lastly, the flight time of the drone is at least 1 minute and can fly to an altitude of at least +10m to prove the viability of the concept explored in this project.

## 4 Project Plan

This section outlines a preliminary responsibilities and tasks to be carried out.

### 4.1 Final Milestone & Ultimate Deliverables

This project concludes on **April 3rd, 2020**, at which point the client and the instructors will receive the following project deliverables.



#### 4.1.1 Hardware Artifacts

1. **Drone Prototype:** A fully integrated FPGA computing platform mounted on a remote-controlled drone that can capture video using an on-board camera. The computing platform utilizes FPGA-based neural network accelerators to process the video data and detect and track one or more designated objects. The computing platform may optionally be equipped with a co-processor. The platform transmits video data and associated machine learning metadata to a ground station.
2. **Ground Station Prototype:** A system that receives wireless video data and machine learning metadata from the drone and display video and overlaid metadata in the form of *bounding-boxes*. The ground station system also log the received data to files for further research and analysis purposes.

#### 4.1.2 Document Artifacts

1. **Requirement Specification:** A document outlining the functional and non-functional requirements of the prototypes.
2. **Design Specification:** A document describing the high-level architecture and design of technical subsystems.
3. **Validation Specification and Results:** A document describing system testbenches, validation techniques, and validation and testing results.
4. **Operations, Maintenance, and Upgrades Specifications:** A document, similar to a operation manual, outlining installation instructions, recommended maintenance, and common troubleshooting guides.
5. **List of Deliverables**

#### 4.1.3 Other Artifacts

1. **Demonstrative Video**
2. **Oral Presentation and Poster**
3. **Project Repositories:** Repositories that include all source code, generated netlists, CAD designs, spreadsheets, and other documents.

### 4.2 Intermediate Milestones

There are three project milestones to track project progress. Milestones necessitate the delivery in-progress documents listed in section 4.1.2. Each milestone contains an oral presentation which summarizes the project progress up to the milestone.

#### 4.2.1 Milestone I

By milestone I (**October 15th, 2019**), the team finishes and submits the *Project Proposal (this document)*. The document outlines the [baseline agreement among all stakeholders with regards to what is to be accomplished.]FIXME

#### 4.2.2 Milestone II

Milestone II (**November 25th, 2019**) is the first prototype review. The review features initial progress in camera interface and machine learning accelerator implementations on the FPGAs. If the project progress is on track or ahead, we will demonstrate video- capturing implementation synergizing with onboard ML model. Otherwise, we will demonstrate these components functioning independently.

#### 4.2.3 Milestone III

Milestone III (**February 10th, 2020**) is the second prototype review. The review features improved ML accelerator and drone implementations. We will focus on effort invested into video transmission, power supply circuitry, and batteries.

### 4.3 Major Responsibilities

This subsection covers the major responsibilities expected from the student team and the client.

#### 4.3.1 Team Responsibilities

The student team is responsible for day-to-day development, management, and operation of the project by coordinating group and client meetings. The team is responsible for the final delivery of all artifacts listed in section 4.1. The team is expected to conduct research from academic or industry sources for implementation methods, techniques, or processes. Finally, the team is responsible for managing project finances, inventory, and acquisition of key material, electronics, and hardware.

#### 4.3.2 Client Responsibilities

The client is responsible for being available to meet in-person or online given a reasonable notice of one week. They are expected to provide (reference to) necessary education or training material. They are also expected to provide additional financial support if needed (see section 4.5 for more detail).

### 4.4 Schedule

#### 4.4.1 Major Schedule

FIXME: we should add a timeline chart with labels of milestone I, II, and III on it. Also on the timeline is approximately how long each specific item/task (e.g. Transport Canada license training, or waiting for parts) is going to take.

Milestone I - 1 week

Milestone II - 6 weeks

Milestone III - 12 weeks

#### 4.4.2 Weekly Schedule

TODO: also make a weekly schedule outlining our availability (or lack of, due to courses).

Weekly team meeting on Tuesday at 3:30PM.

Weekly meeting with instructor on Thursday at 4:30PM.

Client meeting: by appointment.

#### 4.5 Budget

The department provides a budget of \$650.

The following table lists the items required for this project.

Table 1: Required items

Item	Estimated Price	Remark
Camera	\$30	<a href="#">Camera Module</a>
Transceiver	\$10	<a href="#">2.4G Transceiver</a>
Drone	\$150	<a href="#">JJRC H68 Drone</a>
FPGA	\$300	Price estimated based on the price of the DE1 board

#### 4.6 Quality Goals

##### 4.6.1 Efficiency

The power of the whole platform is reasonable to ensure at least 10 mins of flight time.

##### 4.6.2 Performance

The throughput of the video processing unit is more than 30 frames per second.

##### 4.6.3 Accuracy

The result from the machine learning model has an accuracy above 95% F.

##### 4.6.4 Availability

The product can work up to 10 meters altitude under sunny, cloudy, and windy conditions.

#### 4.6.5 Completeness

The final product is expected to be able to perform the object detection on board. It should provide clear evidence on the feasibility of the concept of deploy the machine learning model on board using FPGA.

#### 4.7 Risk Profile

Table 2: Risk profile

Risk description	Likelihood	Impact	Risk
FPGA logic capacity is not enough to implement our solution.	0.5	0.5	0.5
Electronics and battery are too heavy for the drone to take off.	0.5	0.5	0.5
Drone cannot take off at all due to drone hardware failure.	0.5	0.5	0.5
Accidents that damage to drone and FPGA that leads to extra budget that we may not have.	0.7	0.7	0.7
Forced to buy cheaper components which leads to lower performance.	0.8	0.8	0.8
Deliveries is not meeting client's expectations	0.2	0.2	0.2
Knowledge and skill (Machine Learning and Computer vision) required for project is not sufficient.	0.5	0.5	0.5
Our scrum method is not efficient	0.5	0.5	0.5
Scope of the project is underestimated which leads to burn outs	0.5	0.5	0.5
Not have enough money.	0.5	0.5	0.5
Not enough time commitment from team member due to other courses.	0.5	0.5	0.5
Team is indecisive which leads to delay - management is not good enough to make a timely decision.	0.4	0.4	0.4
Team has lack of communication and make assumptions which leads to incompatibility	0.4	0.4	0.4
Documentation is not specific / complete.	0.7	0.7	0.7
Not enough time to work on documentation.	0.7	0.7	0.7
Peter become hostile	0.5	0.5	0.5
Scope of the project is overestimated which leads to diversification of vision of the project.	0.5	0.5	0.5
Not enough machine learning training data.	0.5	0.5	0.5
Arthur becomes hostile	0.5	0.5	0.5
Wilson becomes hostile	0.5	0.5	0.5
Illegal to fly drone.	0.3	0.3	0.3
New technology or research that changes the scope significantly	0.3	0.3	0.3
Technical debt paydown impact project timeline.	0.4	0.4	0.4
Software license does not allow our application to be delivered.	0.3	0.3	0.3
Key hardware components not available	0.2	0.2	0.2
Machine learning resources lack documentation	0.3	0.3	0.3
Camera lack documentation	0.3	0.3	0.3

*Continued on next page*

Table 2 – Continued from previous page

<b>Risk description</b>	<b>Likelihood</b>	<b>Impact</b>	<b>Risk</b>
FPGA lack documentation	0.3	0.3	0.3
Delivered documentation not adequate for usage and upgrade.	0.5	0.5	0.5
Selected software methodology creates issues with team member that decreases productivity	0.2	0.2	0.2
Legal changes significantly affect project - drones in particular	0.2	0.2	0.2
The final product cannot be maintained or extended.	0.7	0.7	0.7
Client becoming hostile	0.2	0.2	0.2
Client wants to modify scope and requirements that leads to delays or cut features.	0.2	0.2	0.2
Development environment is inadequate.	0.2	0.2	0.2
Sabotage	0.1	0.1	0.1
Loss of client	0.1	0.1	0.1
Loss of team member	0.1	0.1	0.1
Equipment shipment is delayed or lost.	0.1	0.1	0.1
Client is not available enough to provide significant help.	0.1	0.1	0.1
Competitive offerings affect project requirement	0.1	0.1	0.1
Camera doesn't work with FPGA	0.1	0.1	0.1
FPGA doesn't work with transmission systems	0.1	0.1	0.1

## 5 Approval

### 5.1 Acceptance Statement

### 5.2 Client Identification

### 5.3 Capstone Team Identification