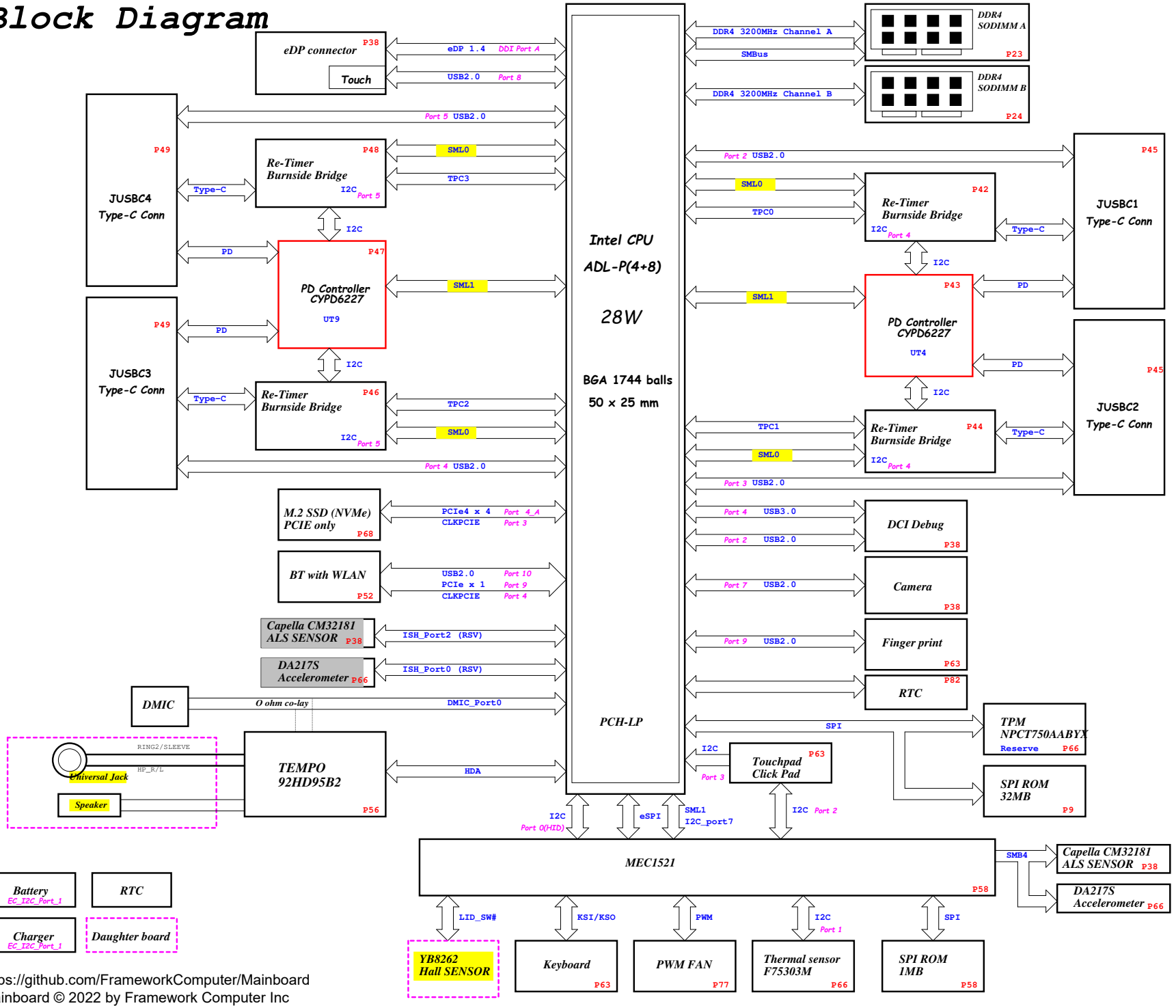
Block Diagram



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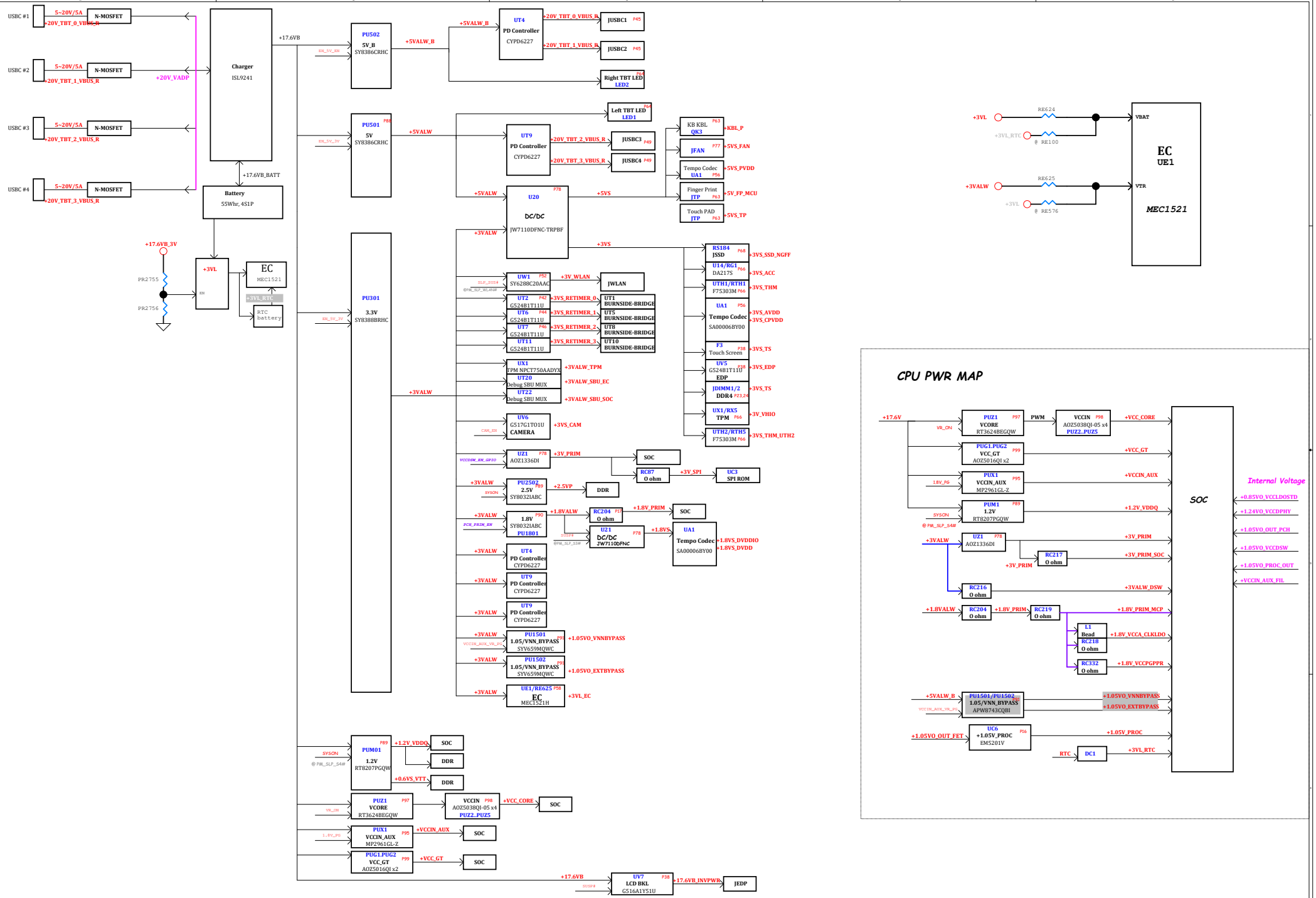

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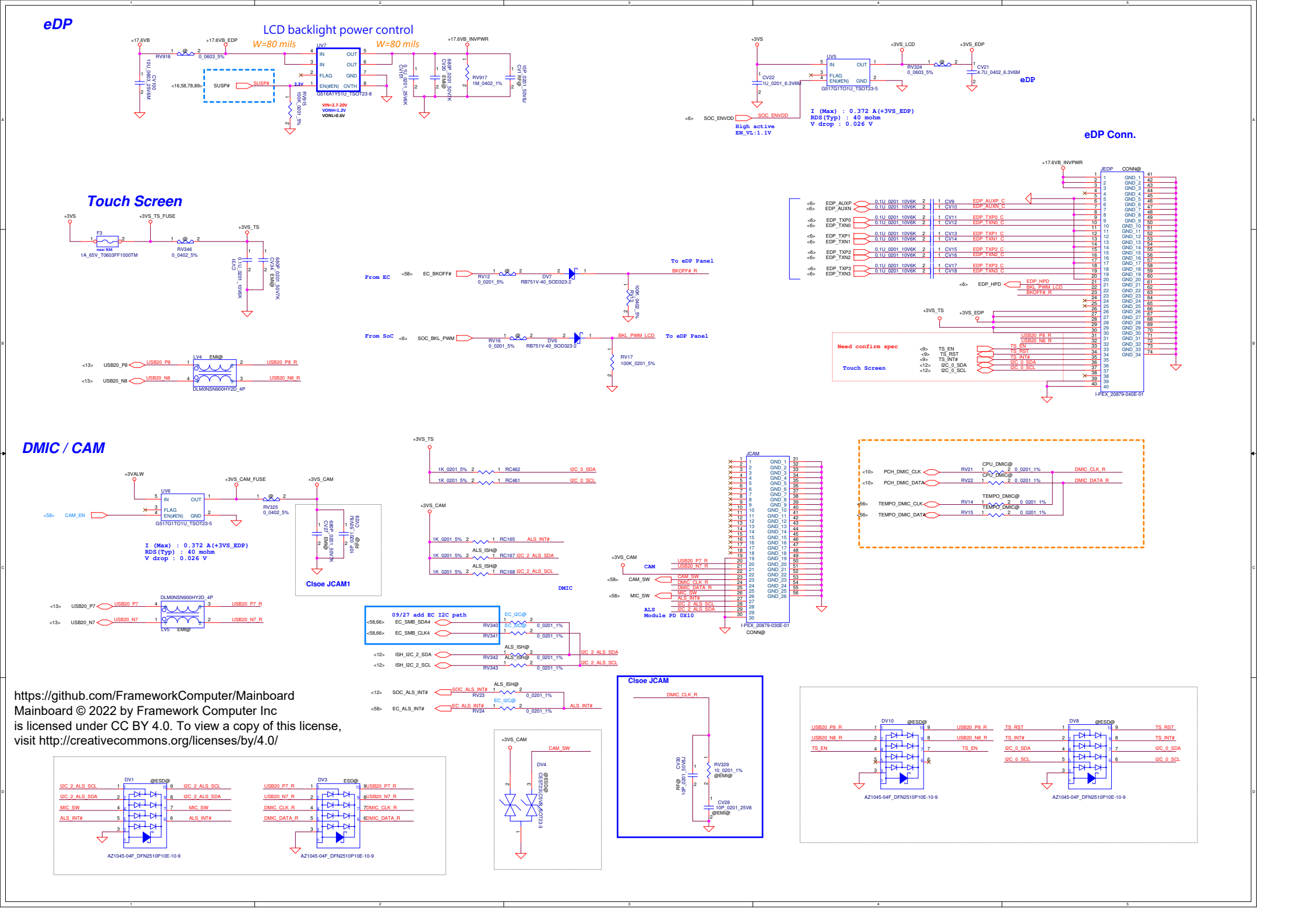

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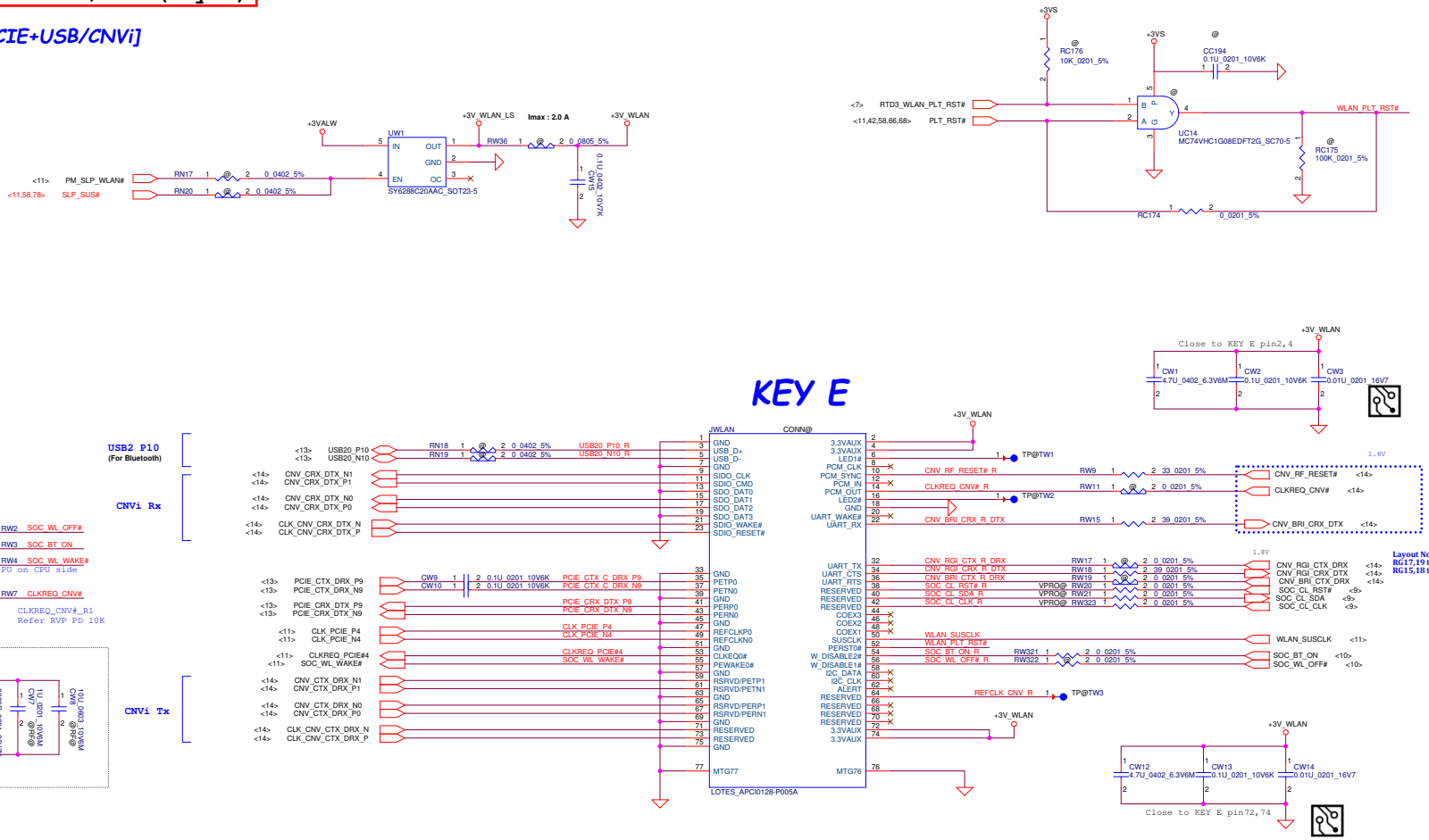
Main Func = NGFF WLAN / BT (Key E)

[PCIE+USB/CNVi]



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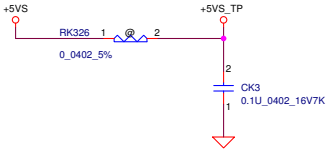
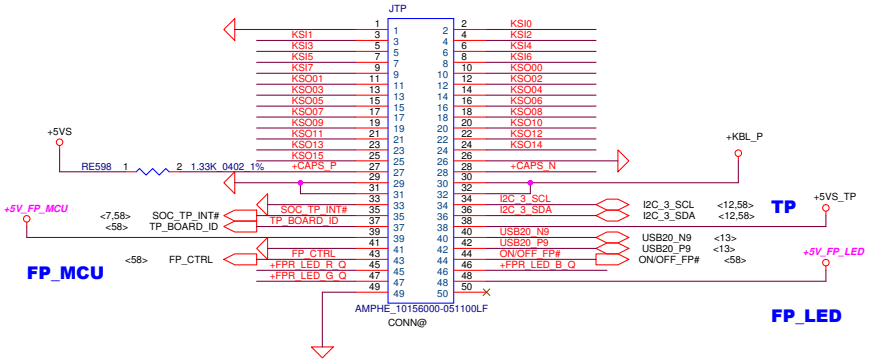
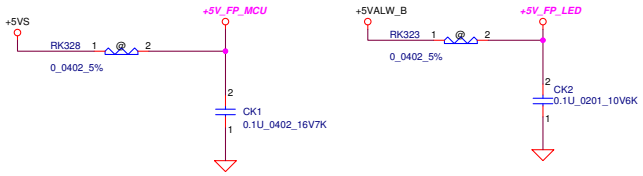
Keyboard

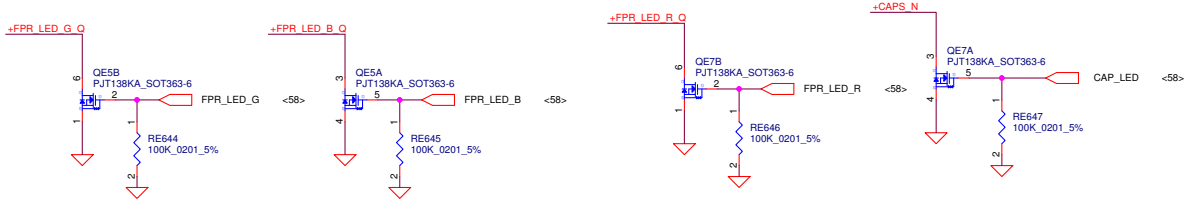
Main Func = TP



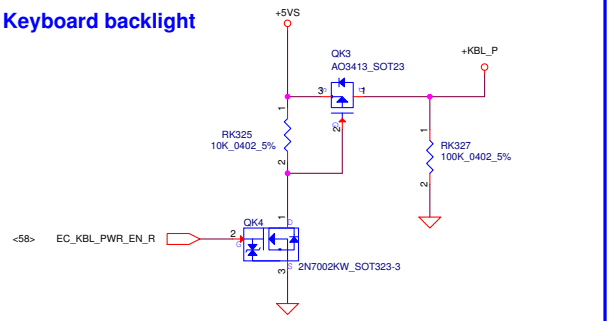


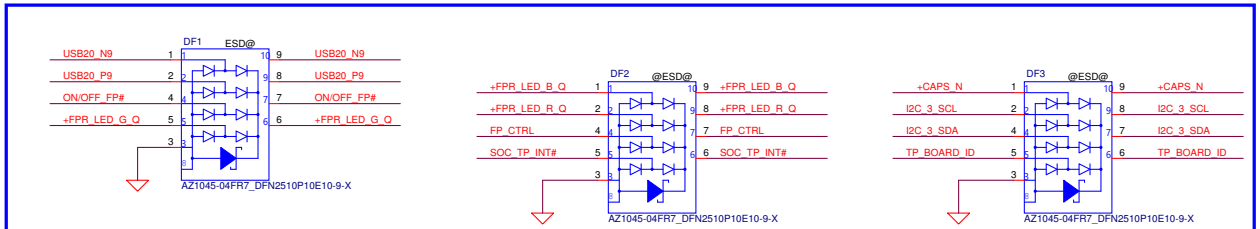
Main Func = FPR





Keyboard backlight





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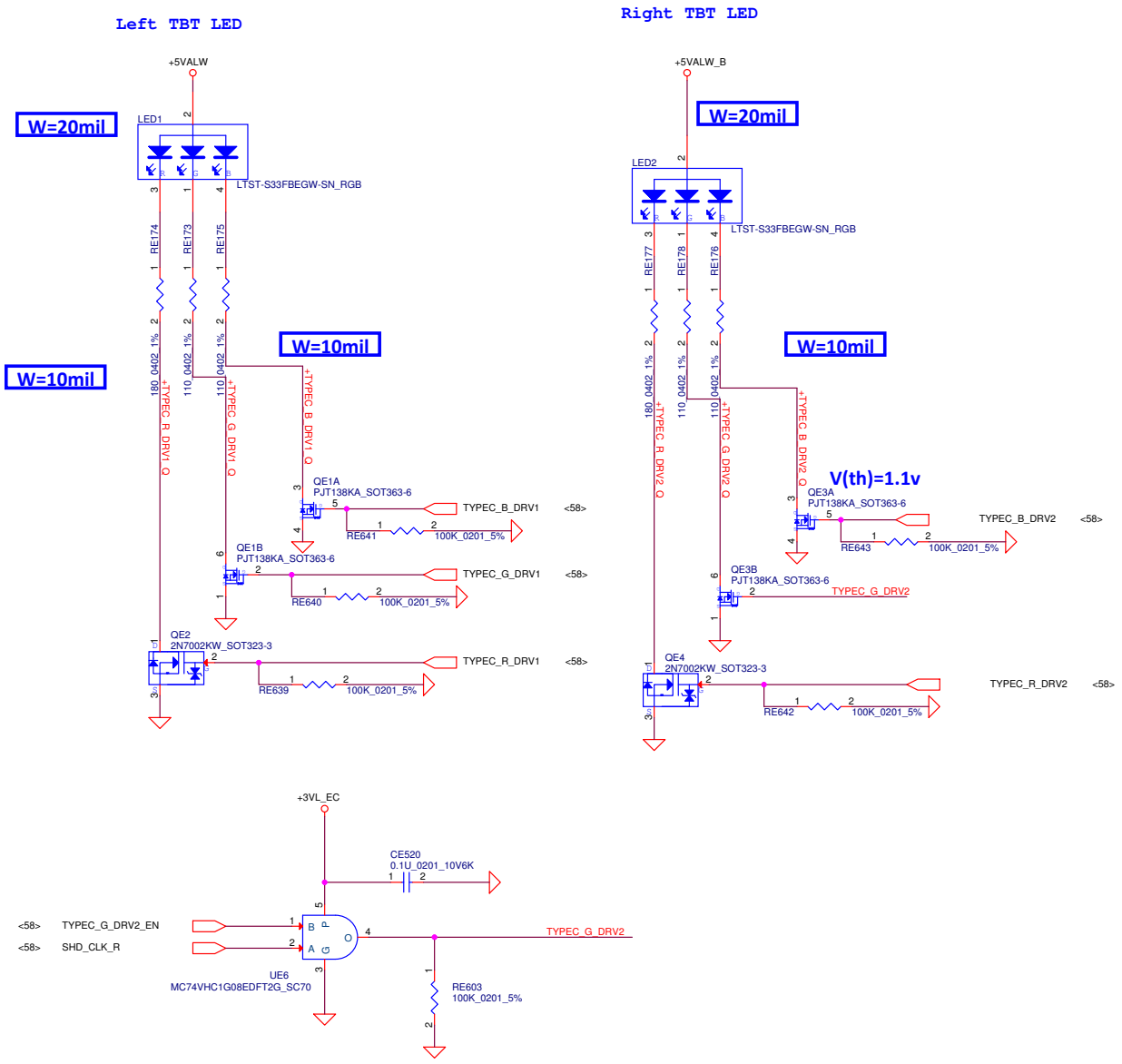
Main Func = LED



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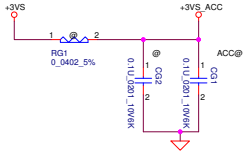
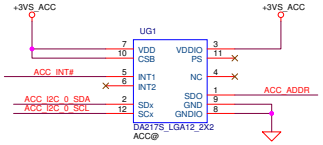
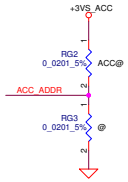
Main Func = Sensor_TPM_LED

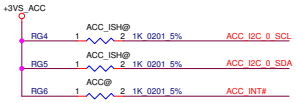


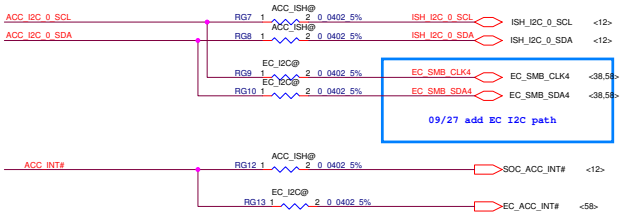
G-Sensor



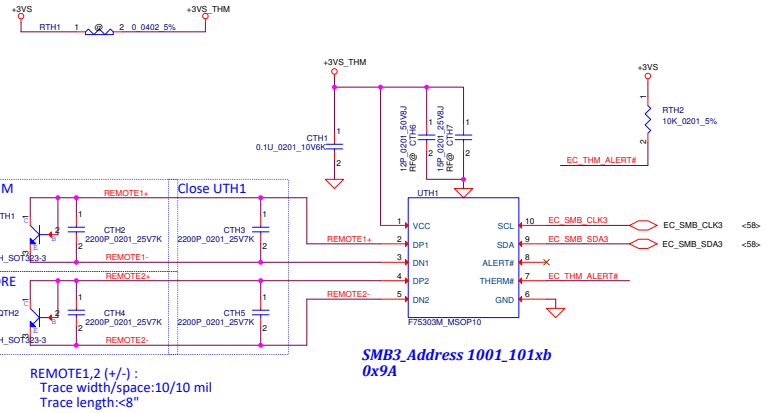


Read 31h
Write 30h





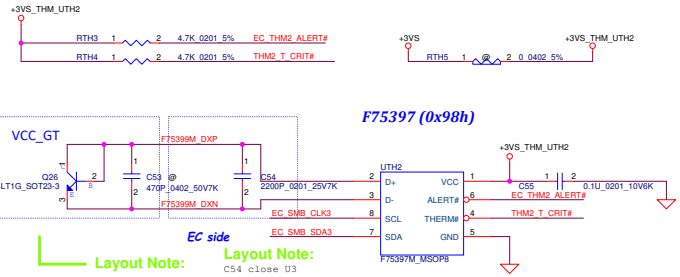
THERMAL SENSOR

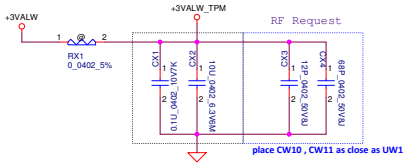


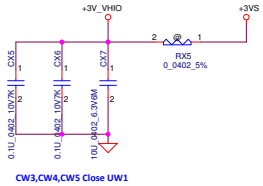
REMOTE1,2 (+/-) :
Trace width/space:10/10 mil
Trace length:<8"

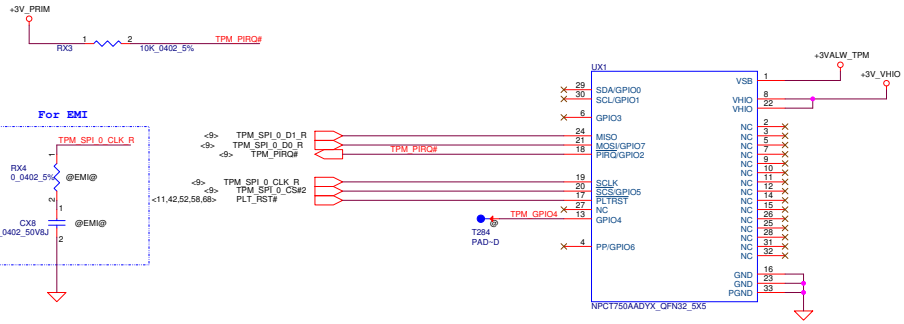
SMB3_Address 1001_101xb
0x9A

THERMAL SENSOR (Reserve for VCC_GT)









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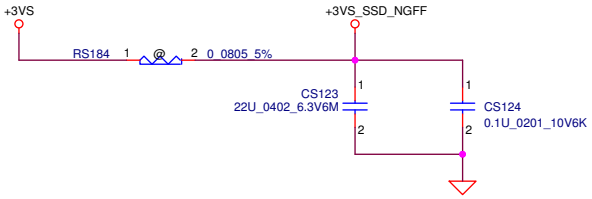
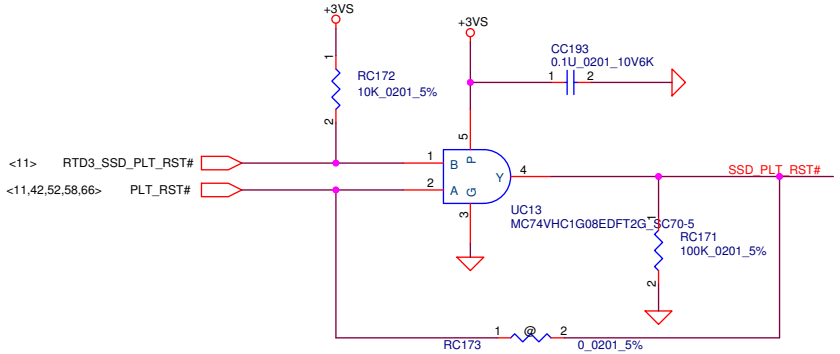
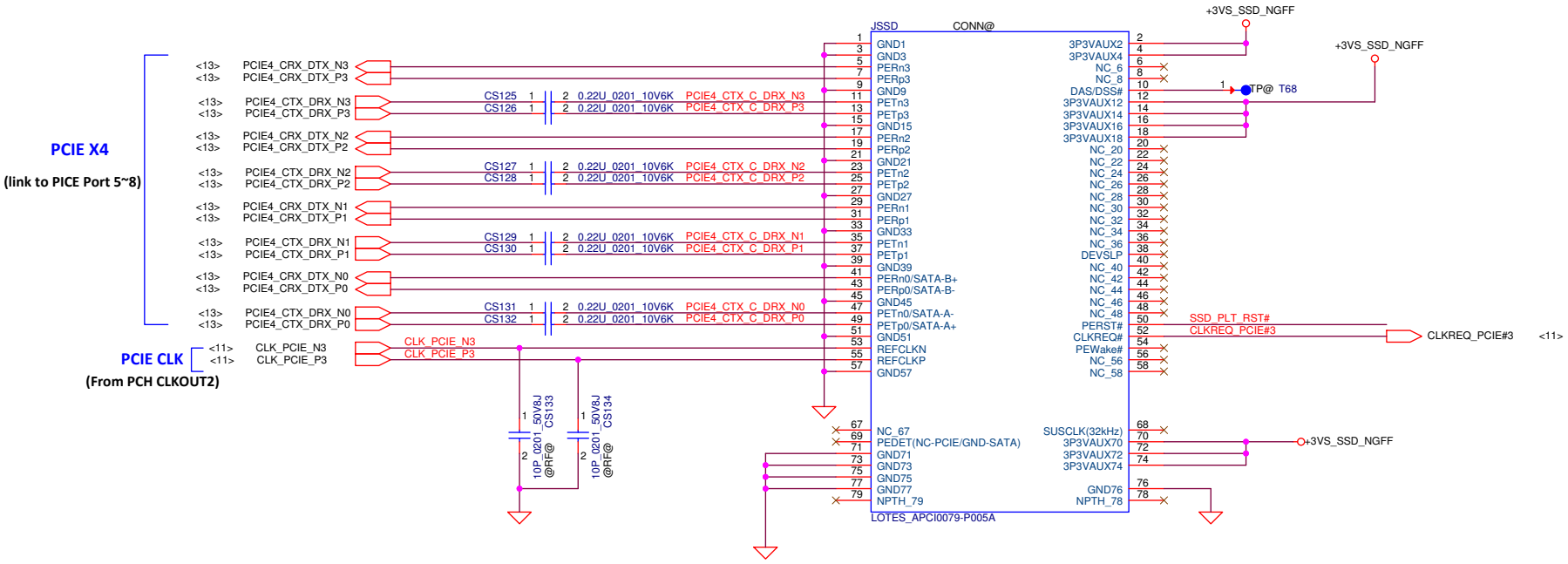
NGFF SSD (KEY M)

PCIE Gen4





KEY M



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Power Button

ON / OFF switch

TOP Side

SW1

TST71A-N-220-S017 SPST H0.6 2P

ON/OFF_BTN#

ON/OFF_BTN# <58>

PU on EC side

DE1
CEST523NC5VB_SOT-523-3
@ESD@

QAD on/off test purpose

Bottom Side (H=1)

NPI@

SW2

TST71A-N-220-S017 SPST H0.6 2P

Test Only

CPU Fan Control Circuit

+3VS

R228
10K_0201_5%

<58> FAN_SPEED0

C149
1000P_0201_50V7K
@

place as close as EC

+5VS

@
C147
10U_0402_6.3V6M

R247 1 @ 2 0_0402_5%

C148
0.1U_0201_10V6K

+5VS_FAN

+3VS

R227
10K_0201_5%

FAN_PWM0

<58> FAN_PWM0

CONN@
JFAN

+5VS_FAN
FAN_SPEED0
FAN_PWM0
GND1
GND2

HEFEN_AWA02-S04FIA-HF
SP02001S900

5/4 update JFAN symbol

+3VL_RTC_EC

RE529
100K_0201_5%

SW3

SPVR310200_4P

CHASSIS_OPEN_Q#

CHASSIS_OPEN_Q# <11,58>

monitor C cover open

normal close

SCREW

H1 H_3P7-G

H2 H_3P7X4P0-G

H3 H_3P7X4P0-G

H4 H_4P4-G

H5 H_4P6-G

H6 H_4P4-G

H7 H_4P4-G

H8 H_4P4-G

H9 H_3P3-G

H10 H_3P3-G

H11 H_2P9X2P6N

H13 H_4P2-G

H14 H_2P6

Fiducial Mark

FD1 @FIDUCIAL

FD2 @FIDUCIAL

FD3 @FIDUCIAL

FD4 @FIDUCIAL

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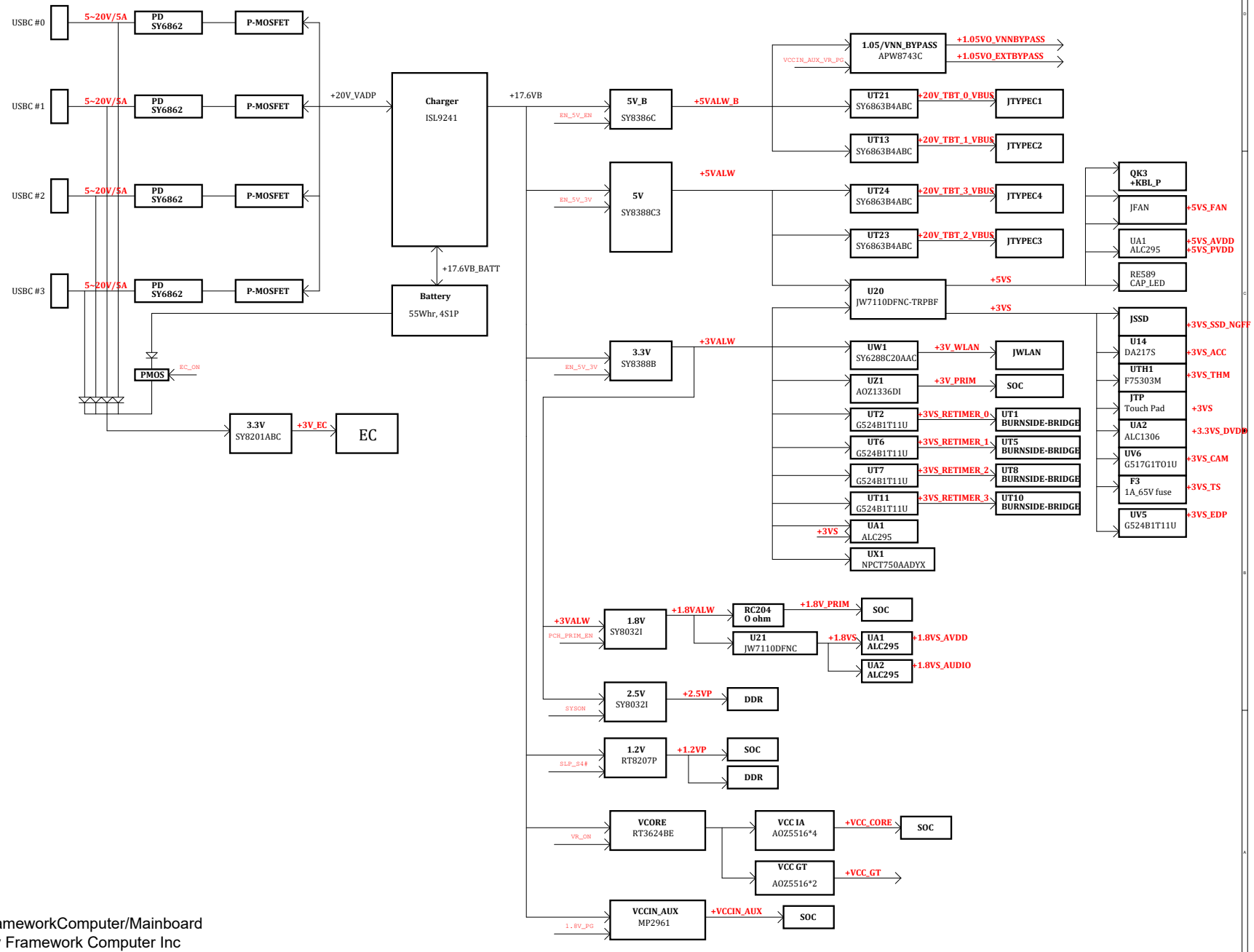

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