DON BOSCO INSTITUTE OF TECHONOLGY, KURLA, MUMBAI Department of Computer Engineering, (Odd semester, 2016-17)						
		Departmen		Comps		
Course Name:	Applied Mathematics III			Comps		
Course Code	CSC301					
Faculty Name:	Manisha G.					
Year	2	Sem	III			
CO Number				Course Outcome		
CSC301.1	(ii) Obtain Inv (iii) Define Ka	olace Trans verse Lapla arl Pearson'		given simple function of 's' ent and Spearman's rank correlation coefficient		
CSC301.2	Students will be able to (i) Obtain the Laplace Transforms, Inverse Laplace Transforms of combinations of standard functions using the properties of Laplace and Inverse Transforms. (ii) Obtain Karl Pearson's correlation coefficient and Spearman's rank correlation coefficient (iii) Obtain the equations of two lines of regression (iv) Fit the curve by the method of least squares (v) Understand the properties of orthogonal and orthonormal functions (vi) Obtain Fourier series, half-range Fourier series and Fourier sine and cosine series of periodic functions. (vii) Obtain toemplex form fourier series of functions. (viii) Obtain the Z Transforms, Inverse Z Transforms of combinations of standard functions using the properties of Laplace and Inverse Transforms. (ix) Find Cauchy – Riemann equations to verify if a function is analytic (x) Obtain the harmonic conjugate and orthogonal trajectory of given family. (xi) Define Conformal mapping and obtain the image under given standard transformation (xii) Define and obtain bilinear transformation and its fixed points. Students will be able to					
	(i) Apply Laplace and Inverse Laplace transfor (ii) Solve initial and boundary value problems					
Course Name:		OOPI	M			
Course Code		CSC30	02			
Faculty Name:		Mayura Ga	avhane			
Year	2	Sem	III			
CO Number				Course Outcome		
CSC302.1 CSC302.2	Solve computational problems using basic constructs like if-else, control structures, array, strings. Apply Object Oriented programming concepts on real world scenarios.					
CSC302.2	Implement rel	lationship b	oetween classes such	as association, aggregation, interfaces		
CSC302.4				ams on exceptions, multi-threading and applets		
Course Name:		Data Structures				
Course Code		CSC30		•		
Faculty Name:		Imran Ali				
Year CO Number	2	Sem	III	Course Outcome		
CSC303.1	Students will	he able to i	mplement various lir	near and nonlinear data structures.		
656505.1				e insertion, deletion, searching and traversing on various data		
CSC303.2	structures.		<u>-</u>			
CSC303.3				rting technique for given problem.		
CSC303.4	Students will be able to select appropriate sear			arching technique for given problem. Incepts in various domains like DBMS		
CSC303.5	and Compiler		11 /	reche in sations domains like ppisis		
CSC303.6	Students will be able to choose appropriate data structure for specified problem domain.					
	A 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1					
Course Name:		DLD				
Course Code		CSC30	•			
Faculty Name:	2	Deepali Ka	•			
Voor	2	Sem	III			
Year CO Number				Course Outcome		
CO Number CSC304.1		prehend th	e theory and logic be	Course Outcome chind designing digital electronic circuits.		
CO Number	Ability to com			Course Outcome chind designing digital electronic circuits. adecimal calculations and conversions.		
CO Number CSC304.1	Ability to com	orm binary	,decimal,octal & hex	chind designing digital electronic circuits.		
CO Number CSC304.1 CSC304.2 CSC304.3 CSC304.4	Ability to com Ability to perf Ability to appl Ability to appl the underlying	form binary ly & design ly & design g logic to so	decimal, octal & hex combinational circui synchronous and as olve real world probl	chind designing digital electronic circuits. adecimal calculations and conversions. its and to verify the circuit with respect to the underlying logic. ynchronous sequential circuits and to verify the circuit with respect to ems		
CO Number CSC304.1 CSC304.2 CSC304.3	Ability to com Ability to perf Ability to appl Ability to appl the underlying	form binary ly & design ly & design g logic to so	decimal,octal & hex combinational circui synchronous and as	chind designing digital electronic circuits. adecimal calculations and conversions. its and to verify the circuit with respect to the underlying logic. ynchronous sequential circuits and to verify the circuit with respect to ems		
CO Number CSC304.1 CSC304.2 CSC304.3 CSC304.4 CSC304.5	Ability to com Ability to perf Ability to appl Ability to appl the underlying Ability to cons	form binary ly & design ly & design g logic to so struct, test	decimal, octal & hex combinational circui synchronous and as olve real world probl and debug digital ne	chind designing digital electronic circuits. adecimal calculations and conversions. its and to verify the circuit with respect to the underlying logic. ynchronous sequential circuits and to verify the circuit with respect to ems		
CO Number CSC304.1 CSC304.2 CSC304.3 CSC304.4	Ability to com Ability to perf Ability to appl Ability to appl the underlying Ability to cons	form binary ly & design ly & design g logic to so	decimal, octal & hex combinational circui synchronous and as olve real world probl and debug digital neo	chind designing digital electronic circuits. adecimal calculations and conversions. its and to verify the circuit with respect to the underlying logic. ynchronous sequential circuits and to verify the circuit with respect to ems		
CO Number CSC304.1 CSC304.2 CSC304.3 CSC304.4 CSC304.5 Course Name:	Ability to com Ability to perf Ability to appl Ability to appl the underlying Ability to cons	form binary ly & design ly & design g logic to so struct, test	decimal, octal & hex combinational circui synchronous and as olve real world probl and debug digital new ructures	chind designing digital electronic circuits. adecimal calculations and conversions. its and to verify the circuit with respect to the underlying logic. ynchronous sequential circuits and to verify the circuit with respect to ems		
CO Number CSC304.1 CSC304.2 CSC304.3 CSC304.4 CSC304.5 Course Name: Course Code	Ability to com Ability to perf Ability to appl Ability to appl the underlying Ability to cons	orm binary ly & design ly & design g logic to so struct, test Discrete Str	decimal, octal & hex combinational circui synchronous and as olve real world probl and debug digital new ructures	chind designing digital electronic circuits. adecimal calculations and conversions. its and to verify the circuit with respect to the underlying logic. ynchronous sequential circuits and to verify the circuit with respect to ems		
CO Number CSC304.1 CSC304.2 CSC304.3 CSC304.4 CSC304.5 Course Name: Course Code Faculty Name:	Ability to com Ability to perf Ability to appl Ability to appl the underlying Ability to cons	orm binary ly & design ly & design g logic to so struct, test Discrete Str CSC30 Priya K	decimal, octal & hex combinational circui synchronous and as olve real world probl and debug digital ner ructures 05 aul	chind designing digital electronic circuits. adecimal calculations and conversions. its and to verify the circuit with respect to the underlying logic. ynchronous sequential circuits and to verify the circuit with respect to ems		

CSC305.2	Infer the importance of generating functions in construction of recursive algorithms like Quick sort, Binary Search, Fibonacci series.			
CSC305.3	Correlate the concepts of discrete structures and their relevance within the context of computer science, in the area of data structures. (tree, graph)			
CSC305.4	Demonstrate a working knowledge of fundamental algebraic structures (e.g., groups, rings, and fields).			
Course Name:	ECCF			
Course Code	CSC306			
Faculty Name:	Sejal Chopra			
Year	2	Sem	III	
CO Number	Course Outcome			
CSC306.1	Ability to understand, describe and explain the basics of semiconductor devices (op-amps and FETs) in an electronic circuit and fundamental concepts for communication.			

CSC306.2

CSC306.3

CSC306.4

circuits

Ability to apply the knowledge of circuit working to conduct experiments and to obtain voltages ,current or waveforms and relate them at different points in electronic and communication circuits

Ability to estimate the voltages ,current or waveforms for given specifications in electronics and communication

Ability to justify the need of specific modulation process in an appropriate application by engaging them in self-learning /independent study through submission of a presentation and two page report.

Course Name:				Comps	
Course Name:	Microprocessor CPC501				
Faculty Name:	Ditty Varghese				
Year	3	Sem	V		
CO Number				Course Outcome	
CPC501.1	Ability to ide	ntify and de	escribe the various ar	chitectures of specific processors.	
CPC501.2	Ability to engage students in learning the prog			gramming of microcontroller.	
CPC501.3	Ability to use appropriate instructions to progra			ram a microprocessor to perform various tasks.	
CPC501.4	Ability to discuss and contrast architectures, w chips and peripheral chips for 16 bit 8086 mid			vays of interfacing supporting chips. Also, design system using memory croprocessor	
CPC501.5	Ability to engage students in self-learning activity/independent activity to give a presentation on "Enhancement in Performance for RISC AND CISC processors."				
Course Name:	Operating System				
Course Code		CPC5	02		
Faculty Name:	Amiya Kumar Tripathy				
Year	3	Sem	V		
CO Number				Course Outcome	
CPC502.1			Roles and Design		
CPC502.2				as used scheduling of tasks in operating systems	
CPC502.3 CPC502.4			how computing resortherent in operating s	arces (e.g., CPU, Memory, etc.) are managed by the operating system	
G1 G302.7	-			approaches to operating system design, and identify and report	
CPC502.5			ces when solving real		
Course Name:		SOO	AD		
Course Code		CPC5	03		
Faculty Name:		Shafaque	e Syed		
Year	3	Sem	V		
CO Number				Course Outcome	
CPC503.1	Understand an information system and system development life-cycle.				
CPC503.2	Understand a	Understand and apply techniques for system analysis and feasibility analysis			
CPC503.3	Apply key mo	odeling con	cepts to both traditio	nal and object oriented approach	
CPC503.4	Design and implement a candidate system following a design methodology and architectural strategy				
Course Name:		Computer N	Networks		
Course Code	CPC504				
Faculty Name:		Nilakshi	Joshi		
Year	3	Sem	V		
CO Number				Course Outcome	
CPC504.1	Conceptualiz				
CPC504.2 CPC504.3	Describe and analyze the hardware, software, components of a network and interrelations.				
CPC504.3	Describe and analyze the hardware, software, con Select and apply appropriate networking tools to				
CPC504.4			open source tool ns2		
CPC504.6	9 1			cal, social issues related to computer networking.	
			·		
Course Name:	Web		y Laboratory		
Course Code		CPL5			
Faculty Name:			dambari Deherkar		
Year	3	Sem	V		
CO Number	A1 111		. 1	Course Outcome	
CPL501.1 CPL501.2				n designing and developing Web Pages. Cascading Styles sheets. Build dynamic and interactive web pages using	
CPL501.2 CPL501.3				Cascading Styles sheets. Build dynamic and interactive web pages using rver side and client side scripting.	
CPL501.3			, ,,,,		
CPL501.5	Prepare simple applications on android platform Demonstrate teamwork , technical communication through verbal and written communication.				
Course Name:		BCI			
Course Code	CPL502				
Faculty Name:		Mohi			
Year	3	Sem	V		
CO Number		44		Course Outcome	
CPL502.1			in both oral and wri		
CPL 502.2				ss the ability for life-long learning	
CPL502.3 CPL502.4			ities and professional		
CPL502.4 CPL502.5	Participate and succeed in campus placements a				
CPL502.6	Judiciously assess the macro/micro scenario and act ethically in any given situation Analyze the needs of the society and evolve engineering solutions for them				

Course Name:		DSF		Comps		
Course Code		CPC7				
Faculty Name:		Sejal Ch		-		
Year	4		•			
CO Number	4 Sem VII Course Outcome					
CPC701.1	The students will be able to learn, describe and assimilate information about the basic theory & manipulation of digital signals & systems, Discrete Fourier Transform, Fast Fourier Transform & applications involving Digital Signal processors.					
CPC701.2	The students will be able to discuss & summarize the different types of signal processing algorithms, stability of system, effects of different parameters on system output and basics of DSP processors.					
CPC701.3	The students will be to apply the use the signal processing algorithms in solving sums based on the DSP algorithm and concepts and decide the outcome of a system when system parameters are changed.					
CPC701.4	The students will be able to analyze the system given to them, understand the effect each parameter has on the output of a system and interpret the general pattern of a stable system.					
CPC701.5	The students will be able to design basic DSP systems by implementing them either theoretically or practically in a simulation environment or on a DSP kit.					
Course Name:		CSS	1			
Course Code		CPC7	02			
Faculty Name:	k	adambari I	Deherkar			
Year	4	Sem	VII			
CO Number				Course Outcome		
CPC702.1	Ability to eyp	lain the pri	nciples and practices	of cryptographic techniques.		
CPC702.1			<u> </u>	neric security threats and vulnerabilities and identify and analyze		
SF G/02.2				·		
CPC702.3	Recognize the	particular security problems for given application. Recognize the application of security techniques and technologies and apply them in solving security problems in practical systems.				
CPC702.4	Ability to des	ign system	security solution for	given situation.		
CPC702.5	Ability to use	latest tools	and technologies in	the field of computer and system security.		
	, ,			1 7		
Course Name:		AI				
Course Code		CPC7	03			
Faculty Name:		Abhishek V				
Year	4	Sem	VII			
CO Number	4	Seili	VII	Course Outcome		
	G. 1	1 11 .	1 1 1 1 1			
CPC703.1		rstanding of AI building blocks presented in intelligent agents.				
CPC703.2 CPC703.3	Students will be able to solve searching problems by applying various search methods. Students will be able analyze efficiency of AI approaches to knowledge– intensive problem solving.					
CD CECC 4	1					
CPC703.4	Students will be able design models involving reasoning, uncertainty and the use of unreliable information.					
CPC703.5	Students will be able design and develop the A					
CPC703.6	Ability to eng	age in self	study/ independent s	study and deliver presentation on topics related to course.		
Course Name:		IP				
Course Code		CPE70)23			
Faculty Name:	Mayura Gavhane					
Year	4	Sem	VII			
CO Number						
CPE7023.1	Explain funda	amental cor	ncepts of a digital ima	age processing systems and image enhancement techniques		
CPE7023.2				nd binary image processing techniques using openCV with C/C++.		
CPE7023.3				solve image compression and decompression techniques		
CPE7023.4				niques and also will be able to apply these techniques to real world		
Course Name:		SA]		
Course Code		CPE70		1		
Faculty Name:		Kalpita Wa		1		
Year	4	Sem Sem	VII	1		
	7	oem	V 11	Course Outcome		
CO Number	Tris - 1	-l 1 1 1	1.1			
CPE7024.1	The students intensive app		ible to recognize the	architectural concepts in development of large, practical software		
	The student should identify the various methods, notations, tools, and proc			ods, notations, tools, and processes used to produce a software		
CPE7024.2	architecture					
CPE7024.3	The student s	hould be al	ole to design the soft	ware architecture for a software product and analyze the same		
CPE7024.4				cess of implementation and deployment of the software architectures		
CPE7024.5				actional properties and domain specific software architectures		
Course Name:		SC	3			
Course Code		CPE70	125	1		
Faculty Name:		Deepali Ka		1		
Year	4	_	-	-		
Year	4 Sem VII					

Year CO Number Sem

VII

Course Outcome

CPE7025.1	Ability to understand the difference between learning and programming and explore practical applications of Neural Networks (NN).				
CPE7025.2	Ability to analyze the fuzzy logic applications and design inference systems.				
CPE7025.3	Ability to design a Neuro-fuzzy network using the knowlege of Neural Network and fuzzy logic .				
CPE7025.4	Apply genetic algorithms to combinatorial optimization problems				
CPE7025.5	Ability to engage in self study /independent study and submit a report on topics related to course.				
Course Name:	Project I				
Course Code	CPP701				
Faculty Name:	Dipti Jadhav		dhav		
Year	4	Sem	VII		
CO Number	Course Outcome				
CPP701.1	To review literature for identifying and formulating project idea.				
CPP701.2	Analyze and assess information, methods and results for a real world purpose.				
CPP701.3	Design a system, component or process to meet a specified goal.				
CPP701.4	To demonstrate and apply project management principles				
CPP701.5	To effectively communicate the project idea and results to engineering community and society through oral presentation and technical writing.				
Course Name:	NTAL				
Course Code	CPL701				
Faculty Name:	Priya Kaul				
Year	4	Sem	VII		
CO Number	Course Outcome				
CPL701.1	To demonstrate the use of network-based tools for network analysis				
CPL701.2	To analyze and evaluate various techniques for network scanning				
CPL701.3	To differentiate various network vulnerabilities and their countermeasures				
CPL701.4	To apply appropriate tools to simulate intrusion detection system				
CPL701.5	To create a firewall and evaluate various security parameters				
CPL701.6	To develop improved communication and collaborative skills in meeting security threats as a team member or team leader				