

How are you getting on with your scientific work now?

- I have been working on vivado (Xilinx) system to program FPGA, I tried to using old method of working on FPGA on personal computer and it didn't work well so I transferred to using online GPUs as proposed by professor Cheon's. It wasn't appropriate using my computer gpu, due to many reasons including odd results.
- My time module is running but its not giving outputs as compared to Professor Cheon's work. So, I am working on improving it.

I just sent you two papers. Have you read them before?

- Yes, I have read them.

FPGA cannot support the work of Craterlake?

- FPGA can support craterlake, but FPGA are inefficient on FHE, this is because GPU's Lake modular arithmetic, and cannot implement all to all operation like NTT's and automorphisms efficiently, and their on-chip memories are too small to enable sufficient reuse. But as per current research craterlake outperforms CPU by a gmean of more than 4000 and best FHE accelerator by more than 11 taking note of same conditions and power supply.