## 1 MILESTONE 4 – THE REPORT

## 1.1 RUBRIC

Group number		Mark	Subtotal
Report	Introduction		10
	System level design: Overview of design concepts and justification for choices.		15
	<ul> <li>Power calculations         <ul> <li>Power supply design and schematics (including safety)</li> </ul> </li> <li>Microcontroller resource allocation</li> <li>Design and Schematics of circuits with Vero/PCB layout</li> </ul>		20
	Motion control design, implementation, and results Include  • Pseudicode functions • UML or flow diagrams of code or MATLAB Simulink layout e.t.c • Results and justifications		15

	Line sensing design, implementation, and results Include     Pseudocode functions     UML or flow diagrams of code or MATLAB Simulink layout etc.     Results and justifications	15
	Treasure Maze solving algorithm design, implementation, and results include  • The modular design of software down to function calls and including interrupt / real-time structure  • UML diagrams (flow diagram of code)  • Pseudocode functions  Conclusion	10
Penalty		Penalty
Total		100
		100

Table 4 Milestone 4 rubric