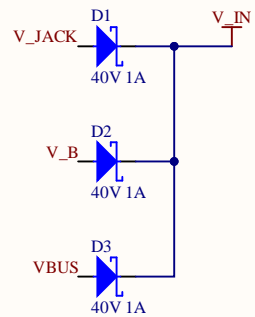
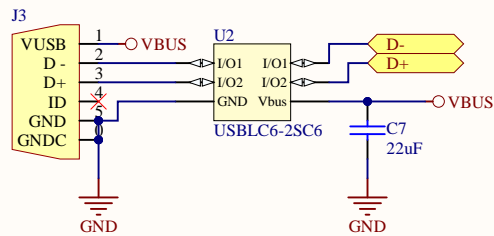
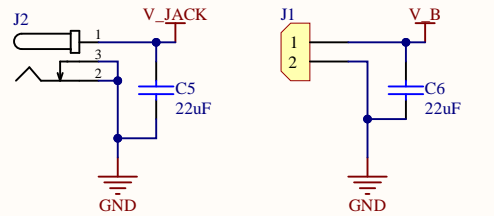
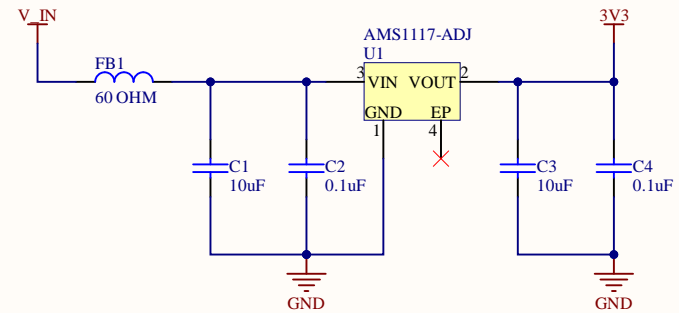


## POWER INPUTS



## LDO



Title **Power and inputs**

Size: **A4**

Number: **2**

Revision:

Date: 30/03/2023 Time: 9:24:04 p.m. Sheet 2 of 6

File: C:\Users\Public\Documents\Altium\project6\000\_POWER\_MODULE.SchDoc



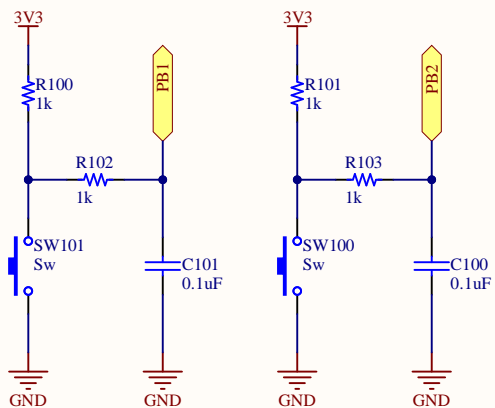
1

2

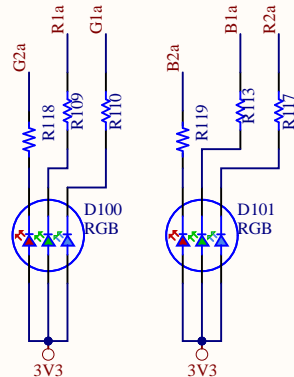
3

4

## push buttons

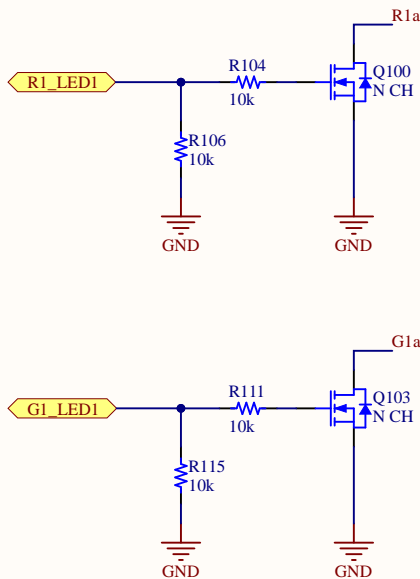


## LEDS

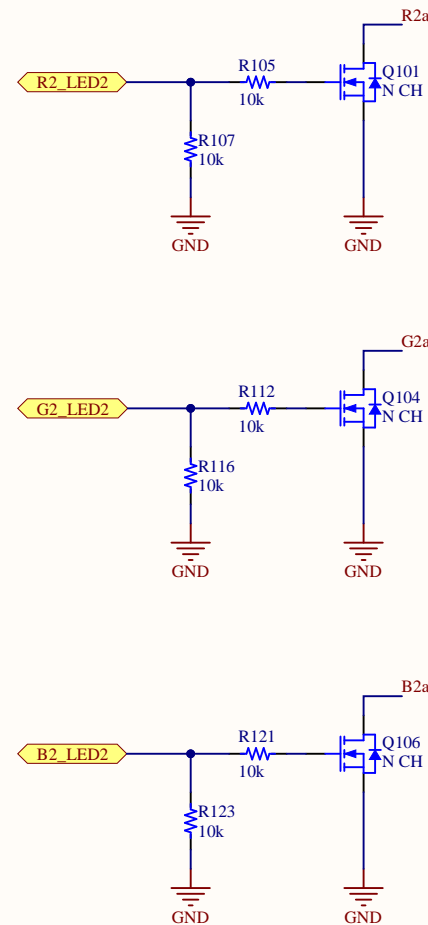


## driver LEDES

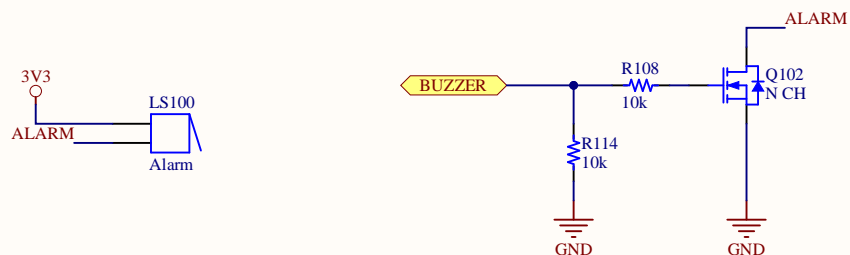
## LED 1



## LED 2



## Alarma

Title *H&M interface*

Size: A4

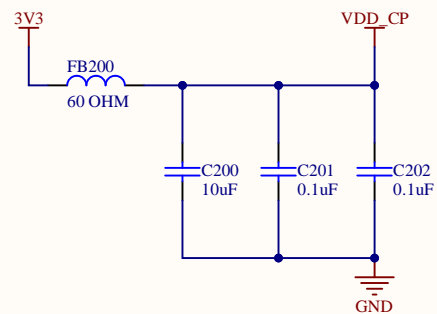
Number:3

Revision:

Date: 30/03/2023 Time: 9:24:04 p.m. Sheet 3 of 6

File: C:\Users\Public\Documents\Altium\project6\100\_H&amp;M\_interface.SchDoc





U200

VBUS\_B 8

VBUS GPIO.0/CLK 2

D- 5

D- GPIO.1/RS485 20

D+ 4

D+ GPIO.2/TXT 19

GPIO.3/RXT 19

TXT

RXT

VDD\_CP

R204

10k

9

11

RSTb

SUSPENDb

RTS

CTS

16

15

13

14

WAKEUP

TXD

SUSPEND

RXD

18

17

RX\_MCU

TX\_MCU

VDD\_CP

10

NC

7

6

VREGIN

VDD

GND

GND

GND

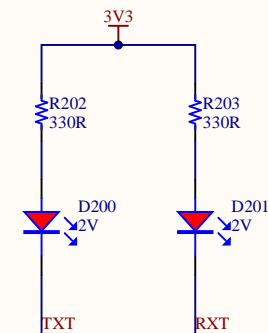
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12

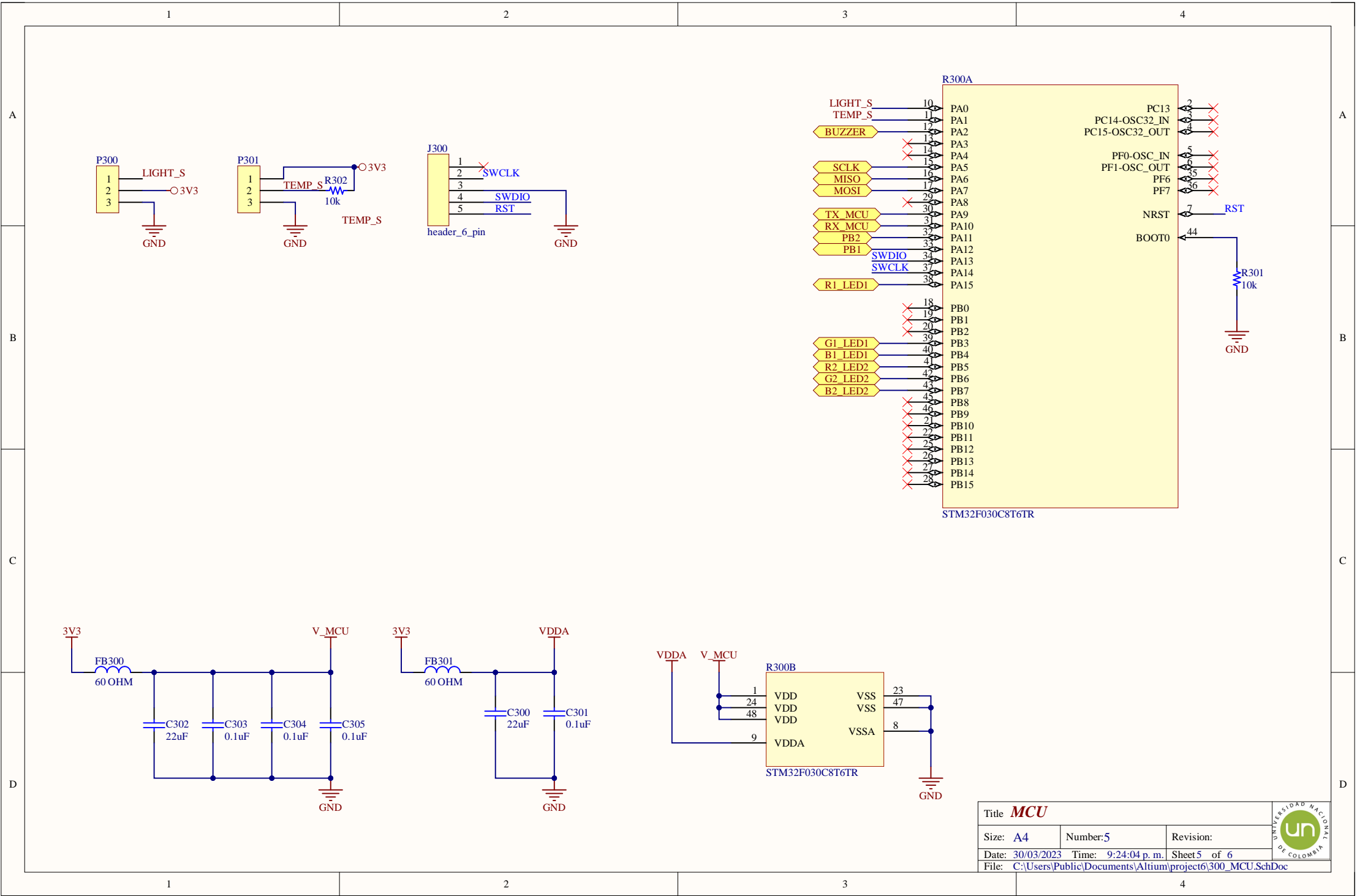
21

GND

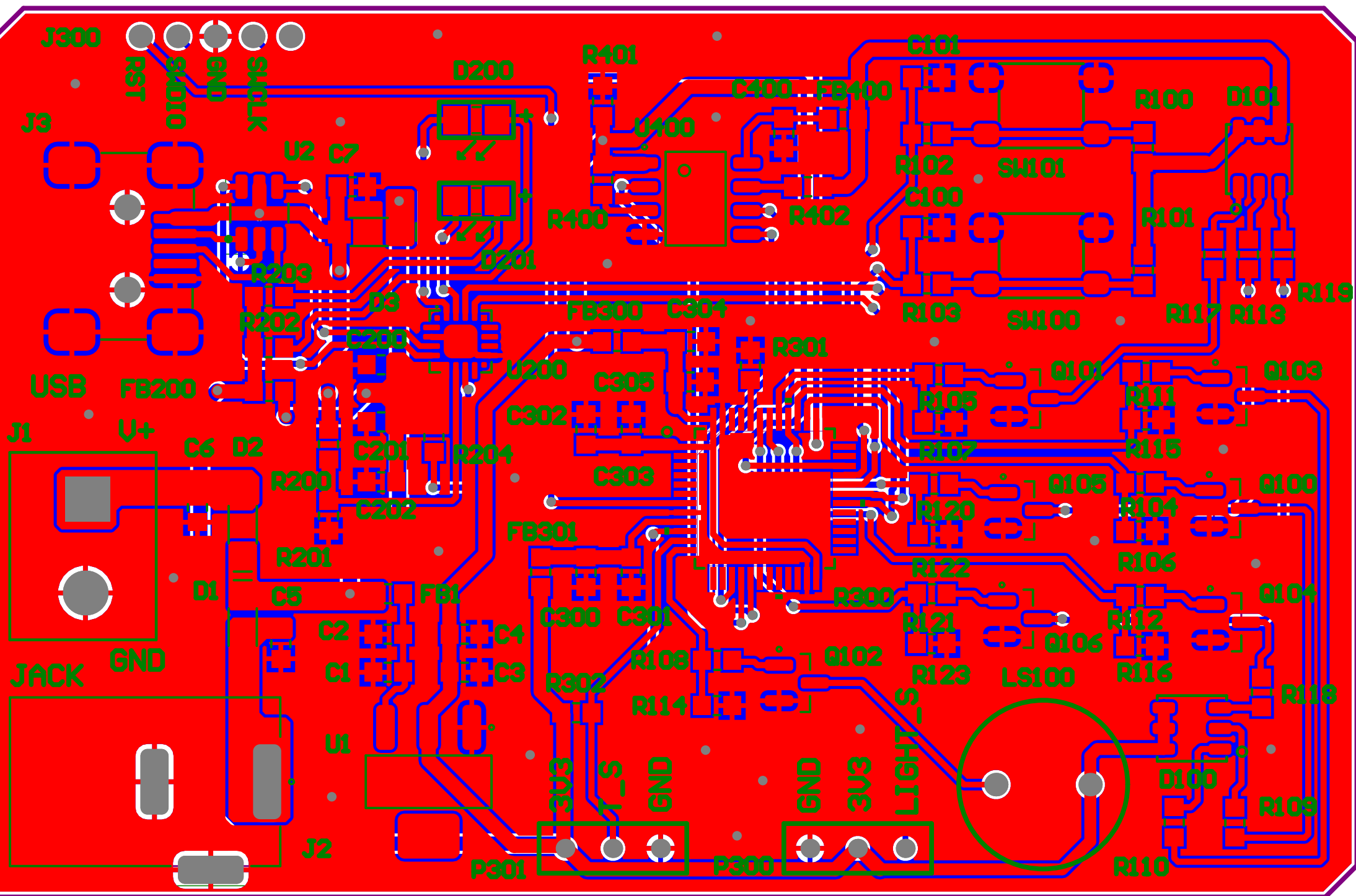
CP2102N-A02-GQFN20R



Title <i><b>USART to USB</b></i>			
Size: <b>A4</b>	Number: <b>4</b>	Revision:	
Date: <b>30/03/2023</b>	Time: <b>9:24:04 p. m.</b>	Sheet <b>4</b> of <b>6</b>	
File: <b>C:\Users\Public\Documents\Altium\project6\200_ U A R T _ T O _ U S B . S c h D o c</b>			







# Board Stack Report