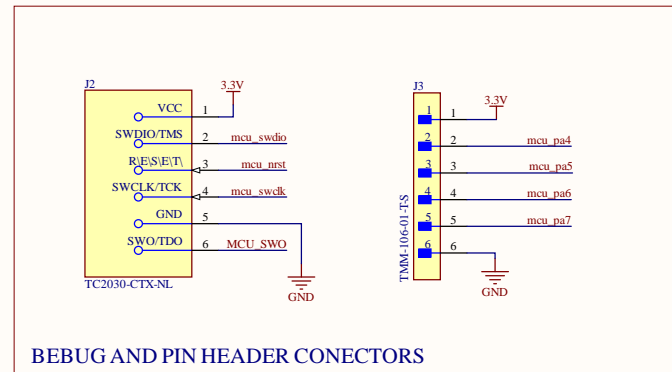
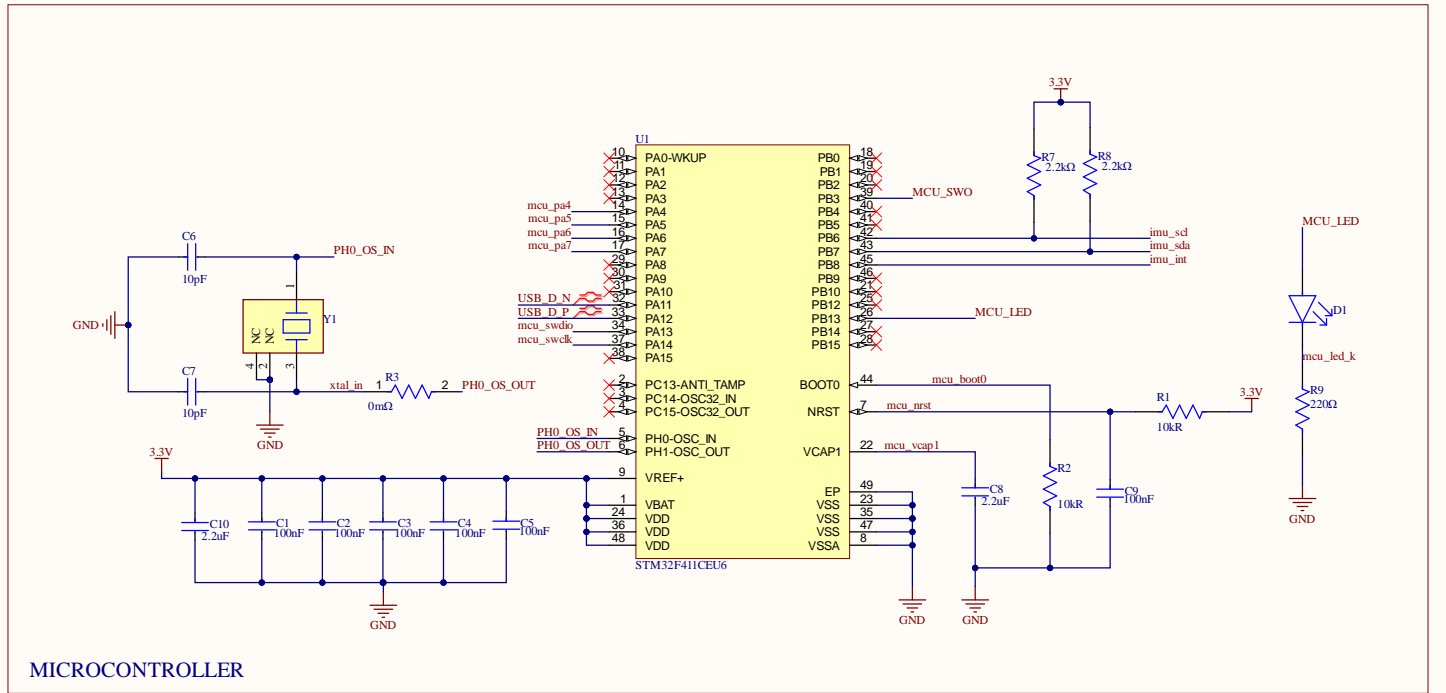
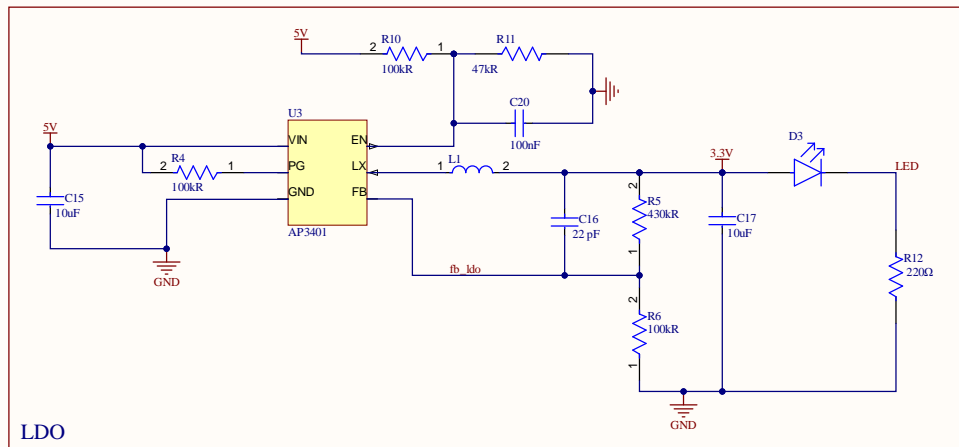
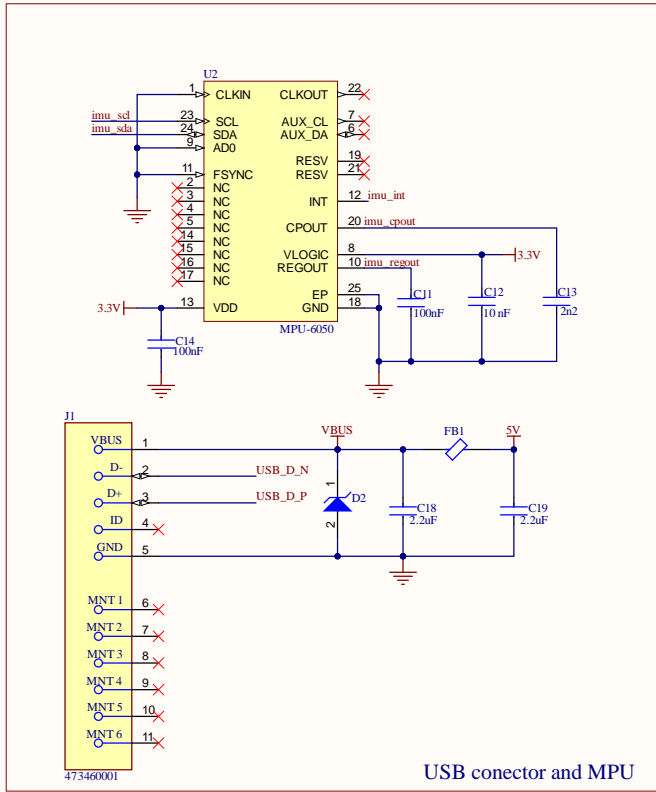


THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

Text Text

REVISION	DESCRIPTION	DATE	APPROVED



APPROVALS	DATE	PROJECT	Altium	
ENG: Carlos Silva		PROJECT REVISION	DOCUMENT REVISION	DESIGN ITEM
DSN: -		Not in version control	Not in version control	Not in version control
CHK: -		TITLE		
REFERENCE DOCUMENTS				
BOM:				
ASSY DWG:	SIZE	CAGE CODE	DWG NO.	REV
FAB DWG:	A3			
PCB DWG:	SCALE:	FILE NAME	SHEET 1 OF 1	
		stm32_board.SchDoc		

3V3

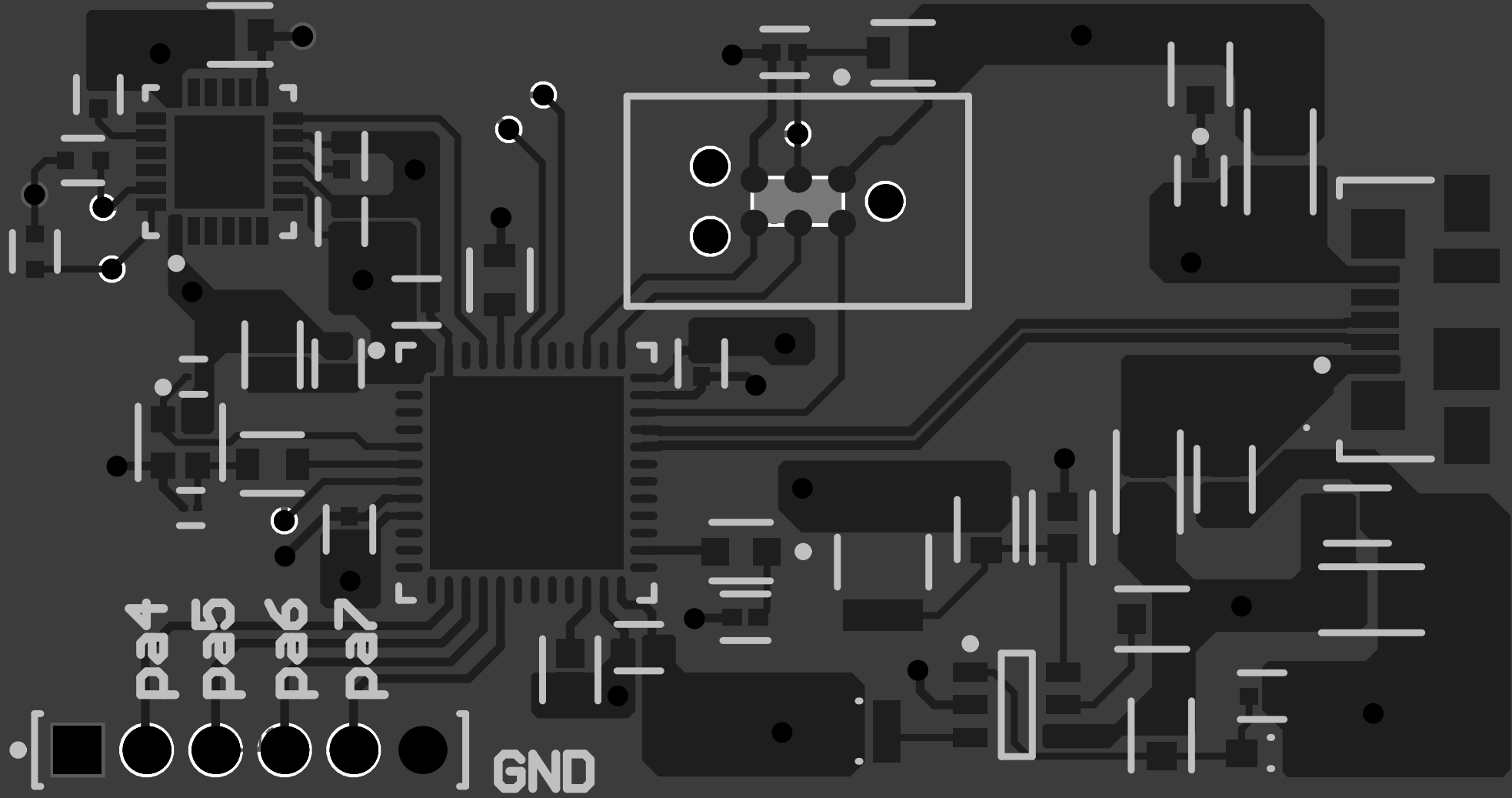
pa4

pa5

pa6

pa7

GND



Board Stack Report