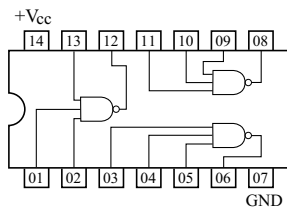
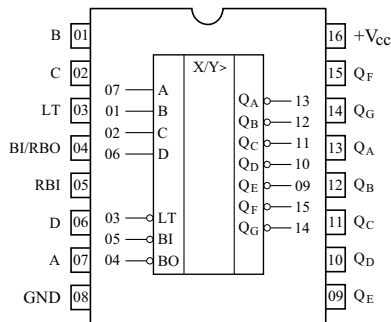


7400 4× 2 vst. NAND



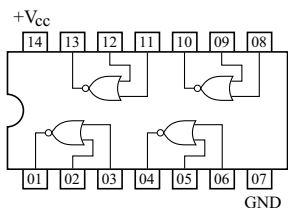
7410 3× 3 vst. NAND



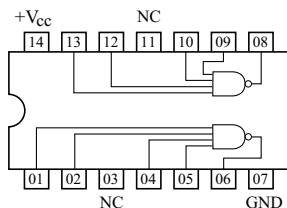
LT (Lamp Test) = log. 0 ...všetky segmenty svietia
RBI = log. 0 ...segmenty pri nule: A-D = log.0 nesvietia
BI/RB0 ...vstupno/výstupný signál, bežne nezapájame

7446..47, D146..147

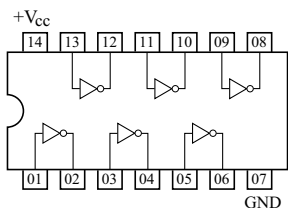
Prevodník BCD na 7seg. displej (*OC výstup*)



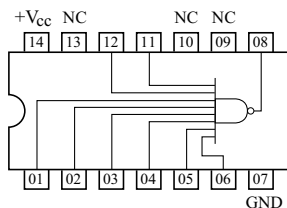
7402 4× 2 vst. NOR



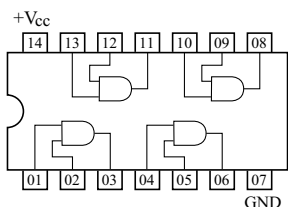
7420 2× 4 vst. NAND



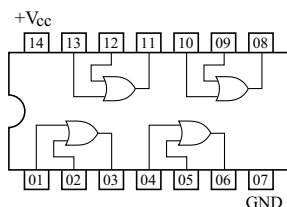
7404 6× INVERTOR



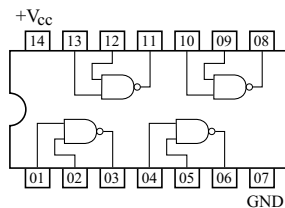
7430 1× 8 vst. NAND



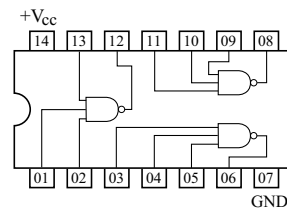
7408 4× 2 vst. AND



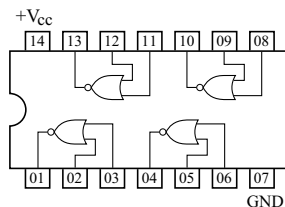
7432 4× 2 vst. OR



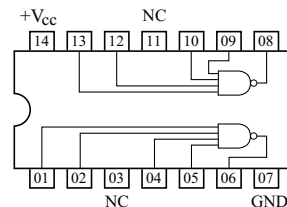
7400 4× 2 vst. NAND



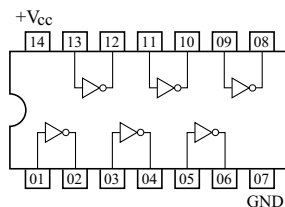
7410 3× 3 vst. NAND



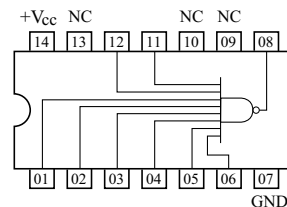
7402 4× 2 vst. NOR



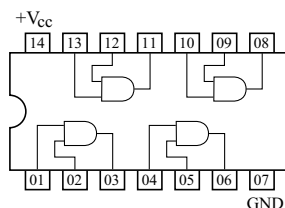
7420 2× 4 vst. NAND



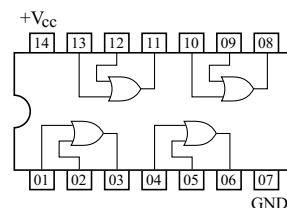
7404 6× INVERTOR



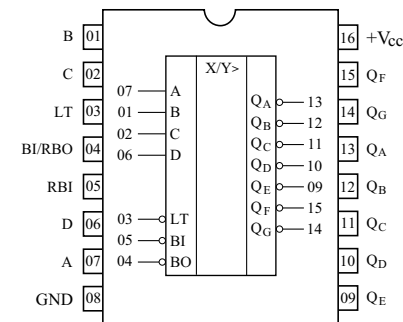
7430 1× 8 vst. NAND



7408 4× 2 vst. AND



7432 4× 2 vst. OR



LT (Lamp Test) = log. 0 ...všetky segmenty svietia
RBI = log. 0 ...segmenty pri nule: A-D = log.0 nesvietia
BI/RB0 ...vstupno/výstupný signál, bežne nezapájame

7446..47, D146..147

Prevodník BCD na 7seg. displej (*OC výstup*)