1. What do you mean by "support formatting"?  Also, don't you mean S0-S9?  
  
2. What should we do with the information in the S5 and S6 records (or any record outside of S1-S3)?  
  
3. How should we represent a parser error?  Do we just use the $display() function?  Also, are we supposed to stop the parser on an error?  Will you be testing this functionality?  
  
4. What do you mean by ending after getting the starting address?  Isn't that stored in an S7-S9?  Is that how we know to stop?

5. Do we add x80002000 to memory address in the memory itself, or have an external controller?

6. How should the test bench read from memory after loading data? Should it just put the data at “address” on output every negative clock regardless of read/write?  
  
7. What sort of input and output signals are supposed to be in the SREC parser?  For instance, is it supposed to generate its own clock that it feeds into the memory, or use a common clock with the memory?  Is the test bench supposed to give it a name of an SREC file to read from, or pass it each character of the SREC file one at a time?