



BITNG LAB UPDATE

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Date 5/17/2021

Progress from last week

- Shriner's project
 - PCB
 - Literature review revisions
- LP ECG
 - PCB redesign
- Chip socket programmer
 - PCB design

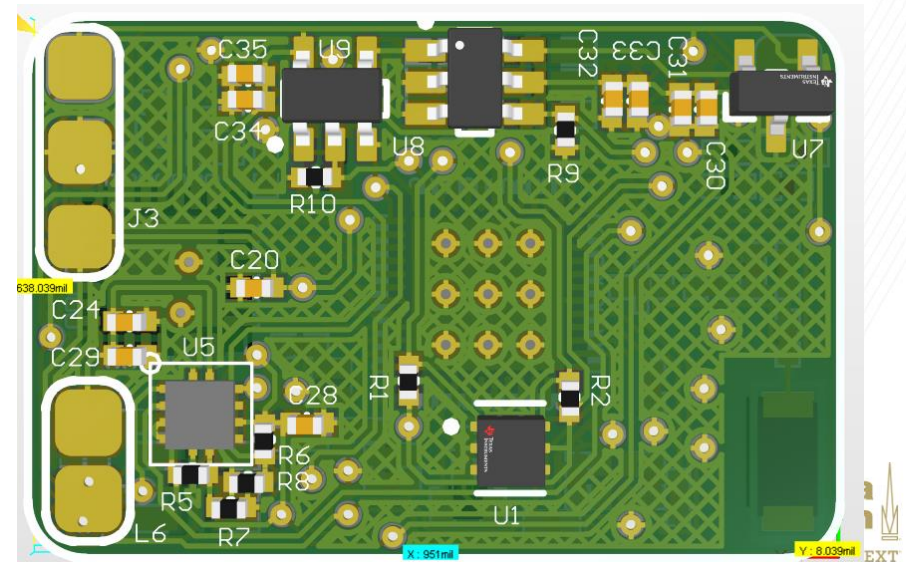
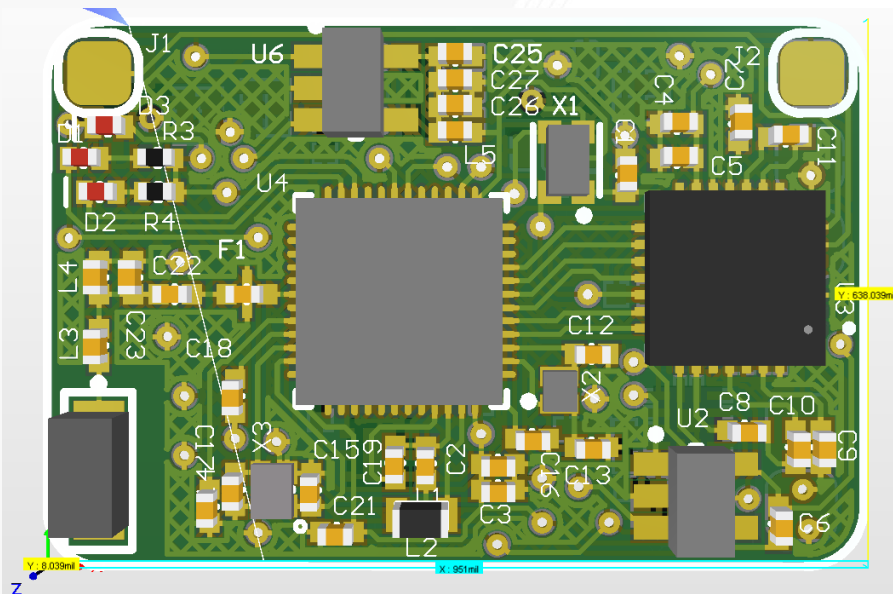
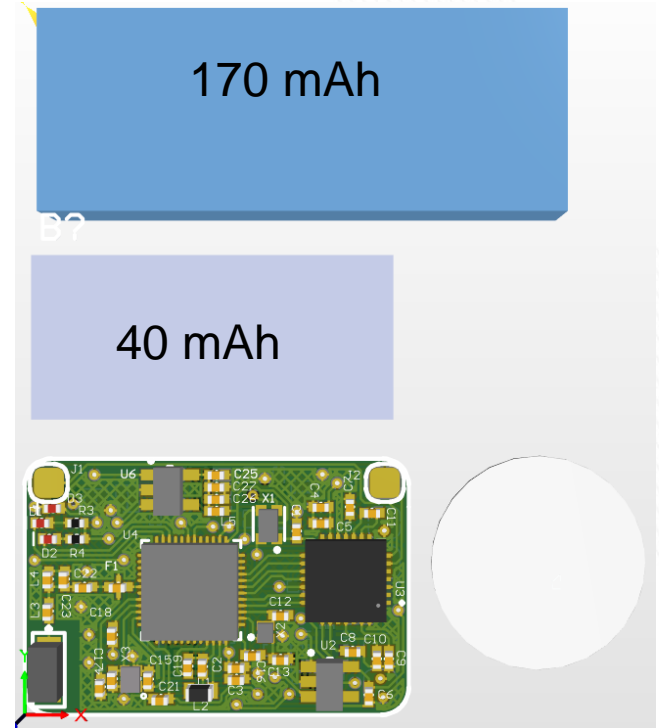
SHRINER'S PROJECT

Literature review revisions

- Editor revisions
 - 4/11 comments addressed
 - Ongoing progress

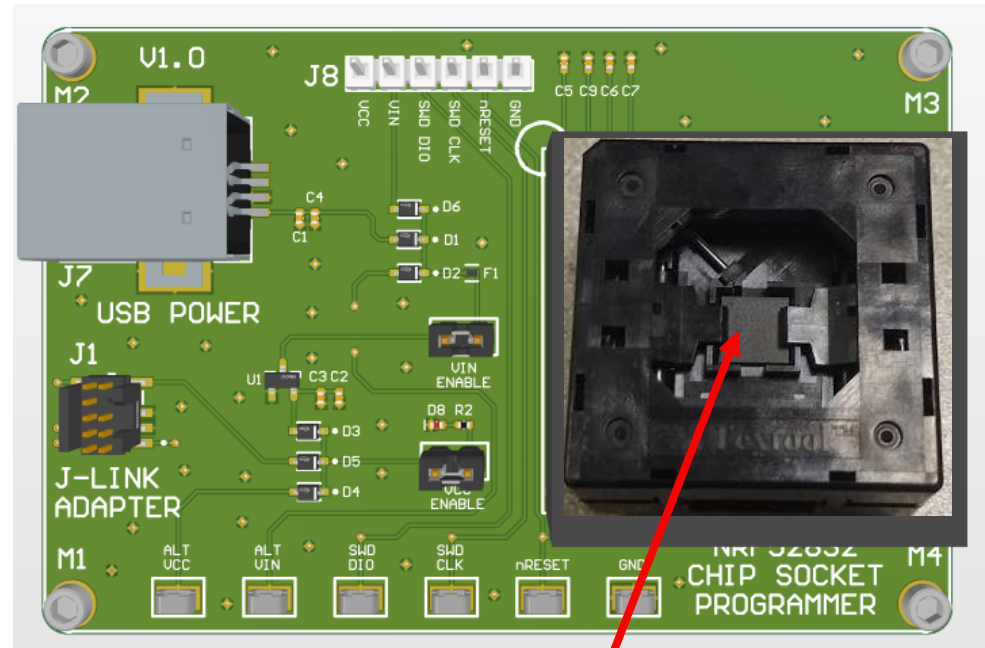
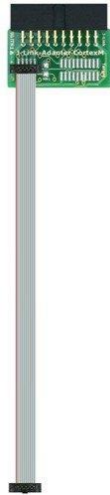
LP-ECG PCB Redesign

- Dimensions
 - 0.95" x 0.64"
- Ground Plane = Cross hatch
 - More flexible PCB
 - Rounded corners
- More efficient LDOs



Chip Socket Programmer

- Place nRF52 in chip socket
- Connect programmer
- Program chip seamlessly
- Solder nRF52 onto Flex PCB



NRF52

PATH FORWARD

Path forward (5/17/21 – 5/24/21)

- Shriner's Project:
 - PCB Redesign
 - Literature review revisions

APPENDIX