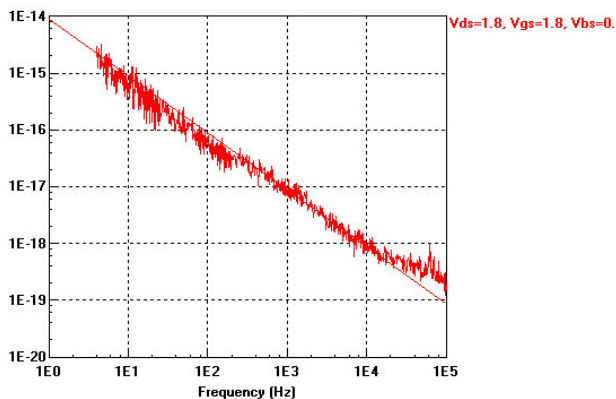


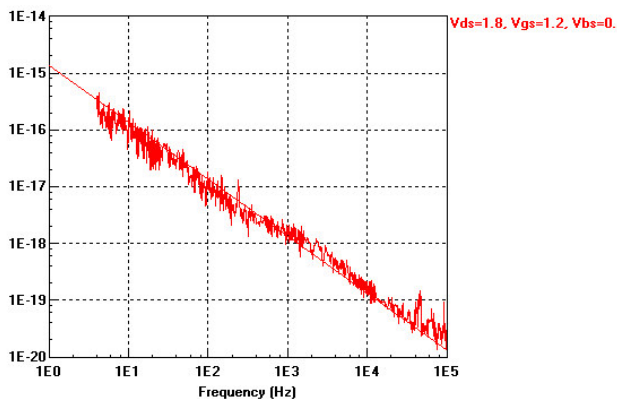


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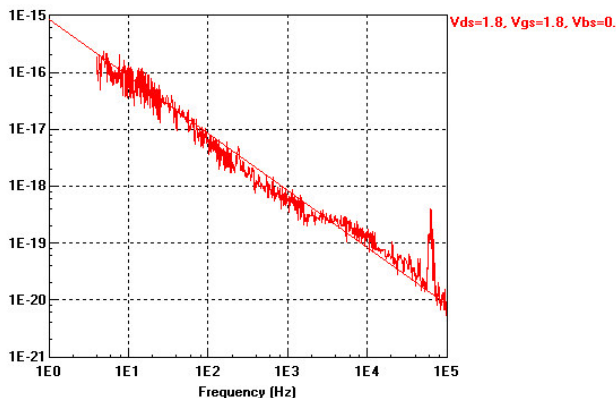
Noise(Sid) W=10.0, L=0.18, Vds=1.80, Vgs=1.80, Vbs=0.00



Noise(Sid) W=10.0, L=0.3, Vds=1.80, Vgs=1.20, Vbs=0.00



Noise(Sid) W=10.0, L=0.5, Vds=1.80, Vgs=1.80, Vbs=0.00



Noise(Sid) W=10.0, L=1.0, Vds=1.80, Vgs=1.80, Vbs=0.00

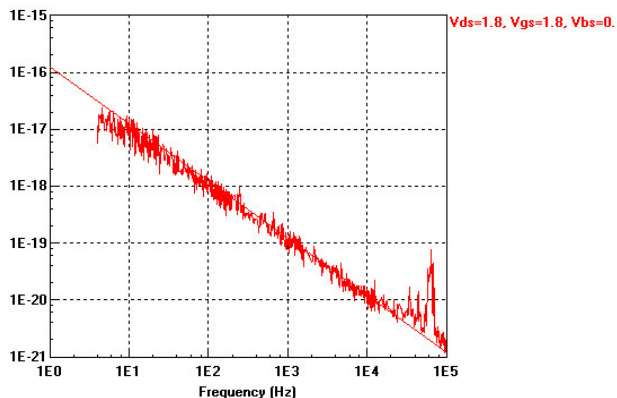


Fig.F1 The measured and simulated noise characteristics of 1.8V NMOS

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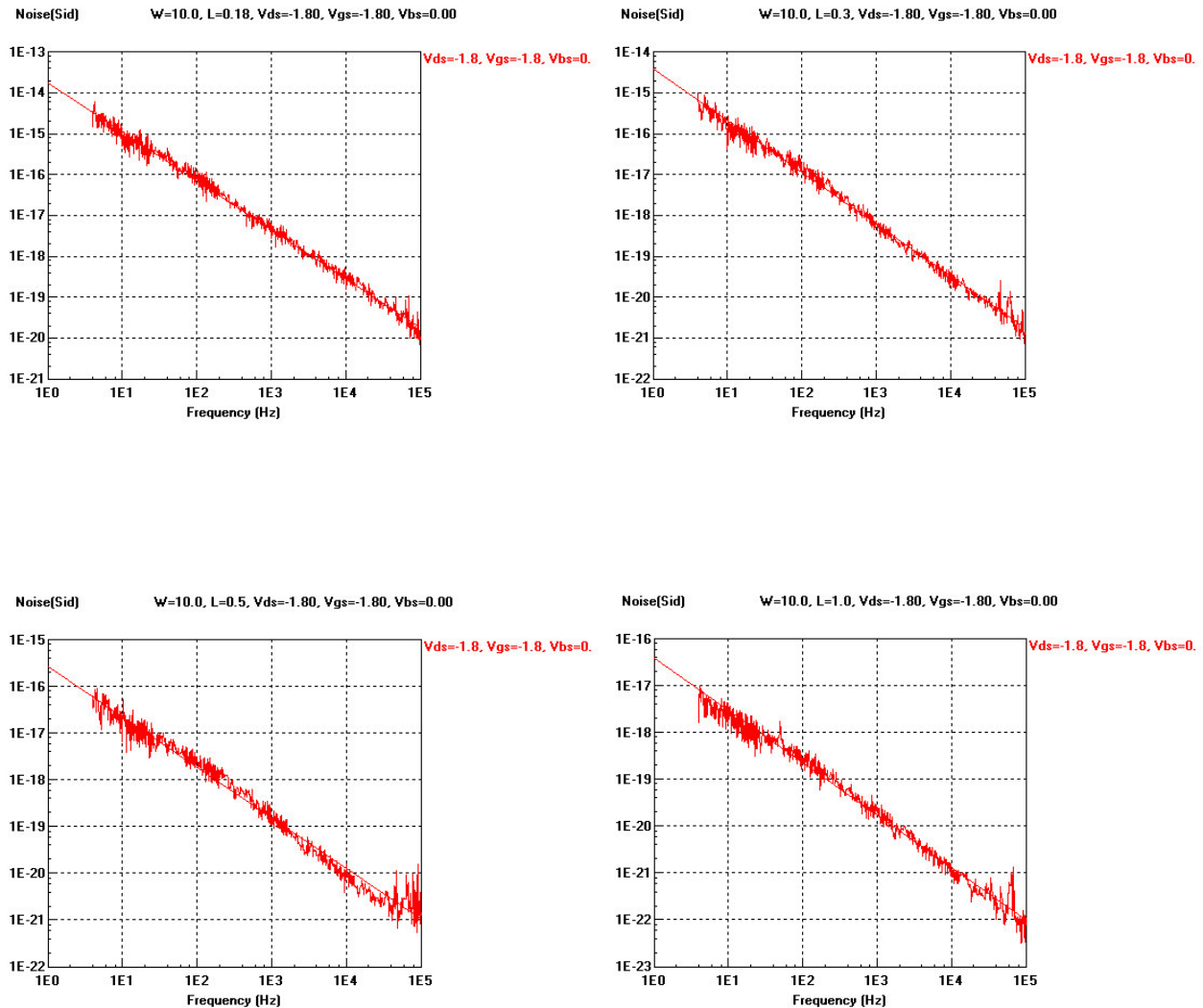


Fig.F2 The measured and simulated noise characteristics of 1.8V PMOS



Doc. No.: TD-LO18-SP-2003	Doc. Title: 0.18um Logic Low Leakage 1P6M (1P5M, 1P4M) Salicide 1.8V/5.0V SPICE Model (Version 1.3)	Doc. Rev: 4R	Tech Dev Rev.: 1.3	Page No.: 3/4
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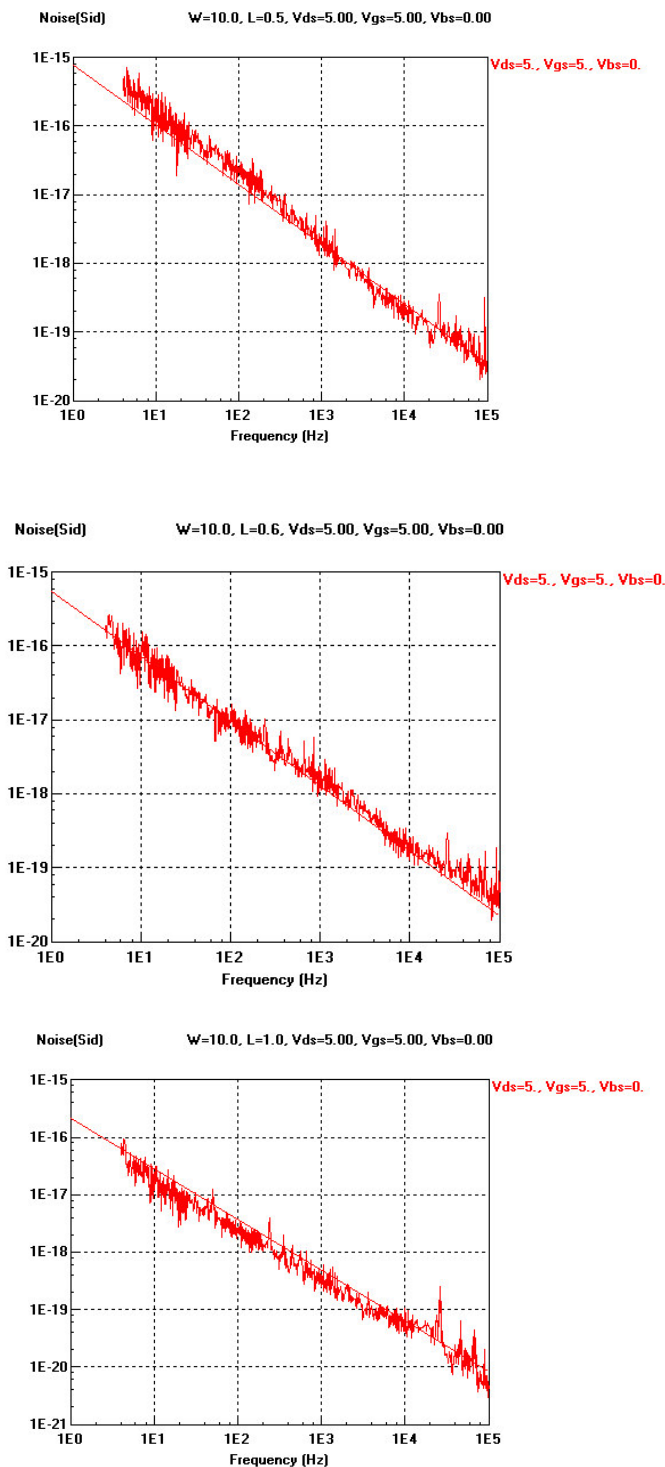


Fig.F3 The measured and simulated noise characteristics of 5V NMOS

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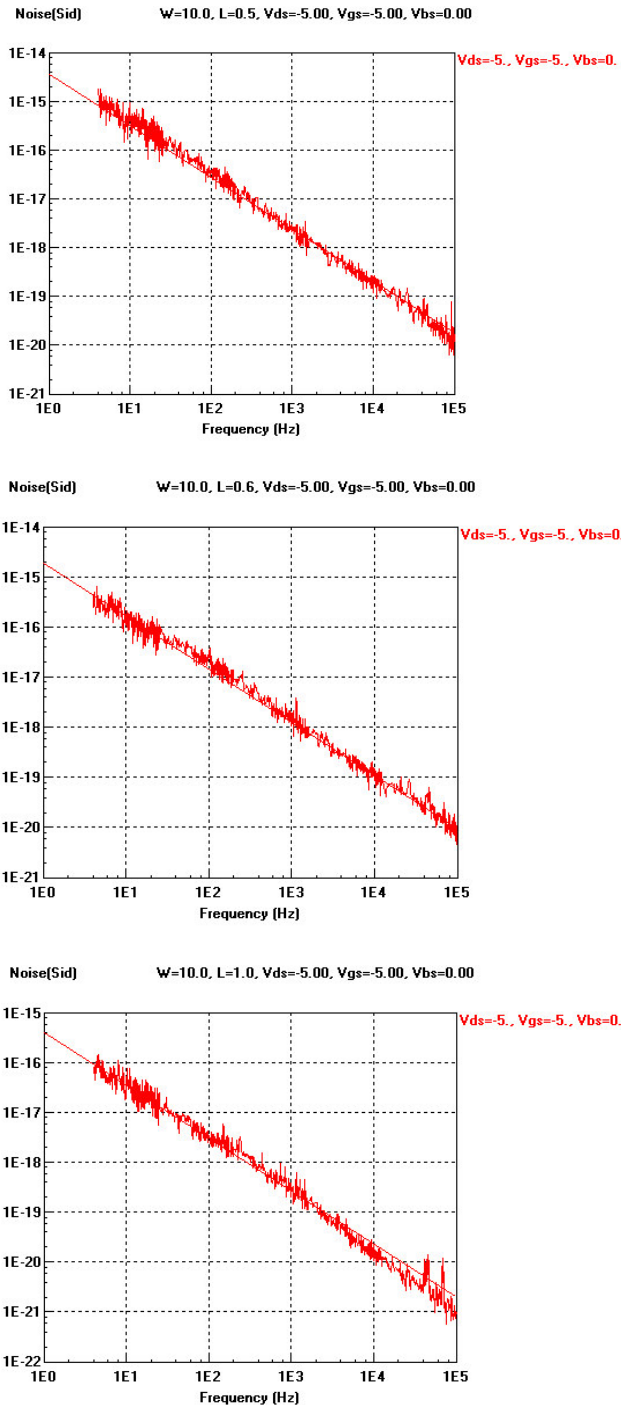


Fig.F4 The measured and simulated noise characteristics of 5V PMOS

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