

For editing ease, Copies the specified settings to ALL CHANNELS 1-16 [Use Channel Assignment Mode] or the specified SVID to ALL SVID 1-32

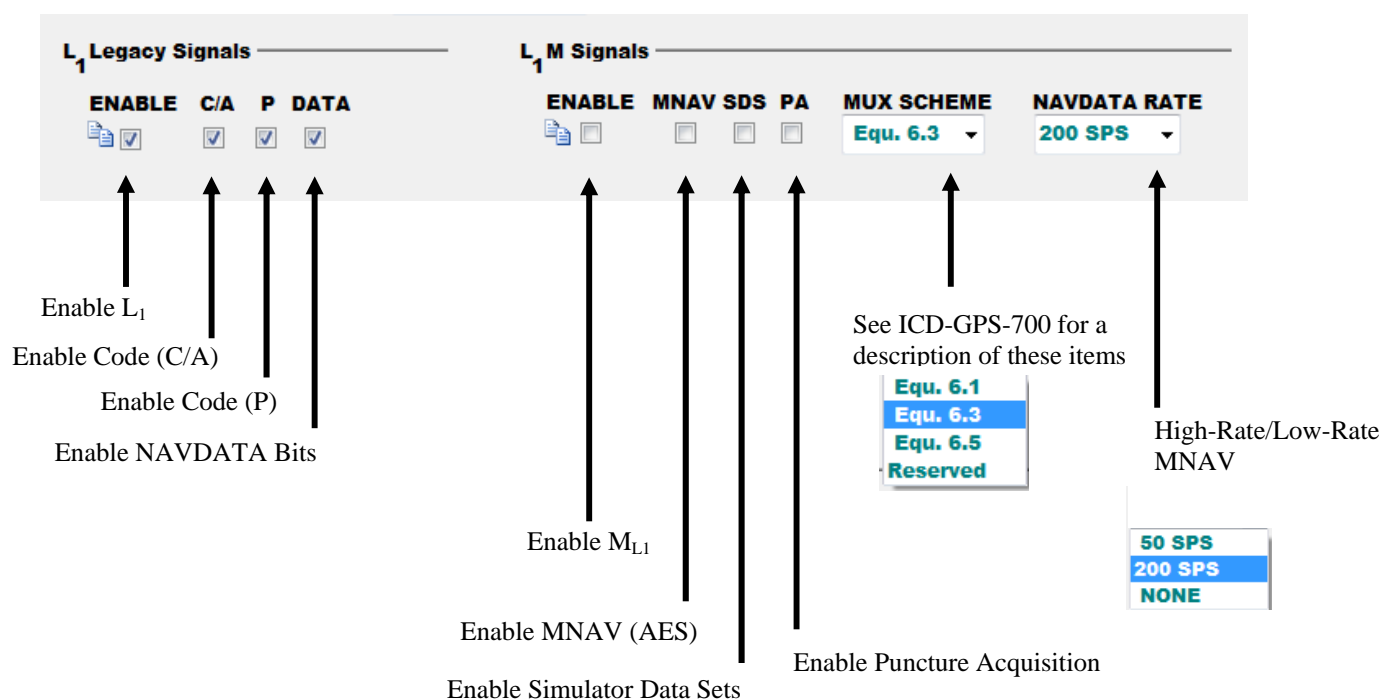
FIRMWARE MEMORY MODEL

LEGACY

LEGACY

MODERNIZED

- For Modernized Systems (M,L₅), **MODERNIZED**
- For a Legacy Series System, **LEGACY**



All other Links are similarly organized.

ChannelCnfgRF1.scn

This file controls the RF output content from the simulator. The first two Satellites / Channels are shown. There are 32 total records. If Channel Assignment Mode is CHECKED, only the first 16 records are processed and interpreted as Hardware Channels, otherwise specific to SVID with no regard to channel assignment.

Channel/SVID

Link and Data Specifications

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1 1 1 1 1 0 0 0 1 1 0 ; SVID/CHNL L1 [E C/A P DATA MENABLE MNAV SDS MMUX MDR MPA]
1 1 0 1 1 0 0 0 1 1 0 ; SVID/CHNL L2 [E C/A P D MENABLE MNAV SDS MMUX MDR MPA]
1 0 0 0 ; SVID/CHNL L2C [E CDATA CFEC]
1 0 0 0 ; SVID/CHNL L5 [E CODE DATA]
1 0 ; SVID/CHNL SASM [ENABLE]
2 1 1 1 1 0 0 0 1 1 0 ; SVID/CHNL L1 [E C/A P DATA MENABLE MNAV SDS MMUX MDR MPA]
2 1 0 1 1 0 0 0 1 1 0 ; SVID/CHNL L2 [E C/A P D MENABLE MNAV SDS MMUX MDR MPA]
2 0 0 0 ; SVID/CHNL L2C [E CDATA CFEC]
2 0 0 0 ; SVID/CHNL L5 [E CODE DATA]
2 0 ; SVID/CHNL SASM [ENABLE]

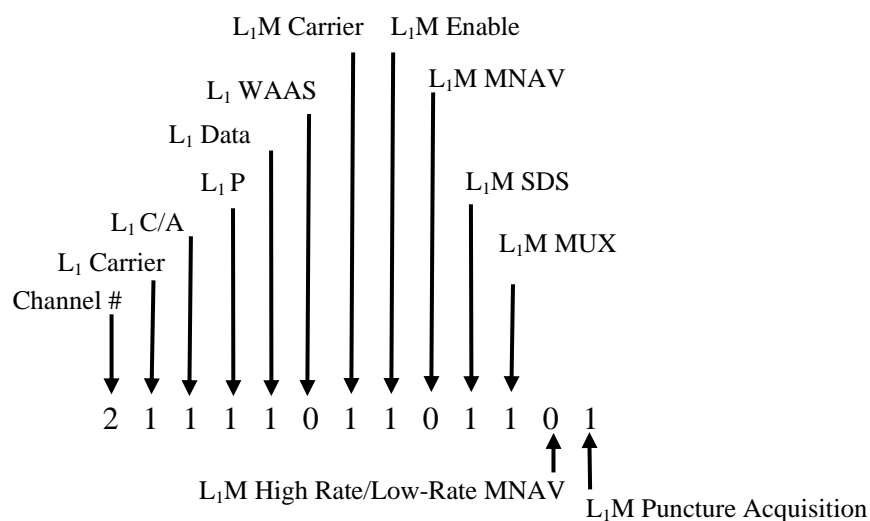
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*** 32 total records ***

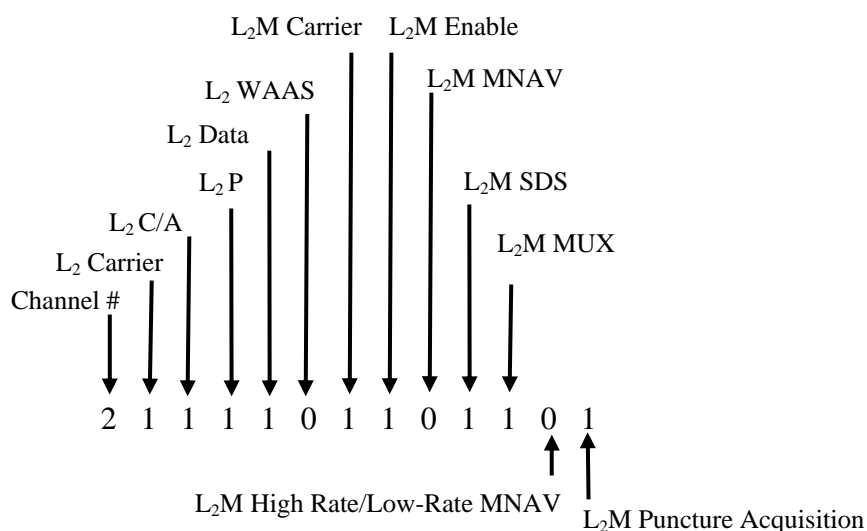
Following is the identification of the various rows and columns in this file:

Explicit expansion of one segment: [Each segment is 5 Lines – there are 32 segments]

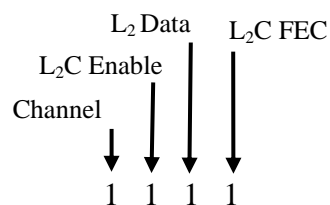
1ST Line (L₁)



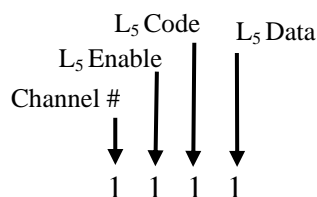
2ND Line (L₂)



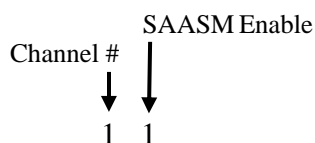
3RD Line (L₂C)



4TH Line (L₅)



5TH Line (SAASM/Y)



The 5 line segment detailed above - is repeated 32 total times.