Encoding of MikroLeo Instruction Word (16-bits)

Program Memory (ROM) - 2 x 6116 SRAM															
ROMH (8-bit)								ROML (8-bit)							
High Nibble (HiNB)				MICRO				MAddr				OPR/LAddr			
b15	b14	b13 b12 / MOD		b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
MICRO2_IN	AMODE	MOD1	MOD0	MICRO3	MICRO2	MICRO1	MICRO0	MAddr3	MAddr2	MAddr1	MAddr0	OPR3	OPR2	OPR1	OPR0

AMODE = Addressing Mode (bit)
MOD = Instruction Modifier (2 bits)
MICRO = Opcode (4 bits)
Maddr = Medium Address (4 bits)
OPR = Operand / Low Address (4 bits)

Note: LAddr (Low Address) = OPR (Operand), so the names are interchangeable