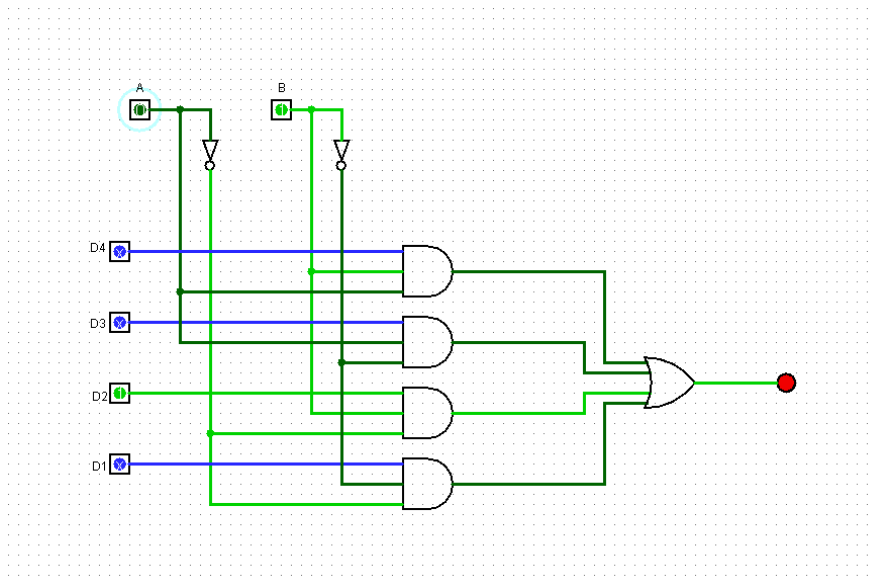
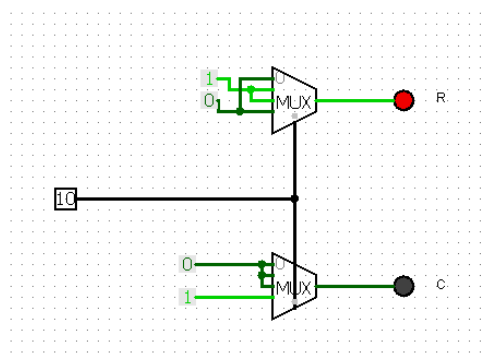


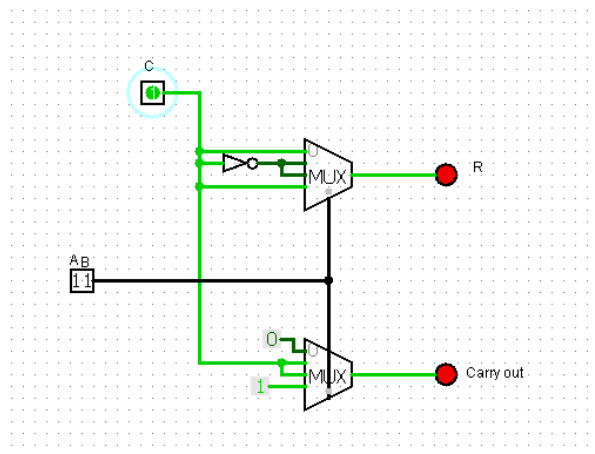
Diseño esquemático



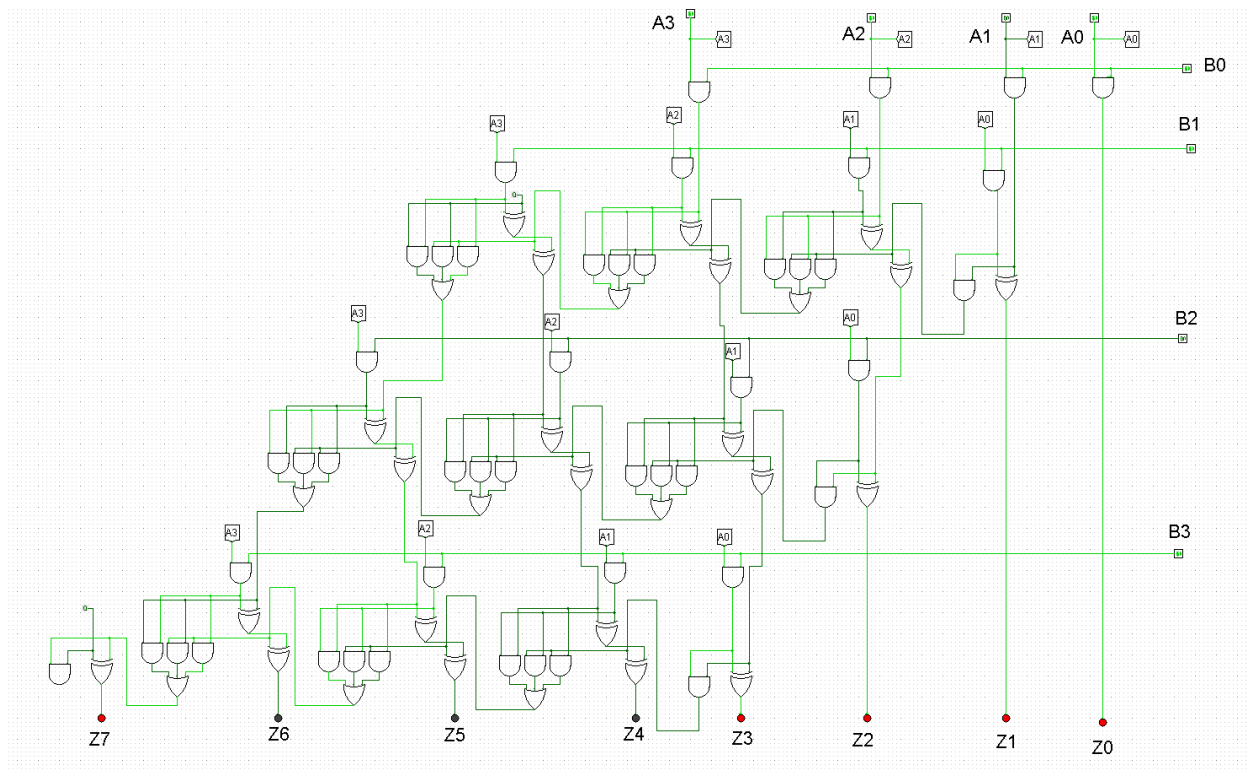
Half Adder



Full Adder



Multiplicador (lo cambie a compuertas lógicas los FA y los HA porque no me funcionaba con mux)



A = 1101

B= 1011

Z= 10001111