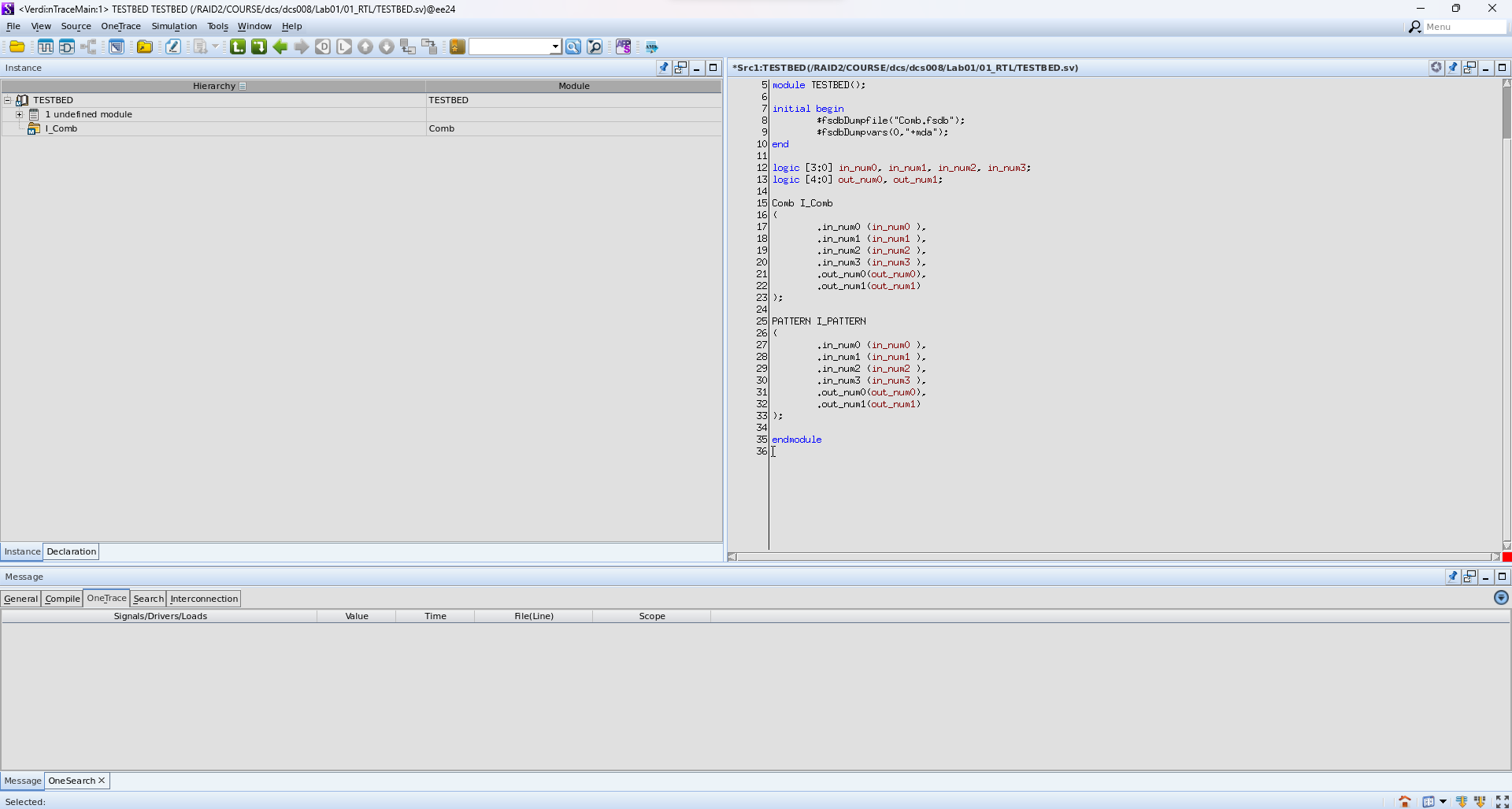
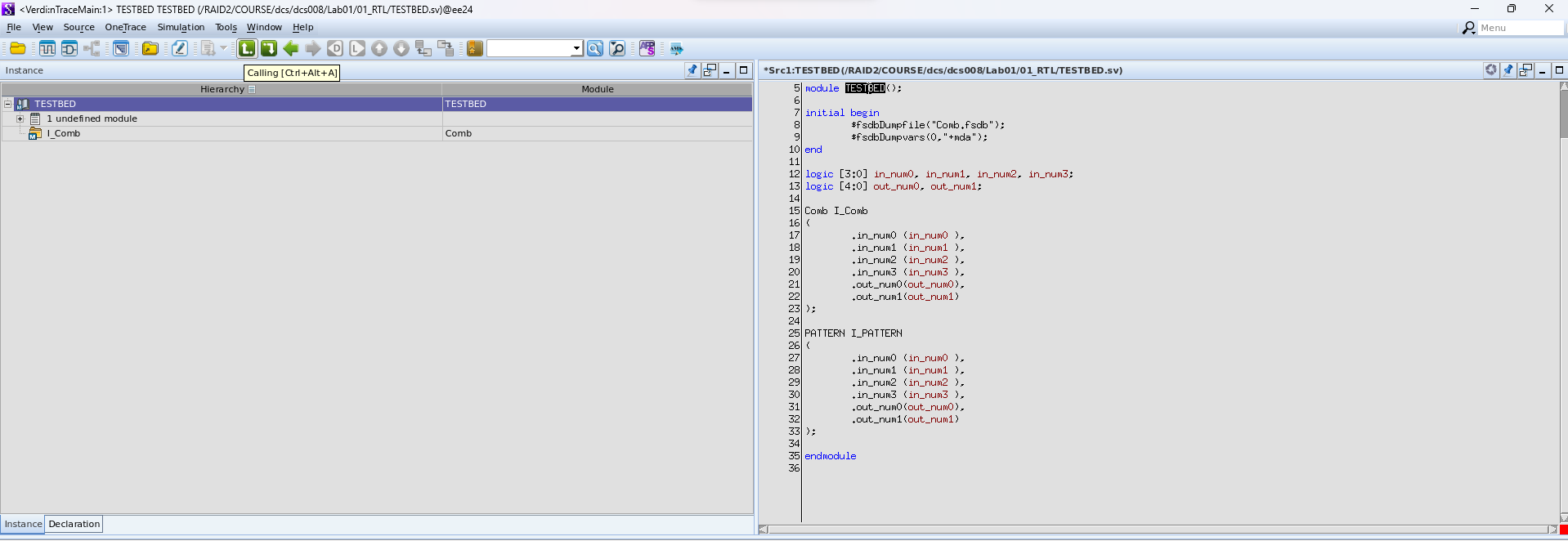
**nTrace screenshot**

Error checking:

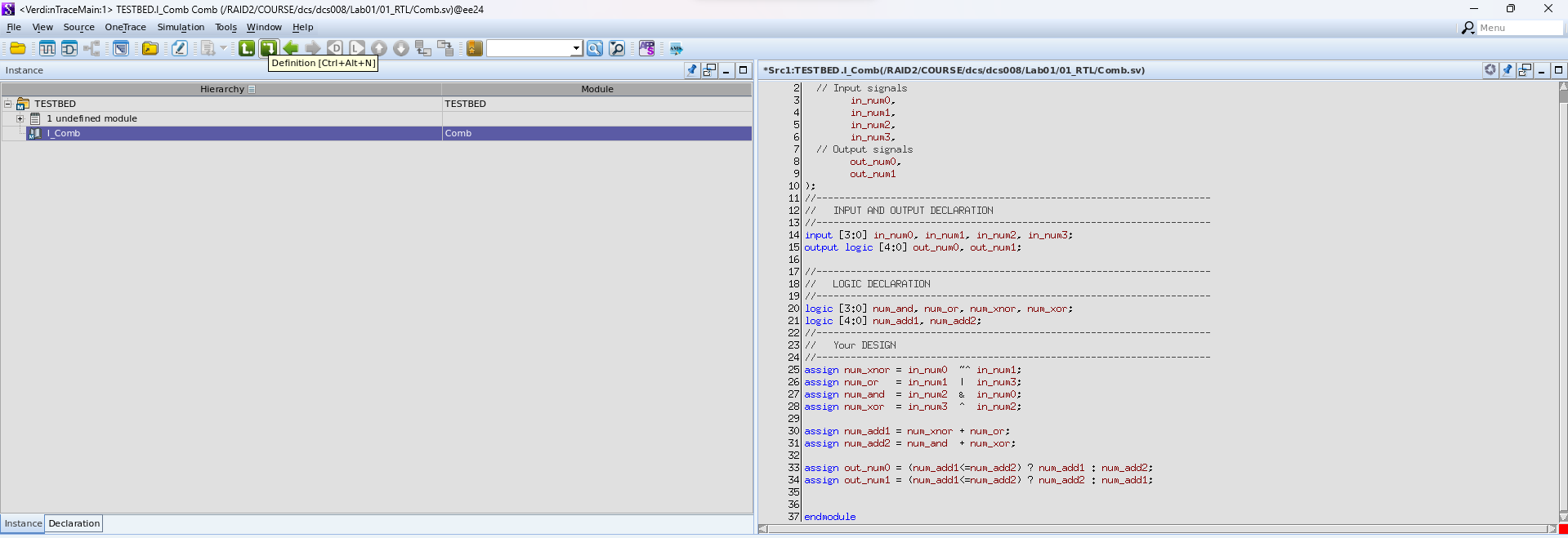


Hierarchy tracing:

(1)model “TESTBED” calling submodule “I\_Comb”

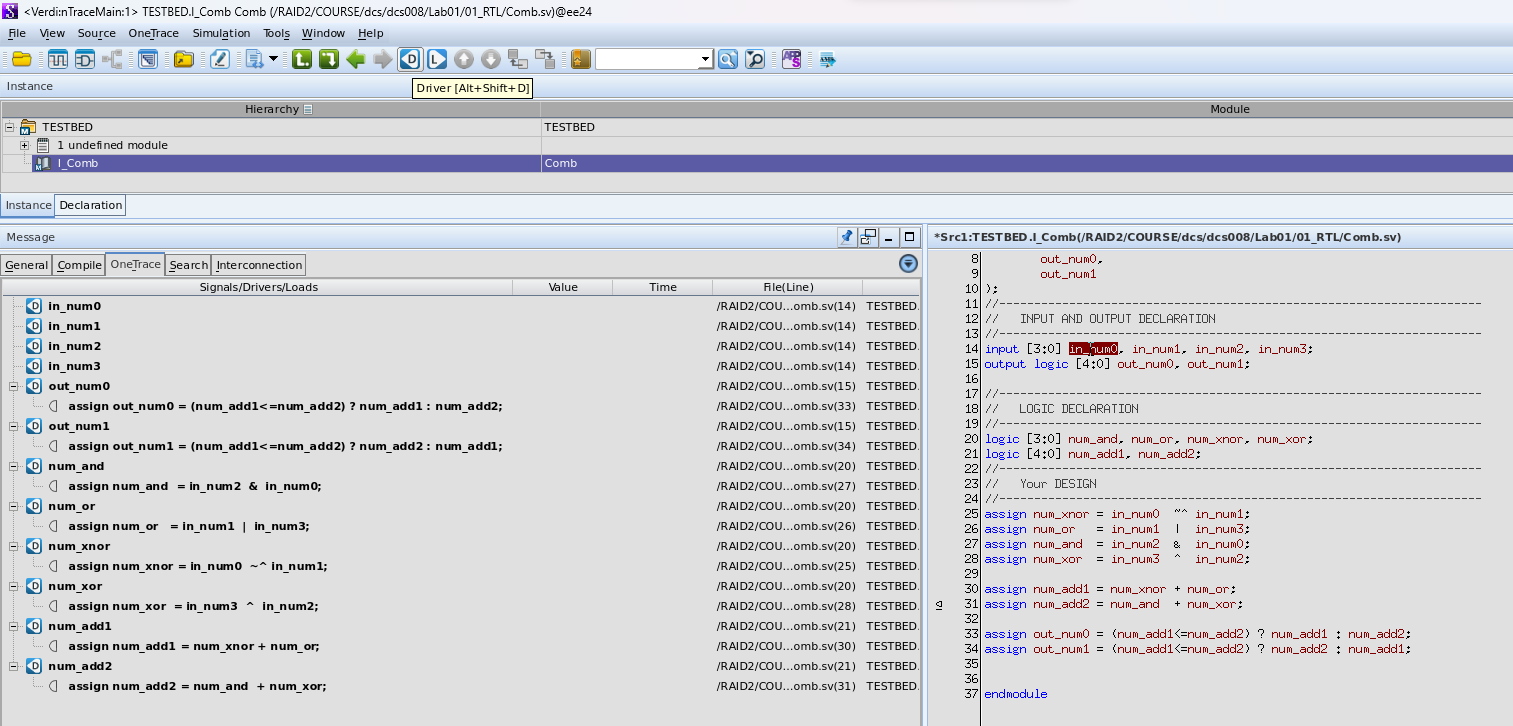


(2)definition of submodule “I\_Comb”

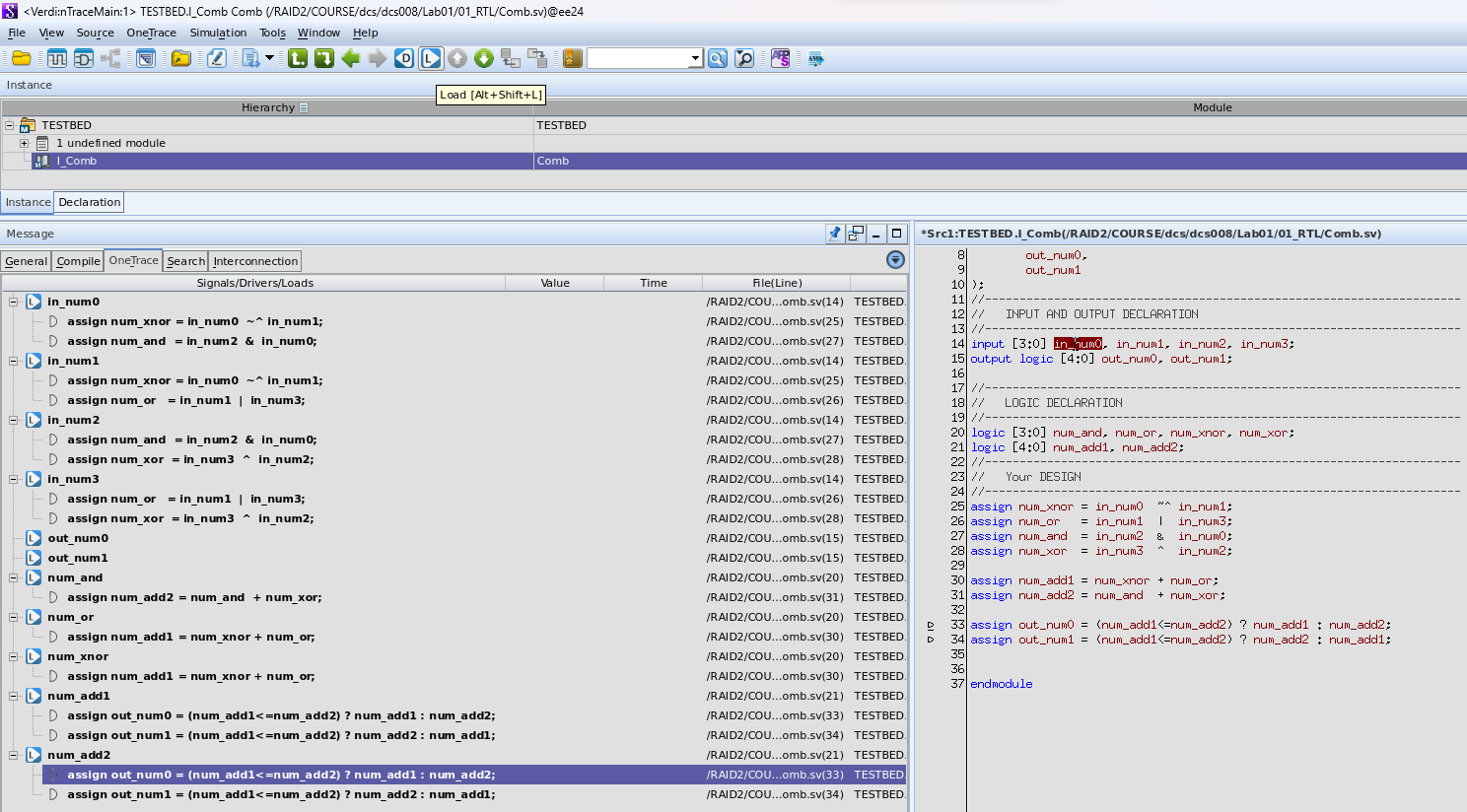


Signal tracing:

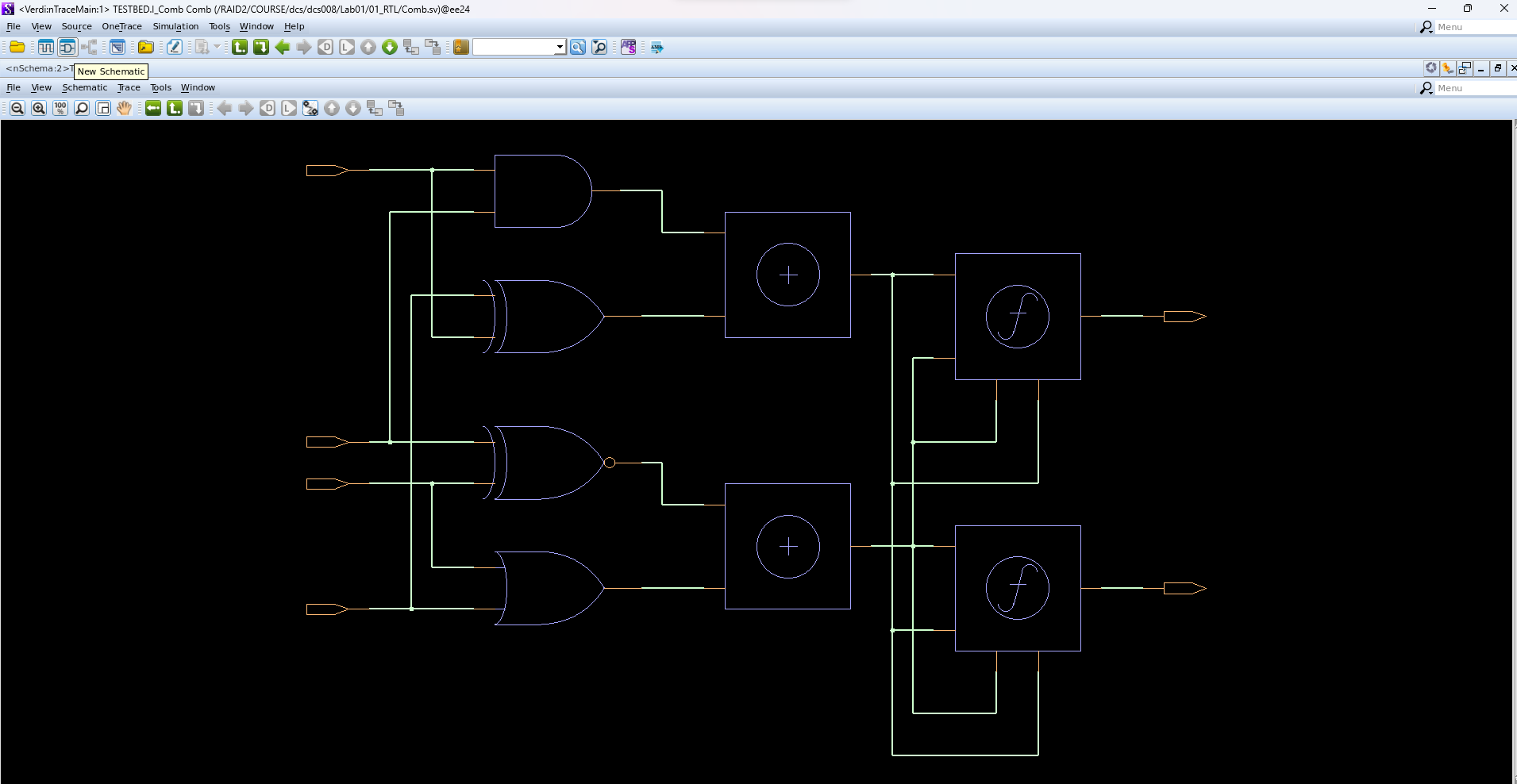
(1)trace the signal is driven by which signals.



(2)trace the signal is loading to which signals.



**nSchema screenshot**

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**nWave screenshot**

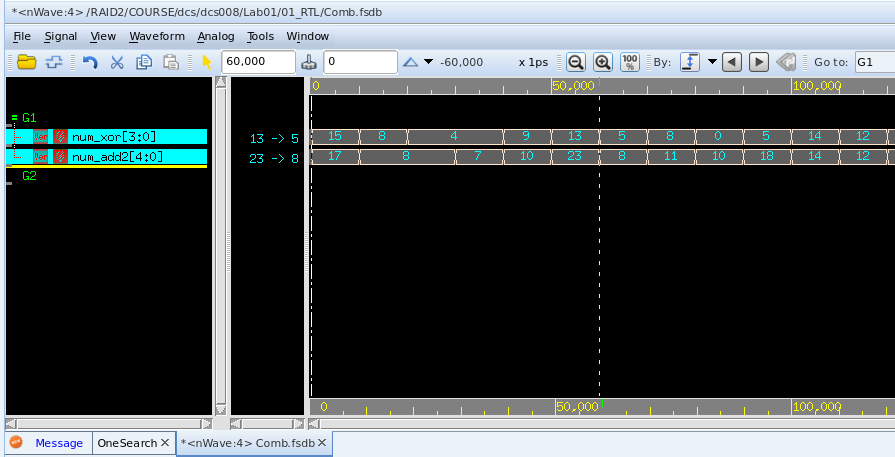
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**Questions**

• At 60ns, what’s the value of num\_xor and num\_add2 in decimal?

num\_xor[3:0] = 13->5

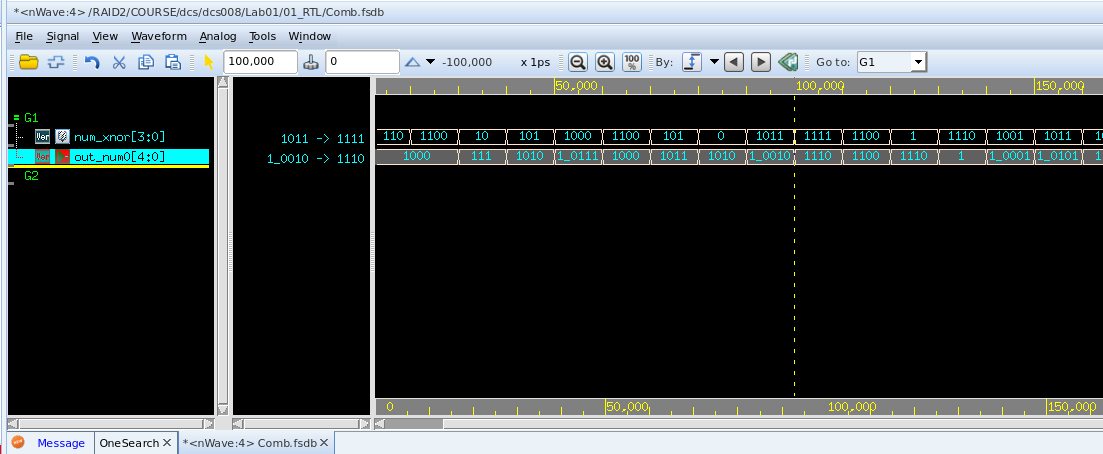
num\_add2[4:0] = 23->8



• At 100ns, what’s the value of num\_xnor and out\_num0 in binary?

num\_xnor[3:0] = 1011->1111

out\_num0[4:0] = 1\_0010->1110



• Trace which signals drives out\_num0 (use nTrace)

Signal “num\_add1”, “num\_add2” drives out\_num0.

• Trace which signal is loading from num\_xor (use nTrace)

Signal “num\_add2” is loading from num\_xor.

