
















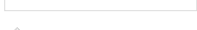




ID	Task Name	Duration	Start	Finish	<div> <div>06</div> <div>13</div> <div>20</div> <div>27</div> <div>May '20</div> <div>04</div> <div>11</div> <div>18</div> </div>						
0	Axis Communication Hub Development	97 days	Wed 29.04.20	Thu 10.09.20							
1	Kick-Off Meeting	0 days	Wed 29.04.20	Wed 29.04.20							
2	SW and HW Development for 1st PCB	23 days	Wed 29.04.20	Fri 29.05.20							
3	Report of tests with 1st PCB	0 days	Tue 02.06.20	Tue 02.06.20							
4	SW for EtherCAT comm/control features	16 days	Wed 03.06.20	Wed 24.06.20							
5	Report of tests with EtherCAT comm/control features	0 days	Thu 25.06.20	Thu 25.06.20							
6	SW for EtherCAT Data features	16 days	Fri 26.06.20	Fri 17.07.20							
7	Report of tests with EtherCAT data features	0 days	Wed 29.07.20	Wed 29.07.20							
8	HW Development for 2nd PCB	15 days	Thu 30.07.20	Wed 19.08.20							
9	Final test with 2nd PCB	3 days	Thu 20.08.20	Mon 24.08.20							
10	Final report	10 days	Tue 25.08.20	Mon 07.09.20							
11	Final presentation	0 days	Mon 07.09.20	Mon 07.09.20							

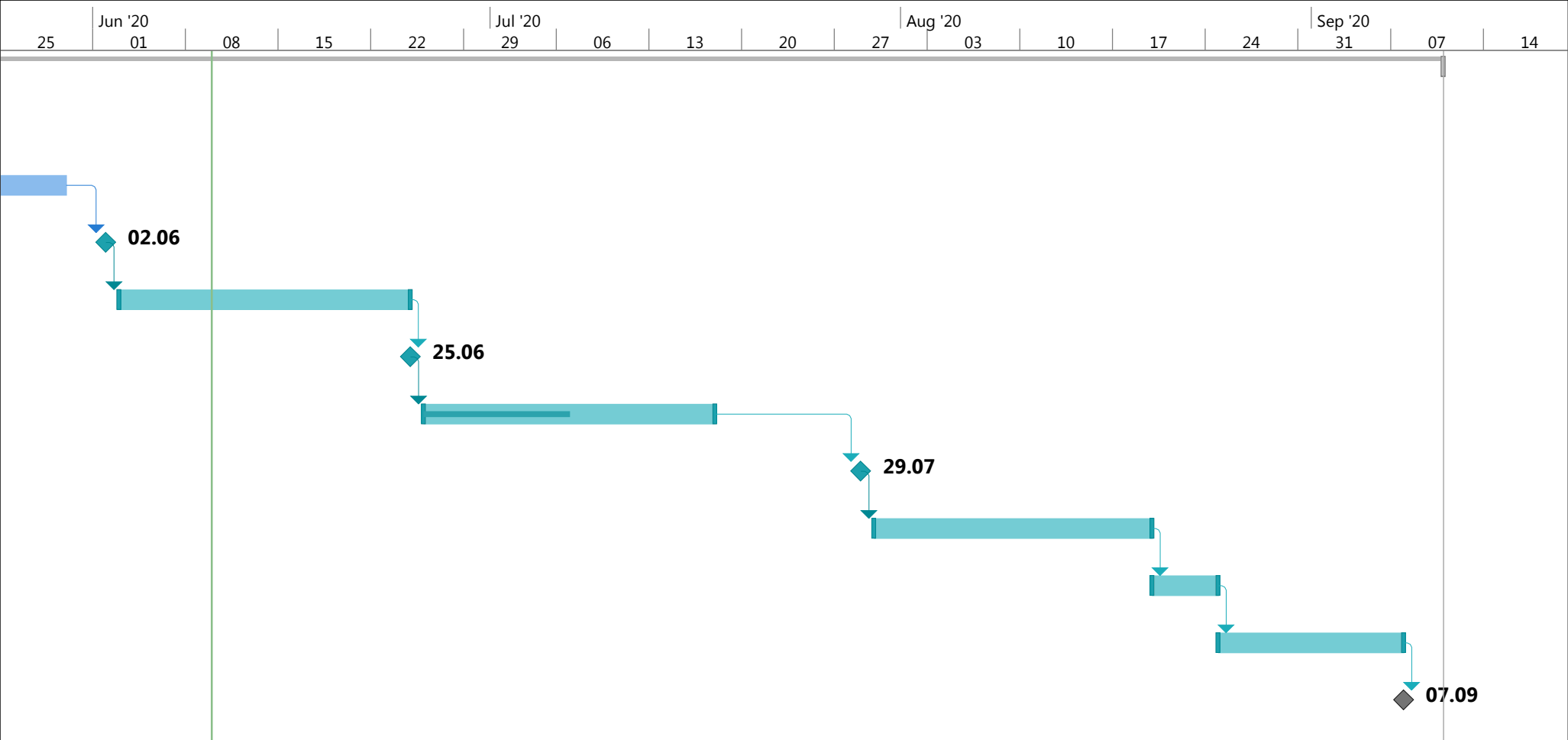
Project: Axis Communication H
Date: Wed 10.06.20

Task		Inactive Summary		External Tasks	
Split		Manual Task		External Milestone	
Milestone		Duration-only		Deadline	
Summary		Manual Summary Rollup		Progress	
Project Summary		Manual Summary		Manual Progress	
Inactive Task		Start-only		Slippage	
Inactive Milestone		Finish-only			

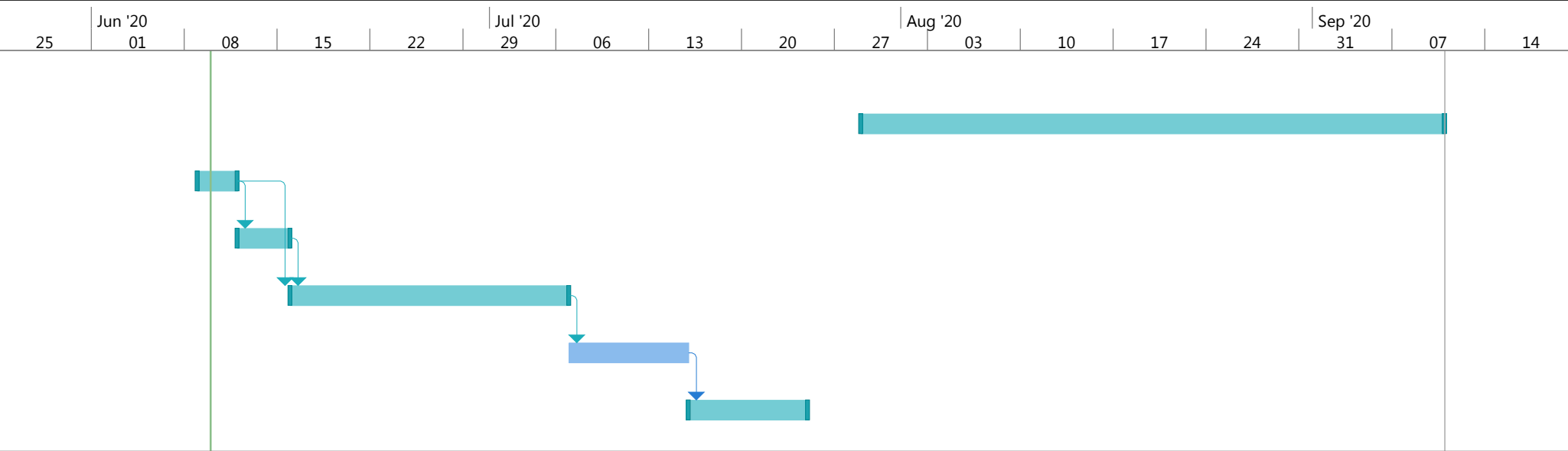
ID	Task Name	Duration	Start	Finish	May '20						
					06	13	20	27	04	11	18
12											
13	SoSe Examination Period	32 days	Wed 29.07.20	Thu 10.09.20							
14	PWM+DMA adjust	3 days	Tue 09.06.20	Thu 11.06.20							
15	PCB Soldering	2 days	Fri 12.06.20	Mon 15.06.20							
16	SOES configuration	15 days	Tue 16.06.20	Mon 06.07.20							
17	1 Wire +DMA	7 days	Tue 07.07.20	Wed 15.07.20							
18	Design of second PCB	7 days	Thu 16.07.20	Fri 24.07.20							

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Task		Inactive Summary		External Tasks	
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