An Introduction to parallel debugging

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Overview

- Introduction to debugging and parallel debugging
- Running the ARM DDT parallel debugger

INTRODUCTION TO DEBUGGING

Traditional standard way to debug: "`printf` debugging"

- Add extra print statements to the code
  - Indicate whether the code reaches a certain stage
  - Print the values of key variable
- Issues with this approach
  - Need to modify the source code, recompile
  - Iterative approach, frequent recompiles
- Debuggers are more convenient
  - Allows working with unmodified source
  - Allows line by line execution

Debuggers

- Linux system come with **gdb** as a debugger
  - Command line execution
  - GUIs exist
  - Often integrated into development platforms

PARALLEL DEBUGGING

Parallel debugging

- Parallel applications offer new levels of complexity
- Before starting, try to simplify the task
  - Problem still there if you **reduce the problem size**?
  - Problem still there if you **reduce the task/thread count**?
- "printf debugging" even more problematic than in serial
  - More output (different tasks/threads printing)
  - Identification of task/thread printing required
  - UNIX `grep` helpful to filter output

Parallel debuggers

- Licenses are expensive
  - Being able to do "`printf`" is an essential skill
- Parallel debuggers bacame more usable over the years
- I am aware of two products
  - Totalview for HPC (https://www.roguewave.com/)
  - ARM DDT - part of ARM FORGE
    - Formerly known as ALLINEA DDT/FORGE
    - There is a SNIC wide license

PREPARATIONS AND STARTING DDT

Preparations

- HPC system needs to display the gui on your monitor
  - VNC solution (e.g. LUNARC HPC desktop, ThinLinc)
  - Connect with X-forwarding (`ssh -X ...`)
- Recompile your application with the flags: `-g -O0`

```
mpif90 -g -O0 -o hello_mpi hello_mpi.f90
```

- Comments:
  - Lack of optimisation slows code (in particular C++)
  - Problem might disappear – hint for overrun array
  - You can use optimisation
    - Though match code line to instruction might not work

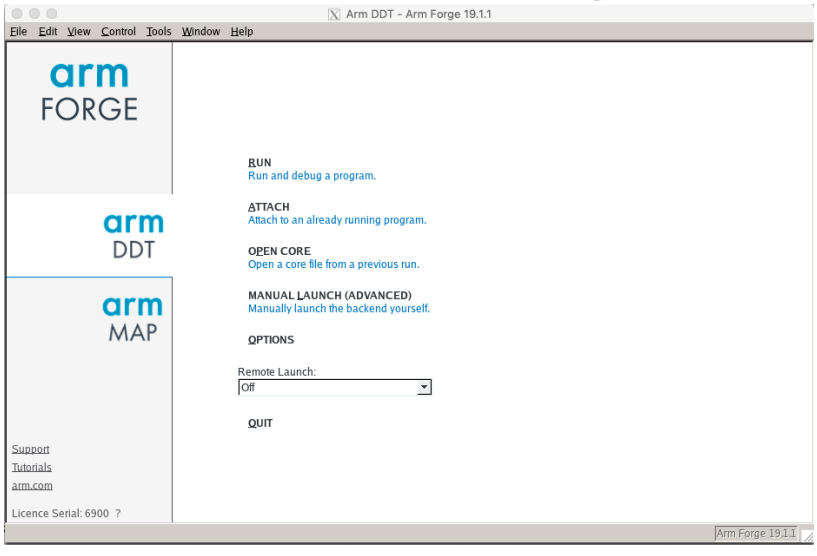
Start the gui

- Best to start the gui on the login node and keep it running

```
module load "arm-forge"
ddt &
```

- Alternative use the ARM remote client on your desktop and connect to the HPC service (e.g. aurora1)

ARM FORGE gui



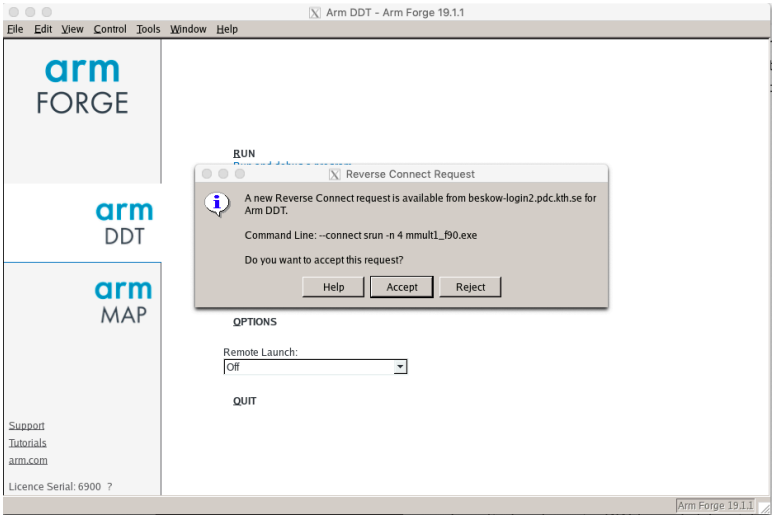
Starting code on the compute nodes

- Transfer to the backend node
  - Jobscript
  - Interactive allocation
- Make sure relevant modules are loaded
  - compiler, MPI lib, other libs, ARM DDT/FORGE
- Prefix job launcher with: `ddt --connect`

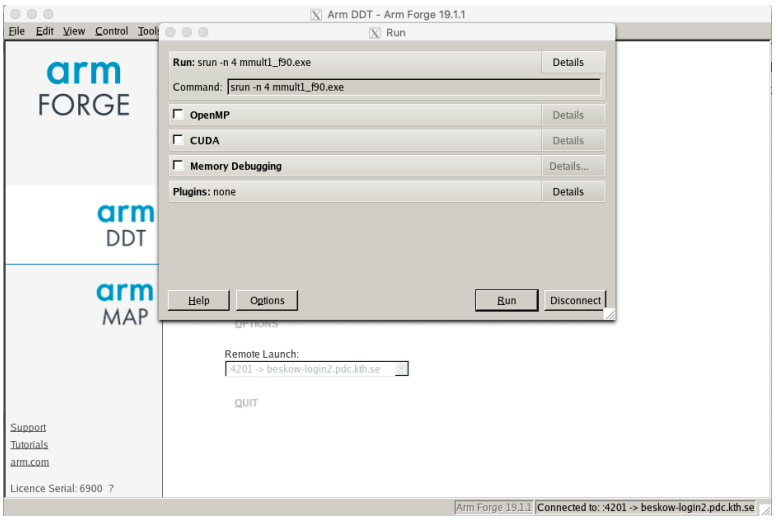
```
ddt --connect mpirun mpihello
```

```
ddt --connect mpirun python3 %allinea_python_debug%  hello_mpi.py
```

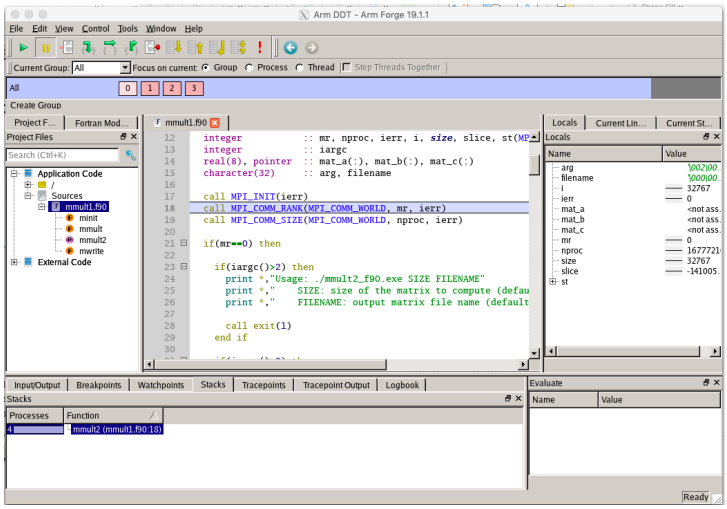
Accept the “Reverse Connect request”



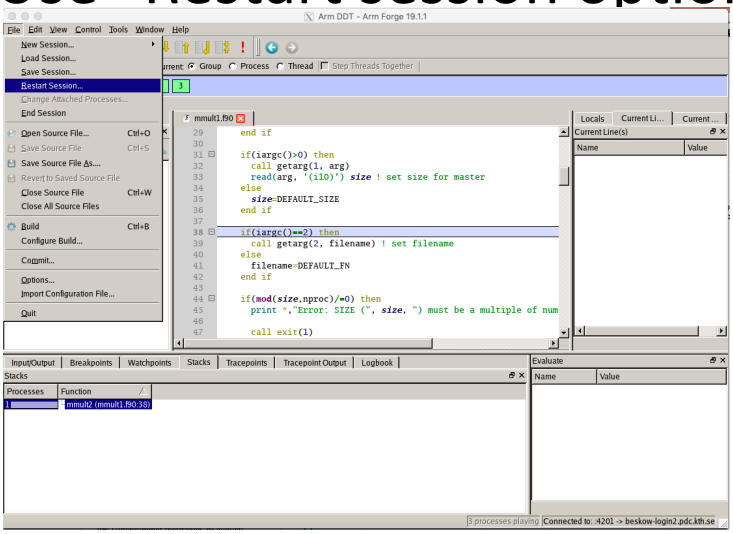
Start running your program



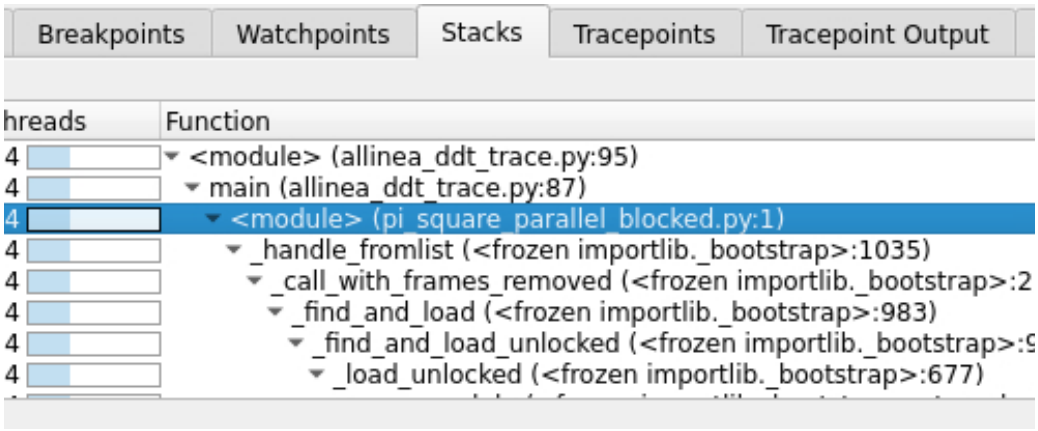
Start debugging in the gui



Starting over – frequently required
Use “Restart session option”

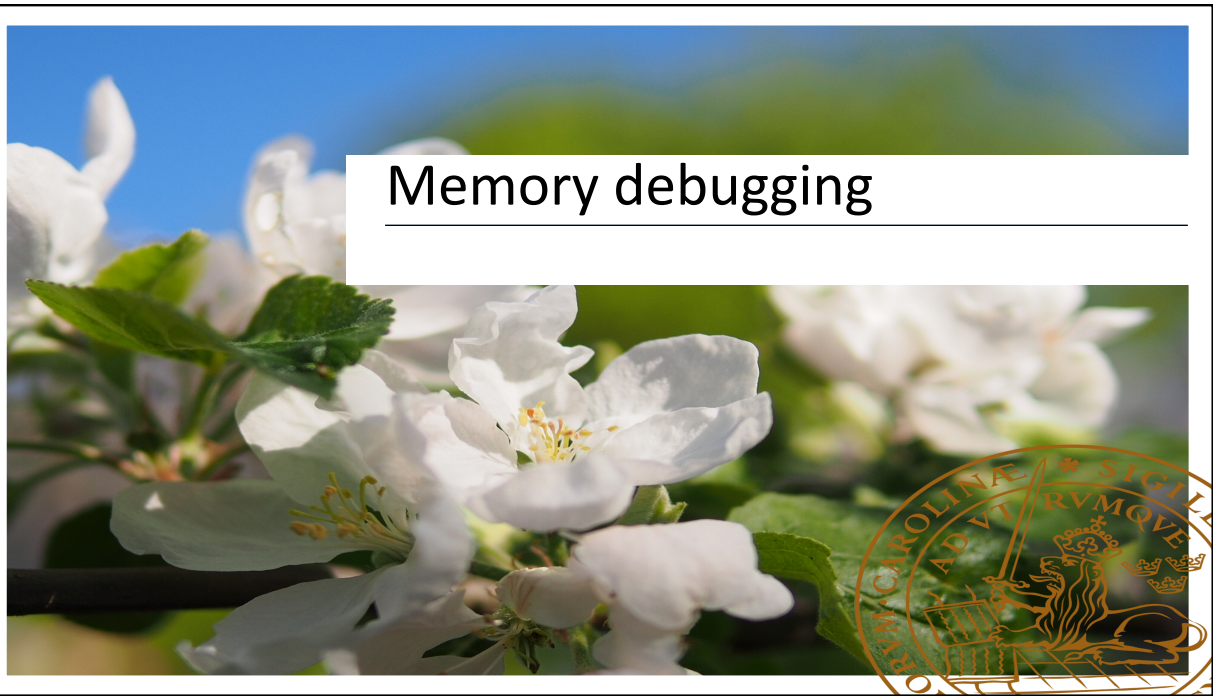


Starting a Pyhon debug session
Locate script in “Stacks” window



Demo

- hello world (Fortran)
- Message on a ring (C)
- Pi-square (Python)



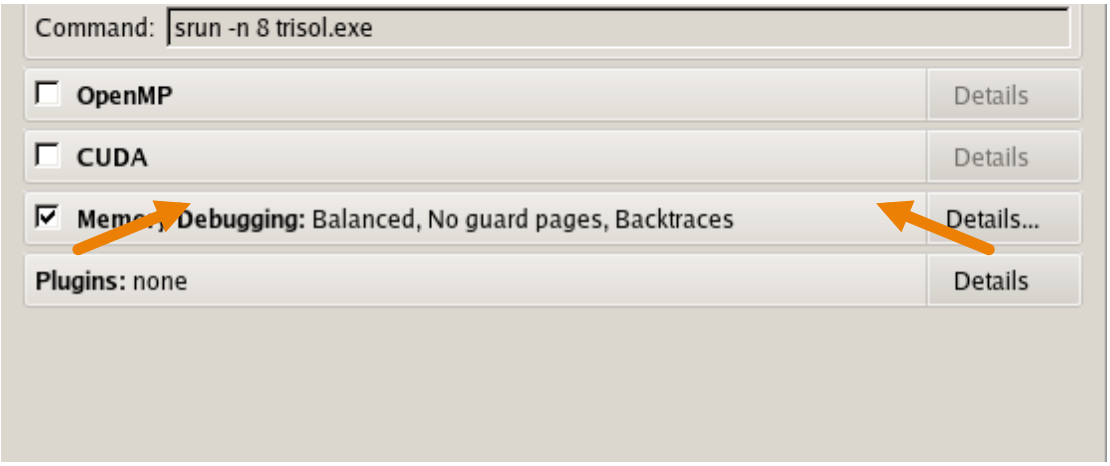
Problematic memory access

- Codes often suffer from memory problems
  - Writing in memory locations they shouldn't
  - Illegal deallocation (double, bad pointer position, ...)
  - Memory leaks
- Typical signatures of memory problems
  - Seg-faults
  - Code behaviour changes when:
    - Editing (e.g. printf debugging)
    - Changing compilers or optimisation flags

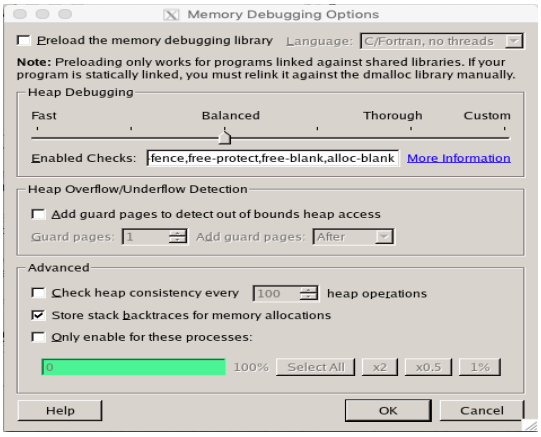
Activating memory debugging in DDT

- Replace the malloc library with ARM's dmalloc
- Comes in 4 versions:
  - C/Fortran no threads
  - C/Fortran threads
  - C++ no threads
  - C++ threads
- Current version seems to prefer "threads"

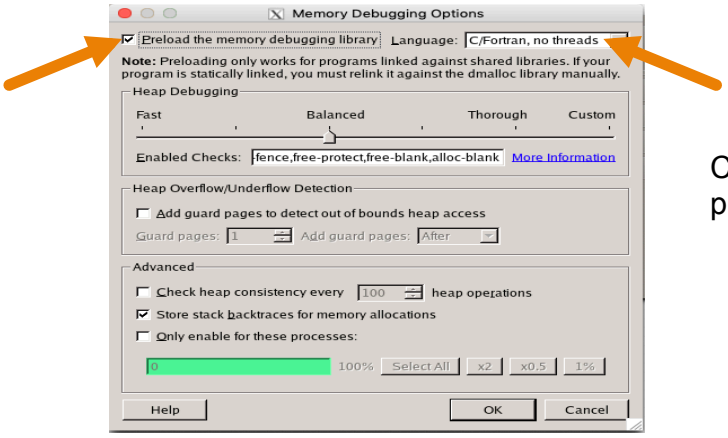
Select memory debugging



Selecting Memory Debugging Option



Dynamic linking



Current version prefers: threads!

Static linking

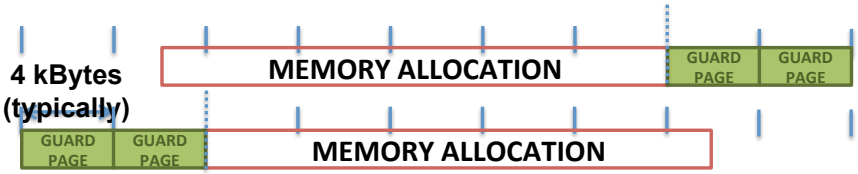
- If you link statically or if dynamic linking fails
- Add a line like (check user guide)

```
-Wl,--allow-multiple-definition,--undefined=malloc /path/lib/64/libdmalloc.a
```

to the link line **before** anything else

– Often required on CRAYs

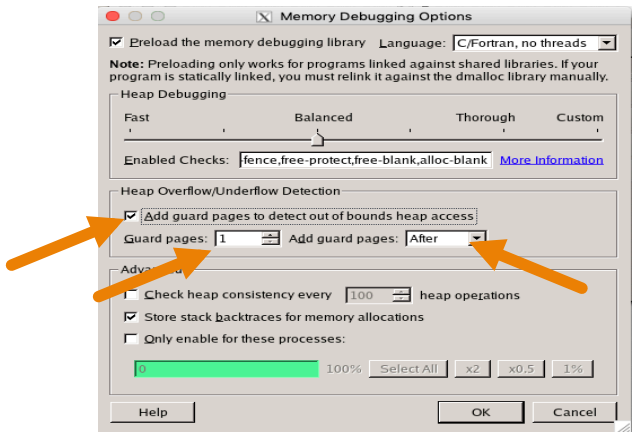
Guard pages (aka “electric fences”)



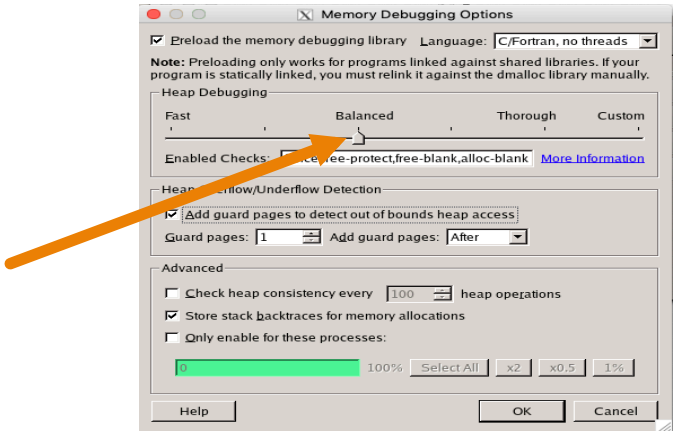
- **A powerful feature…:**
  - **Forbids read/write on guard pages throughout the whole execution**

    (because it overrides C Standard Memory Management library)
- **… to be used carefully:**
  - **Kernel limitation: up to 32k guard pages max**
  - **Beware the additional memory usage cost**

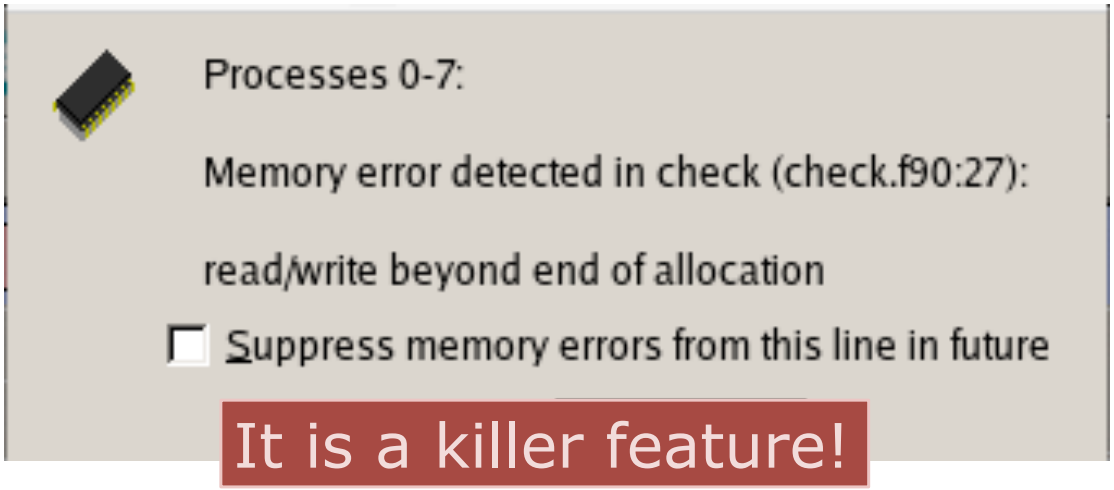
Activate guard pages



Select the depth of the tests



When it finds something you get:



Demo

- Locating memory issue

Recap/Summary

- Starting the gui
- Demonstrating how to run it
- Memory debugging feature
  - This saved me so much time in the years

Acknowledgements

- Juan Gao (ARM)
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