## Computer Architecture Lec 5a

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(Partly taken from Dr. Alon Scholar slides)

Based on slides by:

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Mano/Computer Design and
Architecture 3<sup>rd</sup> Ed.

## General Purpose Digital Computer

Application Algorithm software Operation system Progrmming Language Instruction Set Organization Register Transfer Language nardware Logic Circuits Gates **Electronic Devices** Physics

• Capable of executing various microoperations.

• Can be instructed as to what specific sequence of operations to perform.

• 2

## A Program

- ◆ The user of a computer can control the process by means of a program.
- A program is a set of *instructions* that specify the operations, operand, and the sequence (control)
- A instruction is a binary code that specifies a sequence of microoperations
- Instruction codes together with data are stored in memory (=Stored Program Concept)
- ◆ The computer reads each instruction from memory and places it in control register. The control then interprets the binary code of the instruction and proceeds to execute it by issuing a sequence of microoperations.

Program

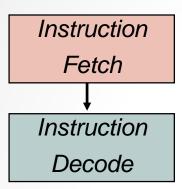
Instruction Fetch Obtain an instruction from program storage in memory

Instruction i

Memory

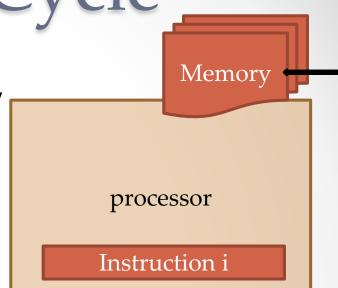
processor

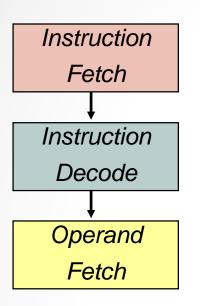
Instruction register



Obtain instruction from program storage in memory

Determine required actions and instruction size

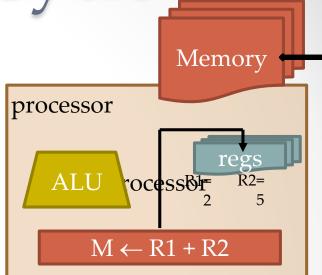


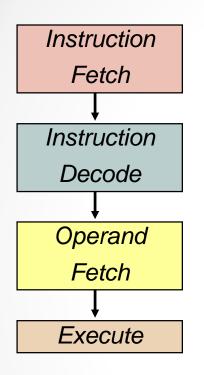


Obtain instruction from program storage in memory

Determine required actions and instruction size

Locate and obtain operand data



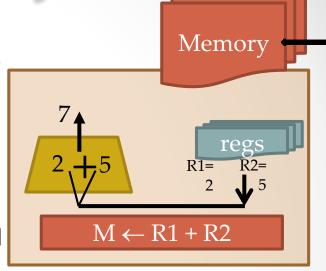


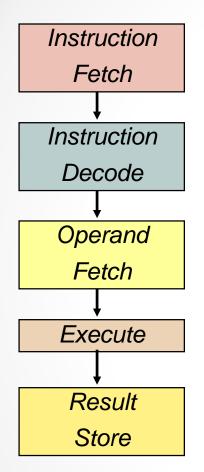
Obtain instruction from program storage in memory

Determine required actions and instruction size

Locate and obtain operand data

Compute result value or status





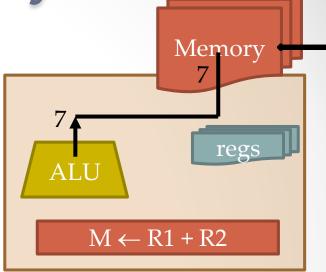
Obtain instruction from program storage in memory

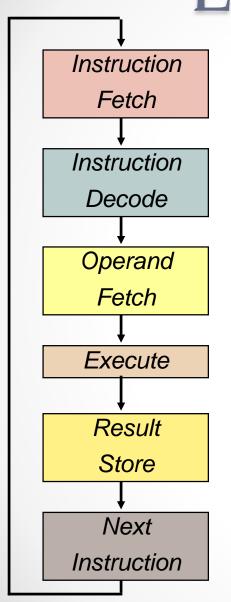
Determine required actions and instruction size

Locate and obtain operand data

Compute result value or status

Deposit results in storage





Obtain instruction from program storage in memory

Determine required actions and instruction size

Locate and obtain operand data

Compute result value or status

Deposit results in storage

Determine next instruction (not the next in case of **branch**)

Memory 7

Regs

Instruction register