

# TEST DETAILS REPORT

2015-06-26, 09:30:30+0200

TC\_005\_17\_reportTest



Project	QTS Tests
Module	005_ReportTests_2
Test Object	TC_005_17_reportTest

## Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	E:\Projects\TESSY_TQP		
Configuration File	\$(PROJECTROOT)\tessy\config\qts_gcc_i386_configuration.xml		
Target Environment	GNU GCC Eclipse CDT (Default)		
Kind of Test	Unit Test		
Linker Options			
Source File(s)			
File	\$(PROJECTROOT)\Source\Report\tessy_qts_report_tests_2.c	Revision: 8	Author: Tobias Bochtler Date: 18.09.2013
Compiler Options	-DHAVE_BOOL -DHAVE_INT8 -DHAVE_INT64 -DHAVE_FLOAT -DHAVE_FLOAT64 -I\$(PROJECTROOT)\Source\Include		

## Attributes

Name	Value
Float Eval Epsilon	0.0
Float Precision	8

## Comments/Description/Specification

Name	Text
Module '005_ReportTests_2'	The ReportTest2 tests are tests with usercode. There is no instrumentation.

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TC\_005\_17\_reportTest



## Declarations

```
// This is the line 1 of the Declarations
// This is the line 2 of the Declarations
// This is the line 3 of the Declarations
```

## Definitions

```
// This is the line 1 of the Definitions
// This is the line 2 of the Definitions
// This is the line 3 of the Definitions
```

## Test Case 1

**Prolog** // This is the Testcase prolog line 1  
**Epilog** // This is the Testcase epilog line 1

## Test Step 1.1 (Repeat Count = 1)

**Prolog** // This is the Teststep prolog line 1  
**Epilog** TESSY\_EVAL\_S32("ext\_input1", ext\_input1, ==, 5);

Name	Actual Value	Expected Value	Result
ext_input1	0x00000005	0x00000005	✓

## Test Case 2

**Prolog** // This is the Testcase prolog line 1  
**Epilog** // This is the Testcase epilog line 1

## Test Step 2.1 (Repeat Count = 1)

**Prolog** // This is the Teststep prolog line 1  
**Epilog** TESSY\_EVAL\_S32("ext\_input1", ext\_input1, ==, 5);

Name	Actual Value	Expected Value	Result
ext_input1	0x00000005	0x00000005	✓

## Test Step 2.2 (Repeat Count = 1)

**Prolog** // This is the Teststep prolog line 1  
**Epilog** TESSY\_EVAL\_S32("ext\_input1", ext\_input1, ==, 5);

Name	Actual Value	Expected Value	Result
ext_input1	0x00000005	0x00000005	✓

## Test Step 2.3 (Repeat Count = 1)

**Prolog** // This is the Teststep prolog line 1  
**Epilog** TESSY\_EVAL\_S32("ext\_input1", ext\_input1, ==, 5);

Name	Actual Value	Expected Value	Result
ext_input1	0x00000005	0x00000005	✓

## Test Case 3

**Prolog** // This is the Testcase prolog line 1  
**Epilog** // This is the Testcase epilog line 1

## Test Step 3.1 (Repeat Count = 1)

**Prolog** // This is the Teststep prolog line 1  
**Epilog** TESSY\_EVAL\_S32("ext\_input1", ext\_input1, ==, 5);

Name	Actual Value	Expected Value	Result
ext_input1	0x00000005	0x00000005	✓

## Test Step 3.2 (Repeat Count = 1)