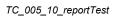
TEST DETAILS REPORT

2015-06-26, 09:29:51+0200





Project QTS Tests Module 005_ReportTests_1 Test Object TC_005_10_reportTest

Statistics

Total Testcases	6	
Successful	6	~
Failed	0	
Not Executed	0	

Module Properties

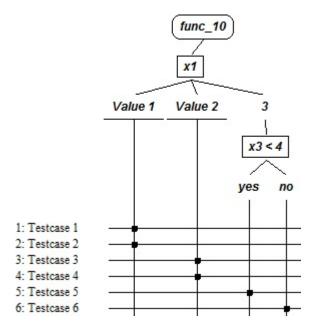
Project Root Directory	E:\Projects\TESSY_TQP		
Configuration File	\$(PROJECTROOT)\tessy\config\qts_gcc_i386_configuration.xml		
Target Environment	GNU GCC Eclipse CDT (Default)		
Kind of Test	Unit Test		
Linker Options			
Source File(s)			
File	\$(PROJECTROOT)\Source\Report\tessy_qts_report_tests_1.c Revision: 8 Author: Tobias Bochtler Date: 17.09.2013		
Compiler Options	-DHAVE_BOOL -DHAVE_INT8 -DHAVE_INT64 -DHAVE_FLOAT -DHAVE_FLOAT64 -I\$(PROJECTROOT)\Source\Include		

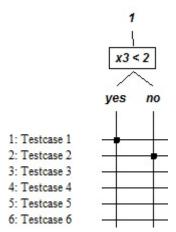
Attributes	
Name	Value
CTE File	\$(PROJECTROOT)\tessy\persist\tessy\project\00000bf3\00000bf5\.database\.tdb\00000CC\TC_005_10_reportTest.cte
Float Eval Epsilon	0.0
Float Precision	8

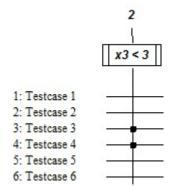
Comments/Description/Specification	
Name	Text
Module '005_ReportTests_1'	The ReportTest1 tests are tests without usercode. There is no instrumentation except the tests 12 - 14 (C1-Instrumentation)



Test Specification

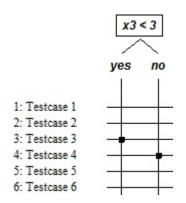








Test Specification



Test Case 1: Testcase 1

Specification

- x1:1 - x3 < 2:yes

Test Step 1.1 (Repeat Count = 1) Input Value Name in_out2 1 in_out3 input1 **Actual Value Expected Value** Name Result in_out2 2 2 0 0 in_out3 output2 1 1

Test Step 1.2 (Repeat Count =	1)		✓
Name	Input Value		
in_out2	1		
in_out3	1		
input1	1		
Name	Actual Value	Expected Value	Result
in_out2	2	2	✓
in_out3	0	0	✓
output2	1	1	✓

Test Step 1.3 (Repeat Count	= 1)			✓
Name	Inp	ut Value		
in_out2	1			
in_out3	1			
input1	1			
Name	Ac	tual Value	Expected Value	Result
in_out2	2		2	~
in_out3	0		0	~
output2	1		1	~

Test Case 2: Testcase 2

Specification

- x1:1 - x3 < 2:no

✓
Input Value
2

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TC_005_10_reportTest

Name	Input Value			
in_out3	2	2		
input1	1			
Name	Actual Value	Expected Value	Result	
in_out2	1	1	~	
in_out2 in_out3	3	3	✓	
output2	1	1	✓	

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
in_out2	2		
in_out3	2		
input1	1		
Name	Actual Value	Expected Value	Result
in_out2	1	1	~
in_out3	3	3	~
output2	1	1	~

Test Case 3: Testcase 3

Specification

- x1:2 - x3 < 3:yes

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
in_out2	2		
in_out3	2		
input1	2		
Name	Actual Value	Expected Value	Result
in_out2	3	3	✓
in_out3	1	1	✓
output2	1	1	✓

Test Step 3.2 (Repeat Count =	1)		✓
Name	Input Value		
in_out2	2		
in_out3	2		
input1	2		
Name	Actual Value	Expected Value	Result
in_out2	3	3	✓
in_out3	1	1	✓
output2	1	1	✓

Test Step 3.3 (Repeat Count =	1)		✓
Name	Input Value		
in_out2	2		
in_out3	2		
input1	2		
Name	Actual Value	Expected Value	Result
in_out2	3	3	~
in_out3	1	1	✓
output2	1	1	~



TC_005_10_reportTest

Test Step 3.4 (Repeat Count = 1)			✓
Name	Input Value		
in_out2	2		
in_out3	2		
input1	2		
Name	Actual Value	Expected Value	Result
in_out2	3	3	~
in_out3	1	1	✓
output2	1	1	✓

input1	2		
Name	Actual Value	Expected Value	Result
in_out2	2	2	~
in_out3	4	4	•
output2	1	1	~

Test Step 4.2 (Repeat Count = 1)			✓	
Name	Input Value			
in_out2	3			
in_out3	3			
input1	2	2		
Name	Actual Value	Expected Value	Result	
in_out2	2	2	✓	
in_out3	4	4	✓	
output2	1	1	✓	

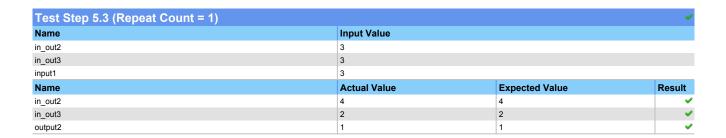
Test Case 5	: Testcase 5	✓
Specification	- x1:3 - x3 < 4:ves	

Test Step 5.1 (Repeat Count = 1)			✓
Name	Input Value		
in_out2	3		
in_out3	3		
input1	3		
Name	Actual Value	Expected Value	Result
in_out2	4	4	✓
in_out3	2	2	✓
output2	1	1	✓

Test Step 5.2 (Repeat Count =	1)		~
Name	Input Value		
in_out2	3		
in_out3	3		
input1	3		
Name	Actual Value	Expected Value	Result
in_out2	4	4	•
in_out3	2	2	✓
output2	1	1	✓



TC_005_10_reportTest



Test Case 6: Testcase 6			✓
Specification - x1:3 - x3 < 4:no			
Test Step 6.1 (Repeat Count = 1)			✓
Name	Input Value		
in_out2	7		
in_out3	2		
input1	3		
Name	Actual Value	Expected Value	Result
in_out2	6	6	✓
in_out3	3	3	✓
output2	1	1	✓

Test Step 6.2 (Repeat Count = 1)			~
Name	Input Value		
in_out2	7		
in_out3	2		
input1	3		
Name	Actual Value	Expected Value	Result
in_out2	6	6	~
in_out3	3	3	✓
output2	1	1	✓