# **TEST DETAILS REPORT**

2015-06-26, 09:29:40+0200



TC\_005\_09\_reportTest

 Project
 QTS Tests

 Module
 005\_ReportTests\_1

 Test Object
 TC\_005\_09\_reportTest

### **Statistics**

Total Testcases	6
Successful	6
Failed	0
Not Executed	0

## **Module Properties**

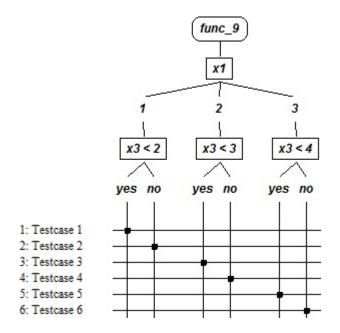
Project Root Directory	E:\Projects\TESSY_TQP		
Configuration File	\$(PROJECTROOT)\tessy\config\qts_gcc_i386_configuration.xml		
Target Environment	GNU GCC Eclipse CDT (Default)		
Kind of Test	Unit Test		
Linker Options			
Source File(s)			
File	\$(PROJECTROOT)\Source\Report\tessy_qts_report_tests_1.c Revision: 8 Author: Tobias Bochtler Date: 17.09.2013		
Compiler Options	-DHAVE_BOOL -DHAVE_INT8 -DHAVE_INT64 -DHAVE_FLOAT -DHAVE_FLOAT64 -I\$(PROJECTROOT)\Source\Include		

Attributes	
Name	Value
CTE File	\$(PROJECTROOT)\tessy\persist\tessy\project\00000bf3\00000bf5\.database\.tdb\00000C09\TC_005_09_reportTest.cte
Float Eval Epsilon	0.0
Float Precision	8

Comments/Description	n/Specification
Name	Text
Module '005_ReportTests_1'	The ReportTest1 tests are tests without usercode. There is no instrumentation except the tests 12 - 14 (C1-Instrumentation)



#### **Test Specification**



## Test Case 1: Testcase 1

Specification

- x1:1 - x3 < 2:yes

Description

Multiline description line 1 Multiline description line 2 Multiline description line 3 Multiline description line 4

Test Step 1.1 (Repeat Count = 3)			✓
Name	Input Value		
in_out2	1		
in_out3	3		
input1	1		
Name	Actual Value	Expected Value	Result
in_out2	2	2	<b>✓</b>
in_out3	2	2	✓
output2	1	1	<b>✓</b>

#### Test Case 2: Testcase 2

Specification

- x1:1 - x3 < 2:no

Description

Multiline description line 1 Multiline description line 2 Multiline description line 3 Multiline description line 4

Test Step 2.1 (Repeat Count = 6)			✓
Name	Input Value		
in_out2	3		
in_out3	3		
input1	1		
Name	Actual Value	Expected Value	Result
in_out2	1	1	<b>✓</b>
in_out3	5	5	<b>✓</b>
output2	1	1	<b>✓</b>



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#### **Test Case 3: Testcase 3**

Specification

- x1:2 - x3 < 3:yes

Multiline description line 1 Description

Multiline description line 2 Multiline description line 3 Multiline description line 4

Test Step 3.1 (Repeat Count = 2)			
Name	Input Value		
in_out2	2		
in_out3	2		
input1	2		
Name	Actual Value	Expected Value	Result
in_out2	2	2	~
in_out3	2	2	•
output2	1	1	~

#### Test Case 4: Testcase 4

Specification

- x1:2 - x3 < 3:no

Description

Multiline description line 1

Multiline description line 2 Multiline description line 3 Multiline description line 4

Test Step 4.1 (Repeat Count = 9) Input Value Name in\_out2 3 3 in\_out3 input1 2 Name **Actual Value Expected Value** in\_out2 2 2 in\_out3 4

#### Test Case 5: Testcase 5

Specification

output2

- x1:3 - x3 < 4:yes

Description

Multiline description line 1

Multiline description line 2 Multiline description line 3 Multiline description line 4

Test Step 5.1 (Repeat Count = 2)			✓
Name	Input Value		
in_out2	3		
in_out3	3		
input1	3		
Name	Actual Value	Expected Value	Result
in_out2	3	3	✓
in_out3	3	3	✓
output2	1	1	<b>✓</b>

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### Test Case 6: Testcase 6

Specification

- x1:3 - x3 < 4:no

Description

Multiline description line 1 Multiline description line 2 Multiline description line 3 Multiline description line 4

Test Step 6.1 (Repeat Count = 3)			V
Name	Input Value		
in_out2	8		
in_out3	4		
input1	3		
Name	Actual Value	Expected Value	Result
in_out2	5	5	~
in_out3	7	7	<b>✓</b>
output2	1	1	~