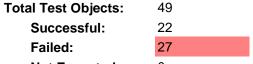


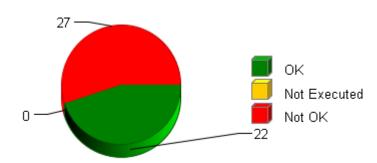
#### **Summary**

# **Overall Test Object Results (including Coverage)**



Not Executed: 0

**Date:** 2015-06-26 **Time:** 12:10:18+0200



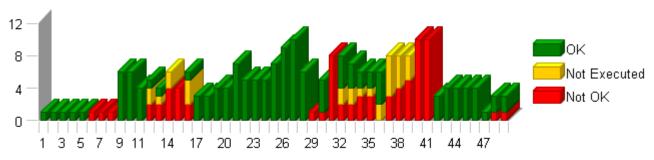
### **Selected Project Items**

Test Collection "005\_ReportTests"

#### **Used Test Environments**

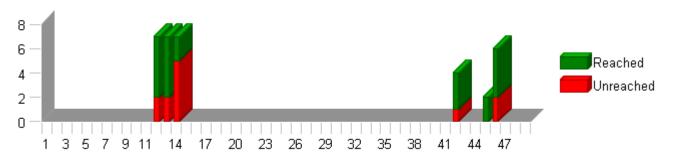
GNU GCC Eclipse CDT (Default)

### Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

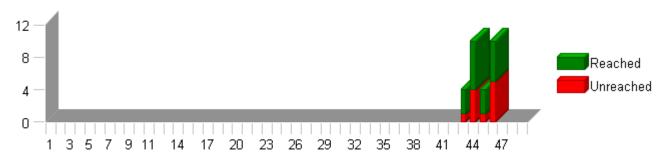
### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.



### MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



Project QTS Tests

# **Test Object List**

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C1	MC/DC	Test Cases	Result
	QTS Tests	63.63 %	60.71 %	71 of 234 failed	×
	005_ReportTests	63.63 %	60.71 %	71 of 234 failed	*
	005_ReportTests_1	57.14 %	-	17 of 49 failed	×
1	TC_005_01_reportTest	-	-	1 of 1 passed	•
2	TC 005 02 reportTest	-	-	1 of 1 passed	~
3	TC 005 03 reportTest	-	-	1 of 1 passed	•
4	TC 005 04 reportTest	-	-	1 of 1 passed	~
5	TC 005 05 reportTest	-	-	1 of 1 passed	~
6	TC 005 06 reportTest	-	-	1 of 1 failed	×
7	TC 005 07 reportTest	-	-	1 of 1 failed	*
8	TC_005_08_reportTest	-	-	1 of 1 failed	×
9	TC 005 09 reportTest	-	-	6 of 6 passed	~
10	TC 005 10 reportTest	-	-	6 of 6 passed	~
11	TC 005 11 reportTest	-	-	4 of 4 passed	~
12	TC 005 12 reportTest	71.42 %	-	2 of 5 failed	×
13	TC_005_13_reportTest	71.42 %	-	2 of 4 failed	*
14	TC_005_14_reportTest	28.57 %	-	4 of 6 failed	×
15	TC_005_15_reportTest	-	-	4 of 4 failed	*
16	TC 005 16 reportTest	-	-	2 of 6 failed	×
	005_ReportTests_2	-	-	52 of 159 failed	*
17	TC 005 17 reportTest	-	-	3 of 3 passed	•

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## **TEST OVERVIEW REPORT** 2015-06-26, 12:10:18+0200

Project QTS Tests



No.	Name	C1	MC/DC	Test Cases Resu	.14
18			MIC/DC		AIL
	TC 005 18 reportTest	-	-	3 of 3 passed	
19	TC 005_19_reportTest	-	-	4 of 4 passed	
20	TC 005 20 reportTest	-	-	4 of 4 passed	
21	TC 005 21 reportTest	-	-	7 of 7 passed	
22	TC 005 22 reportTest	-	-	·	•
23	TC 005 23 reportTest	-	-	0 01 0 passed	•
24	TC 005 24 reportTest	-	-	•	•
25	TC 005 25 reportTest	-	-	7 01 7 passou	•
26	TC 005 26 reportTest	-	-	F	•
27	TC 005 27 reportTest	-	-	10 of 10 passed	
28	TC 005 28 reportTest	-	-	6 of 6 passed	~
29	TC 005 29 reportTest	-	-	1 of 1 falloa	**
30	TC 005 30 reportTest	-	-	1 of 5 failed	*
31	TC 005 31 reportTest	-	-	8 of 8 failed	*
32	TC 005 32 reportTest	-	-	2 of 8 failed	*
33	TC 005 33 reportTest	-	-	2 of 7 failed	**
34	TC 005 34 reportTest	-	-	3 of 6 failed	*
35	TC 005 35 reportTest	-	-	3 of 6 failed	×
36	TC 005 36 reportTest	-	-	4 of 6 passed	~
37	TC 005 37 reportTest	-	-	3 of 8 failed	**
38	TC 005 38 reportTest	-	-	4 of 8 failed	*
39	TC 005 39 reportTest	-	-	5 of 8 failed	*
40	TC 005 40 reportTest	-	-	10 of 10 failed	*
41	TC 005 41 reportTest	-	-	10 of 10 failed	*
	005_ReportTests_3	75 %	60.71 %	19 of 19 passed	×
42	TC 005 42 reportTest	75 %	-	3 of 3 passed	×
43	TC 005 43 reportTest	-	75 %	4 of 4 passed	×

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## **TEST OVERVIEW REPORT** 2015-06-26, 12:10:18+0200

Project QTS Tests



No.	Name	C1	MC/DC	Test Cases F	Result
44	TC 005 44 reportTest	-	60 %	4 of 4 passed	×
45	TC 005 45 reportTest	100 %	75 %	4 of 4 passed	*
46	TC 005 46 reportTest	66.66 %	50 %	4 of 4 passed	×
	005_ReportTests_4	-	-	2 of 7 failed	*
47	TC 005 47 reportTest	-	-	1 of 1 passed	~
48	TC 005 48 reportTest	-	-	1 of 3 failed	*
49	TC_005_49_reportTest	-	-	1 of 3 failed	×

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