

Circuito mux com input de 4 registros de 4 bits cada um :

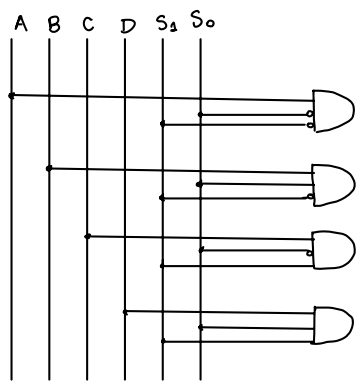


tabela verdade:

S ₁	S ₀	y
0	0	A
0	1	B
1	0	C
1	1	D