



# Report

## Laboratory 2



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*Term:* HT 2017

*Course:* 1DT301 - Computer  
Technology I

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# 1 Assignment 1

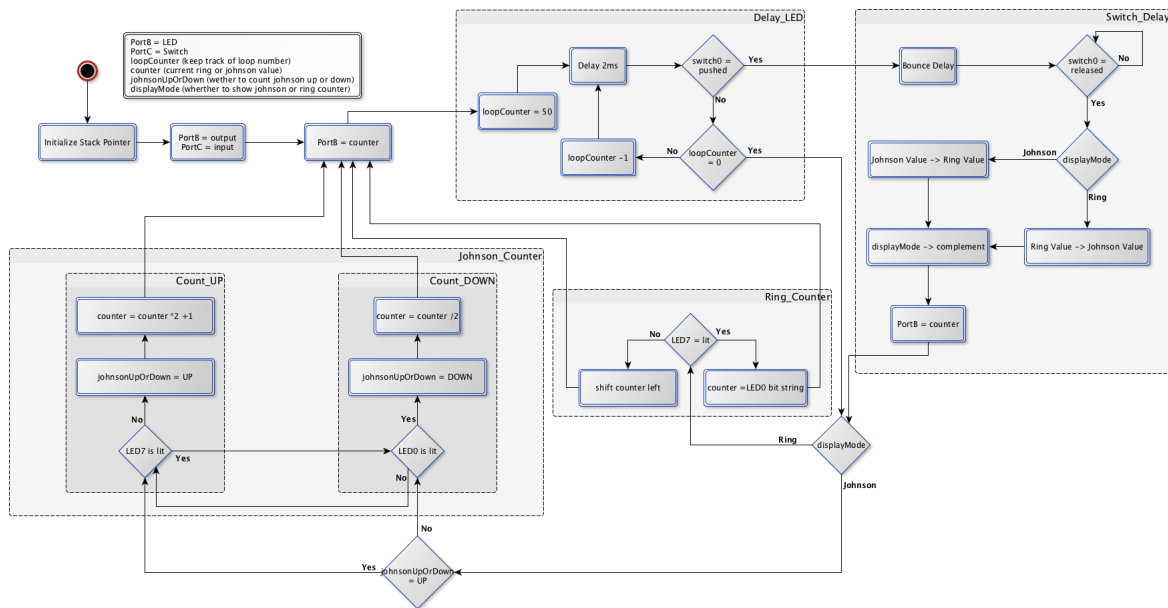


Figure 1: Switch between Johnson and Ring counter using switch0

[illegible]

```

57 .equ DOWN = 0x00                ;constant: value of down
58 .equ JOHNSON = 0x00            ;constant: Johnson display mode
59 .equ RING = 0xFF               ;constant: Ring display mode
60 .equ SWITCH = PINCO            ;constant: PIN of switch to check
61
62 ;Initialize stack pointer
63 ldi r18, HIGH(RAMEND)
64 out SPH, r18
65 ldi r18, LOW(RAMEND)
66 out SPL, r18
67
68 ;set PORTB to output
69 ldi dataDir, 0xFF
70 out DDRB, dataDir
71
72 ;set PORTC to input
73 ldi dataDir, 0x00
74 out DDRC, dataDir
75
76 ;initialize starting state
77 ldi displayMode, JOHNSON
78 ldi counter, 0x01
79 ldi johnUpOrDown, UP
80
81 main_loop:
82     cpi displayMode, JOHNSON        ;if displaymode = johnson
83     brq johnson1                    ;then jump to johnson branch
84
85     ring1:                          ;else jump to ring
86         rcall ring_counter
87         rjmp main_loop
88
89     johnson1:
90         rcall johnson_counter
91
92     rjmp main_loop
93
94 ;Outputs complement of the current value of counter to LEDs
95 led_out:
96     mov complement, counter
97     com complement
98     out PORTB, complement
99     ret
100
101 ;Delay with continuous switch checking
102 delay_led:
103     ldi loopCounter, 50
104
105     loop_led:
106         rcall delay_short
107         rcall check_switch
108
109         cpi loopCounter, 0          ;if loopcounter = 0
110         brq delay_led_end          ;then jump to end
111
112         dec loopCounter
113         rjmp loop_led
114
115     delay_led_end:
116         ret
117
118 ;Creates the ring counter by writing the complement of counter
119 ;to PORTB and then increments the ring counter
120 ring_counter:
121     sbis PORTB, PINB7              ;if the 7th led is lit
122     ldi counter, 0x01              ;then set counter to one
123
124     sbic PORTB, PINB7              ;else
125     lsl counter                    ;shift counter to the left
126
127     rcall led_out
128     rcall delay_led
129
130     ret
131
132 ;Creates the johnson counter by writing the complement of counter
133 ;to PORTB and then checks wheter to count up or down
134 johnson_counter:
135     cpi johnUpOrDown, UP           ;if count up is active
136     brq count_up                   ;then jump to count up
137
138     rjmp count_down                ;else jump to count down
139
140 ;checks whether to continue to count up and
141 ;increments the johnson value
142 count_up:
143     sbis PORTB, PINB7              ;if the 7th led is lit
144     rjmp count_down                ;then jump to count down
145
146     ldi johnUpOrDown, UP
147     lsl counter                    ;shift to the left
148     inc counter                    ;add one
149
150     rjmp end
151
152 ;checks whether to continue to count down and
153 ;decrease the johnson value
154 count_down:
155     sbic PORTB, PINB0              ;if the right most led is not lit
156     rjmp count_up                  ;then jump to count up

```

```

158
159     ldi johnUpOrDown, DOWN
160     lsr counter
161                                     ;shift to the right
162
163 end:
164     rcall led_out
165     rcall delay_led
166
167     ret
168
169 ;Checks if the switch is pressed and in that case calls on_switch_pressed
170 check_switch:
171     sbic PINC, SWITCH                ;if switch is not pressed
172     rjmp check_switch_end           ;then jump to end of subroutine
173
174     switch_pressed_down:
175         rcall delay_switch
176
177         ;wait until button is released
178     loop_switch:
179         sbis PINC, SWITCH            ;if switch to the right most is still pressed
180         rjmp loop_switch            ;then jump to loop switch
181
182         ;When the button has been released we consider the switch pressed
183         rcall on_switch_pressed
184
185     check_switch_end:
186     ret
187
188
189 ;Handles what should happen when the switch gets pressed
190 on_switch_pressed:
191     cpi displayMode, JOHNSON        ;if displaymode = johnson
192     brq johnson_to_ring             ;then jump to johnson to ring
193
194     cpi displayMode, RING           ;if displaymode = ring
195     brq ring_to_johnson             ;then jump to ring to johnson
196
197     ;convert ring value to johnson value
198     ring_to_johnson:
199         lsl counter
200         dec counter
201
202         rjmp switch_end
203
204     ;convert johnson value to ring value
205     johnson_to_ring:
206         lsr counter
207         inc counter
208
209     switch_end:
210         com displayMode
211         ;TODO: needed? rcall led_out
212         rcall led_out
213                                     ;toogle displaymode between ring and johnson
214     ret
215
216 ;Delay for 2 ms
217 delay_short:
218     ldi r31, 13
219     ldi r30, 252
220     L1:
221         dec r30
222         brne L1
223         dec r31
224         brne L1
225         nop
226
227     ret
228
229 ;Delay 10 ms to avoid bouncing when switch is pressed
230 delay_switch:
231     ldi r31, 13
232     ldi r30, 252
233     L2:
234         dec r30
235         brne L2
236         dec r31
237         brne L2
238         nop
239
240     ret
241

```

## 2 Assignment 2

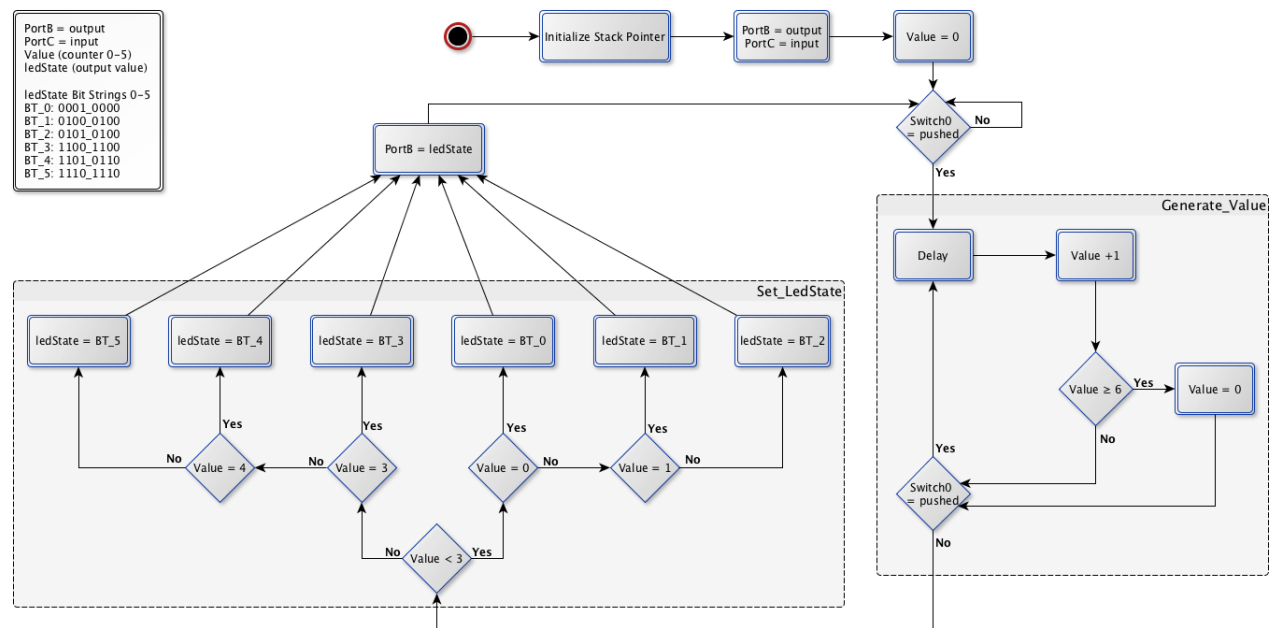


Figure 2: Simulating electronic dice

```

1 >>>=====
2 ; IDT301, Computer Technology I
3 ; Date: 2017-09-19
4 ; Author:
5 ;           Caroline Nilsson             (cn222nd)
6 ;           Daniel Alm Grundström        (dg222dw)
7 ;
8 ; Lab number:          2
9 ; Title:               Subroutines
10 ;
11 ; Hardware:            STK600, CPU ATmega2560
12 ;
13 ; Function:            Generates a random value between 1 and 6 when the user
14 ;                     presses down switch SW0 and, when the user releases
15 ;                     the switch, outputs a representation of a dice value
16 ;                     to the LEDs
17 ;
18 ; Input ports:         PIN0 on PORTC
19 ;
20 ; Output ports:        PORTB
21 ;
22 ; Subroutines:         generate_value      - Generate a pseudorandom value
23 ;                                     between 1 and 6
24 ;                     set_led_state       - Set value to output to LEDs
25 ;                     delay_switch        - Delay of 10 ms used after switch is
26 ;                                         pressed down
27 ;
28 ; Included files:      m2560def.inc
29 ;
30 ; Other information:   N/A
31 ;
32 ; Changes in program:
33 ;                 2017-09-14:
34 ;                 Implements flowchart design .
35 ;
36 ;                 2017-09-19:
37 ;                 Adds constants for LED dice states and comments .
38 ;
39 <<<<=====
40 #include "m2560def.inc"
41
42 #define dataDir = r16
43 #define randomNumber = r17
44 #define ledState = r18
45 #define complement = r19
46
47 #equ LED_DICE_1 = 0b0001_0000
48 #equ LED_DICE_2 = 0b0100_0100
49 #equ LED_DICE_3 = 0b0101_0100
50 #equ LED_DICE_4 = 0b1100_1100
51 #equ LED_DICE_5 = 0b1101_0110
52 #equ LED_DICE_6 = 0b1110_1110
53
54 ldi r16 , HIGH(RAMEND)

```

```

55 out SPH, r16
56 ldi r16, LOW(RAMEND)
57 out SPL, r16
58
59 ldi dataDir, 0xFF
60 out DDRB, dataDir
61
62 ldi dataDir, 0x00
63 out DDRC, dataDir
64
65 ldi complement, 0xFF
66 out PORTB, complement
67
68 loop:
69 sbic PINC, PINC0 ;Wait until switch is pressed down
70 rjmp loop
71
72 rcall generate_value
73 rcall set_led_state
74 mov complement, ledState
75 com complement
76 out PORTB, complement
77
78 rjmp loop
79
80 ;Generate a pseudorandom value by repeatedly incrementing a counter for as long
81 ;as the switch is pressed down
82 generate_value:
83 ldi ledState, 0xFF ;Reset LEDs
84 out PORTB, ledState
85 start:
86
87 rcall delay_switch ;Delay to avoid bouncing effects
88
89 inc randomValue
90 cpi randomValue, 6
91 brge reset_value
92 rjmp end
93
94 reset_value:
95 ldi randomValue, 0
96
97 end:
98 sbis PINC, PINC0 ;If switch is still pressed down
99 rjmp start ; then jump to start
100
101 ret
102
103 ;Set LED output value to bit pattern representing different dice values
104 ;depending of value of the pseudorandomly generated value
105 set_led_state:
106 cpi randomValue, 3
107 brlo less
108 rjmp more
109
110 less:
111 cpi randomValue, 0
112 breq one
113
114 cpi randomValue, 1
115 breq two
116
117 rjmp three
118
119 one:
120 ldi ledState, LED_DICE_1
121 rjmp end_led_state
122
123 two:
124 ldi ledState, LED_DICE_2
125 rjmp end_led_state
126
127 three:
128 ldi ledState, LED_DICE_3
129 rjmp end_led_state
130
131 more:
132 cpi randomValue, 3
133 breq four
134
135 cpi randomValue, 4
136 breq five
137
138 rjmp six
139
140 four:
141 ldi ledState, LED_DICE_4
142 rjmp end_led_state
143
144 five:
145 ldi ledState, LED_DICE_5
146 rjmp end_led_state
147
148 six:
149 ldi ledState, LED_DICE_6
150 rjmp end_led_state
151
152 end_led_state:
153 ret
154
155 ;Delay 10 ms to avoid bouncing effects when a switch is first pressed down

```

```
156 delay_switch:
157     ldi r31, 13
158     ldi r30, 252
159     L1:
160         dec r30
161         brne L1
162         dec r31
163         brne L1
164         nop
165
166     ret
```



### 3 Assignment 3

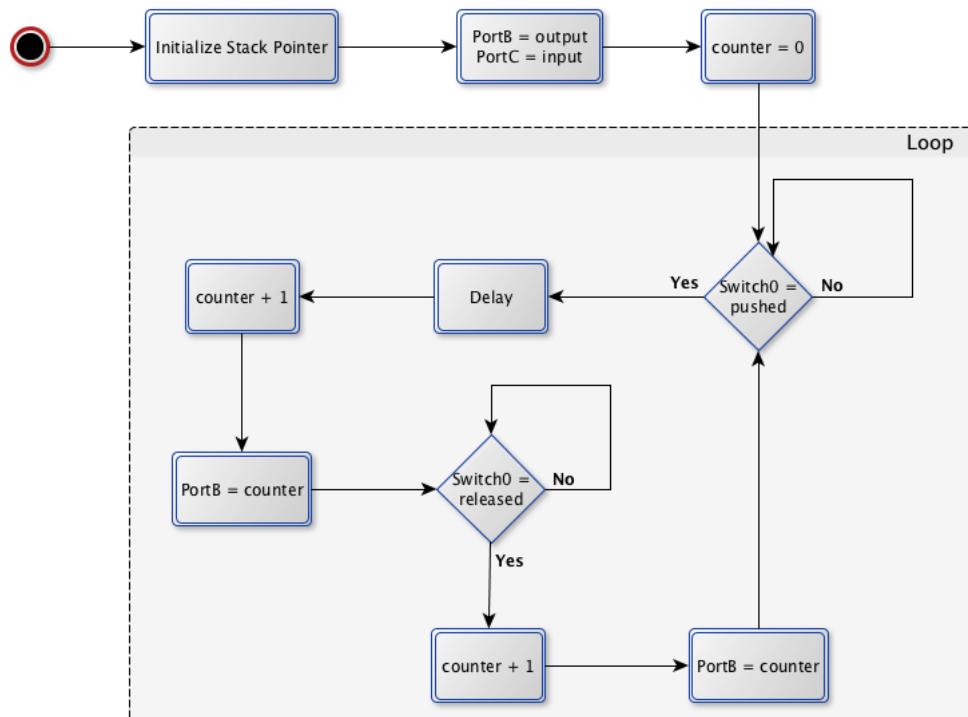


Figure 3: Change counter on switch0

[illegible]

```

49
50 ldi r16, HIGH(RAMEND)
51 out SPH, r16
52 ldi r16, LOW(RAMEND)
53 out SPL, r16
54
55 ldi dataDir, 0xFF
56 out DDRB, dataDir
57
58 ldi dataDir, 0x00
59 out DDRC, dataDir
60
61 ldi counter, 0x00
62 rcall led_out
63
64 main_loop:
65     rcall wait_for_switch_press
66     rcall on_switch_down
67
68     rcall wait_for_switch_release
69     rcall on_switch_up
70
71     rjmp main_loop
72
73 ;Pauses execution of program until SW0 is pressed down
74 wait_for_switch_press:
75     loop:
76         sbic PINC, PINC0                ;If SW0 is not pressed down
77         rjmp loop                    ; then continue waiting
78         ret                          ;return when SW0 gets pressed down
79
80 ;Handles what should happen when SW0 gets pressed down
81 on_switch_down:
82     rcall delay_switch                ;Delay to avoid bouncing effects
83     inc counter
84     rcall led_out                    ;Output new counter value
85     ret
86
87 ;Pauses execution of program until SW0 is released
88 wait_for_switch_release:
89     loop_2:
90         sbis PINC, PINC0                ;If SW0 is still pressed down
91         rjmp loop_2                    ; then continue waiting
92         ret                          ;return when SW0 gets released
93
94 ;Handles what should happen when SW0 gets released
95 on_switch_up:
96     inc counter
97     rcall led_out                    ;Output new counter value
98     ret
99
100 ;Outputs a binary representation of the current counter value to the LEDs
101 led_out:
102     mov complement, counter
103     com complement
104     out PORTB, complement
105
106 ;Delay of 10 ms. Used to avoid effects of switch bouncing
107 delay_switch:
108     ldi r31, 13
109     ldi r30, 252
110     L1:
111         dec r30
112         brne L1
113         dec r31
114         brne L1
115         nop
116
117     ret

```

## 4 Assignment 4

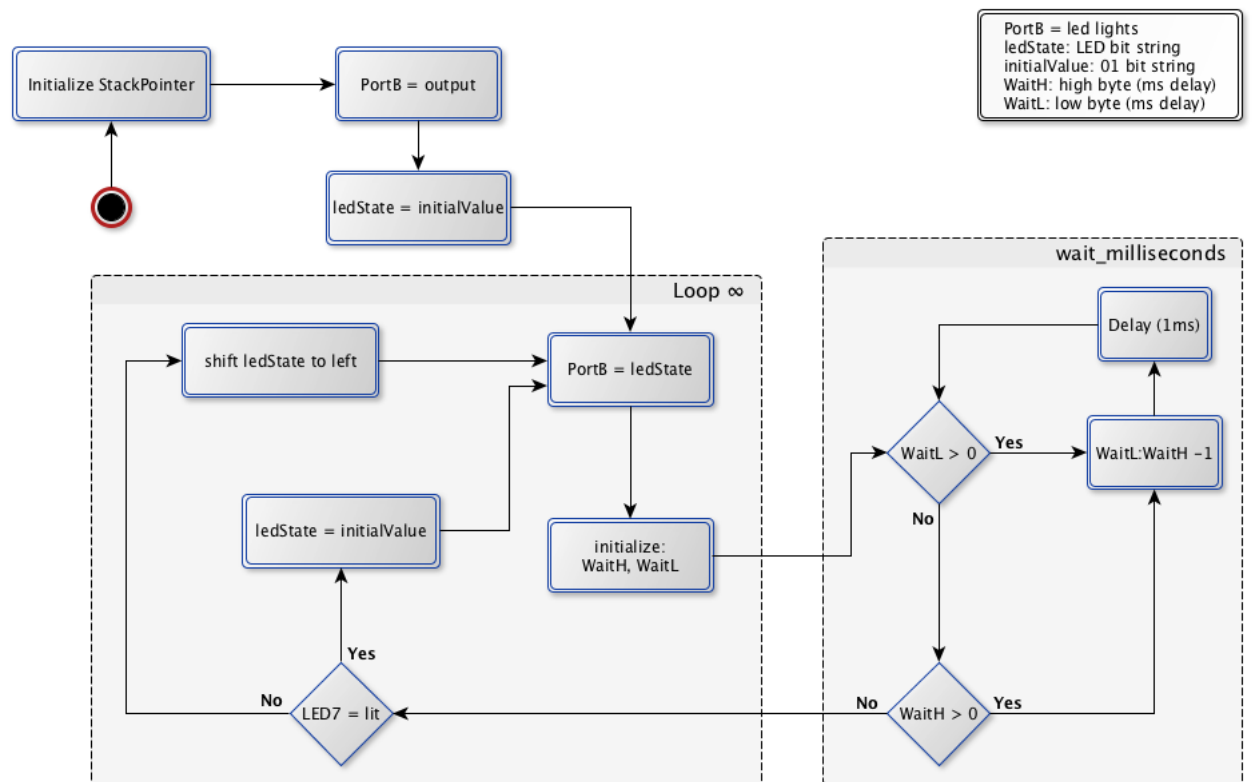


Figure 4: Ring counter with "wait-milliseconds" delay

[illegible]

```

46 .def waitH = r25
47 .def waitL = r24
48 .equ INITIAL_LED_STATE = 0x01
49
50
51 ; Initialize SP, Stack Pointer
52 ldi r20, HIGH(RAMEND) ; R20 = high part of RAMEND address
53 out SPH,R20 ; SPH = high part of RAMEND address
54 ldi R20, low(RAMEND) ; R20 = low part of RAMEND address
55 out SPL,R20 ; SPL = low part of RAMEND address
56
57 ; Set PORTB to output
58 ldi dataDir, 0xFF
59 out DDRB, dataDir
60
61 ldi ledState, INITIAL_LED_STATE ; Set initial LED state
62
63 loop1:
64 mov complement, ledState
65 com complement
66 out PORTB, complement ; Write state to LEDs
67
68 ldi waitH, HIGH(1000)
69 ldi waitL, LOW(1000)
70 rcall wait_milliseconds ; Delay to make changes visible
71
72 sbis PORTB, PINB7
73 ldi ledstate, INITIAL_LED_STATE
74 sbic PORTB, PINB7
75 lsl ledState ; Rotate LED state to the left
76 rjmp loop1
77
78 wait_milliseconds:
79 loop2:
80 cpi waitL, 0x00
81 breq low_zero
82 rjmp wait
83
84 low_zero:
85 cpi waitH, 0x00
86 breq high_zero
87 rjmp wait
88
89 high_zero:
90 ret
91
92 wait:
93 sbiw waitH:waitL, 0x01
94
95 ldi r20, 2
96 ldi r19, 74
97 L1: dec r19
98 brne L1
99 dec r20
100 brne L1
101 rjmp PC+1
102
103 rjmp loop2

```