



Review Test Submission: Pop Quiz 9 (10/22)

User	Yuchen Wang
Course	CSE661/CIS655 - Advanced Computer Architecture - F20
Test	Pop Quiz 9 (10/22)
Started	10/22/20 10:02 AM
Submitted	10/22/20 10:12 AM
Due Date	10/22/20 12:20 PM
Status	Completed
Attempt Score	7 out of 10 points
Time Elapsed	9 minutes out of 10 minutes
Results Displayed	All Answers, Submitted Answers, Correct Answers, Feedback, Incorrectly Answered Questions

Question 1

2 out of 2 points



Name dependencies are detected at runtime in superscalar processors.

Selected Answer: ☒ TrueAnswers: ☒ True
☐ False

Question 2

2 out of 2 points



Amdahl's law does not apply to multi issue processors.

Selected Answer: ☒ FalseAnswers: ☐ True
☒ False

Question 3

0 out of 1 points



GCD can be used to determine if a loop is parallel or not for certain.

Selected Answer: ☒ TrueAnswers: ☐ True
☒ False

Question 4

0 out of 2 points



Which of the following statements is false?

Selected
Answer:



VLIW architectures execute instructions in parallel based on a fixed schedule determined when the code is compiled.

Answers:

A scalar processor processes one data item at a time.

VLIW architectures execute instructions in parallel based on a fixed schedule determined when the code is compiled.



Superscalar architectures with speculative executions are suitable for embedded processors

In a vector processor, a single instruction operates simultaneously on multiple data items.

Question 5

2 out of 2 points



Which of the following statements is false?

Selected
Answer:



SIMD extension can efficiently handle sparse data by scatter and gather.

Answers:

Software-based scheduling has the advantage of viewing the entire program, and can be more sophisticated.



SIMD extension can efficiently handle sparse data by scatter and gather.

Dynamic scheduling implies out-of-order execution.

In VLIW, several different operations can be executed in one clock cycle.

Question 6

1 out of 1 points



GPU hardware handles

Selected Answer:



Thread management

Answers:

Applications



Thread management

Operating System

Virtual memory

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← OK

