Review Test Submission: Pop Quiz 11 (11/5)

User	Yuchen Wang
Course	CSE661/CIS655 - Advanced Computer Architecture - F20
Test	Pop Quiz 11 (11/5)
Started	11/5/20 9:49 AM
Submitted	11/5/20 9:56 AM
Due Date	11/5/20 12:30 PM
Status	Completed
Attempt Score	10 out of 16 points
Time Elapsed	7 minutes out of 15 minutes
Results Displayed	All Answers, Submitted Answers, Correct Answers, Feedback, Incorrectly Answered Questions

Question 1
2 out of 2 points

In a vector processor, a single instruction operates simultaneously on multiple data items.

Selected Answer: ✅ True

Answers: ✅ True

False

Question 2
2 out of 2 points

Only multicore processors can have simultaneous multithreading.

Selected Answer: ✅ False

Answers: True

✅ False

Question 3
2 out of 2 points

Superscalar processors exploit Data Level Parallelism.

Selected Answer: ✅ False

Answers: True

✅ False

Question 4

2 out of 2 points

Simultaneous multithreading uses threads to improve resource utilization of dynamically scheduled processors.

Selected Answer: ✅ True

Answers: ✅ True

False

Question 5

2 out of 2 points

In modern computers, CPU and Memory are connected by BUS

Selected Answer: ✅ True

Answers: ✅ True

False

Question 6

0 out of 2 points

Which of the following variations of cache technology is strongly associated with superscalar processing?

Selected Answer: ❌ Sub-block placement

Answers: "Hit under Miss"

✅ Multi-ported caches

Sub-block placement

Write-back caches

Question 7

0 out of 2 points

Consider a Simultaneous Multithreading (SMT) machine with limited hardware resources. Which of the following hardware constraints would limit the total number of threads that the machine can support most.

Selected Answer: ❌ Data Cache Size

Answers: Number of Functional Unit

✅ Number of Physical Registers

Data Cache Size

Data Cache Associativity

Question 8

0 out of 2 points

CUDA groups 16 parallel threads into a

Selected Answer: ❌ Warp

Answers: Unit

Warp

Convoy

✅ None of the above

Wednesday, December 2, 2020 11:34:02 PM EST

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