



Review Test Submission: Quiz 9 (11/12)

User	Yuchen Wang
Course	CSE661/CIS655 - Advanced Computer Architecture - F20
Test	Quiz 9 (11/12)
Started	11/12/20 8:45 AM
Submitted	11/12/20 8:47 AM
Due Date	11/12/20 11:59 PM
Status	Completed
Attempt Score	50 out of 50 points
Time Elapsed	2 minutes out of 1 hour
Results Displayed	All Answers, Submitted Answers, Correct Answers

Question 1

10 out of 10 points

In the Centralized Shared Memory, there is uniform access time/latency.

Selected Answer: ☒ True

Answers: ☒ True
☐ False

Question 2

10 out of 10 points

To support Fine-Grained Multithreading, CPU must be able to switch threads every clock.

Selected Answer: ☒ True

Answers: ☒ True
☐ False

Question 3

10 out of 10 points

SMT makes sense with coarse-grained implementation

Selected Answer: ☒ False

Answers: ☐ True
☒ False

Question 4

10 out of 10 points

Each thread requires its own user state such as PC, GPRs.

Selected Answer: ☒ True

Answers: ☒ True
☐ False

Question 5

10 out of 10 points

Course-Grained Multithreading also slows down threads

Selected Answer: ☒ False

Answers: ☐ True
☒ False

Wednesday, December 2, 2020 10:56:20 PM EST

← OK