

# 《数字电路实验》实验报告

实验名称: 静态和动态数码管显示实验 指导教师: 高兴宇, 李扬波  
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小组成员: 尹超 张硕 李雨恒

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## 小组成员分工

- 尹超: 负责基础实验内容, 实验报告撰写、实验结果分析。
- 张硕: 负责基础实验及拓展实验内容、实验代码编写。
- 李雨恒: 负责基础实验及拓展实验协助、实验结果分析。

## 实验 GitHub 仓库地址

[https://github.com/CarterYin/Digital\\_electronics\\_experiments](https://github.com/CarterYin/Digital_electronics_experiments)

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## §1 实验仪器与用具

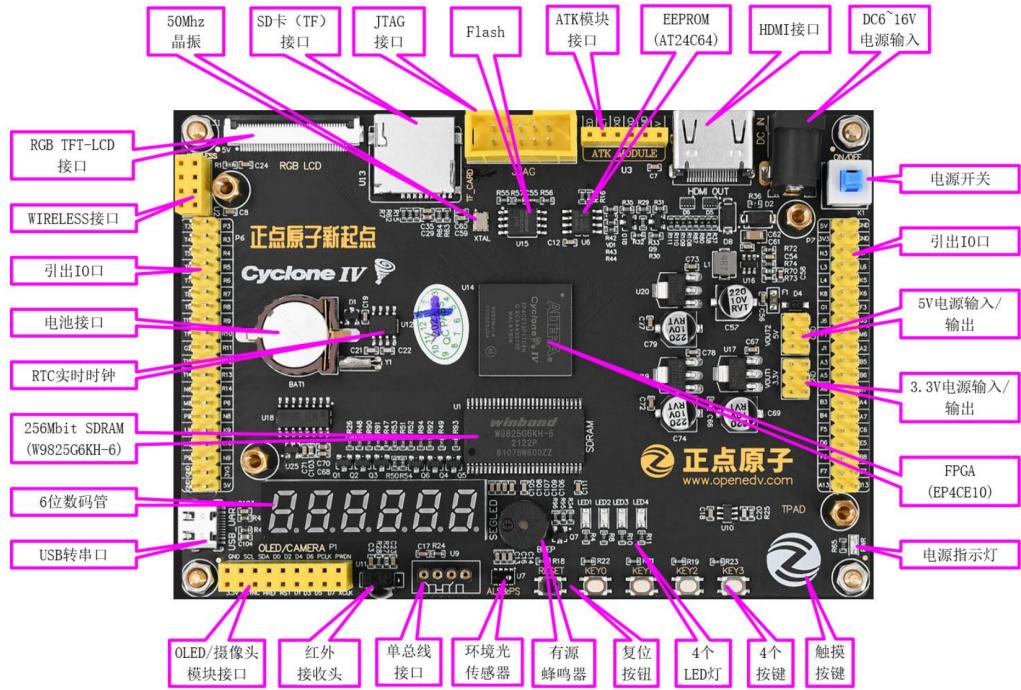


图 1: 实验仪器与用具

新起点 FPGA 开发板板载资源如下:

- 主控芯片: EP4CE10F17C8N, 封装: BGA256
- 晶振: 50 MHz
- FLASH: W25Q16, 容量: 16 Mbit (2M 字节)
- SDRAM: W9825G6KH-6, 容量: 256 Mbit (32 M 字节)
- EEPROM: AT24C64, 容量: 64 Kbit (8 K 字节)
- 1 个电源指示灯 (蓝色)
- 4 个状态指示灯 (DS0 DS3: 红色)
- 1 个红外接收头, 并配备一款小巧的红外遥控器
- 1 个无线模块接口, 支持 NRF24L01 无线模块
- 1 路单总线接口, 支持 DS18B20/DHT11 等单总线传感器
- 1 个 ATK 模块接口, 支持正点原子蓝牙/GPS/MPU6050/RGB 灯模块
- 1 个环境光传感器, 采用 AP3216C 芯片
- 1 个标准的 RGB TFT-LCD 接口
- 1 个 OLED/摄像头模块接口
- 1 个 USB 串口
- 1 个有源蜂鸣器
- 1 个 SD 卡接口 (在板子背面)
- 1 个 HDMI 接口
- 1 个标准的 JTAG 调试下载口
- 1 组 5V 电源供应/接入口
- 1 组 3.3V 电源供应/接入口
- 1 个直流电源输入接口 (输入电压范围: DC6~16V)

- 1 个 RTC 后备电池座, 并带电池 (在板子背面)
- 1 个 RTC 实时时钟, 采用 PCF8563 芯片
- 1 个复位按钮, 可作为 FPGA 程序执行的复位信号
- 4 个功能按钮
- 1 个电容触摸按键
- 1 个电源开关, 控制整个开发板的电源
- 两个  $20 \times 2$  扩展口, 共 72 个扩展 IO 口 (除去电源和地)



图 2: RGB-LCD 屏

LCD 的全称是 Liquid Crystal Display, 即液晶显示屏, 它显示的每个像素点都是由集成在液晶后面的薄膜晶体管独立驱动, 因此 LCD 具有较高的响应速度以及较好的图像质量。正点原子推出的 RGB-LCD 液晶屏较多, 7 寸 RGB-LCD 屏的实物图如上图所示。

LCD 的构造是在两片平行的玻璃基板当中放置液晶盒, 下基板玻璃上设置 TFT(薄膜晶体管), 上基板玻璃上设置彩色滤光片, 通过 TFT 上的信号与电压改变来控制液晶分子的转动方向, 从而达到控制每个像素点偏振光出射与否而达到显示目的。

## § 2 实验内容与步骤

### 2.1 概述

#### RGB-LCD 彩条显示实验

- A. 按照指南开发指南第二十二章 (P391) 复现 RGB-LCD 彩条显示实验, 熟悉引脚分配及 LCD 屏幕显示实现

#### RGB-LCD 字符和图片显示实验

- (1) 参考指南开发指南第二十三章 (P414) RGB-LCD 字符和图片显示实验, 修改实验代码与逻辑, 使其分别实现下述功能:

- A. 显示的内容为 UCAS 文字与 UCAS-LOGO 图像, 字宽和字高都设置为 64
- B. 显示小组成员 3(2) 人的名字 (并排与换行均可), 并修改显示区域的大小, 字宽和字高都设置为 64
- C. (拓展内容) 添加控制功能, 与按键绑定, 按下按键 1, 2, 3, 4 分别显示小组成员姓名, 小组成员学号, “中国科学院大学数字电路”, 自定义图像 (非 UCAS-LOGO, 注意图像大小)

## 2.2 本次实验的快速实验步骤

LCD 连接开发板

### LCD屏幕连接

连接时, 先掀起 FPC 连接器上的黑色翻盖, 将 FPC 排线蓝色面朝上插入连接器, 最后将黑色翻盖压下以固定 FPC 排线。

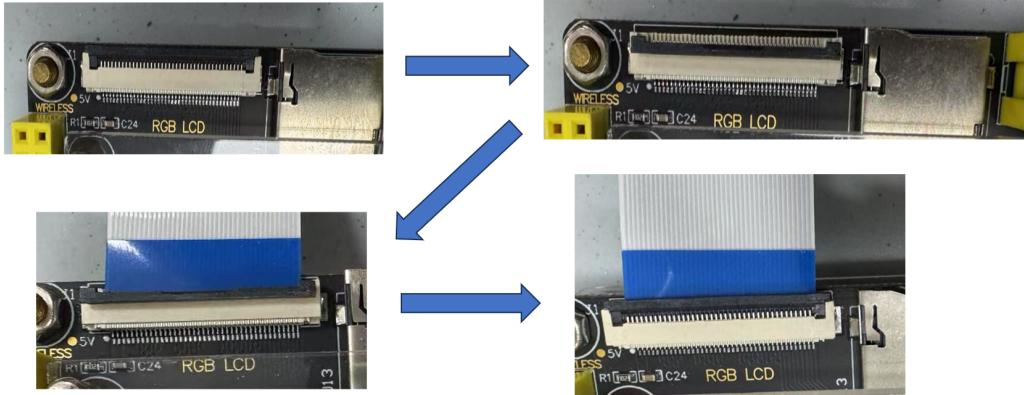


图 3: LCD 连接开发板

字符到点阵—使用 PCLtoLCD 工具

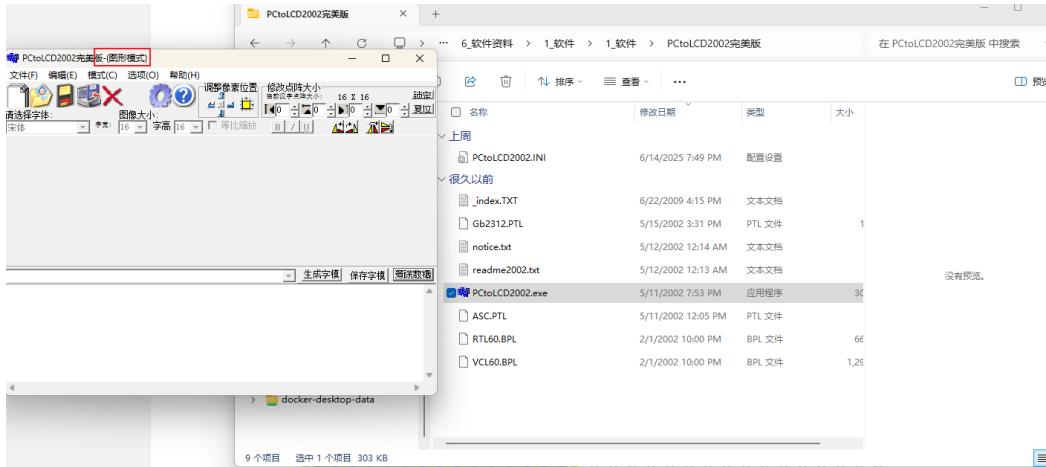


图 4: PCLtoLCD 工具

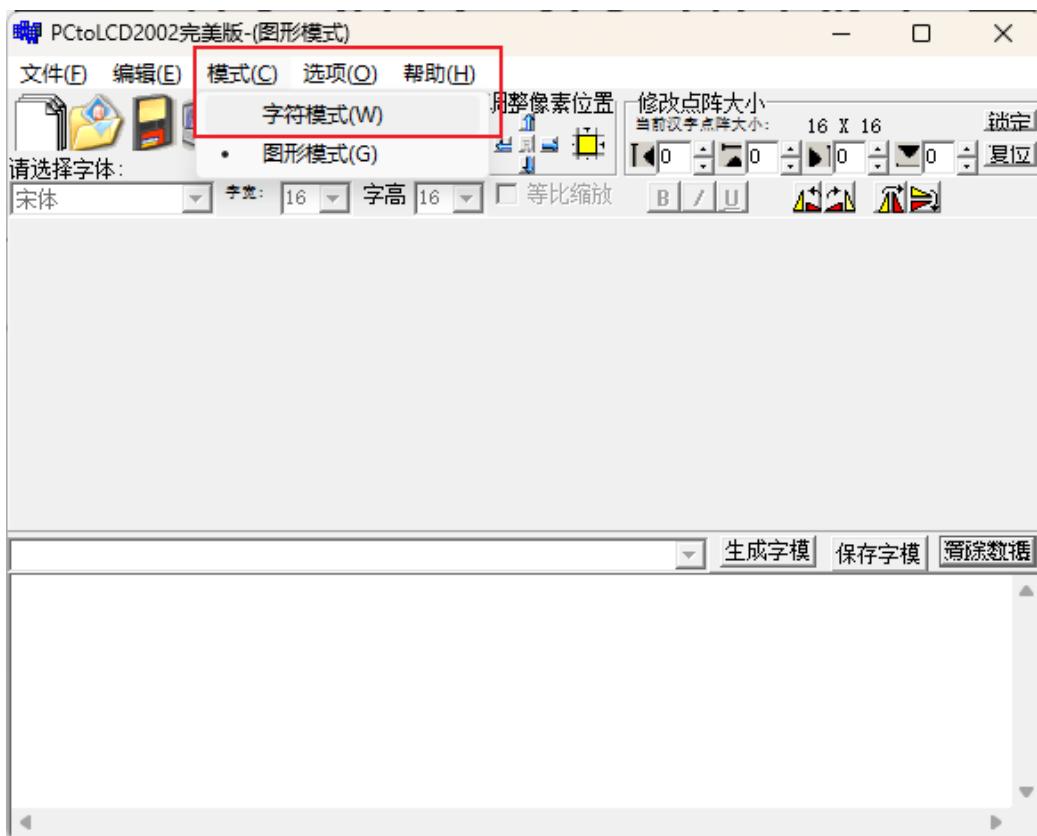


图 5: PCLtoLCD 工具

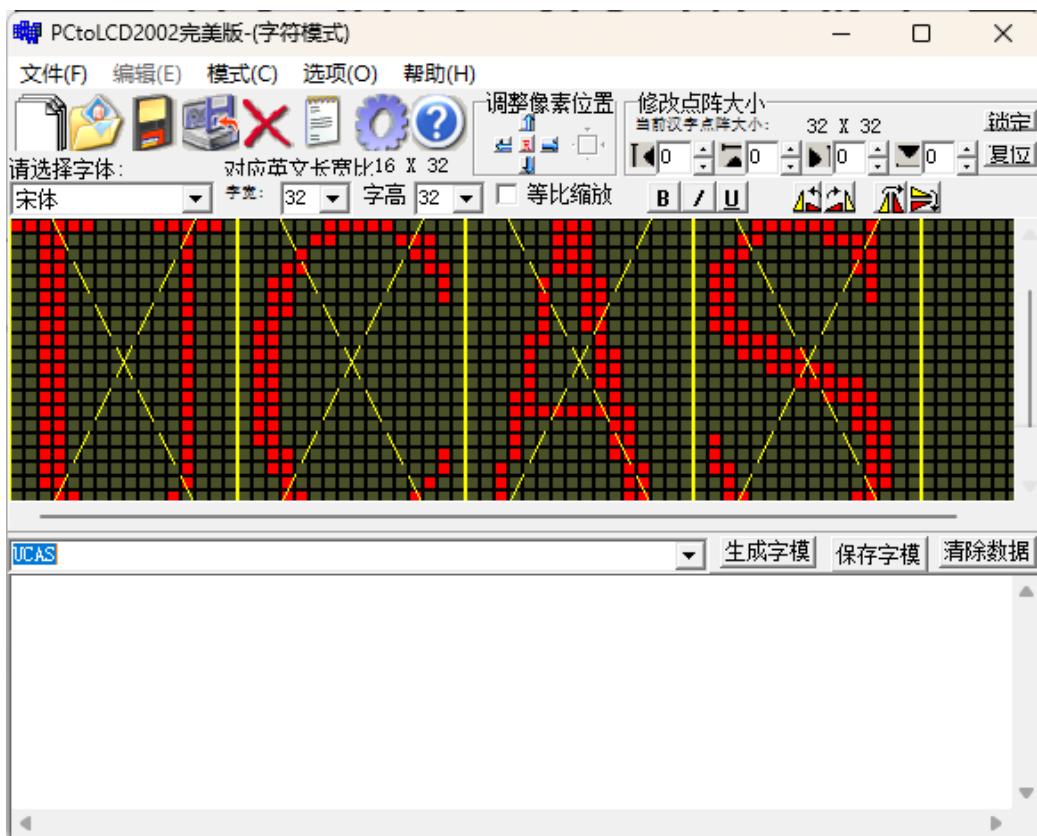


图 6: PCLtoLCD 工具

先另存为 BMP 再转为整体

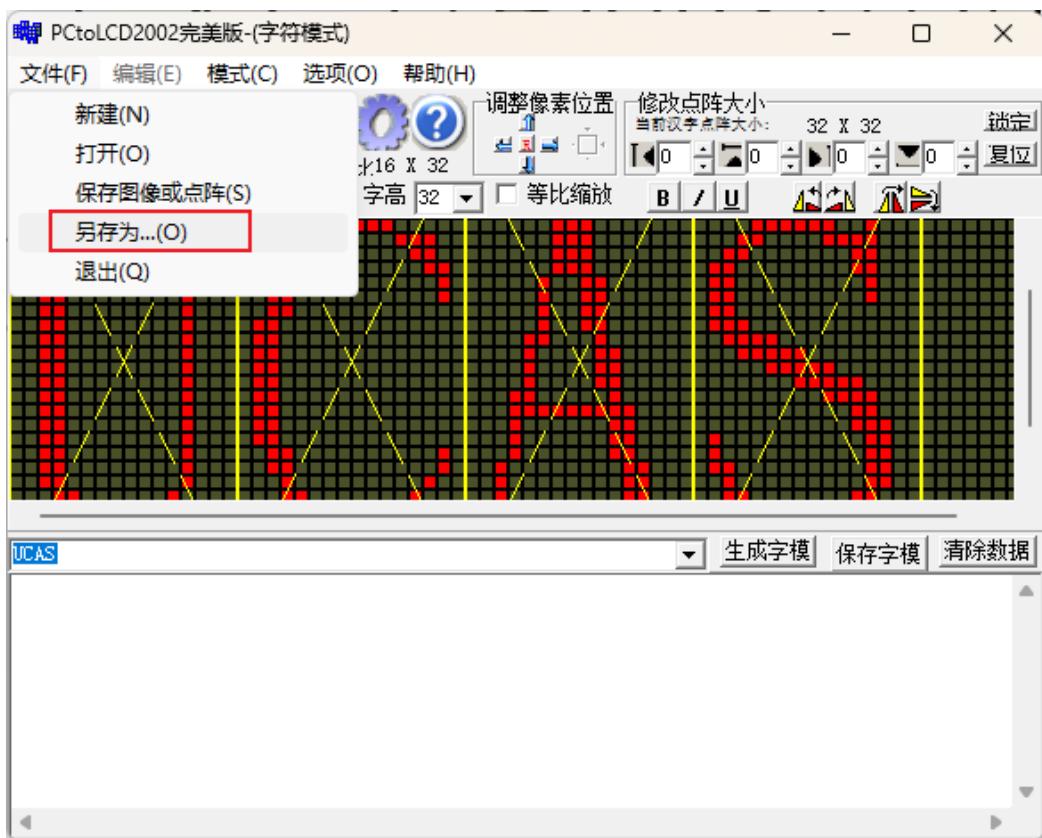


图 7: 转化为 bmp 格式

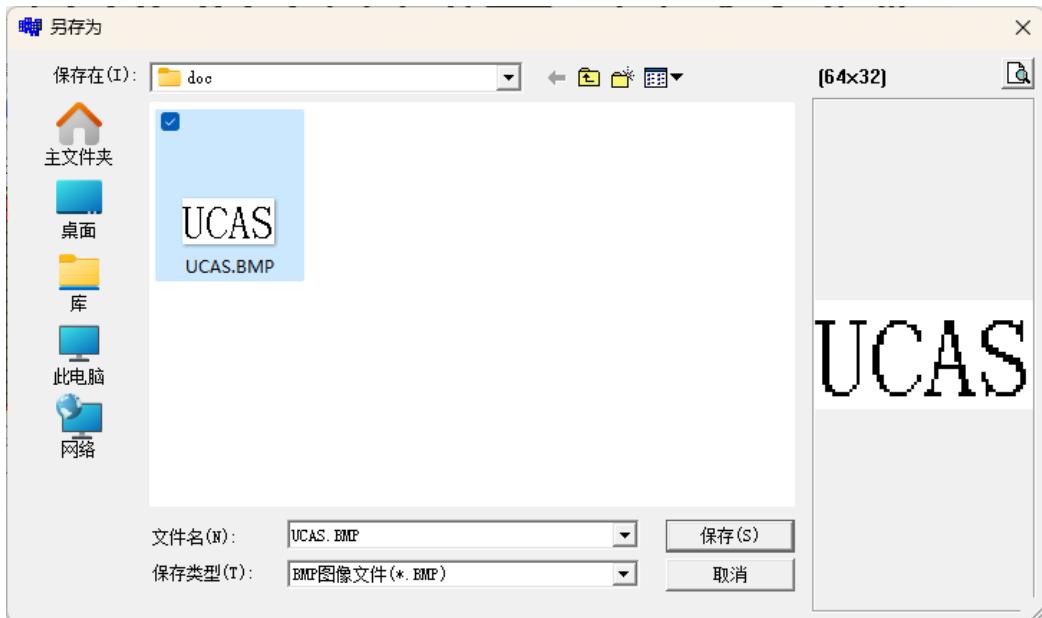


图 8: 转化为整体

切换回图形模式, 打开保存的 BMP 文件

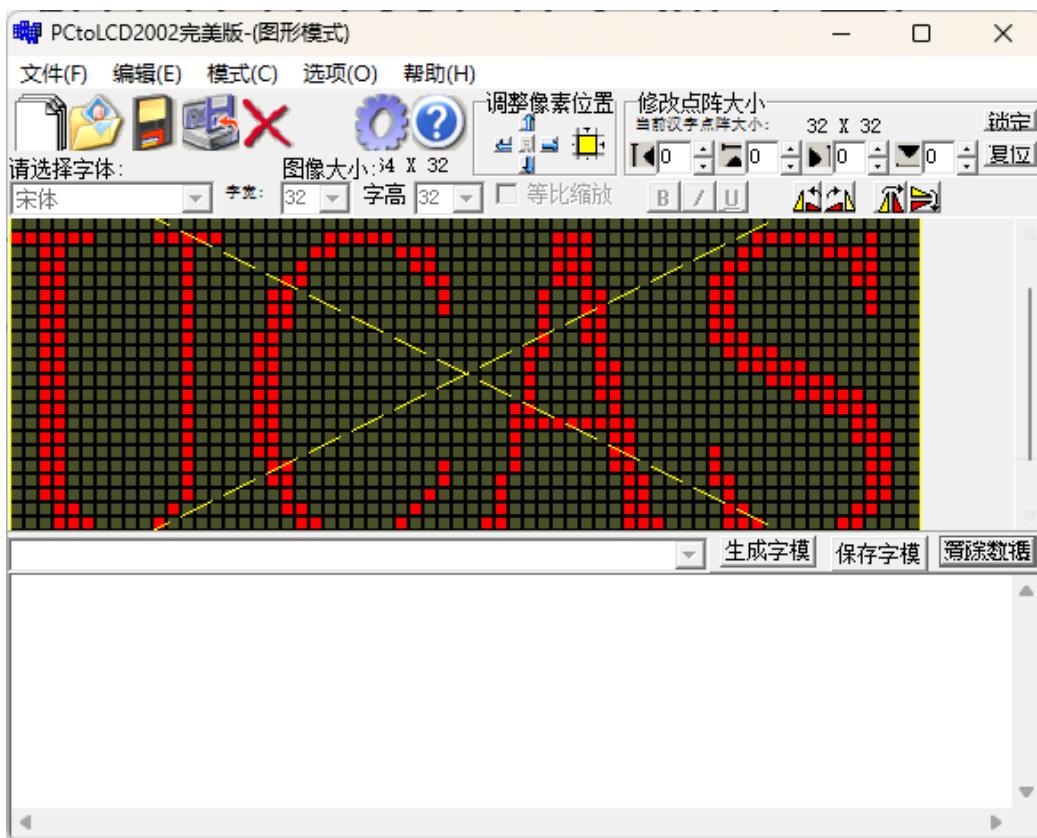


图 9: 切换回图形模式

点击选项修改字模选项如下

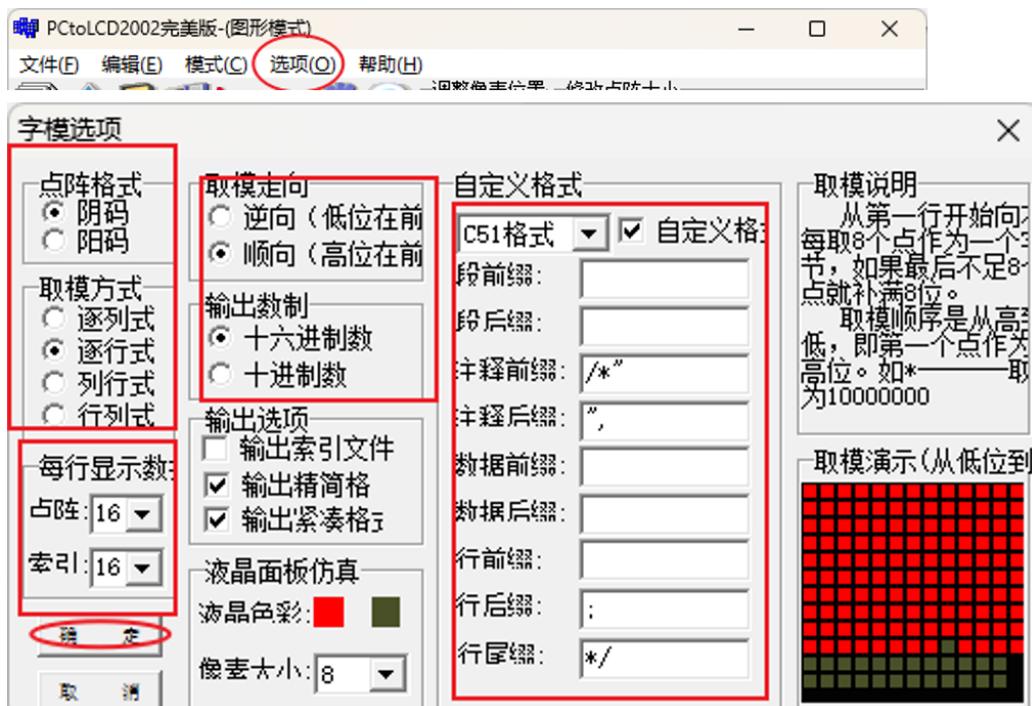


图 10: 修改字模选项

接着生成字模后保存字模即可

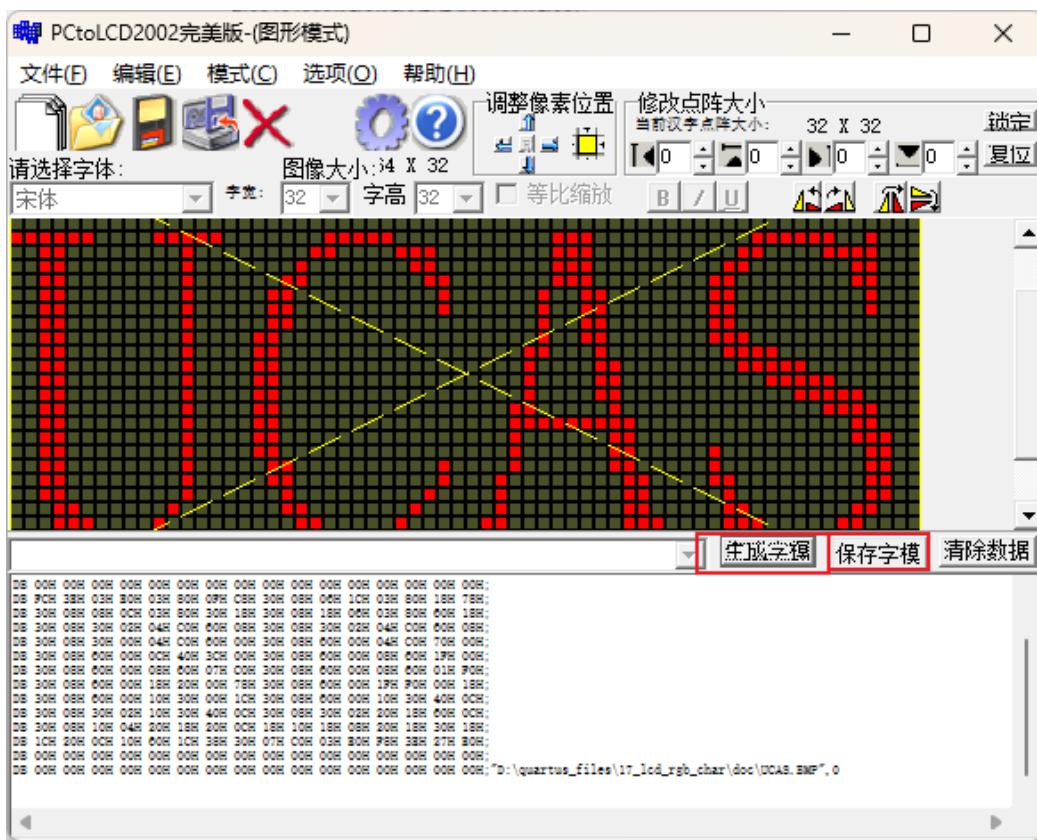


图 11: 生成字模

图像传输一转为 MIF 格式通过 ROM 传入使用 PicToMif 进行转换，加载 UCASLOGO 后进行转换

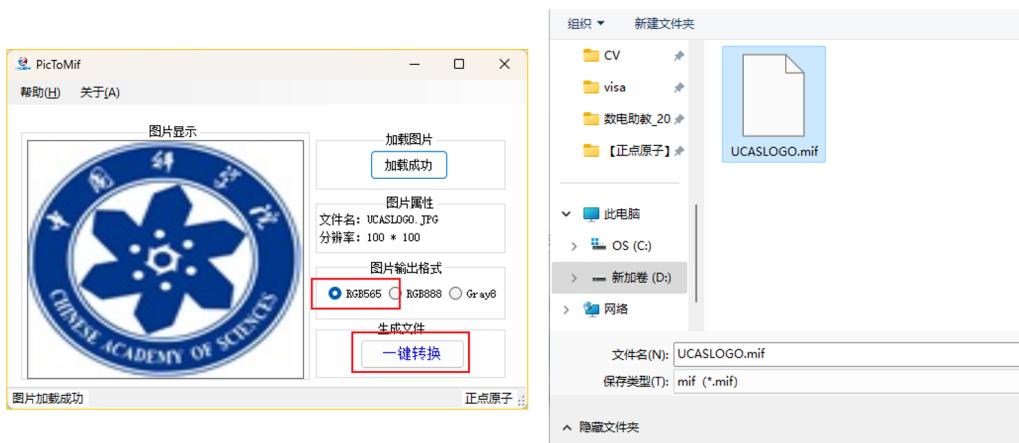


图 12: UCASLOGO 转为 MIF 格式

接着通过 ip-core 访问 mif 格式图像

```

75 // 根据当前扫描点的横纵坐标为 ROM 地址赋值
76 always @(posedge lcd_clk or negedge sys_rst_n) begin
77     if (!sys_rst_n)
78         rom_addr <= 14'd0;
79     else if ((pixel_ypos >= PIC_Y_START) && (pixel_ypos < PIC_Y_START + PIC_HEIGHT) &&
80             (pixel_xpos >= PIC_X_START) && (pixel_xpos < PIC_X_START + PIC_WIDTH))
81         rom_addr <= rom_addr + 1'b1;
82     else if (pixel_ypos >= PIC_Y_START + PIC_HEIGHT)
83         rom_addr <= 14'd0;
84 end
85
86 // ROM 实例化: 存储图片
87 urom_10000x16b u_rom_10000x16b(
88     .address (rom_addr),
89     .clock   (lcd_clk),
90     .rden    (rom_rd_en),
91     .q       (rom_rd_data)
92 );
93
94 endmodule

```

图 13: ip-core 访问 mif 格式图像

在 FPGA 开发中, 使用 Quartus 提供的 IP Core 中的 ROM 模块, 可以高效、安全、资源友好地加载和显示固定图像等静态数据, 尤其适用于像 LCD 显示模块这类场景

这里需要借助 Quartus 里的 Ipcore 创建一个 ROM 模块, 并指定其读取的固定 mif 文件

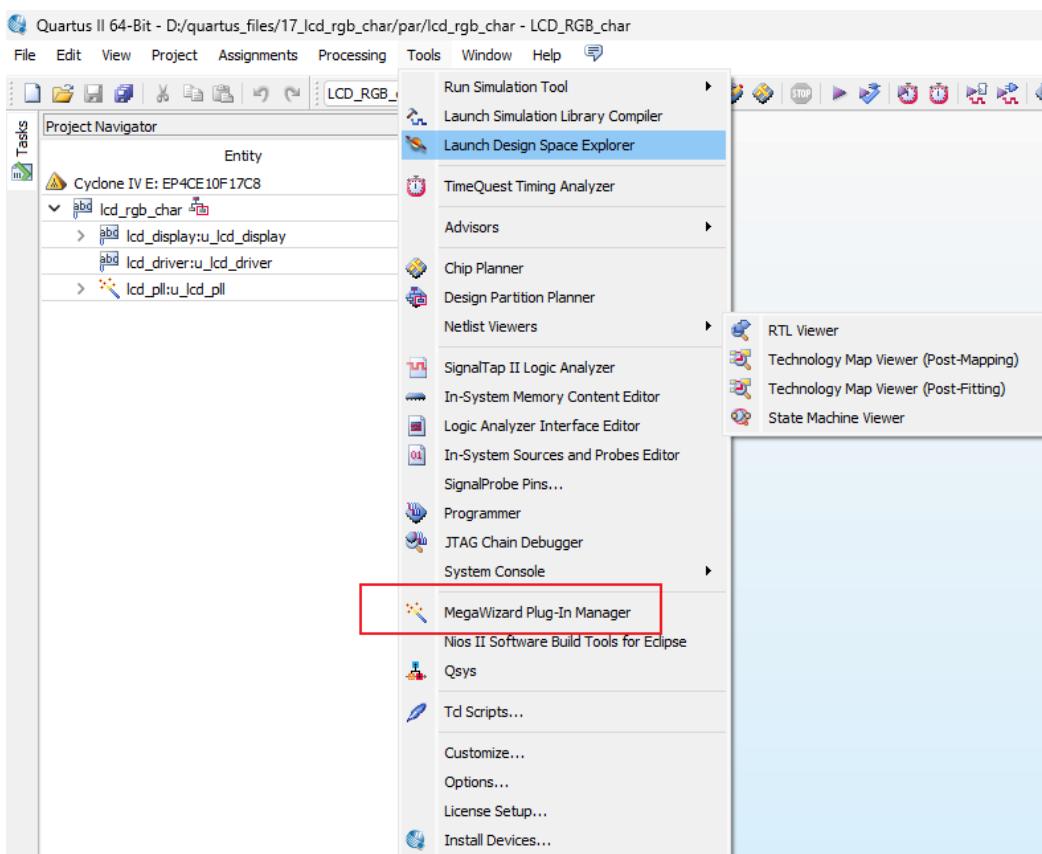


图 14: ip-core 创建 ROM 模块

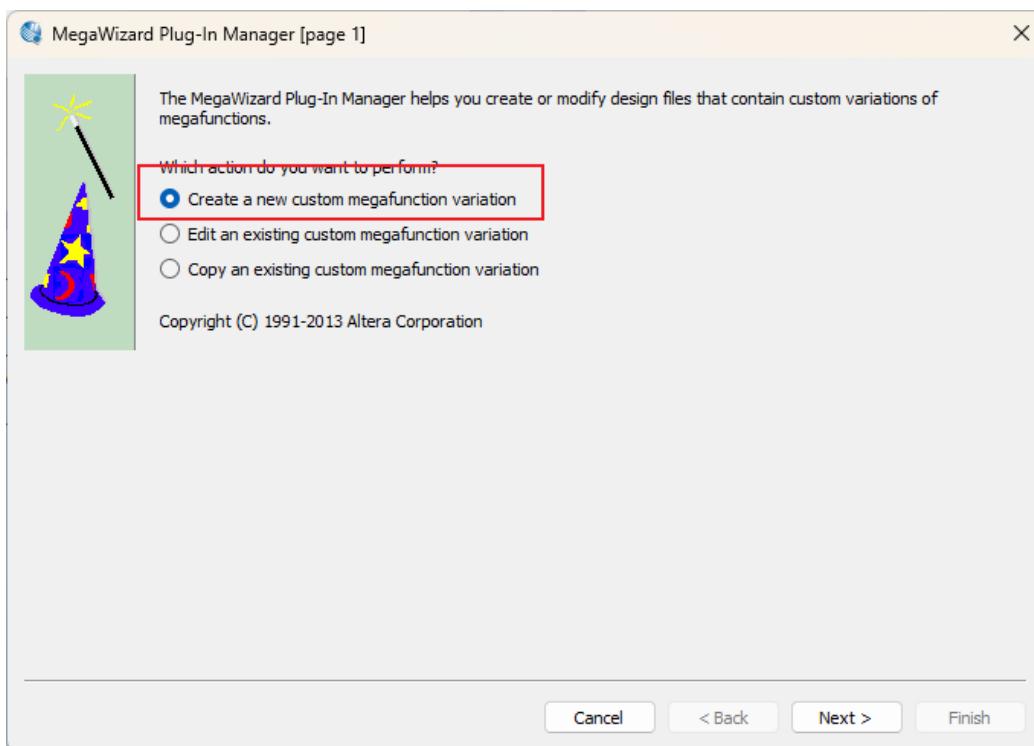


图 15: ip-core 创建 ROM 模块

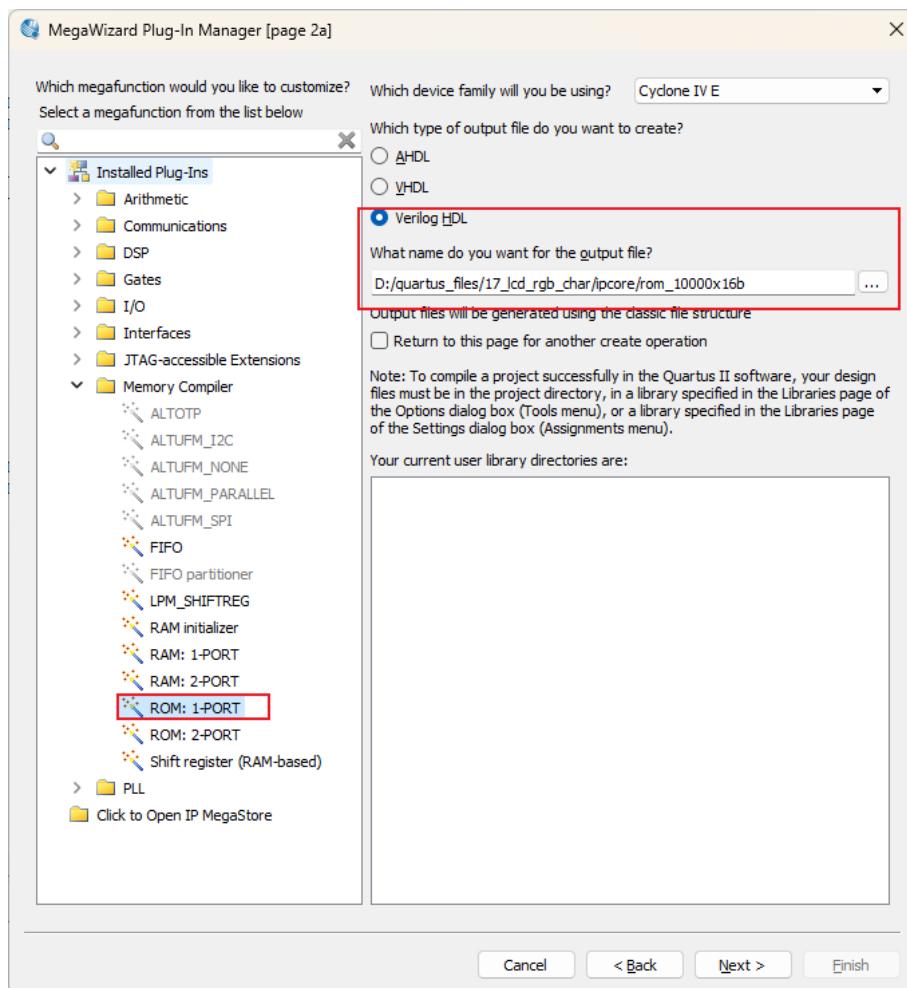


图 16: ip-core 创建 ROM 模块

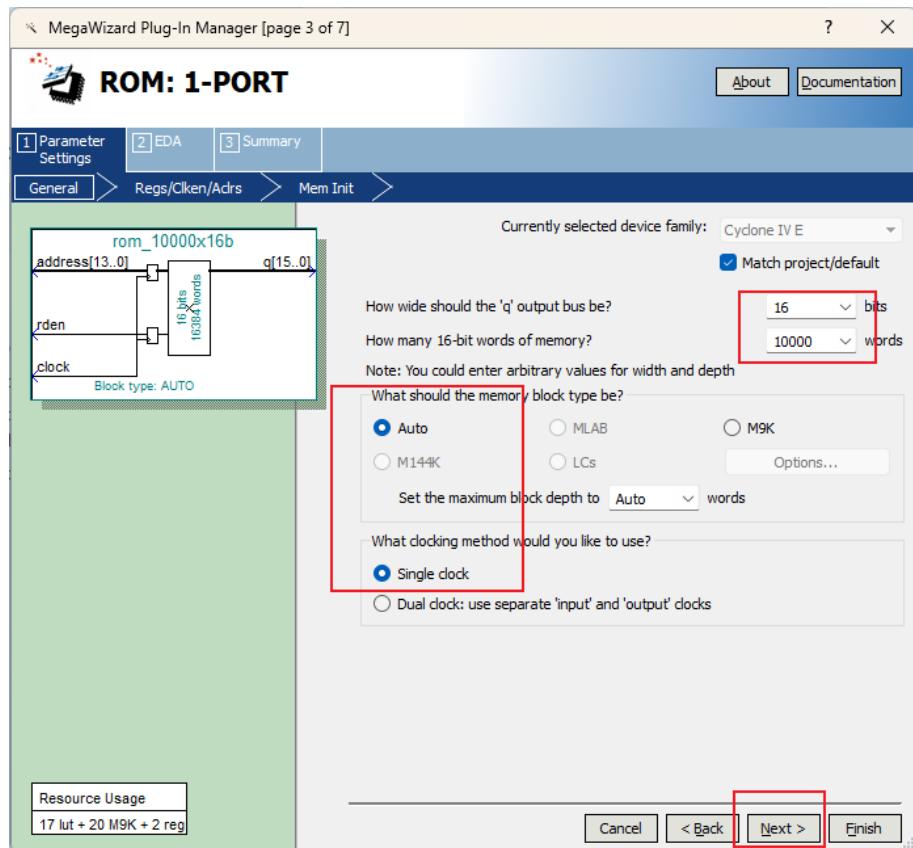


图 17: ip-core 创建 ROM 模块

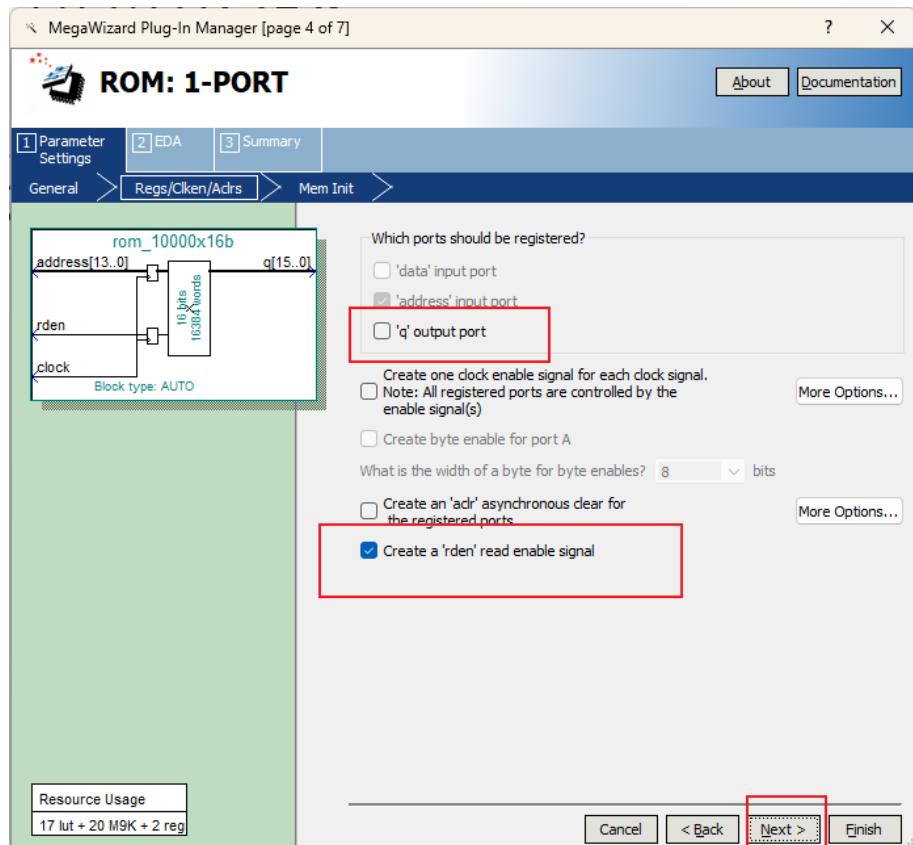


图 18: ip-core 创建 ROM 模块

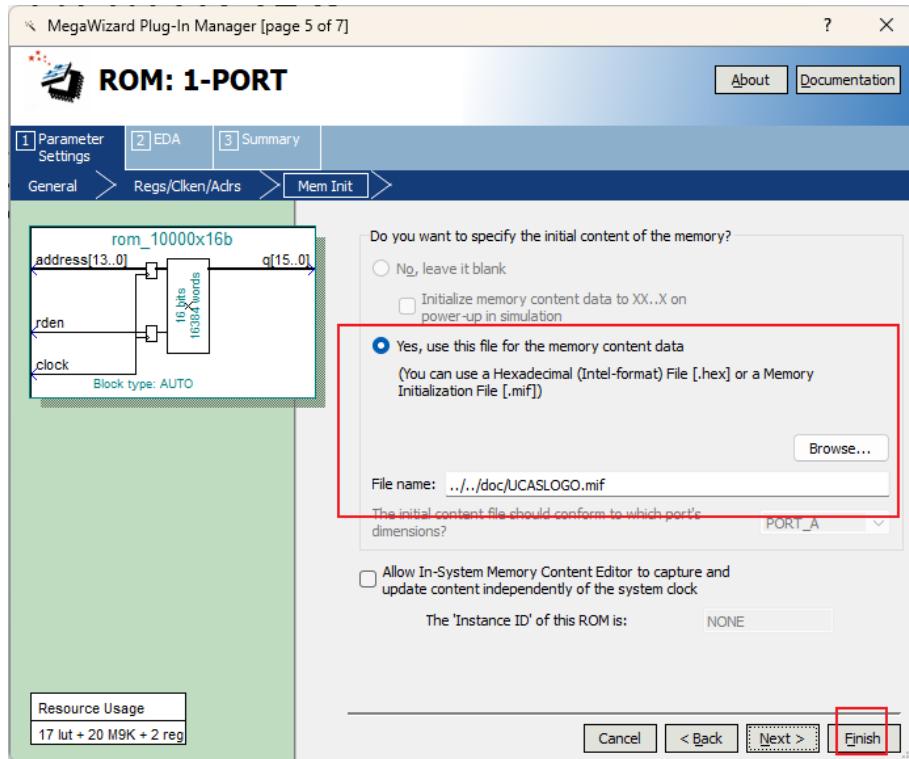


图 19: ip-core 创建 ROM 模块

直接打开老师提供的项目 QPF 文件即可，打开后会自动加载所有的源文件和设置。然后就是编译和下载程序到开发板。(部分实验需要修改 verilog 代码)

#### 步骤 1：完整编译工程

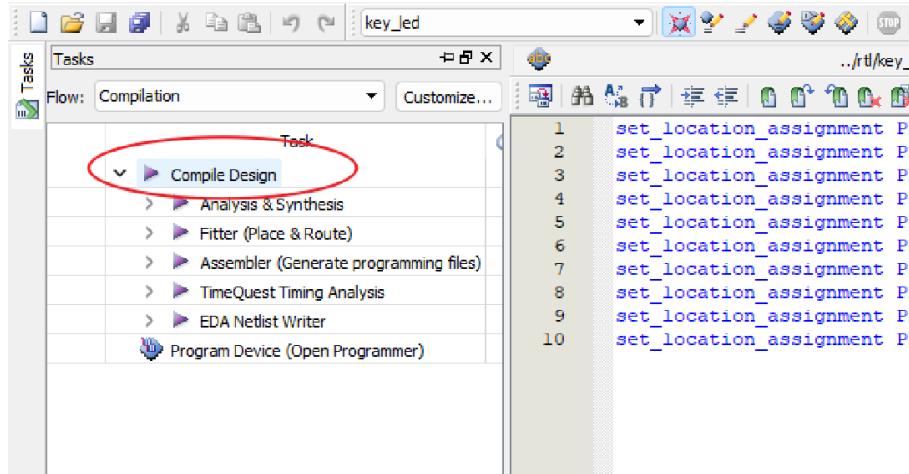


图 20: 完整编译工程界面

**说明：**完整编译流程包括：首先点击主界面的“Start Compilation”按钮，Quartus 会自动依次执行 Analysis & Synthesis、Fitter、Assembler、TimeQuest Timing Analysis 等步骤。编译过程中如遇到错误（Error）或警告（Warning），可在 Messages 窗口查看详细信息，双击可定位到相关代码或设置。常见注意事项包括：确保所有源文件已正确添加到工程、顶层模块设置无误、引脚分配无冲突、时钟约束合理等。建议每次修改代码或引脚分配后都重新完整编译，确保设计的正确性和可用性。

## 步骤 2: 下载程序至开发板

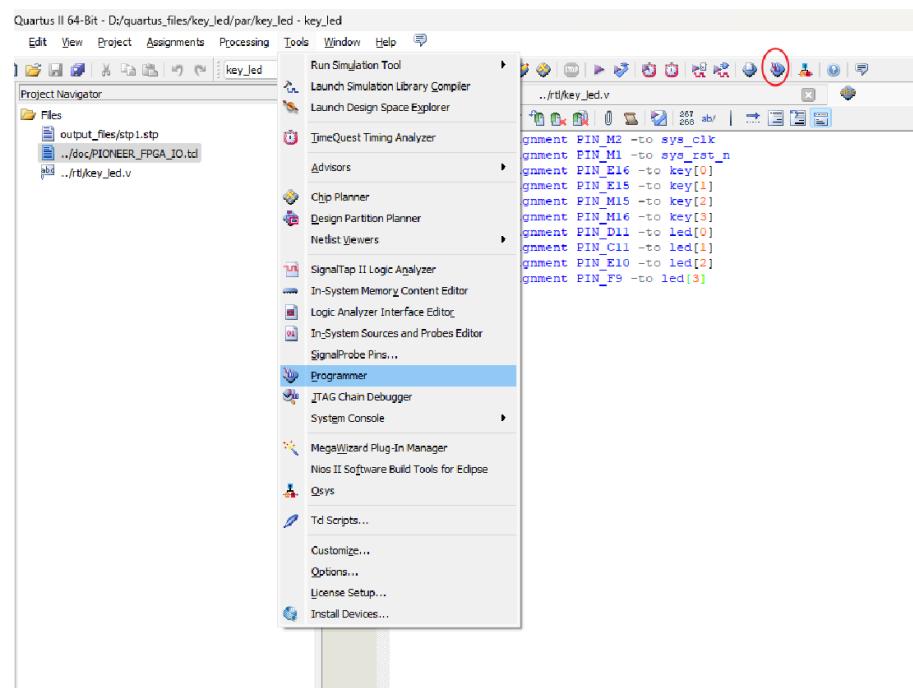


图 21: 下载程序到开发板界面 1

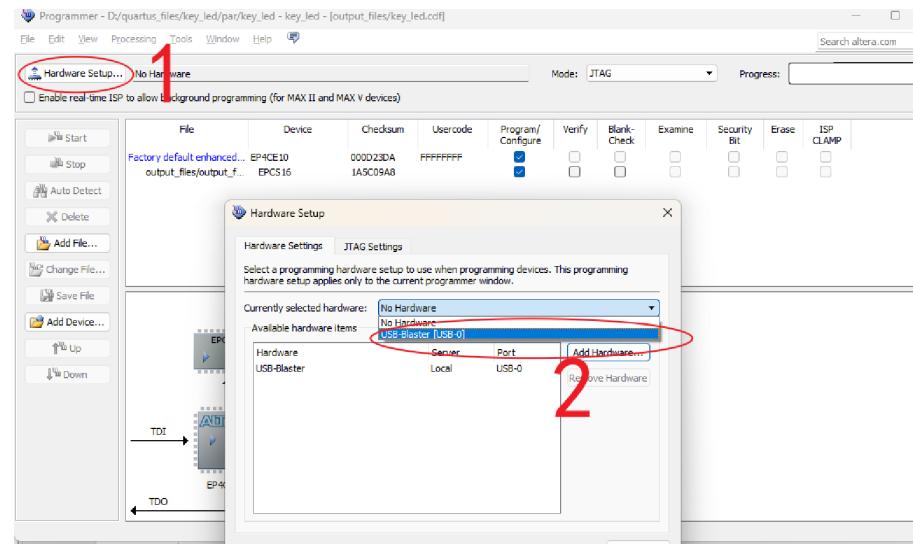


图 22: 下载程序到开发板界面 2

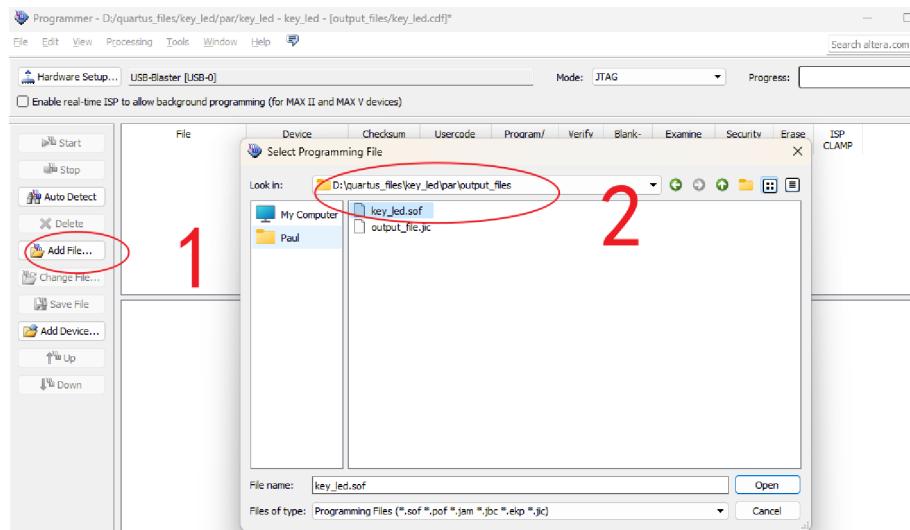


图 23: 下载程序到开发板界面 3

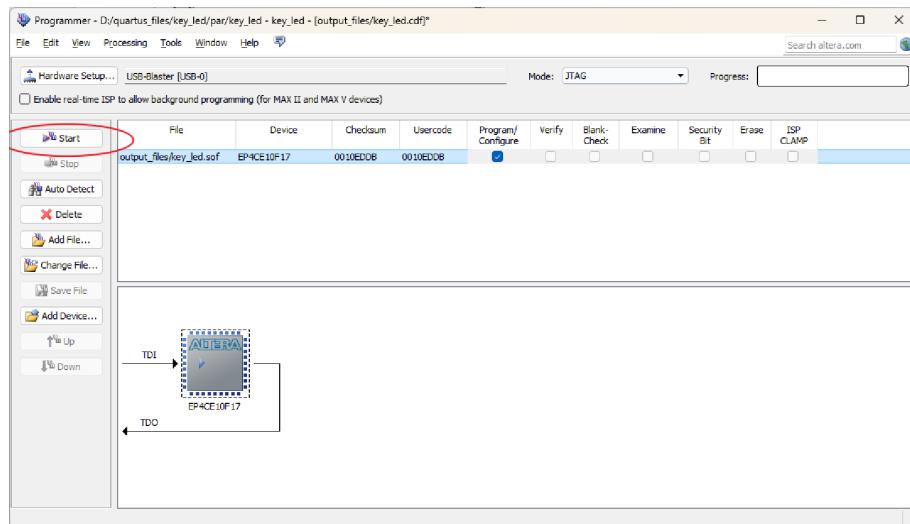


图 24: 下载程序到开发板界面 4

**说明:** 下载程序到开发板的步骤如下：首先确保开发板已正确连接至电脑，并安装好驱动程序。打开Quartus，点击工具栏的“Programmer”按钮，进入下载界面。点击“Hardware Setup”选择正确的下载器型号（如USB-Blaster），然后点击“Auto Detect”自动识别芯片。添加编译生成的.sof文件，勾选“Program/Configure”，最后点击“Start”按钮开始下载。下载完成后，观察开发板上的数码管显示是否正常。常见问题包括：未识别到下载器、芯片型号选择错误、下载过程中断等，遇到问题可尝试重新连接设备、检查驱动或重启软件。

## §3 实验结果

### 3.1 实验结果: RGB-LCD 彩条显示实验

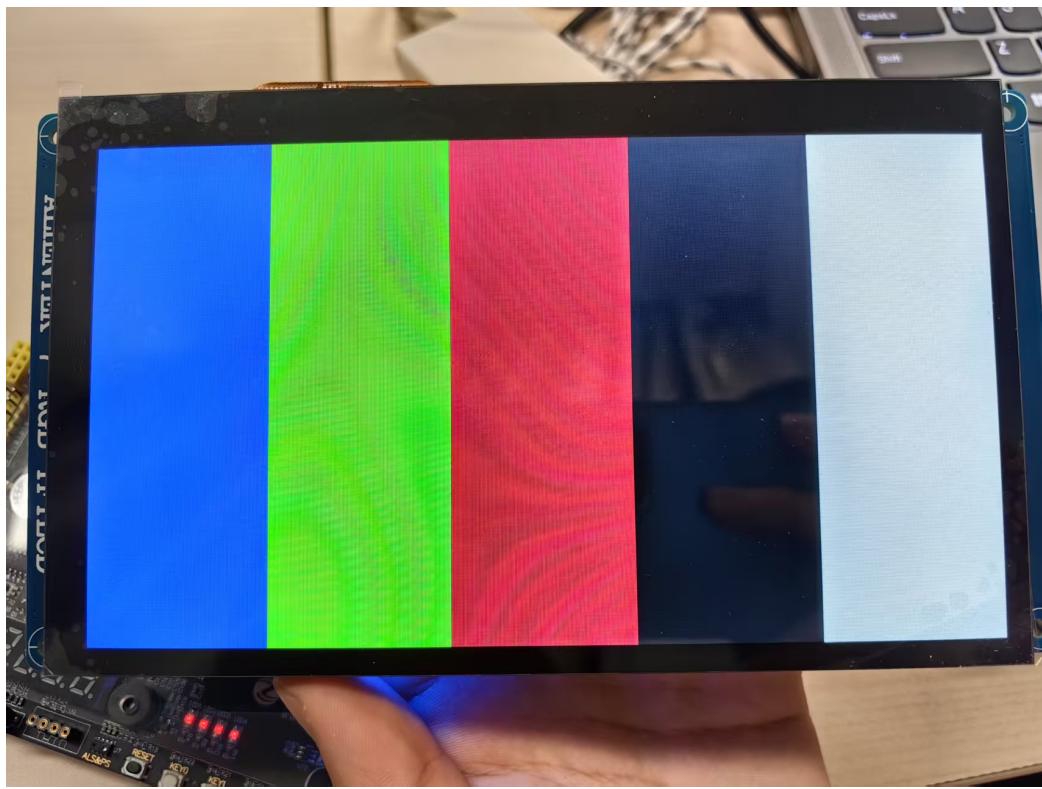


图 25: RGB-LCD 彩条显示实验结果

### 3.2 实验结果: RGB-LCD 字符和图片显示实验

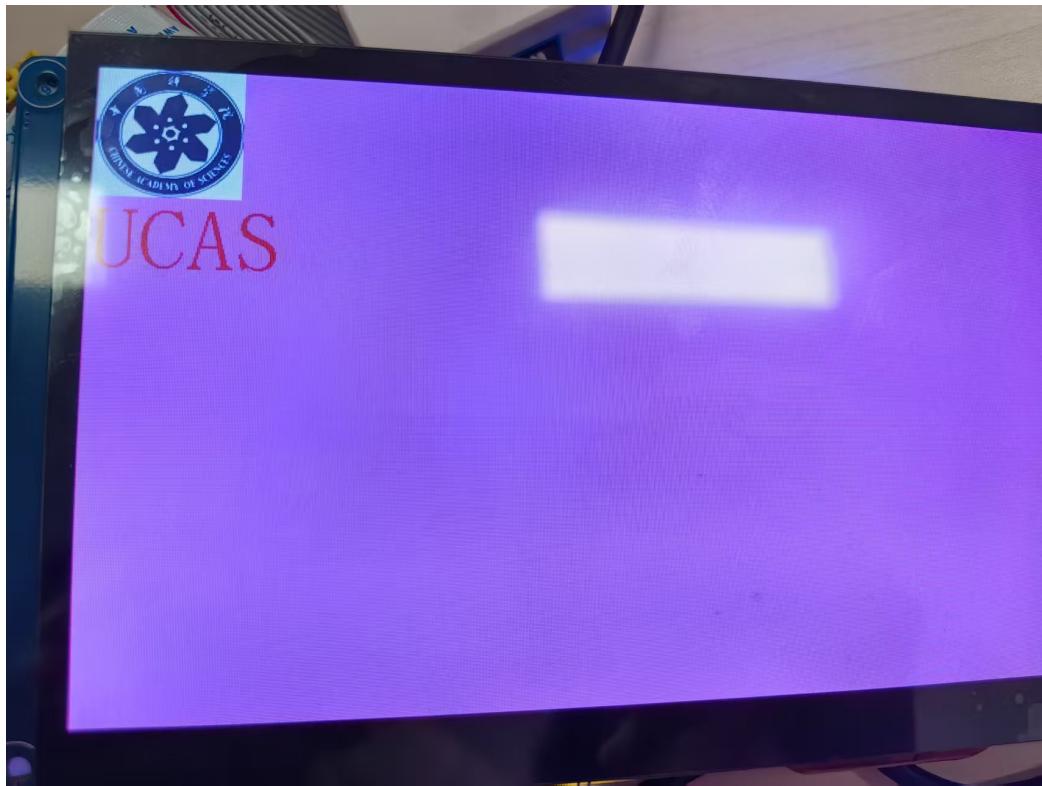


图 26: RGB-LCD 字符和图片显示实验结果

### 3.3 实验结果: 小组成员姓名显示



图 27: 小组成员姓名显示结果

### 3.4 拓展实验结果: 按键绑定实现

网页端（哔哩哔哩）演示视频: [点击查看演示视频](#)

## §4 实验总结

### 4.1 实验中的问题

- (1) 在这次实验的过程中，除了拓展实验之外的所有代码老师已经提供，直接放置在英文目录下即可打开项目，稍加修改编译后即可运行下载。
- (2) 在显示中文字符的时候，要注意中文字符是 2 个字节的，而英文字符是 1 个字节的，所以在显示中文字符的时候要注意字宽和字高的设置，不能设置为 1 个字节的大小，否则会出现乱码。
- (3) 对于除拓展实验外不同实验的不同文字，我们要注意修改代码的不同部分，最多的修改需要修改五处。
- (4) 拓展实验中字符的宽、高，我们遇到了字符的宽不统一的问题（两个 256 一个 352），最后将较短数组与 96 位 0 拼接，也就是在字符后加空格，统一了字符的宽。
- (5) 由于我们在拓展实验中 lcd\_display.v 中改变了图片的分辨率(从 100\*100 的 UCASLOGO 改成 180\*108)，这里的 rom 地址会出现越界问题，14 位二进制不够用，最后生成的图片会发生上半部分重复出现下半部分不出现的问题，所以改成 15 位二进制解决问题。
- (6) 更多有关拓展实验的内容可以参考实验报告的附录代码部分。

### 4.2 实验收获

- (1) 通过这次实验，我们对 RGB-LCD 屏幕的工作原理有了更深入的了解，掌握了如何使用 Quartus 软件进行 FPGA 开发和编程。
- (2) 学会了如何使用 PCLtoLCD 工具将字符转换为点阵数据，并通过 PicToMif 工具将图像转换为 MIF 格式，以便在 FPGA 中使用。
- (3) 掌握了如何使用 Quartus 中的 IP Core 创建 ROM 模块，并将静态图像数据加载到 FPGA 中进行显示。
- (4) 通过拓展实验，我们学会了如何使用按键控制显示内容，增强了对 FPGA 编程的理解和实践能力。
- (5) 在实验过程中我们深知耐心的重要性，尤其是在完成拓展实验时，遇到各种问题需要不断调试和修改代码，最终成功实现了预期功能。

## §5 附录：基础实验 Verilog 代码修改部分

RGB-LCD 彩条显示实验没有代码修改;

RGB-LCD 字符和图片显示实验之 UCAS 文字与 UCAS-LOGO 图像显示 Verilog 代码修改部分如下（仅需修改 lcd\_display）：

```

1 localparam CHAR_WIDTH  = 11'd128;
2 localparam CHAR_HEIGHT = 11'd64;
3
4 // reg define
5 reg [127:0] char [63:0];           // 字符数组
6
7 // 字符数组赋值, 显示“UCAS-LOGO与UCAS文字”
8 always @(posedge lcd_clk) begin
9   char[ 0] <= 128'h00000000000000000000000000000000;
10  char[ 1] <= 128'h00000000000000000000000000000000;
11  char[ 2] <= 128'h00000000000000000000000000000000;
12  char[ 3] <= 128'h00000000000000000000000000000000;
13  char[ 4] <= 128'h00000000000000000000000000000000;
14  char[ 5] <= 128'h00000000000000000000000000000000;
15  char[ 6] <= 128'h00000000000000000000000000000000;
16  char[ 7] <= 128'h00000000000000000000000000000000;
17  char[ 8] <= 128'h00000000000000000000000000000000;
18  char[ 9] <= 128'h00000000000000000000000000000000;
19  char[10] <= 128'h00000000000000000000000000000000;
20  char[11] <= 128'h7FF807FE0003FC100003C000001FE000;
21  char[12] <= 128'h7FF807FE000FFFF00003C000007FF880;
22  char[13] <= 128'h0FC000F0003C07F00003C00000F03F80;
23  char[14] <= 128'h07800060007801F00007E00001C00F80;
24  char[15] <= 128'h0780006000E000F80007E00003C00780;
25  char[16] <= 128'h0780006001C000780006E000078003C0;
26  char[17] <= 128'h0780006003C000380006E000078001C0;
27  char[18] <= 128'h0780006003800018000CF000070001C0;
28  char[19] <= 128'h0780006007800018000CF0000F0000C0;
29  char[20] <= 128'h07800060070000C000C70000F0000C0;
30  char[21] <= 128'h078000600F00000C000C70000F000000;
31  char[22] <= 128'h078000600E000008001878000F000000;
32  char[23] <= 128'h078000601E000000001878000F000000;
33  char[24] <= 128'h078000601E000000001878000F800000;
34  char[25] <= 128'h078000601E0000000018380007C00000;
35  char[26] <= 128'h078000601E00000000303C0007E00000;
36  char[27] <= 128'h078000603C00000000303C0007F00000;
37  char[28] <= 128'h078000603C00000000303C0003FC0000;
38  char[29] <= 128'h078000603C00000000301C0001FF0000;
39  char[30] <= 128'h078000603C00000000601E00007FC000;
40  char[31] <= 128'h078000603C00000000601E00003FF000;
41  char[32] <= 128'h078000603C00000000601E00000FFC00;
42  char[33] <= 128'h078000603C00000000600E00003FE00;
43  char[34] <= 128'h078000603C00000000E00E00000FF00;
44  char[35] <= 128'h078000603C00000000C00F000003F80;
45  char[36] <= 128'h078000603C00000000C00F0000000FC0;
46  char[37] <= 128'h078000603C00000000FFFF00000007E0;

```

Listing 1: UCAS 文字与 UCAS-LOGO 图像显示 Verilog 代码修改部分

RGB-LCD 字符和图片显示实验之小组成员姓名显示 Verilog 代码修改部分如下:

```
22 char[13] <= 192'h0FC000F0003C07F00003C00000F03F8000000000000000000000000000000;
23 char[14] <= 192'h0F8000F0007801F00007E00001C00F8000000000000000000000000000000;
24 char[15] <= 192'h0F8000F000E000F80007E00003C0078000000000000000000000000000000;
25 char[16] <= 192'h0F8000F001C000780006E000078003C00000000000000000000000000000000;
26 char[17] <= 192'h0F8000F003C000380006E000078001C00000000000000000000000000000000;
27 char[18] <= 192'h0F8000F003800018000CF000070001C00000000000000000000000000000000;
28 char[19] <= 192'h0F8000F007800018000CF0000F0000C00000000000000000000000000000000;
29 char[20] <= 192'h0F8000F0070000C000C70000F0000C00000000000000000000000000000000;
30 char[21] <= 192'h0F8000F00F00000C000C70000F00000000000000000000000000000000000000;
31 char[22] <= 192'h0F8000F00E000008001878000F000000000000000000000000000000000000000;
32 char[23] <= 192'h0F8000F01E00000001878000F000000000000000000000000000000000000000;
33 char[24] <= 192'h0F8000F01E00000001878000F8000000000000000000000000000000000000000;
34 char[25] <= 192'h0F8000F01E000000018380007C000000000000000000000000000000000000000;
35 char[26] <= 192'h0F8000F01E0000000303C0007E000000000000000000000000000000000000000;
36 char[27] <= 192'h0F8000F03C00000000303C0007F000000000000000000000000000000000000000;
37 char[28] <= 192'h0F8000F03C00000000303C0003FC00000000000000000000000000000000000000;
38 char[29] <= 192'h0F8000F03C00000000301C0001FF00000000000000000000000000000000;
39 char[30] <= 192'h0F8000F03C00000000601E00007FC00000000000000000000000000000000;
40 char[31] <= 192'h0F8000F03C00000000601E00003FF00000000000000000000000000000000;
41 char[32] <= 192'h0F8000F03C00000000601E00000FFC000000000000000000000000000000;
42 char[33] <= 192'h0F8000F03C00000000600E000003FE00000000000000000000000000000000;
43 char[34] <= 192'h0F8000F03C00000000E00E000000FF00000000000000000000000000000000;
44 char[35] <= 192'h0F8000F03C00000000C00F0000003F8000000000000000000000000000000;
45 char[36] <= 192'h0F8000F03C00000000C00F0000000FC0000000000000000000000000000000;
46 char[37] <= 192'h0F8000F03C00000000FFFF00000007E000000000000000000000000000000;
47 char[38] <= 192'h0F8000F03C00000001FFFF00000003E000000000000000000000000000000;
48 char[39] <= 192'h0F8000F03C0000000180078000001E00000000000000000000000000000000;
49 char[40] <= 192'h0F8000F01E0000000180078000001F00000000000000000000000000000000;
50 char[41] <= 192'h0F8000F01E0000000180078000000F00000000000000000000000000000000;
51 char[42] <= 192'h0F8000F01E0000080380078008000F00000000000000000000000000000000;
52 char[43] <= 192'h0F8000E01E00000C030003C0180000F00000000000000000000000000000000;
53 char[44] <= 192'h0F8000E01E00000C030003C0180000F00000000000000000000000000000000;
54 char[45] <= 192'h0F8000E00F000008030003C01C0000F00000000000000000000000000000000;
55 char[46] <= 192'h0F8000E00F000018070003C00C0000F00000000000000000000000000000000;
56 char[47] <= 192'h0FC000E007800010060001E00E0000E000000000000000000000000;
57 char[48] <= 192'h07C001C007800030060001E00E0001E00000000000000000000000000000000;
58 char[49] <= 192'h07C003C003C00060001E00F0001C00000000000000000000000000000000;
59 char[50] <= 192'h03E0078001E000C00E0001E00F8003C000000000000000000;
60 char[51] <= 192'h01F81F0000F001C00E0001F00FC0078000000000000000000000000000000;
61 char[52] <= 192'h00FFF00007C07001F0001F807F81F00000000000000000000000000000000;
62 char[53] <= 192'h001FF000001FFE007FC00FFE061FFC00000000000000000000000000000000;
63 char[54] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
64 char[55] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
65 char[56] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
66 char[57] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
67 char[58] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
68 char[59] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
69 char[60] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
70 char[61] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
71 char[62] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
72 char[63] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
73 char[64] <= 192'h0000000000000000000000000000000000000000000000000000000000000000;
```

```
74 char[65] <= 192'h00000000000000000000000000000000000000000000000000000000000000;
75 char[66] <= 192'h00000000000000000000000000000000000000000000000000000000000000;
76 char[67] <= 192'h00000000000000000000000000000000000000000000000000000000000000;
77 char[68] <= 192'h00000000000000000000000000000000E000000000000000000000000000000;
78 char[69] <= 192'h0000000000000000000000F800000000000000000000000000000000000000;
79 char[70] <= 192'h000000000000E0000000F80000000800000000000000000000000000000000;
80 char[71] <= 192'h000000000001F0000000F00000001C00000000000000000000000000000000;
81 char[72] <= 192'h007FFFFFFF8000000F001FFFFFE0000000000000000;
82 char[73] <= 192'h007FFFFFFFC000000F000FFFFFE0000000000000000;
83 char[74] <= 192'h003E007C0003F0000000F00061E003C0000000000000000;
84 char[75] <= 192'h000007C0003F0000000F00001E00380000000000000000;
85 char[76] <= 192'h000007C0003F0000000F02001E00380000000000000000;
86 char[77] <= 192'h000007C0003F0000000F07001E00380000000000000000;
87 char[78] <= 192'h000007C0003F00007FFFFF801E00380000000000000000;
88 char[79] <= 192'h000007C0003F00003FFFFFC01E00380000000000000000;
89 char[80] <= 192'h000007C0003F0000100F00001E00380000000000000000;
90 char[81] <= 192'h000007C0003F1000000F00003C00780000000000000000;
91 char[82] <= 192'h000007C0003F3800000F00003C00780000000000000000;
92 char[83] <= 192'h000007C0003F7C00000F00003C00780000000000000000;
93 char[84] <= 192'h000007C0003FFE00000F00007800780000000000000000;
94 char[85] <= 192'h0FFFFFFFFF00000F0000780078000000000000000000;
95 char[86] <= 192'h07FFFFFFFFF80000F0000F000700000000000000000000;
96 char[87] <= 192'h03E0007C0003F0000000F0000E060F0000000000000000;
97 char[88] <= 192'h0000007C0003F0000000F0301E03FF0000000000000000;
98 char[89] <= 192'h0000007C0003F0000000F0783C00FE0000000000000000;
99 char[90] <= 192'h0000007C0003F0001FFFFFC38003E0000000000000000;
100 char[91] <= 192'h0000007C0003F0000FFFFFE7000180000000000000000;
101 char[92] <= 192'h0000007C0003F00004007001E00010000000000000000;
102 char[93] <= 192'h0000007C0003F00000007003800000000000000000000;
103 char[94] <= 192'h0000007C0003F00000007007000000000000000000000;
104 char[95] <= 192'h0000007C0003F00000007004200020000000000000000;
105 char[96] <= 192'h00000FC0003F000000070003800070000000000000000;
106 char[97] <= 192'h00000FC0003F00000E070003FFFFF80000000000000000;
107 char[98] <= 192'h00FFFFFFFFF00000F870003FFFFFC0000000000000000;
108 char[99] <= 192'h007FFFFFFFFF00000F870003C000F8000000000000000;
109 char[100] <= 192'h003E00F80003F00000F070183C000F0000000000000000;
110 char[101] <= 192'h000000F80003F00000F0703C3C000F0000000000000000;
111 char[102] <= 192'h000001F80003F00000E07FFE3C000F0000000000000000;
112 char[103] <= 192'h000001F00003800000E07FFF3C000F0000000000000000;
113 char[104] <= 192'h000001F00000000000E070003C000F0000000000000000;
114 char[105] <= 192'h000003F00000000000E070003C000F0000000000000000;
115 char[106] <= 192'h000003F00000000000E070003C000F0000000000000000;
116 char[107] <= 192'h000003E00000000000E070003C000F0000000000000000;
117 char[108] <= 192'h000007E00000000001E070003C000F0000000000000000;
118 char[109] <= 192'h000007C00000000001E070003C000F0000000000000000;
119 char[110] <= 192'h00000FC00000000001F070003C000F0000000000000000;
120 char[111] <= 192'h00001F80000000001D870003FFFFF0000000000000000;
121 char[112] <= 192'h00001F00000000001DC70003FFFFF0000000000000000;
122 char[113] <= 192'h00003E00000000001CE70003C000F0000000000000000;
123 char[114] <= 192'h00007E000000000038770003C000F0000000000000000;
124 char[115] <= 192'h0000FC0000000000383F0003C000E0000000000000000;
125 char[116] <= 192'h0001F80000000000301F0003C00080000000000000000;
```

```
126 char[117] <= 192'h0003F000000000000700F80030000000000000000000000000000;
127 char[118] <= 192'h0007C0000000000006003F00000000000000000000000000000000;
128 char[119] <= 192'h000F8000000000006001FF000000000000000000000000000000;
129 char[120] <= 192'h003F000000000000C0007FFF000000000000000000000000;
130 char[121] <= 192'h007C000000000000C0000FFFFFFFFFF8000000000000000;
131 char[122] <= 192'h01F0000000000001800001FFFFFFE0000000000000000;
132 char[123] <= 192'h07C000000000000100000007FFFF8000000000000000;
133 char[124] <= 192'h0700000000000000200000000000FFF8000000000000000;
134 char[125] <= 192'h0000000000000000000000000000000000000000000000000;
135 char[126] <= 192'h0000000000000000000000000000000000000000000000000;
136 char[127] <= 192'h0000000000000000000000000000000000000000000000000;
137 char[128] <= 192'h0000000000000000000000000000000000000000000000000;
138 char[129] <= 192'h0000000000000000000000000000000000000000000000000;
139 char[130] <= 192'h0000000000000000000000000000000000000000000000000;
140 char[131] <= 192'h00000001C0000000000000000000000000000000000000000;
141 char[132] <= 192'h00000001F0000000000000C000000800000000000000000;
142 char[133] <= 192'h00000001FC00000000001E000003C0000000000000000;
143 char[134] <= 192'h00000E01FC000001FFFFFF7FFFFFFE0000000000000000;
144 char[135] <= 192'h00001F81F00000000FFFFFFFBFFFFFFF0000000000000000;
145 char[136] <= 192'h1FFFFFFC1F00018000603C00100780000000000000000;
146 char[137] <= 192'h0FFFFF81F0003800007800007800000000000000000000;
147 char[138] <= 192'h07C01F01F0003C000078000070000000000000000000;
148 char[139] <= 192'h00001F01F0007E000007800007000000000000000000;
149 char[140] <= 192'h00001F01F0007F0000780000E00000000000000000000;
150 char[141] <= 192'h00001F01F000FF000007000000E0000000000000000000;
151 char[142] <= 192'h00001F01F001FC00000F000000C0000000000000000000;
152 char[143] <= 192'h00001F01F003F80000F000000C0000000000000000000;
153 char[144] <= 192'h00001F01F007E000000E00000080020000000000000000;
154 char[145] <= 192'h00001F01F00FC000000E00018180070000000000000000;
155 char[146] <= 192'h00001F01F00F800001E0001FFFFFFC0000000000000000;
156 char[147] <= 192'h01801F01F000001C0001FFFFFF80000000000000000;
157 char[148] <= 192'h01C01F01F03E000001C0001E000070000000000000000;
158 char[149] <= 192'h01FFFF01F0F8000003C0001E000070000000000000000;
159 char[150] <= 192'h01FFFF01F1F000000380001E000070000000000000000;
160 char[151] <= 192'h01E01F01F3E000000380381E040070000000000000000;
161 char[152] <= 192'h01E01F01F7C0000007FFFC1E020070000000000000000;
162 char[153] <= 192'h03E01C01FF00000007FFFC1E038070000000000000000;
163 char[154] <= 192'h03E00001FE0000000F80781E03E070000000000000000;
164 char[155] <= 192'h03E00001F800038000F80781E03C070000000000000000;
165 char[156] <= 192'h03E00001F00007C000F80781E03807000000000000000;
166 char[157] <= 192'h03E00001F0000FE001F80781E07807000000000000000;
167 char[158] <= 192'h03E003FFFFFFE001B80781E0780700000000000000000;
168 char[159] <= 192'h03C019FFFFFFF003B80781E0780700000000000000000;
169 char[160] <= 192'h07C03EF9F1C000003380781E078070000000000000000;
170 char[161] <= 192'h07FFFF01F1C000006380781E078070000000000000000;
171 char[162] <= 192'h0FFFFF01F1C000004380781E078070000000000000000;
172 char[163] <= 192'h07C07E01F0E00000C380781E07807000000000000000;
173 char[164] <= 192'h03807C01F0E0000018380781E07807000000000000000;
174 char[165] <= 192'h00007C01F0F0000010380781E07807000000000000000;
175 char[166] <= 192'h00007C01F07000000380781E07007000000000000000;
176 char[167] <= 192'h00007C01F07800000380781E07007000000000000000;
177 char[168] <= 192'h00007C01F07800000380781E0F0070000000000000000;
```



Listing 2: 小组成员姓名显示 Verilog 代码修改部分

## §6 拓展实验 Verilog 代码

lcd\_driver.v 文件不用修改

lcd rgb char key.v 文件需要修改，增加了四个按键。 lcd rgb char key.v

```
1 module lcd_rgb_char_key(
2     input sys_clk,
3     input sys_RST_N,
4     input[3:0]key,
5     output lcd_hs,
6     output lcd_vs,
7     output lcd_de,
8     output[15:0]lcd_rgb,
```

```

9   output lcd_b1,
10  output lcd_RST,
11  output lcd_PCLK
12 );
13 wire lcd_CLK_W;
14 wire locked_W;
15 wire rst_N_W;
16 wire[15:0]pixel_Data_W;
17 wire[9:0]pixel_XPos_W;
18 wire[9:0]pixel_YPos_W;
19 assign rst_N_W=SYS_RST_N&&locked_W;
20
21 lcd_PLL u_lcd_PLL(
22   .inclk0(sys_clk),
23   .areset(~SYS_RST_N),
24   .c0(lcd_CLK_W),
25   .locked(locked_W)
26 );
27 lcd_driver u_lcd_driver(
28   .lcd_CLK(lcd_CLK_W),
29   .SYS_RST_N(rst_N_W),
30   .lcd_HS(lcd_HS),
31   .lcd_VS(lcd_VS),
32   .lcd_DE(lcd_DE),
33   .lcd_RGB(lcd_RGB),
34   .lcd_B1(lcd_B1),
35   .lcd_RST(lcd_RST),
36   .lcd_PCLK(lcd_PCLK),
37   .pixel_Data(pixel_Data_W),
38   .pixel_XPos(pixel_XPos_W),
39   .pixel_YPos(pixel_YPos_W)
40 );
41 lcd_display u_lcd_Display(
42   .lcd_CLK(lcd_CLK_W),
43   .SYS_RST_N(rst_N_W),
44   .key(key),
45   .pixel_XPos(pixel_XPos_W),
46   .pixel_YPos(pixel_YPos_W),
47   .pixel_Data(pixel_Data_W)
48 );
49 endmodule

```

Listing 3: lcd\_rgb\_char\_key.v 代码修改片段

由于 lcd\_display 代码过长, 无法在此处展示, 完整代码请查看附件, 附件代码包含注释。

lcd\_display.v 文件需要修改。[lcd\\_display.v](#)