

		wl (-)	wl (+)	wc (-)	wc (+)	bl (-)	bl (+)	bc (-)	bc (+)		status
explicit error	post bl < post wl		1			1					#1 fix
explicit error	post bl < post wl		1				1				#1 fix
explicit error	post bl < post wl	1				1					#1 fix
explicit error	post bc < post wc				1			1			#2 fix
explicit error	post bc < post wc				1				1		#2 fix
explicit error	post bc < post wc			1				1			#2 fix
explicit error	pre wl > post bl					1					error
implicit error	pre wl > post bl	1				1					#1/#3 fix
implicit error	pre wl > post bl		1			1					#1 fix
invalid state	pre wl > post bl	1					1				invalid state
invalid state	pre wl > post bl		1				1				invalid state
explicit error	pre wc > post bc							1			error
implicit error	pre wc > post bc			1				1			#2/#4 fix
implicit error	pre wc > post bc				1			1			#2 fix
invalid state	pre wc > post bc			1					1		invalid state
invalid state	pre wc > post bc				1				1		invalid state
auto fix	pre bl < post wl		1								auto fix
implicit error	pre bl < post wl		1			1					#1 fix
implicit error	pre bl < post wl		1				1				#1 fix
invalid state	pre bl < post wl	1				1					invalid state
invalid state	pre bl < post wl	1					1				invalid state
auto fix	pre bc < post wc				1						auto fix
implicit error	pre bc < post wc				1			1			#2 fix
implicit error	pre bc < post wc				1				1		#2 fix
invalid state	pre bc < post wc			1				1			invalid state
invalid state	pre bc < post wc			1					1		invalid state

prioritized fixes

#1 fix	remove bl setting gives wl priority. Or order wl, bl gives correct "err: minimum buf lin equal to win lin 80"
#2 fix	remove bc setting gives wc priority. Or order wc, bc gives correct "err: minimum buf col equal to win col 80"
#3 fix	order wl, bl. Sets "buf lin set nn" (if valid nn), else "err: minimum buf lin equal to win lin nn" and buf lin unchanged.
#4 fix	order wc, bc. Sets "buf col set nn" (if valid nn), else "err: minimum buf col equal to win col nn" and buf col unchanged.

pre wl	post wl	pre bl	post bl
60	70	60	55
60	70	60	65
60	50	60	45

60	60	60	55
60	50	60	45/55
60	65	60	55
60	55	>=60	>60
60	65	>=60	>60

60	65	60	60
60	65	60	55
60	70	60	65
60	<60	>=60	60
60	<60	>=60	>60

pre wc post wc pre bc post bc

100	110	100	95
100	110	100	105
100	90	100	85

100	100	100	95
100	90	100	85/95
100	105	100	95
100	95	>=100	>100
100	105	>=100	>100

100	105	100	100
100	105	100	95
100	110	100	105
100	>100	>=100	100
100	>100	>=100	>100