Machine Architecture Assignment 2

Casper B. Hansen
University of Copenhagen
Department of Computer Science
fvx507@alumni.ku.dk

Sine Vestergård Jensen University of Copenhagen Department of Computer Science kms698@alumni.ku.dk

Nikolaj Høyer University of Copenhagen Department of Computer Science ct1533@alumni.ku.dk

October 12, 2013

Abstract

Lorem ipsum dolor sit amet, consectetuer adipiscing elit. Ut purus elit, vestibulum ut, placerat ac, adipiscing vitae, felis. Curabitur dictum gravida mauris. Nam arcu libero, nonummy eget, consectetuer id, vulputate a, magna. Donec vehicula augue eu neque. Pellentesque habitant morbi tristique senectus et netus et malesuada fames ac turpis egestas. Mauris ut leo. Cras viverra metus rhoncus sem. Nulla et lectus vestibulum urna fringilla ultrices. Phasellus eu tellus sit amet tortor gravida placerat. Integer sapien est, iaculis in, pretium quis, viverra ac, nunc. Praesent eget sem vel leo ultrices bibendum. Aenean faucibus. Morbi dolor nulla, malesuada eu, pulvinar at, mollis ac, nulla. Curabitur auctor semper nulla. Donec varius orci eget risus. Duis nibh mi, congue eu, accumsan eleifend, sagittis quis, diam. Duis eget orci sit amet orci dignissim rutrum.

Contents

1	Introduction	
	1.1 Instruction set	2
2	Preliminary design	3
3	Tests	4

1 Introduction

...

1.1 Instruction set

Our pipeline is quite simple in nature, its instruction set (see Figure 1) consists of only 14 instructions.

Instruction	
addu	
addiu	
slt	
slti	
subu	
and	
andi	

Instruction	
or	
ori	
lw	
sw	
beq	
jal	
jr	

Figure 1: Instruction set

2 Preliminary design

...

3 Tests

...

REFERENCES REFERENCES

References

[1] David A. Patterson, John L. Hennessy, Computer Organization and Design. Morgan Kaufmann, Revised 4th Edition, 2009.