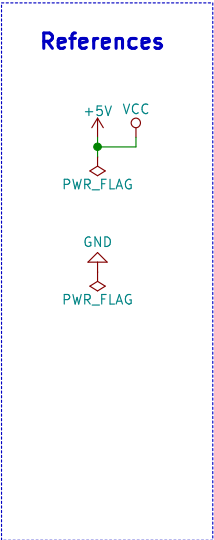
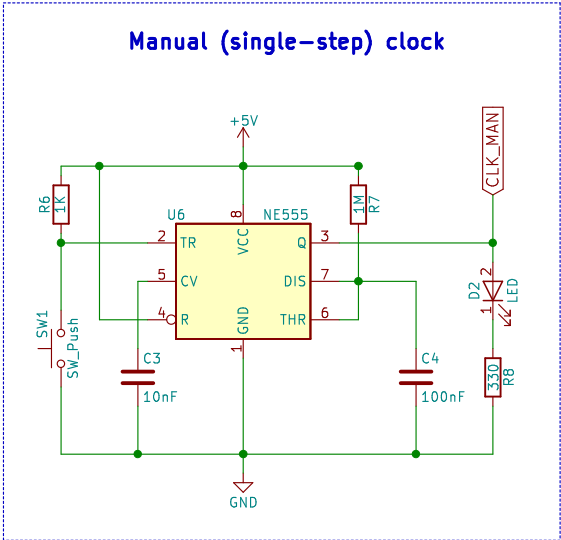
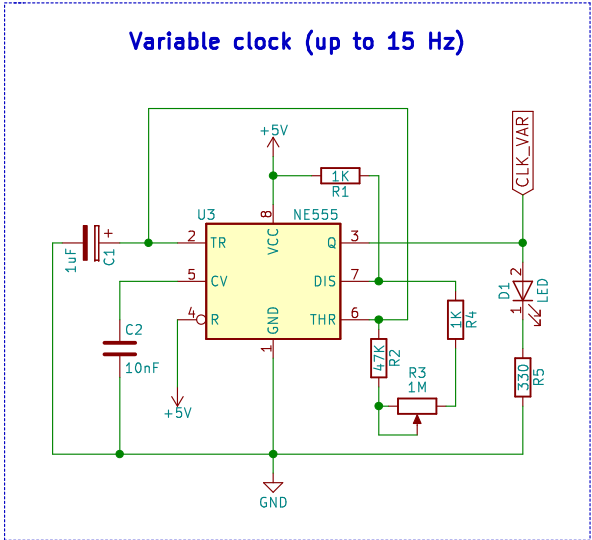


## References

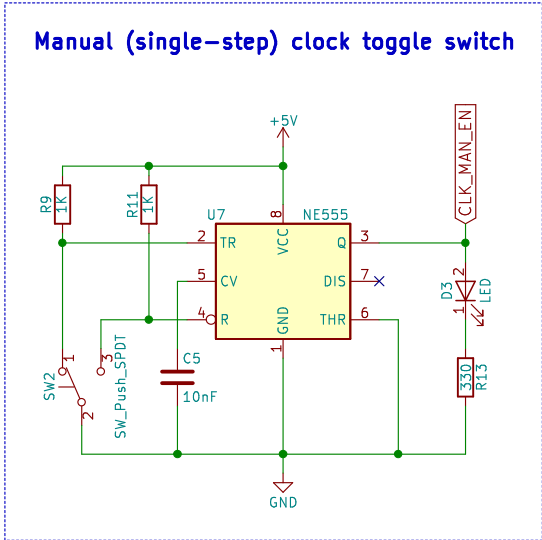


## Variable clock (up to 15 Hz)

The diagram shows an NE555 timer (U3) configured as an astable multivibrator. The power supply is +5V, with GND as the common reference. The timer's VCC (pin 8) and GND (pin 1) are connected to the supply rails. The control voltage (pin 5, CV) is connected to GND through a 10nF capacitor (C2). The timing network consists of a 1K resistor (R1) connected to VCC, a 47K resistor (R2) connected to the discharge pin (pin 6, DIS), and a 1M resistor (R3) connected to the threshold pin (pin 6, THR). The output of the timer (pin 3, Q) is connected to a variable capacitor (CLK\_VAR) and a 1K resistor (R4). The output signal is coupled to an LED (D1) through a 330 resistor (R5). The LED is connected to GND through a 1K resistor (R4). The output frequency is adjustable by varying the capacitance of CLK\_VAR.

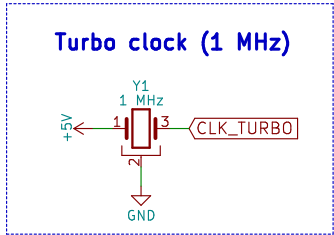
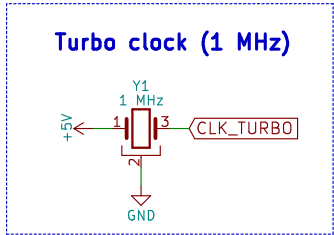


## Manual (single-step) clock toggle switch

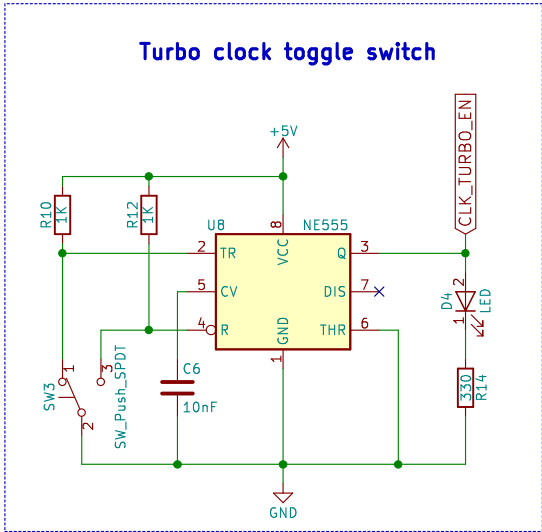


### Clock mode toggle logic

The diagram illustrates the clock mode toggle logic. It features several inputs: CLK\_VAR, CLK\_MAN\_EN, CLK\_MAN, CLK\_TURBO\_EN, CLK\_TURBO, and CLK\_HLTD. The circuit is composed of 74LS04 inverters (U1A, U1B, U1C), 74LS08 AND gates (U2A, U2B, U2C, U4A, U4B, U5A, U5C), and 74LS32 OR gates (U3A, U3B, U4C). The logic combines these inputs to produce the final CLOCK signal.



The diagram shows a circuit for a Turbo clock toggle switch. It features an NE555 timer (U8) configured as a monostable multivibrator. The timer's GND pin (1) is connected to ground. The VCC pin (8) is connected to a +5V supply. The TR pin (2) is connected to a +5V supply through a 1K resistor (R10). The CV pin (5) is connected to a +5V supply through a 1K resistor (R12). The R pin (4) is connected to the SW\_Push\_SPDT switch. The Q pin (3) is connected to the CLK\_TURBO\_EN input of the Turbo clock module. The DIS pin (7) is connected to ground. The THR pin (6) is connected to ground. The timer's output (Q) is connected to an LED (D4) through a 330Ω resistor (R14). The LED is also connected to ground. A 10nF capacitor (C6) is connected between the R pin (4) and ground. The SW\_Push\_SPDT switch is connected to the R pin (4) and ground. The SW3 switch is connected to the R pin (4) and ground.



<p>The clock module can switch between different modes: Variable (slow) clock that can run at a maximum speed of 15 Hz, Turbo (fast) clock that runs at a constant speed of 1 MHz and Manual (single-step) clock for debug purposes. SW2 is used to toggle manual mode, SW3 is used to toggle turbo mode. While in manual mode, SW1 performs a single step.</p> <p><b>Ruud van Falier</b></p> <p>Sheet: /Clock/ File: Clock.sch</p>	D
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**Ruud van Falier**

Sheet: /Clock/

File: Clock.sch

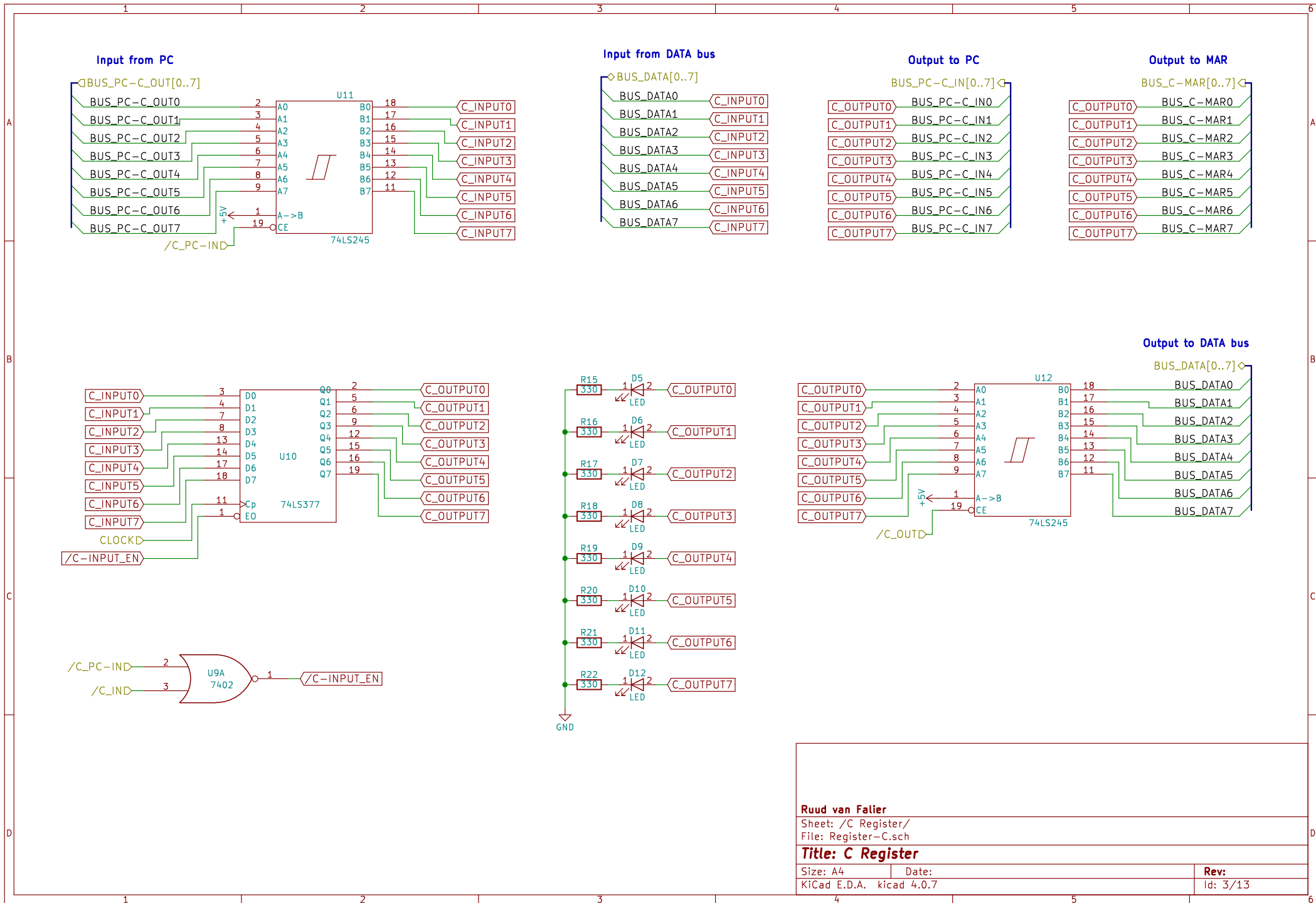
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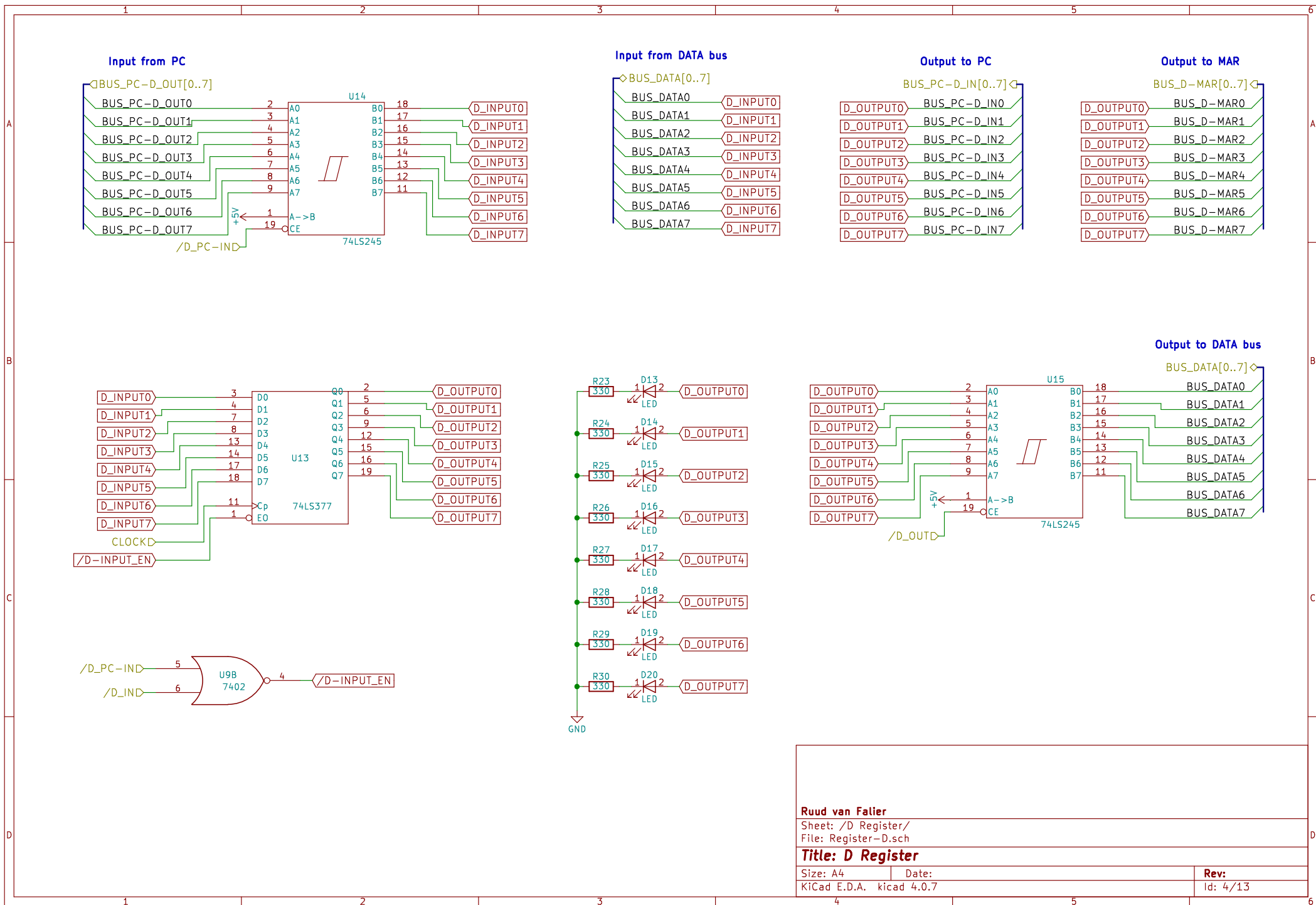
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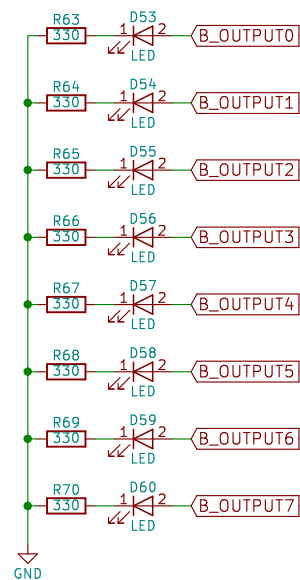
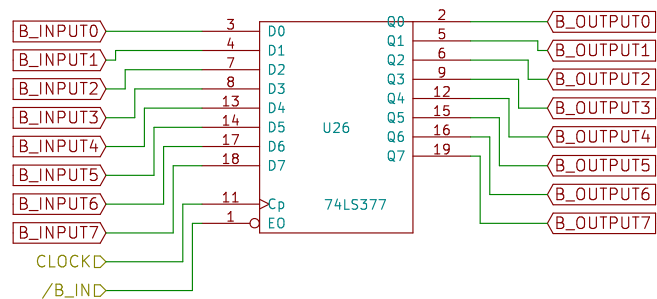
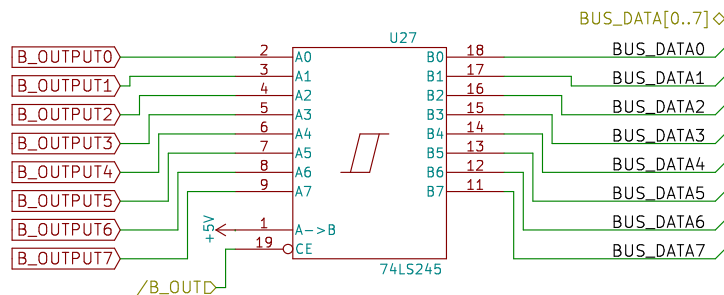
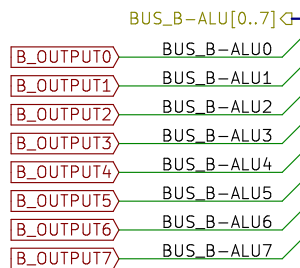
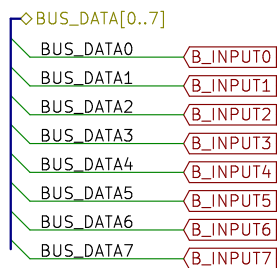
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Ruud van Falier

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File: Register-B.sch

**Title: B Register**

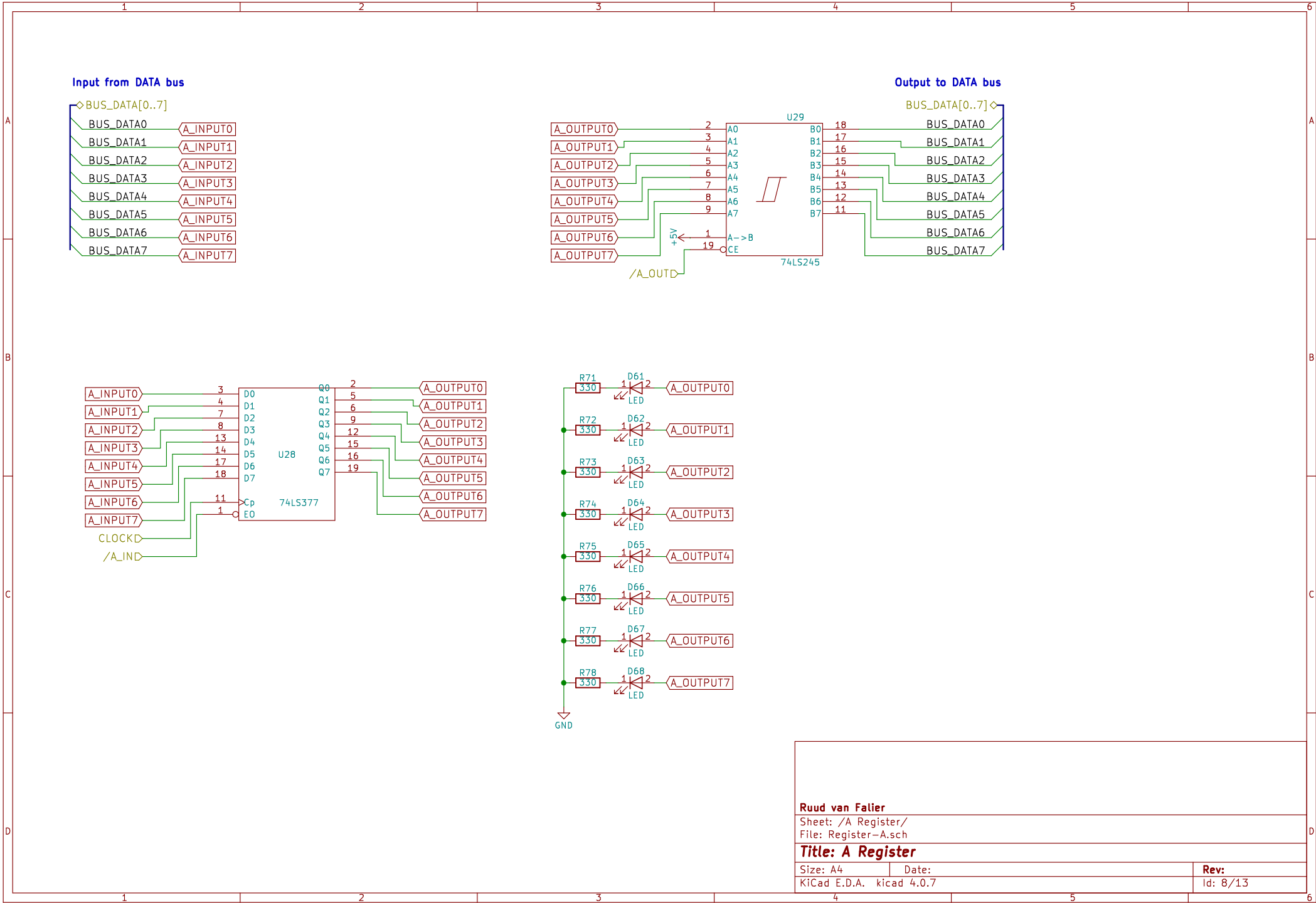
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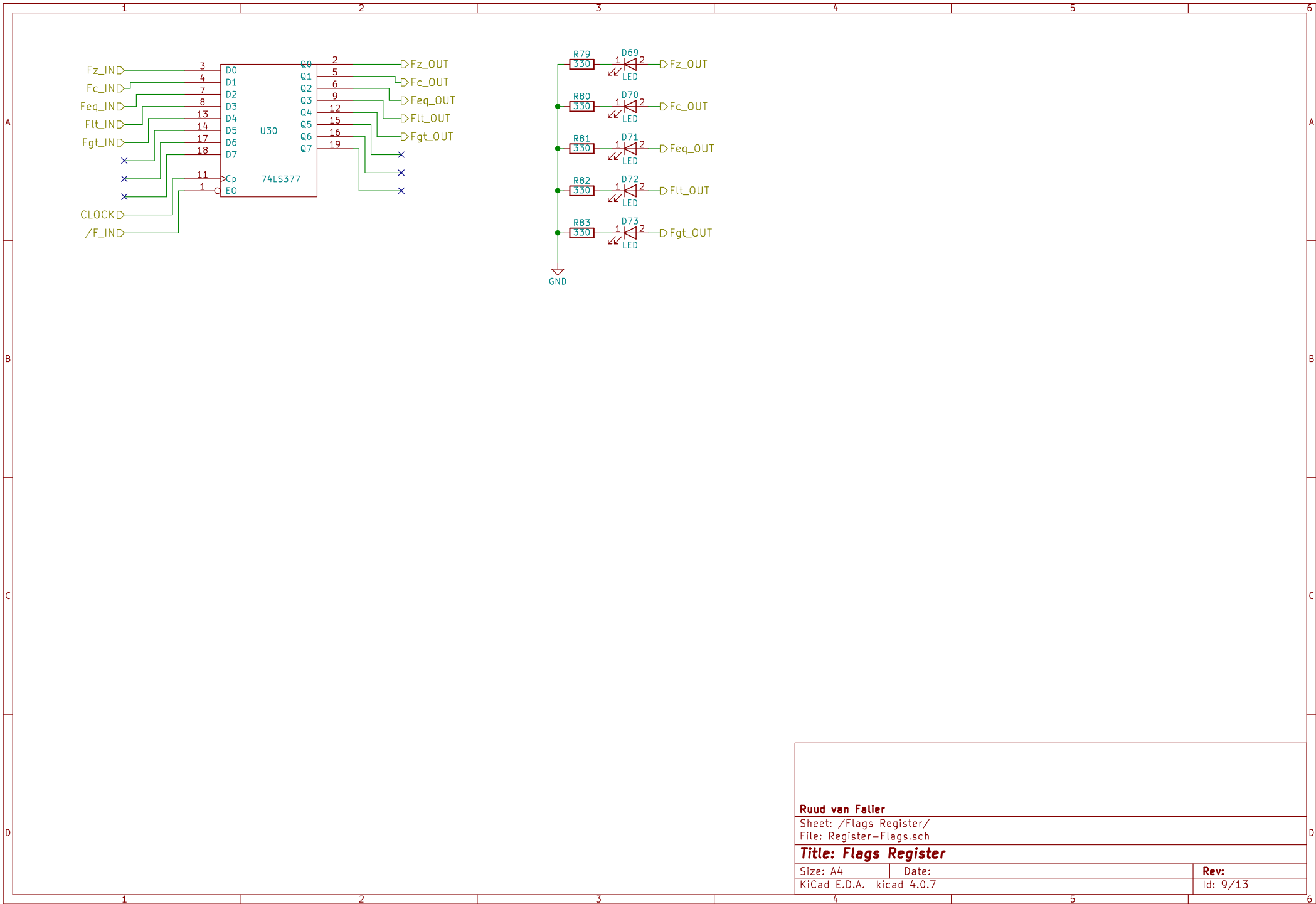
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**Ruud van Falier**

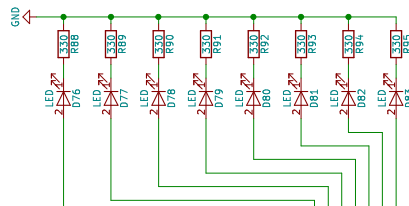
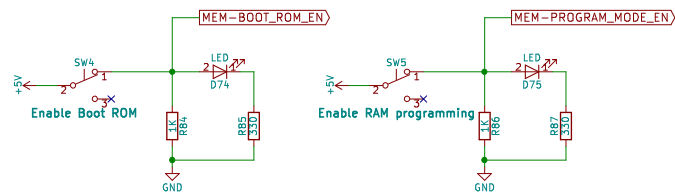
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File: Register-Flags.sch

**Title: Flags Register**

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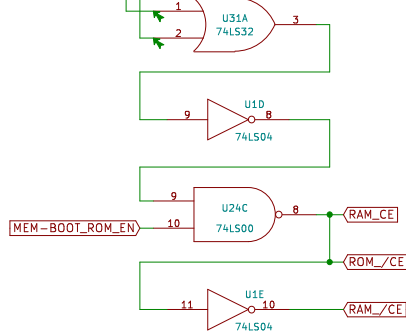
Rev:  
Id: 9/13



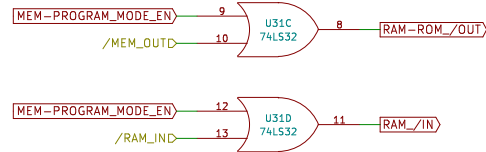
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 BUS\_MAR-MEM0  
 BUS\_MAR-MEM1  
 BUS\_MAR-MEM2  
 BUS\_MAR-MEM3  
 BUS\_MAR-MEM4  
 BUS\_MAR-MEM5  
 BUS\_MAR-MEM6  
 BUS\_MAR-MEM7  
 BUS\_MAR-MEM8  
 BUS\_MAR-MEM9  
 BUS\_MAR-MEM10  
 BUS\_MAR-MEM11  
 BUS\_MAR-MEM12  
 BUS\_MAR-MEM13  
 BUS\_MAR-MEM14  
 BUS\_MAR-MEM15

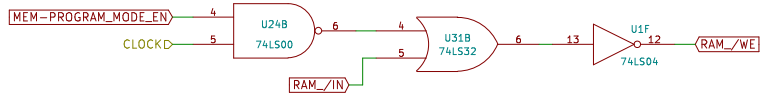
#### RAM/ROM selection



#### Toggle output/Input from/to memory chips



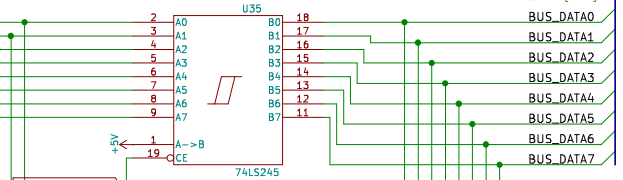
#### RAM write circuit



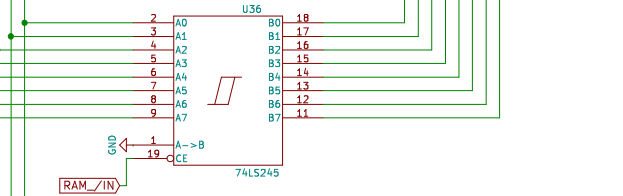
#### Connection to DATA bus

BUS\_DATA[0..7]  
 BUS\_DATA0  
 BUS\_DATA1  
 BUS\_DATA2  
 BUS\_DATA3  
 BUS\_DATA4  
 BUS\_DATA5  
 BUS\_DATA6  
 BUS\_DATA7

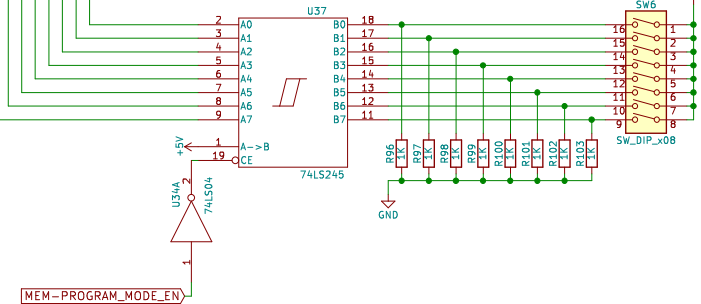
#### Output buffer >>>



#### <<< Input buffer



#### <<< Manual input buffer



Ruud van Falier

Sheet: /Memory/

File: Memory.sch

Title: Memory

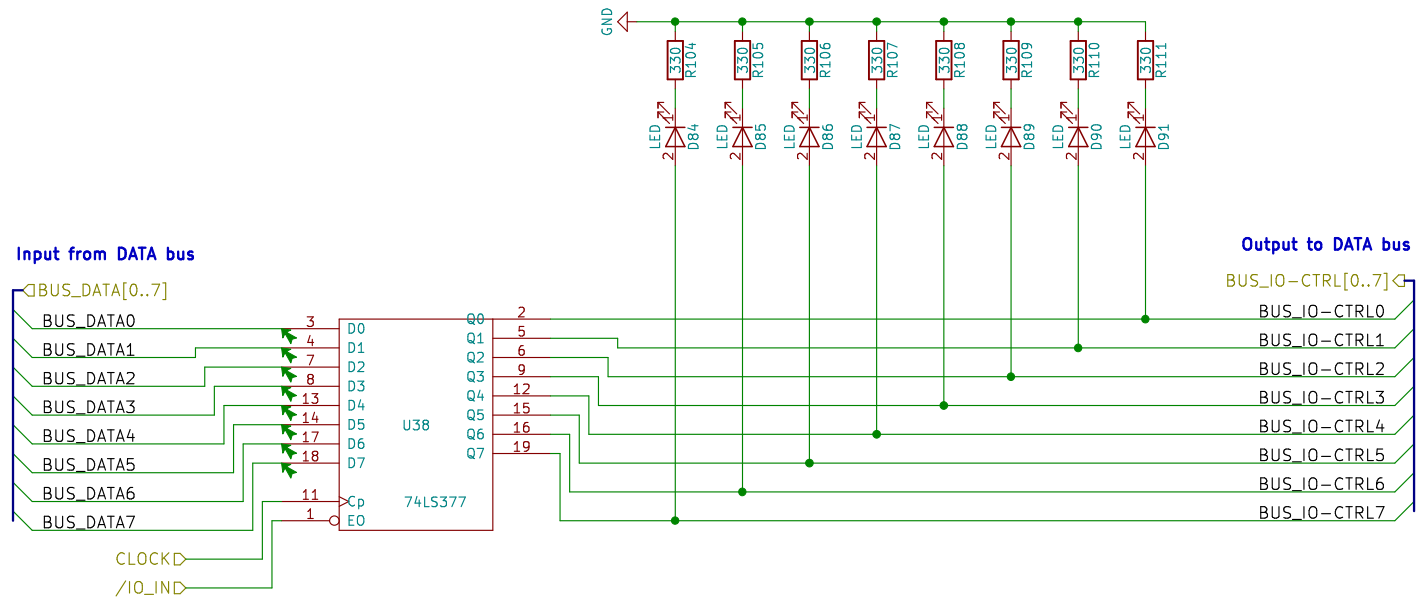
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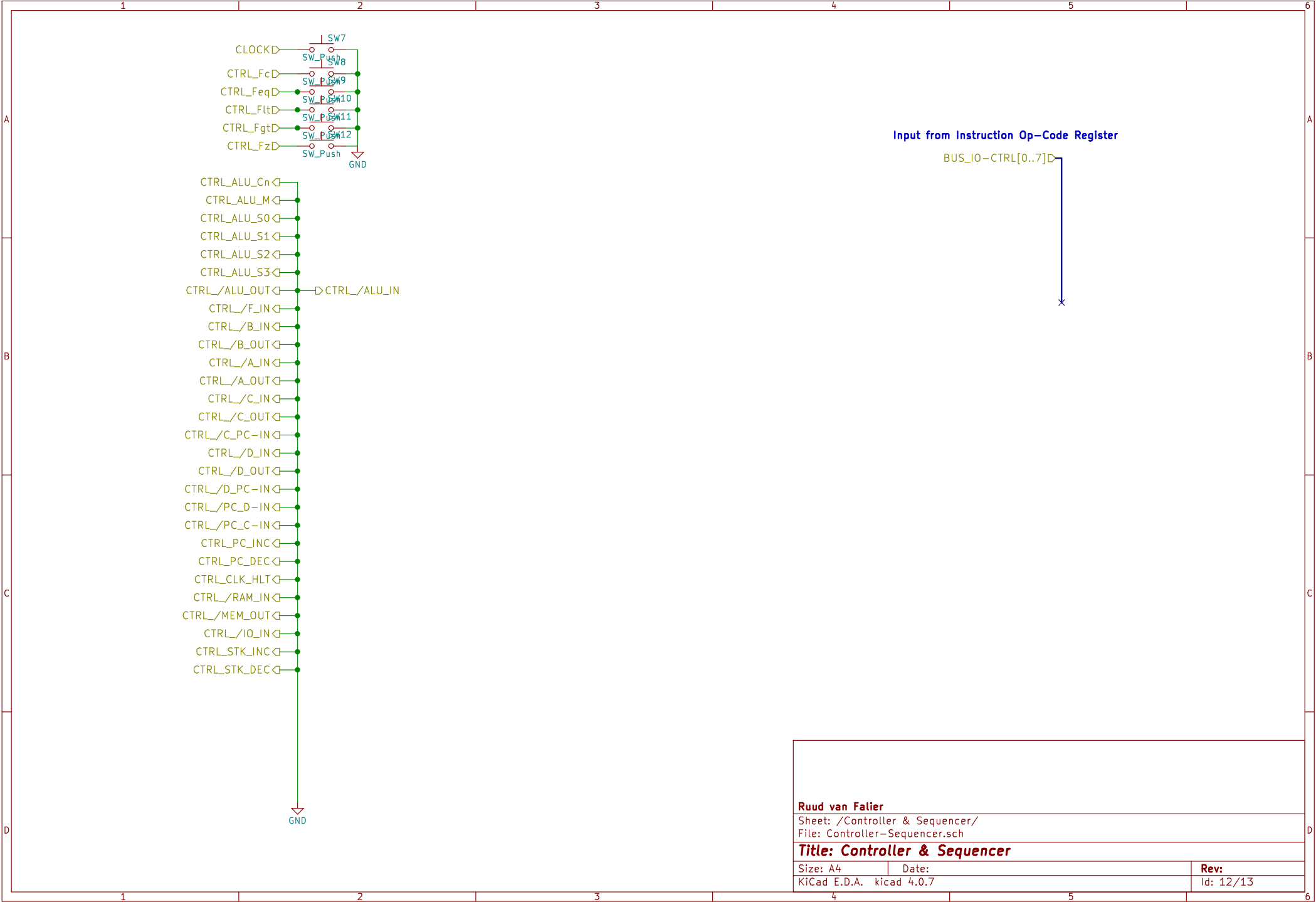
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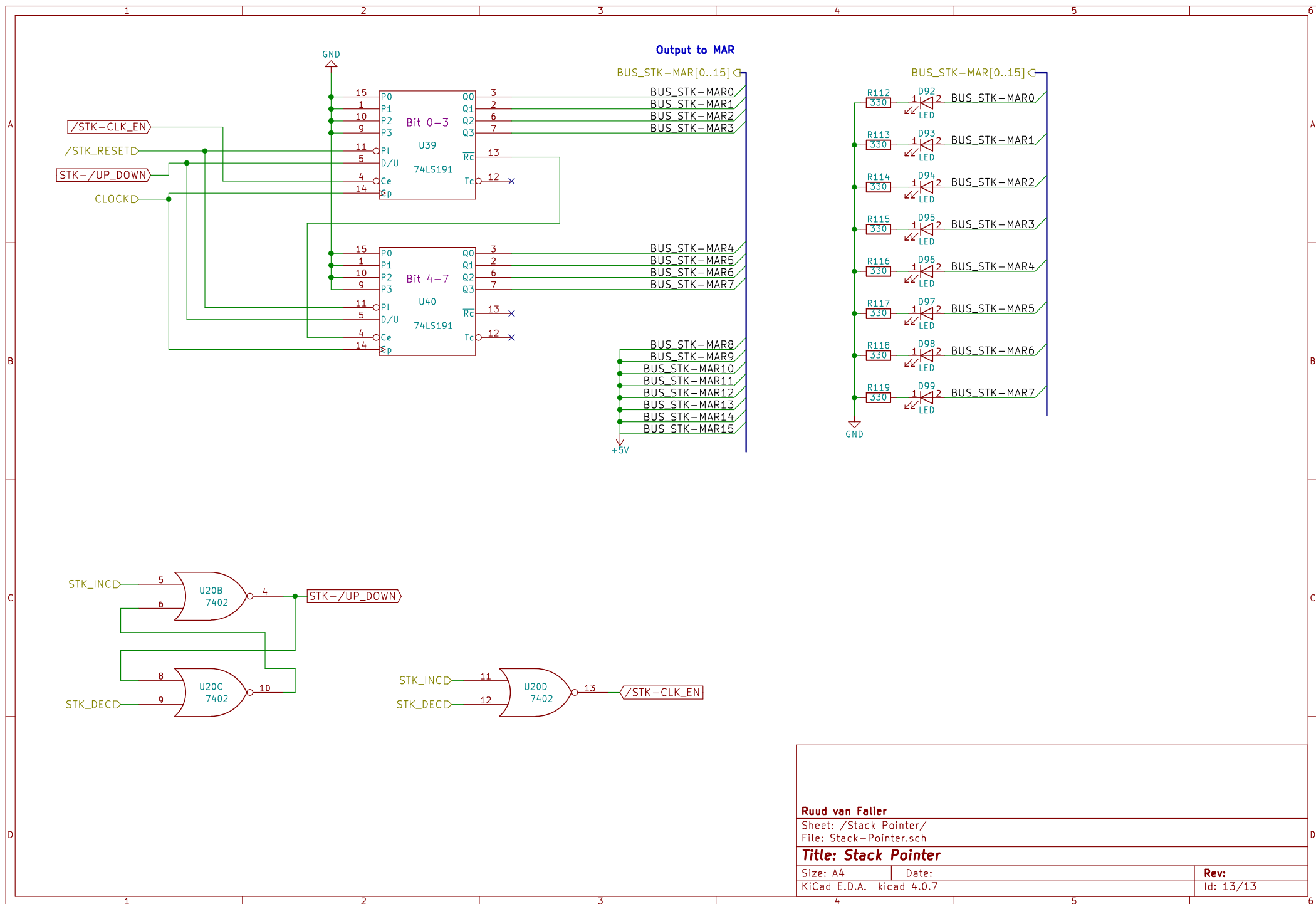
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Ruud van Falier		
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File: Controller-Sequencer.sch		
Title: Controller & Sequencer		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 4.0.7		Id: 12/13



**Ruud van Falier**

Sheet: /Stack Pointer/

File: Stack-Pointer.sch

**Title: Stack Pointer**

Size: A4

Date:

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**Rev:**

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