

Fig: Block Diagram of main circuit

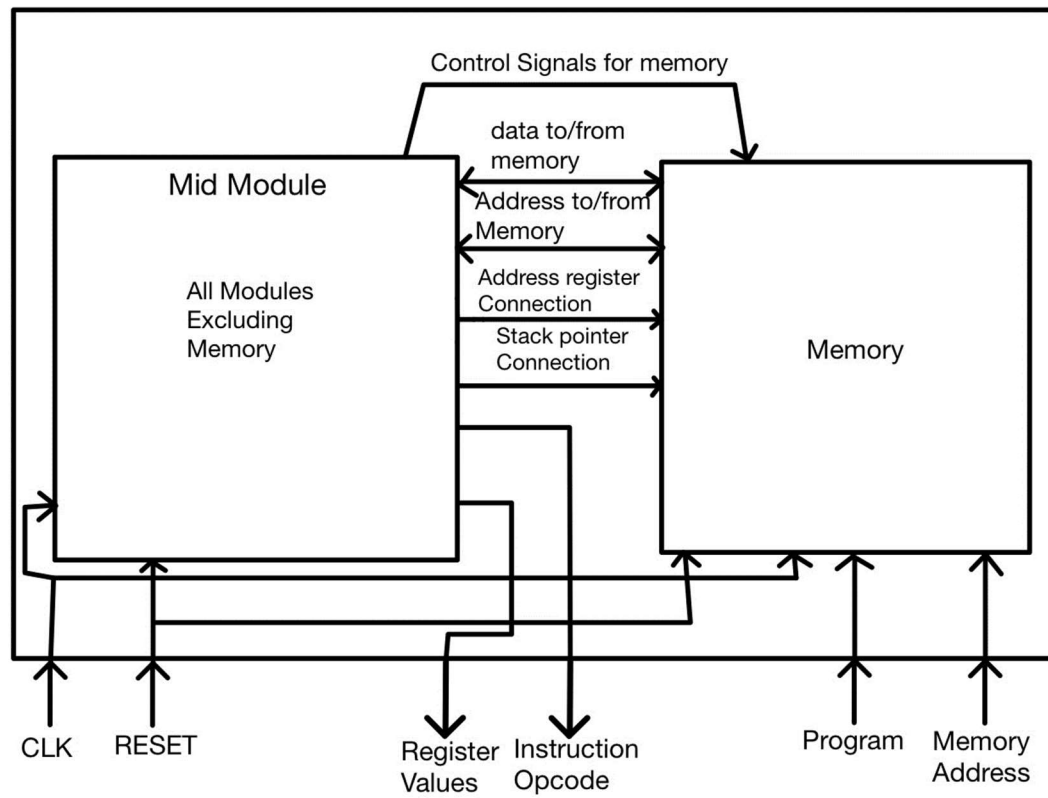


Fig: Top Module