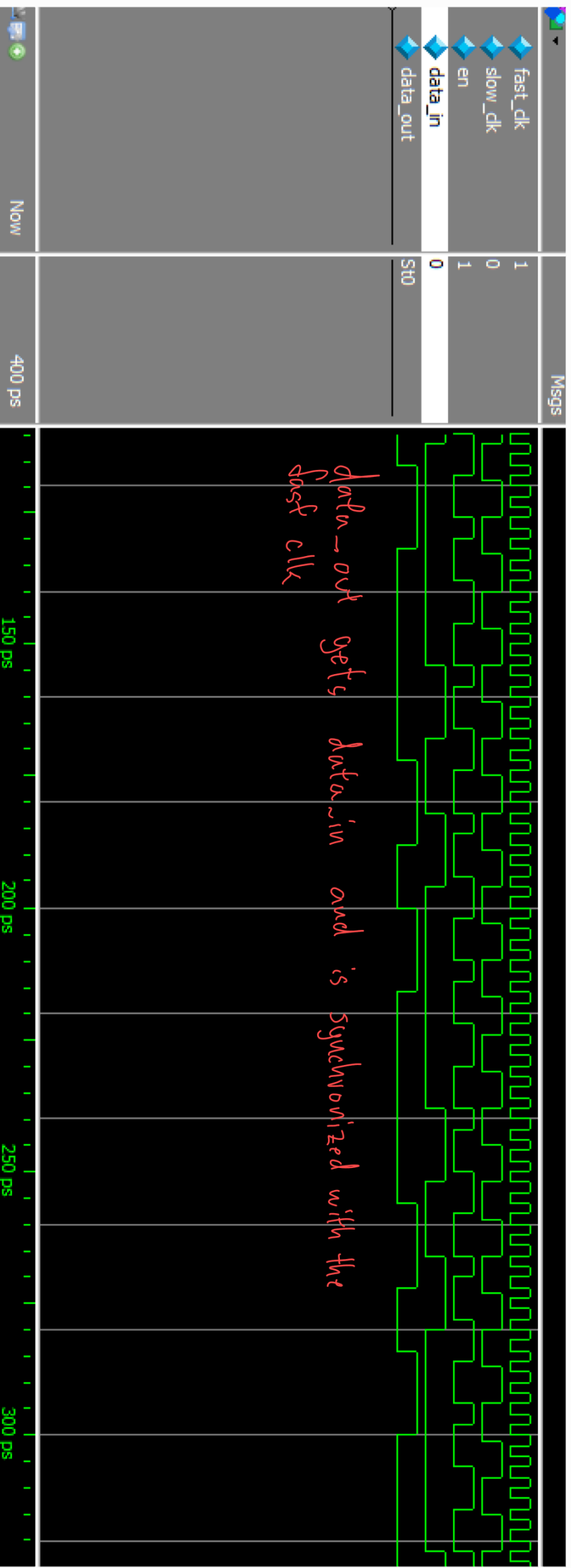
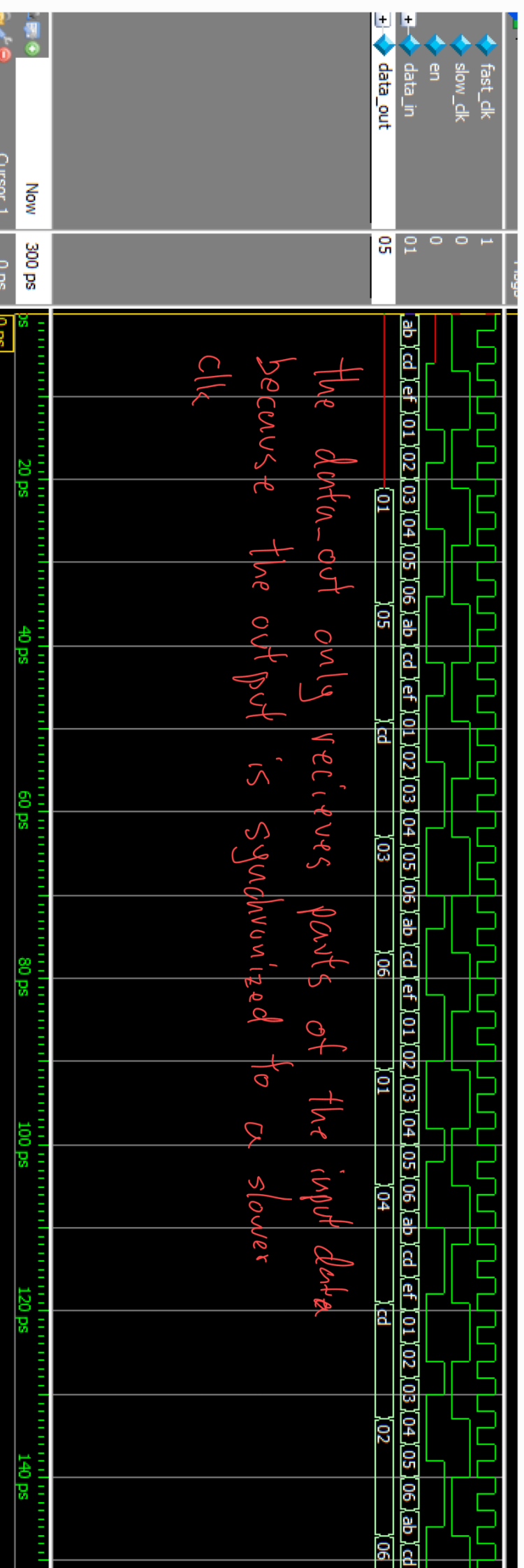


Slow-to-fast



fast-to-slow



the data-out only receives parts of the input data because the output is synchronized to a slower clk

LFSR

