1. Where (which directory path) is your SOF file located, and what it is called

The project file is in ../rtl/Lab5\_template\_de1soc directory

1. What is the status of the lab (what works, what doesn't)

EVERYTHING UPTO FSK

1. Annotated simulation screenshots as required by the lab

Annotated simulations are located in the /sim directory

1. Annotated SignalTap screenshots as required by the lab

Annotated screenshot of the SignalTap is in /sim directory

1. Information on how to run the simulations (i.e. where the files are located, which program you used to run your simulation)

You can run the test bench through model sim

Project file is in /sim directory and all the verilog / system Verilog files are in the rtl directory

1. Any additional information that would be relevant for the TA marking your project.

Lab partners

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