



Chapter 4: Digital Transmission

Outline

4.1 DIGITAL-TO-DIGITAL CONVERSION

4.2 ANALOG-TO-DIGITAL CONVERSION

4.3 TRANSMISSION MODES

4-1 DIGITAL-TO-DIGITAL CONVERSION

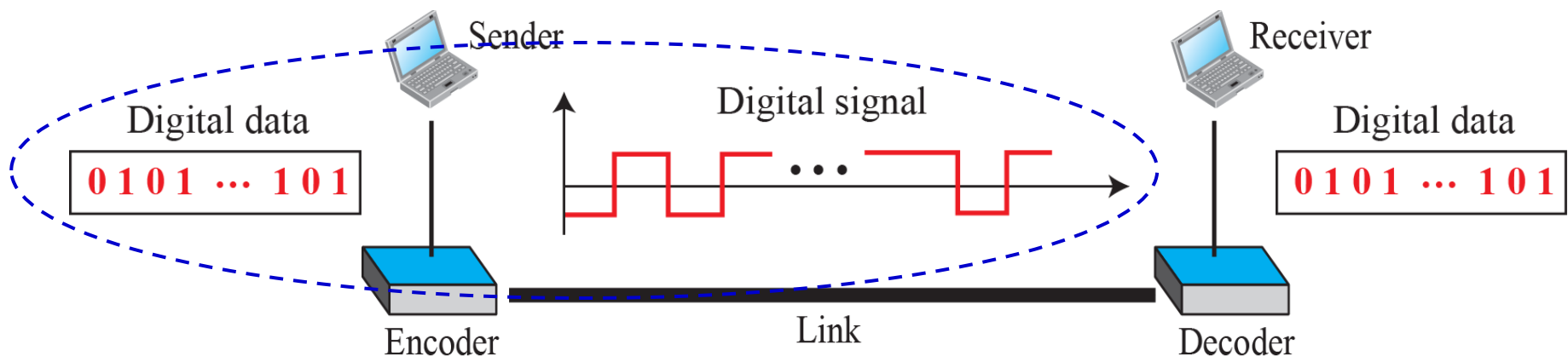
In Chapter 3, we discussed data and signals. We said that data can be either digital or analog. We also said that signals that represent data can also be digital or analog.

In this section, we see how we can represent digital data using digital signals.

4.1.1 Line Coding

Line coding is the process of converting digital data to digital signals. It converts a sequence of bits to a digital signal.

At the sender, digital data are encoded into a digital signal; at the receiver, the digital data are recreated by decoding the digital signal.

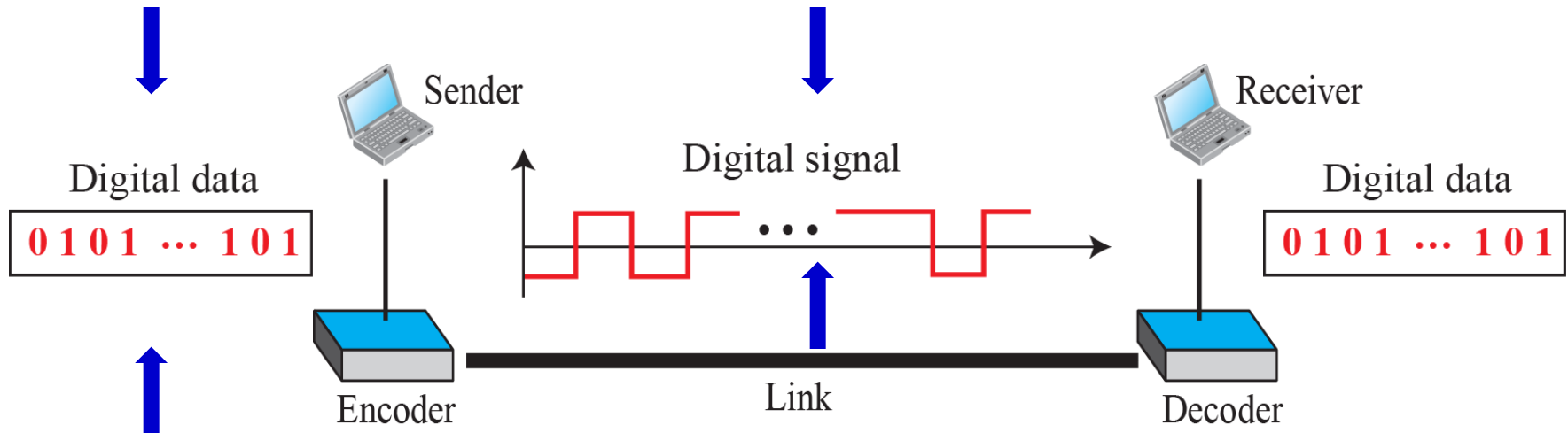


Data vs. Signal

Data elements
(bits) are what we
NEED to send.

Signal elements
are what we
CAN send.

r = number of data
elements carried by
each signal element



Data rate, N ,
defines the
number of data
elements sent in
1 sec (bps).

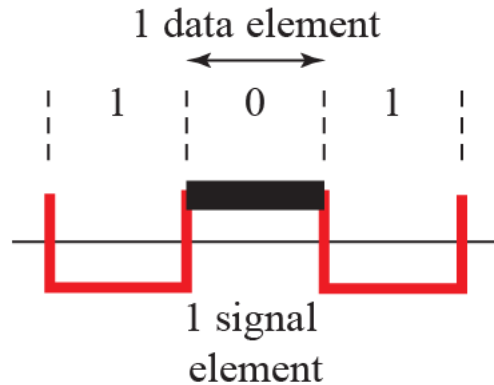
Signal rate, S ,
defines the
number of signal
elements sent in
1 sec (baud).

$$r = N/S$$

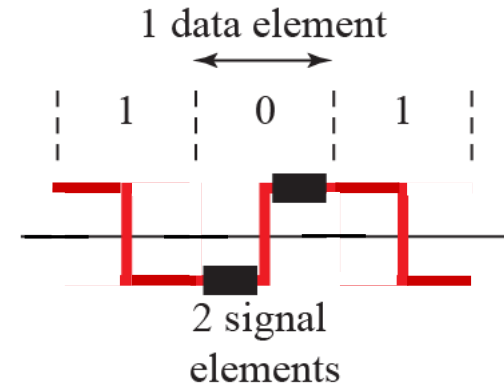
Figure 4.2: Data element versus signal element

$$r = N/S$$

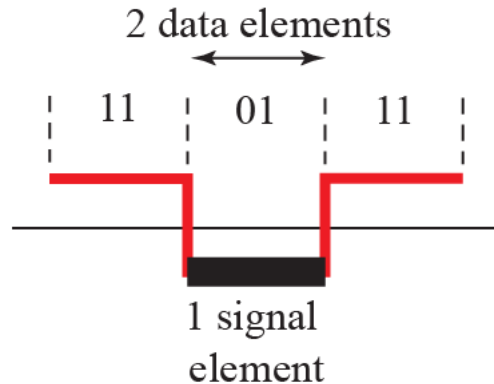
N : data elements in 1 sec
 S : signal elements in 1 sec



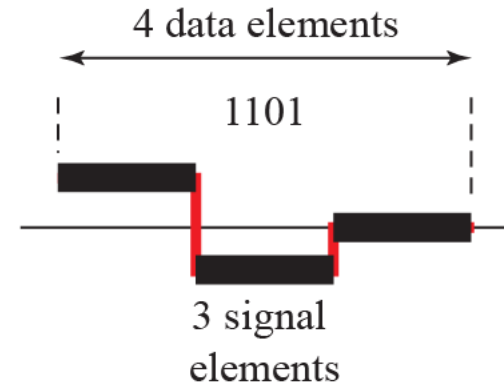
- a. One data element per one signal element ($r = 1$)



- b. One data element per two signal elements ($r = \frac{1}{2}$)



- c. Two data elements per one signal element ($r = 2$)



- d. Four data elements per three signal elements ($r = \frac{4}{3}$)

4.1.2 Line Coding Schemes

We can roughly divide line coding schemes into five broad categories. We will look at three categories: (A) unipolar, (B) polar and (C) bipolar. There are several schemes in each category.

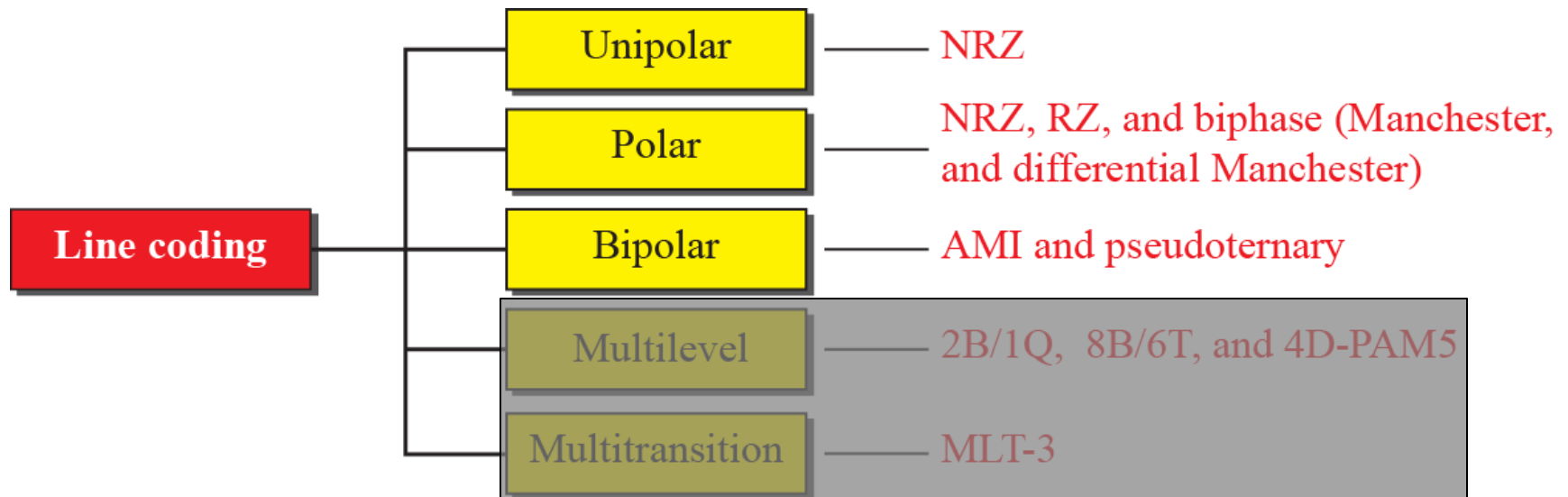
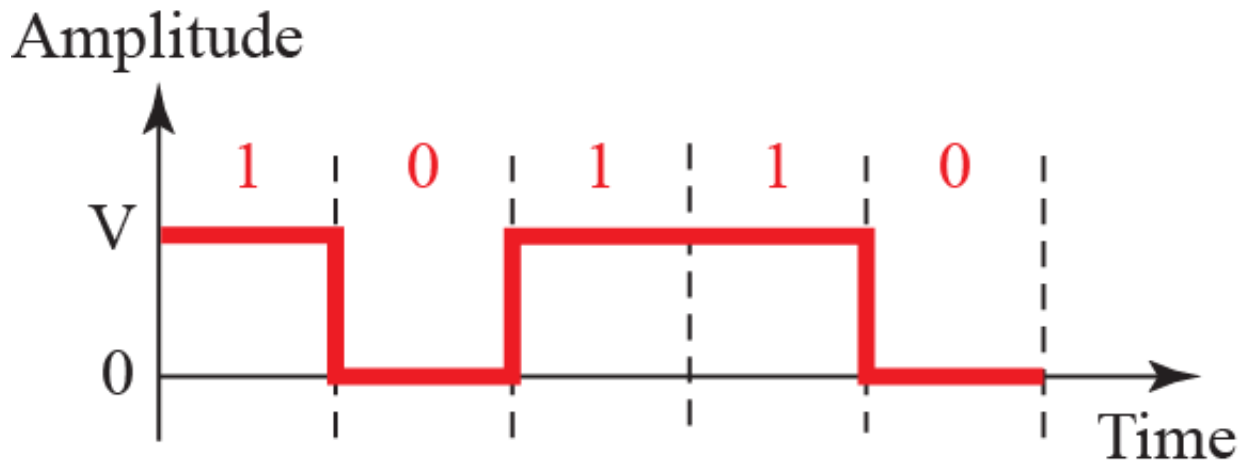


Figure 4.5: (A) Unipolar scheme (NRZ)

- In a unipolar scheme, all voltage levels are on one side of the time axis (above or below).
- A positive voltage defines bit 1 and a zero voltage defines bit 0.

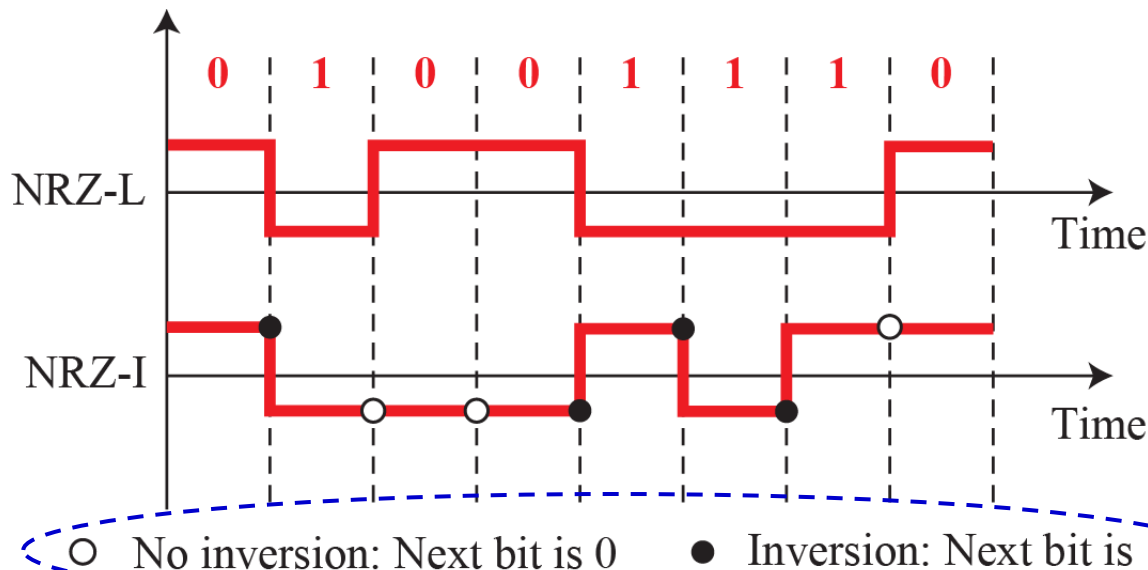


Note:

Non-Return-to-Zero (NRZ): signal does not return to zero in the middle of the bit.

Figure 4.6: (B.1) Polar schemes (NRZ-L and NRZ-I)

- In polar schemes, the voltage levels are on both sides of the time axis.
- A positive voltage can define bit 0 and a negative voltage can define bit 1.



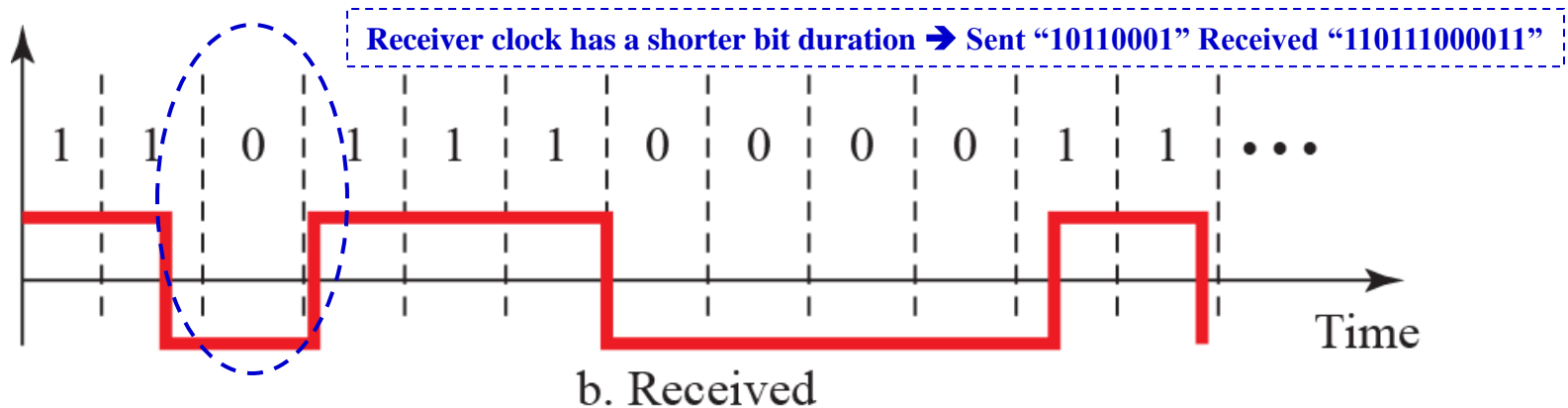
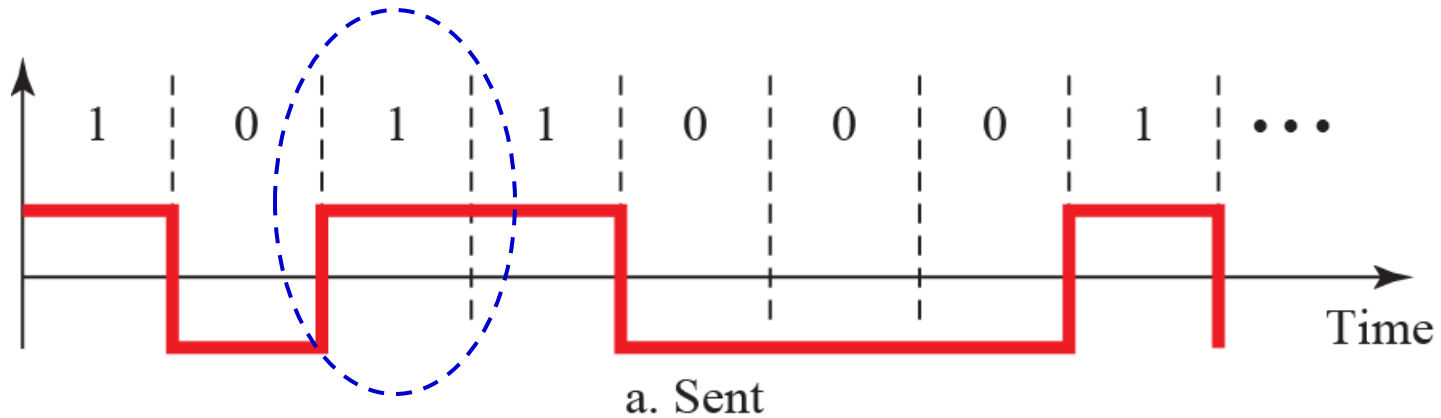
NRZ-Level (NRZ-L): the level of voltage determines the value of the bit.

NRZ-Invert (NRZ-I): the change or lack of change in the voltage level determines the value of the bit.

Note: An issue with NRZ encoding schemes is that when the sender and receiver clocks are not synchronized, the receiver does not know when one bit has ended and when the next bit is starting.

Clock Synchronization

To correctly interpret the signals received from the sender, the receiver's bit period needs to correspond to the sender's bit period.



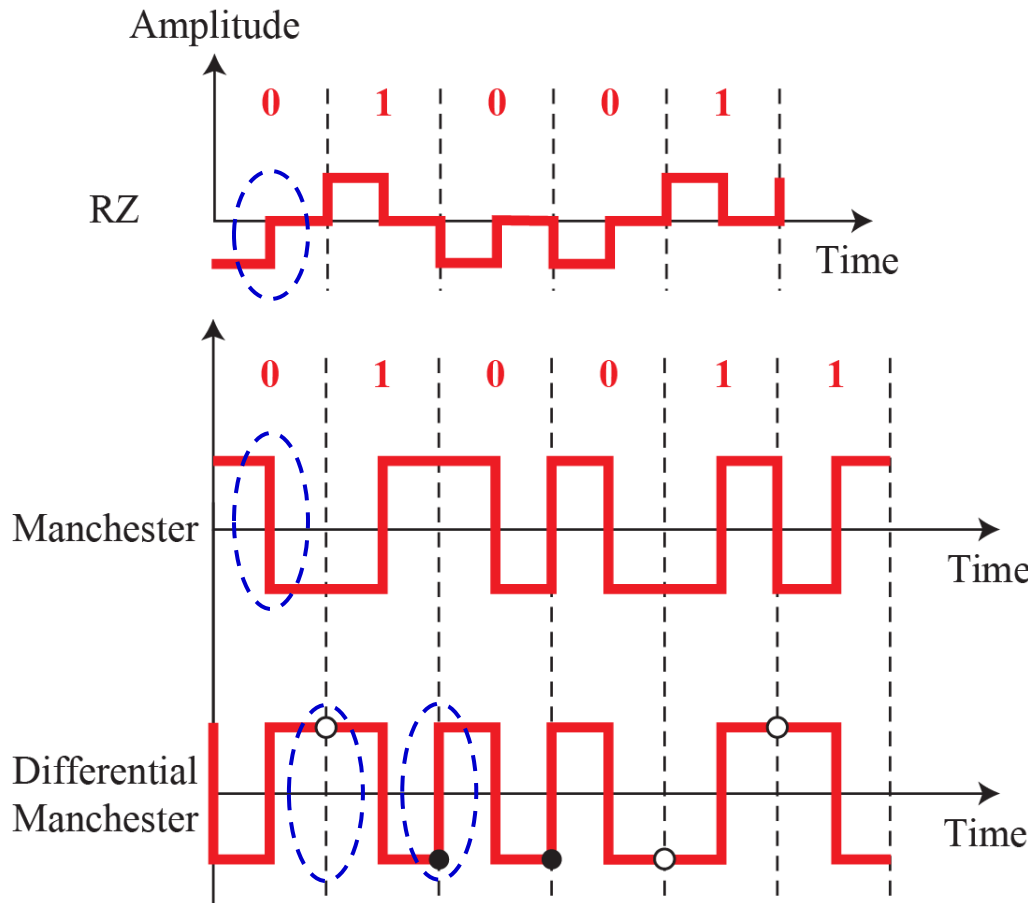
Note: If the receiver clock is faster or slower, the bit intervals are not matched and the receiver might misinterpret the results.

Problem

In a digital transmission, the receiver clock is 0.1% faster than the sender clock. How many extra bits per second does the receiver receive if the data rate is a) 1 kbps and b) 1 Mbps?

Figure 4.7: (B.2) Polar schemes (RZ, Manchester, Differential Manchester)

A solution to the clock synchronization issue in NRZ is return-to-zero (RZ) schemes. In RZ, the signal changes not between bits but during the bit, i.e., self-synchronizing signal that includes timing information in the data being transmitted.



RZ: the signal changes during the bit, i.e., signal goes to 0 in the middle of each bit.

Manchester: combines RZ and NRZ-L. Voltage remains at one level during the first half and moves to other level in the second half.

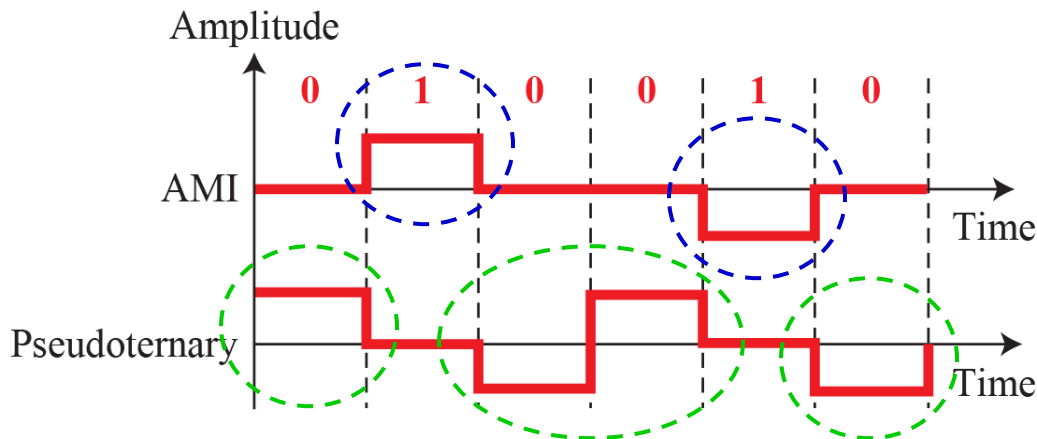
Differential Manchester: combines RZ and NRZ-I. If the next bit is a 0, there is a transition; if the next bit is 1, there is no transition.

○ No inversion: Next bit is 1 ● Inversion: Next bit is 0

The main disadvantage of RZ, Manchester and Differential Manchester schemes is that it requires signal changes to encode a bit and therefore occupies greater bandwidth.

Figure 4.9: (C) Bipolar schemes: AMI and Pseudoternary

- In bipolar schemes, there are three voltage levels: positive, negative and zero.
- The voltage for one data element is at zero, while the voltage level for the other element alternates between positive and negative.



Alternate Mark Inversion (AMI): a zero voltage represents bit 0 and bit 1 is represented by alternating positive and negative voltages.

Pseudoternary: variation of AMI. Bit 1 is represented by a zero voltage and bit 0 is represented by alternating positive and negative voltages.

As with physical topologies, the choice of line coding schemes depends on the desired characteristics. There are many factors for consideration, including: complexity, signaling rate and bandwidth requirement, self-synchronization, etc., (additional details in Section 4.1.2).

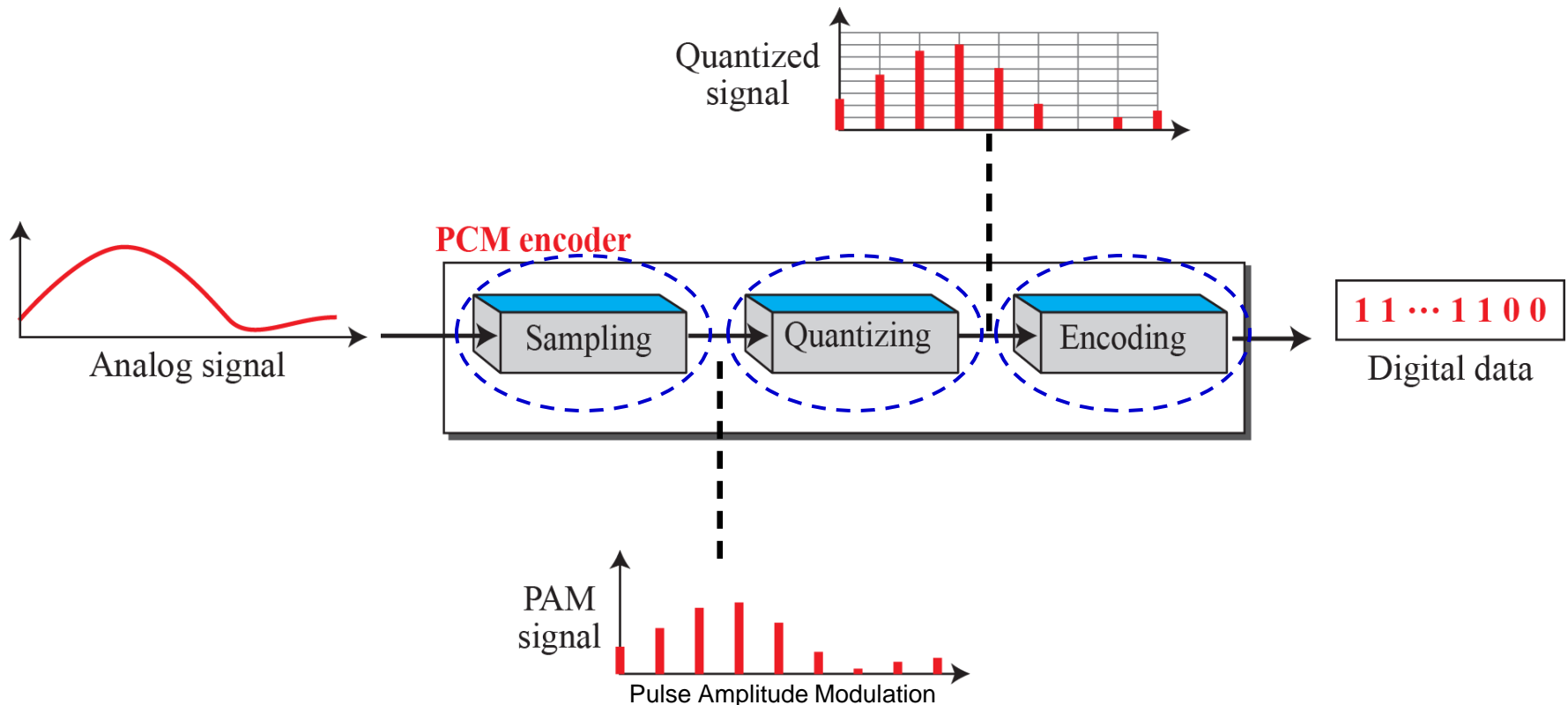
4-2 ANALOG-TO-DIGITAL CONVERSION

The techniques described in Section 4.1 convert digital data to digital signals. Sometimes, however, we have an analog signal such as one created by a microphone or camera.

In this section, we see how we can represent analog data using digital signals.

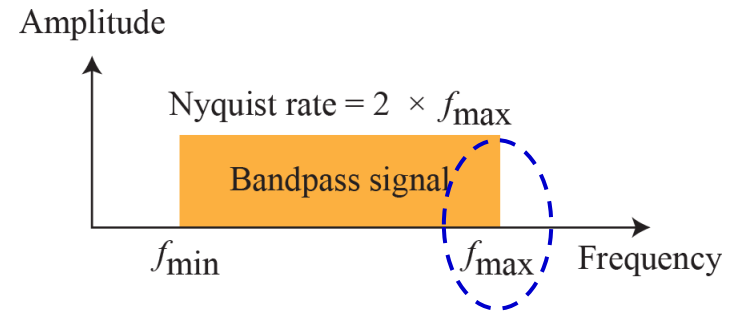
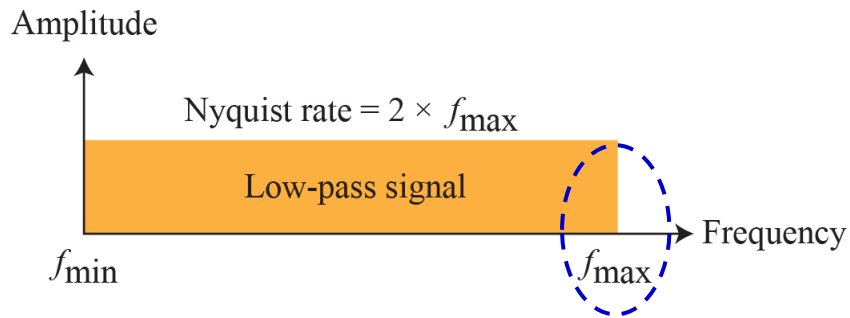
4.2.1 Pulse Code Modulation (PCM)

The most common technique to change an analog signal to digital data (digitization) is called pulse code modulation (PCM). A PCM encoder has three processes (sampling, quantizing and encoding), as shown below:



Step 1: Sampling

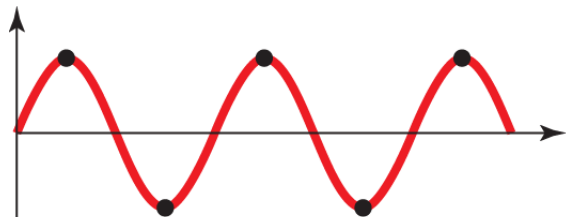
Based on the Nyquist theorem, to reproduce the original analog signal, the sampling rate must be at least 2 times the highest frequency, f_{max} , contained in the signal.



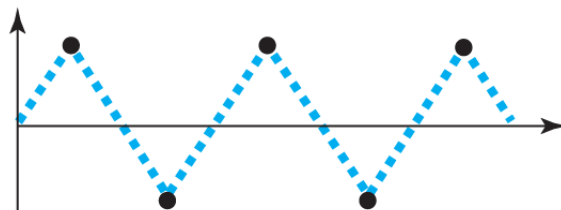
Nyquist rate, $f_s = 2 \times f_{max}$
($f_{max} \neq B$ for bandpass signal)

Recovery of a sine wave with different sampling rates

For an example of the Nyquist theorem, let us sample a simple sine wave with frequency, f , at three sampling rates: (a) Nyquist rate, (b) 2 times the Nyquist rate and (c) $\frac{1}{2}$ the Nyquist rate.

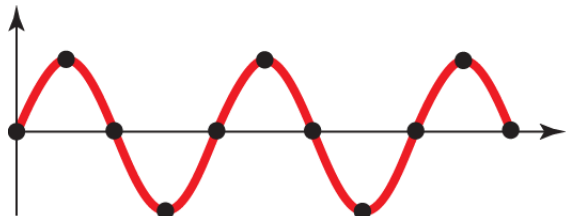


a. Nyquist rate sampling: $f_s = 2f$

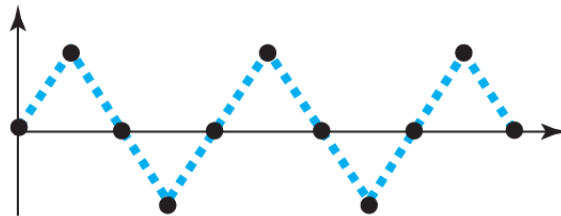


$f_s = 2f$ (e.g., Nyquist):

Can create a good approximation of the original sine wave.

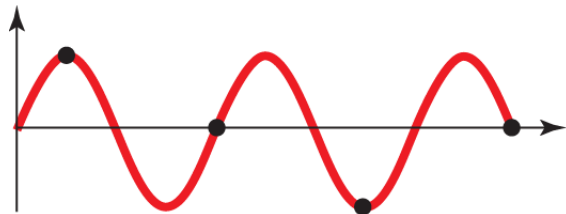


b. Oversampling: $f_s = 4f$

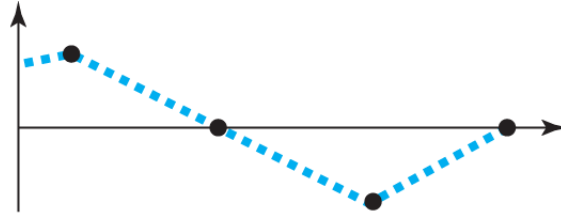


$f_s > 2f$ (e.g., 2 x Nyquist):

Oversampling can also create the same approximation but it is redundant and unnecessary.



c. Undersampling: $f_s = f$

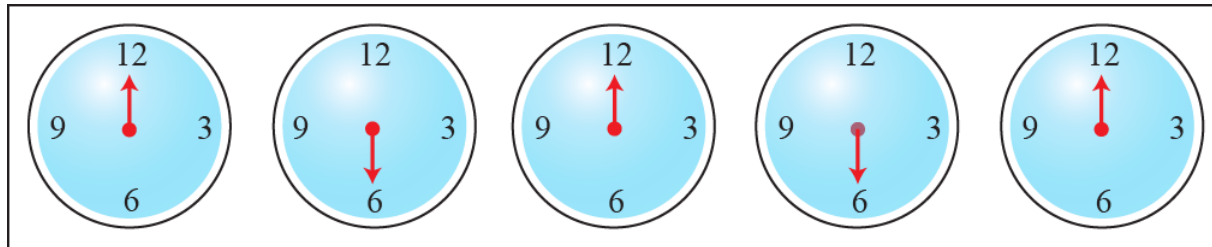


$f_s < 2f$ (e.g., $\frac{1}{2}$ Nyquist):

Undersampling does not produce a signal that looks like the original sine wave
← **aliasing** (note that we are not only losing information, we are getting the wrong information about the signal).

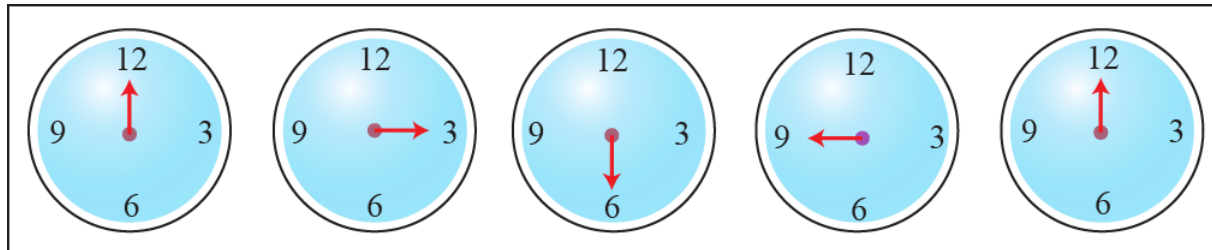
Example

An interesting example: let's sample a periodic event such as the revolution of a clock with only a second hand (i.e., no hour hand) and has a period, T , of 60 sec.



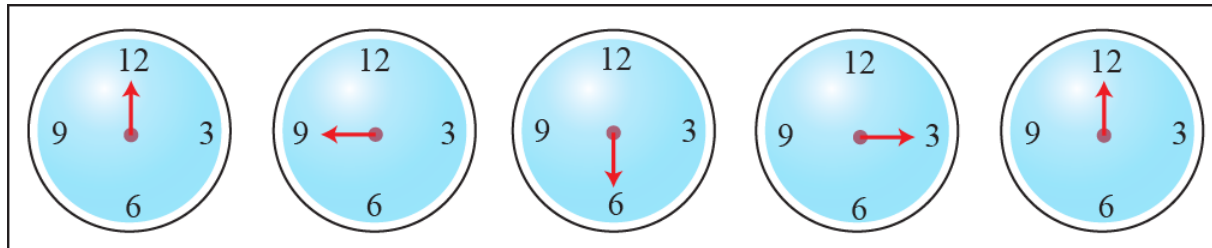
Samples can mean that the clock is moving either forward or backward.
(12-6-12-6-12)

a. Sampling at Nyquist rate: $T_s = T \frac{1}{2} = 30$ sec



Samples show clock is moving forward.
(12-3-6-9-12)

b. Oversampling (above Nyquist rate): $T_s = T \frac{1}{4} = 15$ sec



Samples show clock is moving backward.
(12-9-6-3-12)

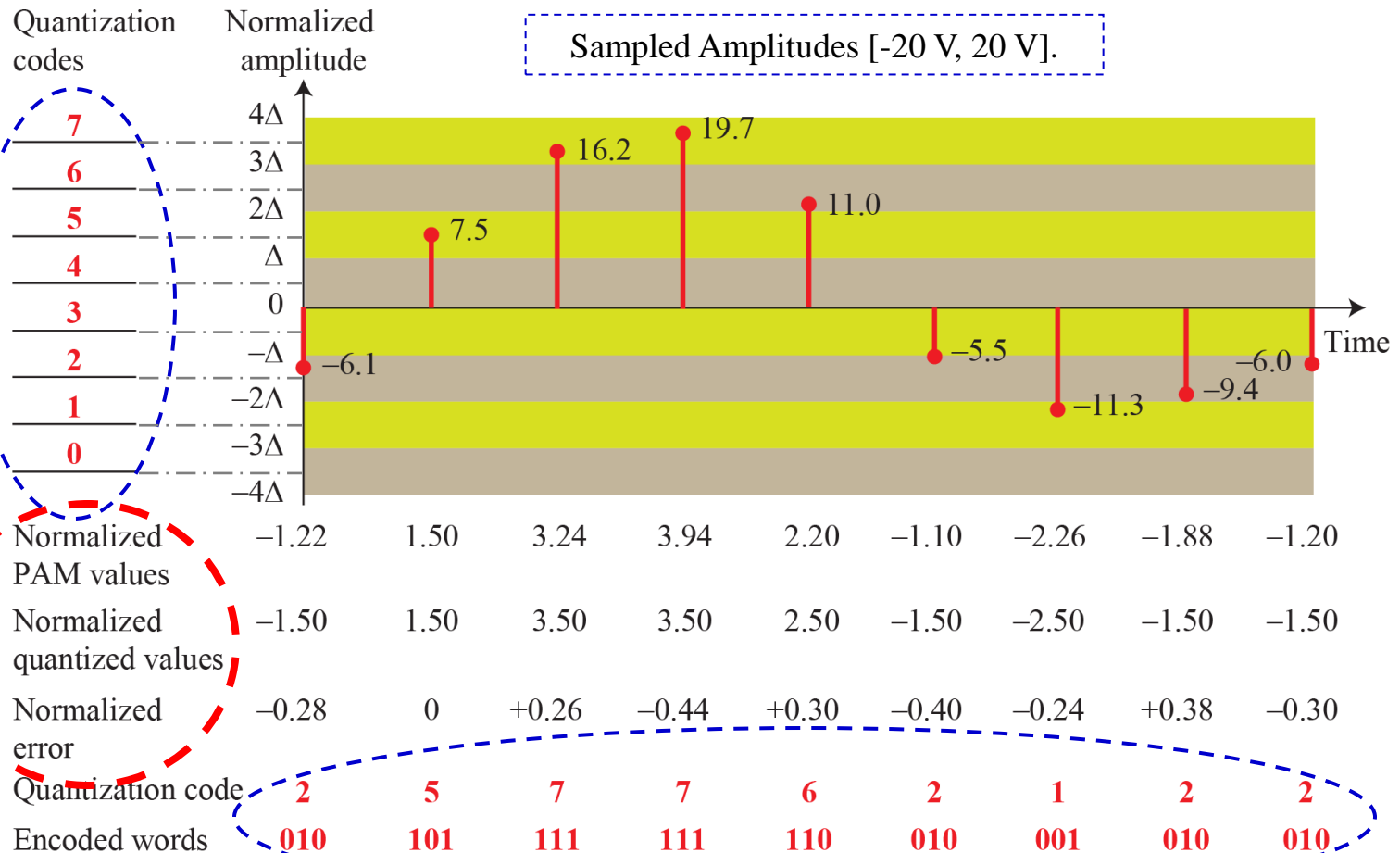
c. Undersampling (below Nyquist rate): $T_s = T \frac{3}{4} = 45$ sec

Problem

A low-pass signal has a bandwidth of 200 kHz. What is the minimum sampling rate for this signal?

Step 2 & 3: Quantizing and Encoding

Step 2: Quantization



Step 3: Encoding



Quantization Error

Quantization is an approximation process and an important issue is the error created. The quantization error changes the signal-to-noise ratio of the signal, which reduces the limit of the Shannon capacity.

The signal strength in relation to the quantization error, SNR_Q , in dB, is estimated by

$$SNR_Q = 6n_b - 1.25$$

where $n_b = \log_2 L$, is the number of bits per sample.
Note that in Forouzan, $SNR_Q = 6n_b + 1.76$ is used.

Problem

A telephone subscriber line must have a quantizing signal-to-noise ratio of no less than 40 dB. What is the minimum number of bits per sample?



Step 3: Encoding and Bit rate

The last step in PCM is encoding. After each sample is quantized and the number of bits per sample, n_b , is determined, each sample is represented by an n_b -bit code word. (Recall if the number of quantization levels is L , then the number of bits per sample is $n_b = \log_2 L$.)

The bit rate can be found as

$$\text{Bit rate} = f_s \times n_b$$

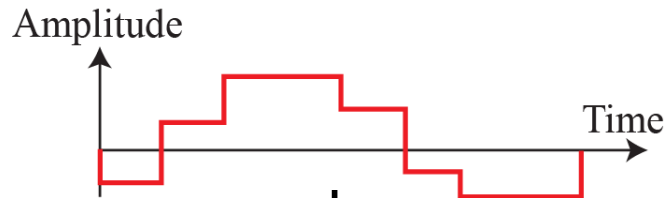
where f_s is the sampling rate and n_b is the number of bits per sample.

Problem

We want to digitize the human voice. The human voice normally contains frequencies from 0 to 4000 Hz. What is the bit rate, assuming 8 bits per sample?

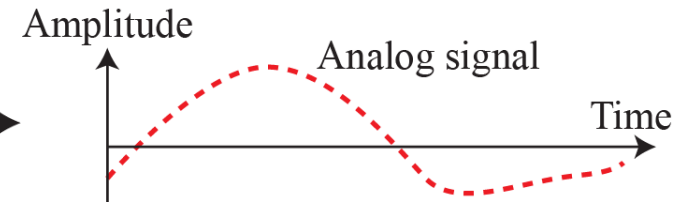
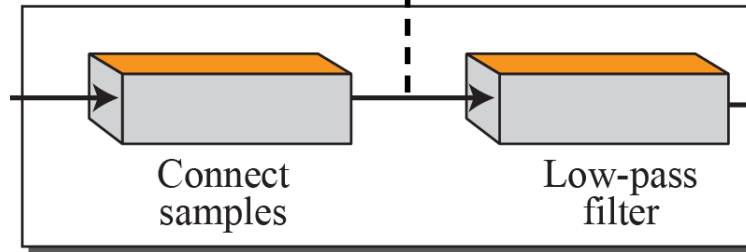
Figure 4.27: PCM decoder

The decoder first uses circuitry to convert the code words into a pulse that holds the amplitude until the next pulse (staircase signal).



PCM decoder

1 1 ... 1 1 0 0
Digital data



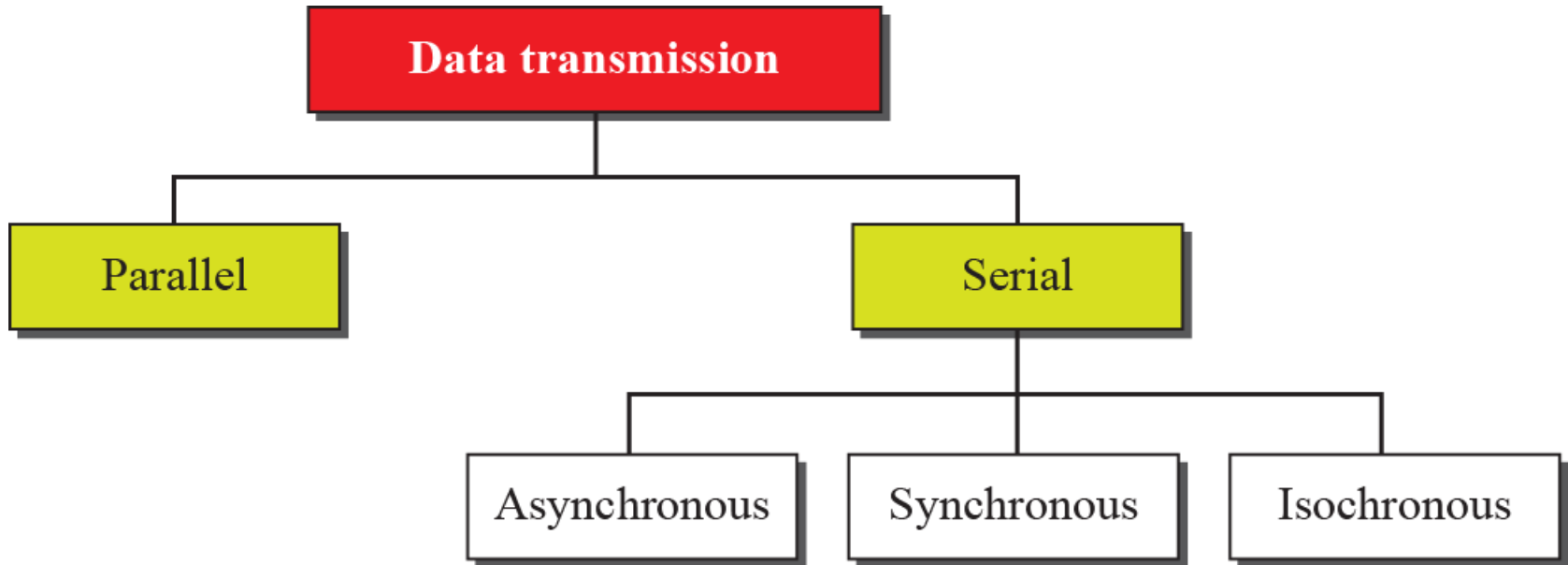
The staircase signal is passed through a low-pass filter to smooth the signal into an analog signal. If the signal has been 1) sampled at (or greater than) the Nyquist sampling and 2) if there are enough quantization levels,
➔ the original signal will be recreated.

4-3 TRANSMISSION MODES

Of primary concern when we are considering the transmission of data from one device to another is the data stream. Do we send 1 bit at a time or do we group bits into larger groups?

The transmission of binary data across a link can be accomplished in either parallel or serial mode.

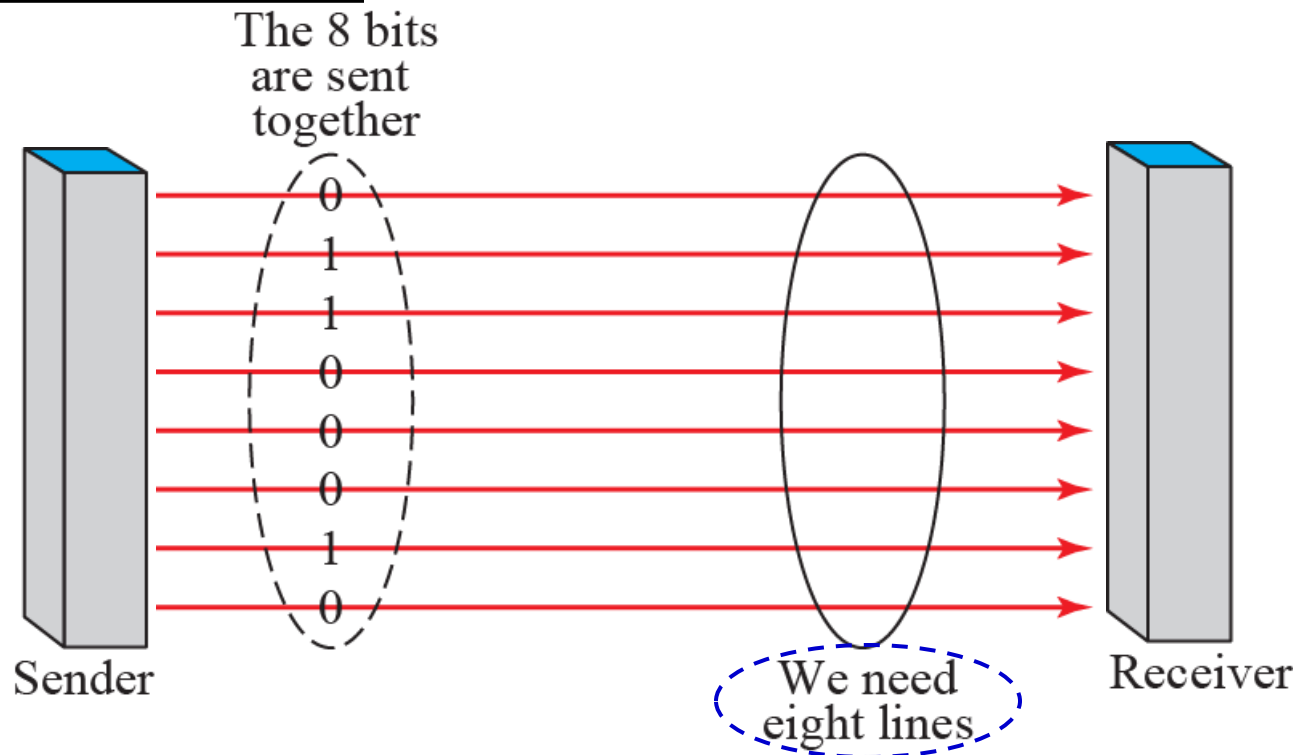
Figure 4.31: *Data transmission modes*



Which transmission mode should one use?

4.3.1 Parallel Transmission

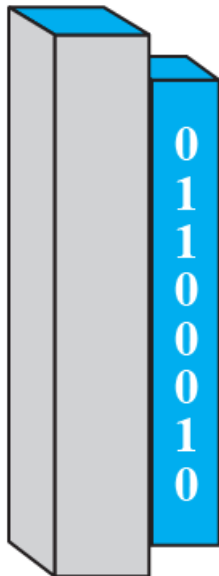
Binary data, consisting of 1s and 0s, may be organized into groups of n bits each. Computers produce and consume data in groups of bits much as we conceive language in the form of words rather than letters. By grouping, we can send data n bits at a time instead of 1 bit at a time. This is called parallel transmission.



4.3.2 Serial Transmission

In serial transmission, one bit follows another, so we need only 1 communication channel rather than n channels to transmit data between two communicating devices.

Parallel/serial
converter



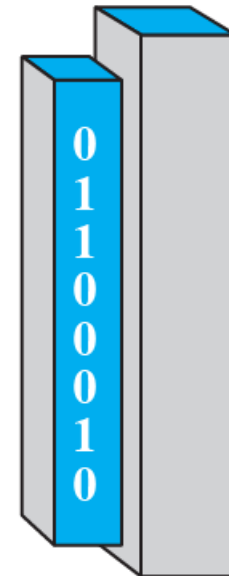
Sender

The 8 bits are sent
one after another.

0 1 1 0 0 0 1 0

We need only
one line (wire).

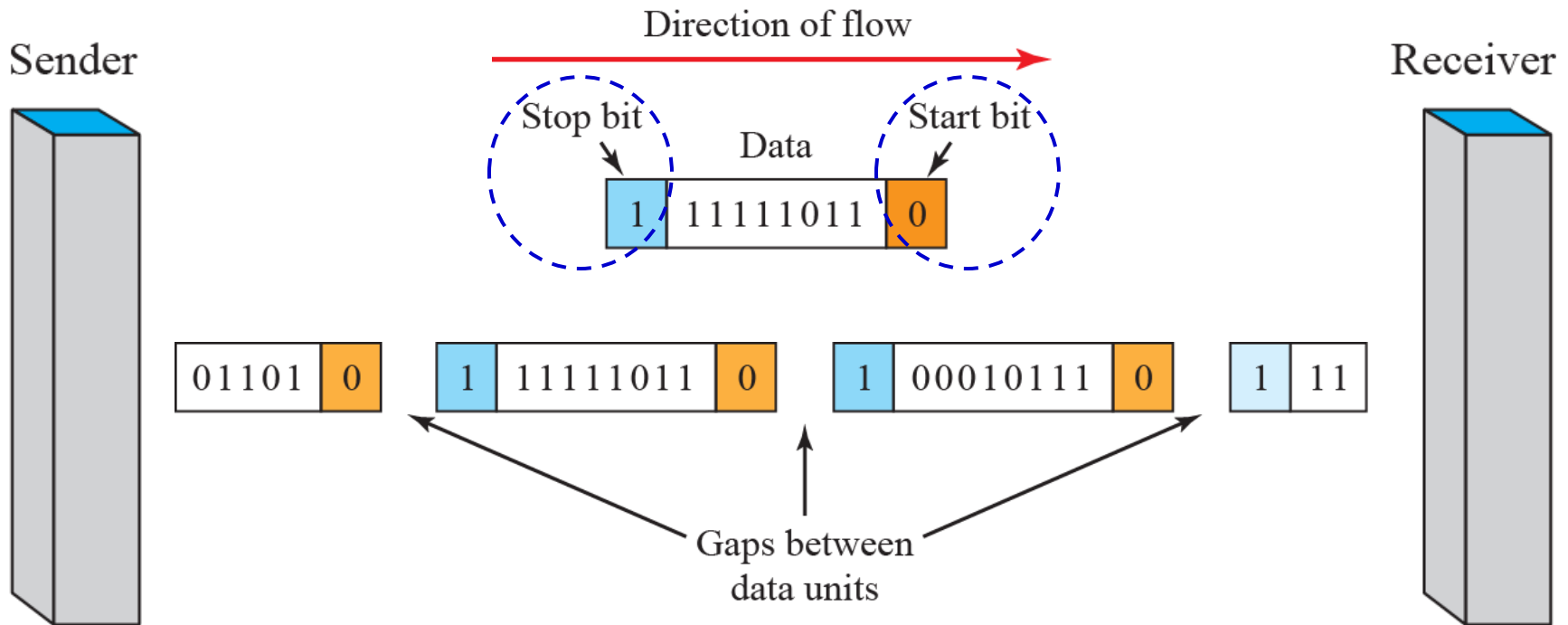
Serial/parallel
converter



Receiver

Figure 4.34: Asynchronous transmission

The timing of a signal is unimportant in asynchronous transmission, information is received and translated by agreed upon patterns, e.g., start and stop bits.

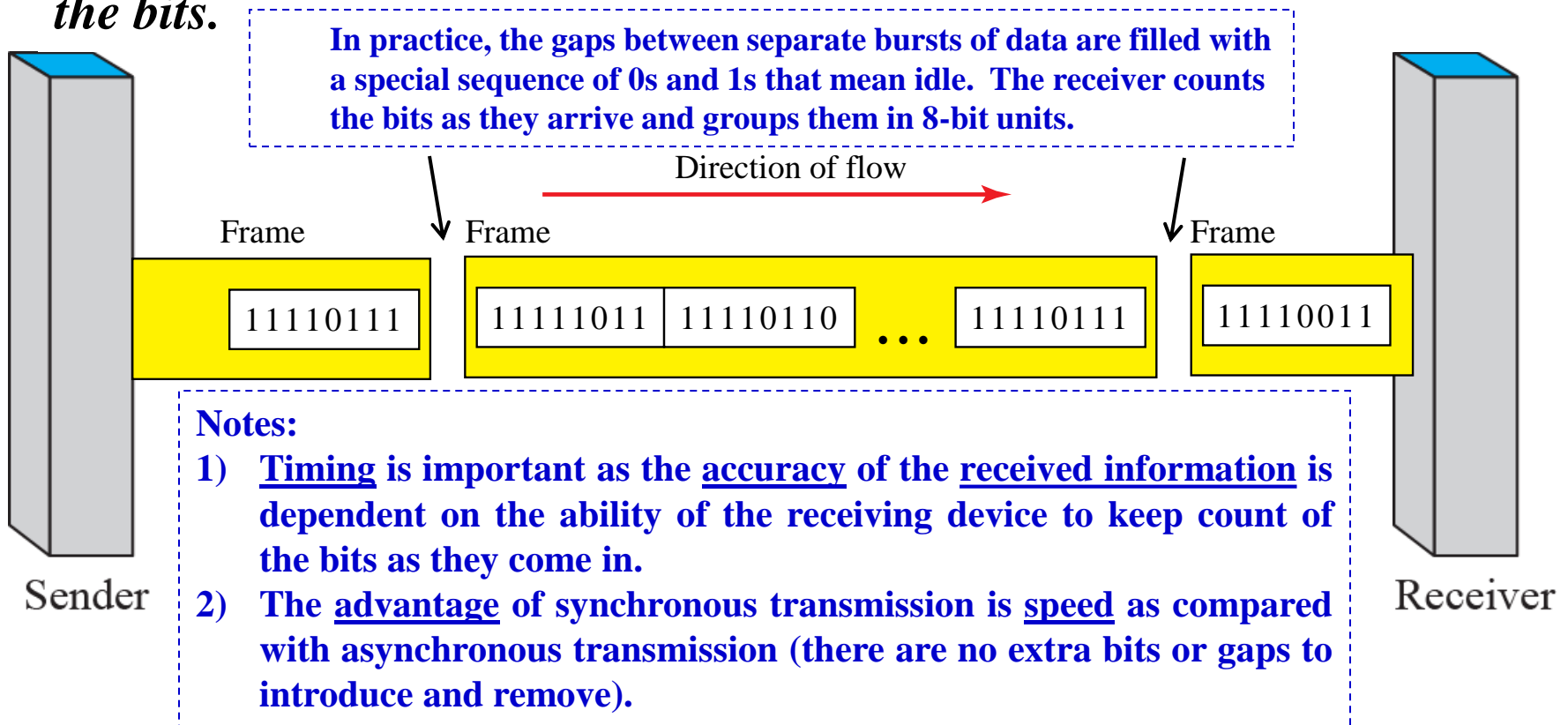


Notes:

- 1) Asynchronous means asynchronous at the byte level; the bits are still synchronized.
- 2) The gaps between data units can be represented by either an idle channel or by a stream of additional stop bits.

Figure 4.35: Synchronous transmission; Isochronous transmission

In synchronous transmission, the bit stream is combined into frames containing multiple bytes. Bits are sent one after another as one long string without start or stop gaps. The receiver groups the bits.



In isochronous transmission, synchronization between characters is not enough; the entire stream of bits are synchronized, e.g., TV broadcast (data arrive at a fixed rate).