Main (1 of 1)
4 BIT CPU

Output register

reg
(1b)

Zero

reg
(4b)

Negative

Carry

Full Adder

Hex display A

Hex display B

Register A

Register B

MUX
MUX
MUX
MUX

Clock

ROM

16B ROM

D

A

Counter

ctr
(4b)

Carry