**Y86-Simulator Final Report**

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1. **TTY** written by rockyRocky
   1. **Usage**

*cd $Y86\_ROOT*

*python main.py*

**

-g is not finished yet

-d is for simple debug, supports step, back, speed up and slow down.

For details please see help (enter ‘help’ or ‘h’).



* 1. **Implementation**
* Python has a set of powerful option-parser functions, so it won’t take much effort to master.
* Attend to run the simulator in the thread, or it could block the user interface. Python provides a simple thread class like:



* To obtain the key knocked down, we need to consider different implementations for different platforms. (unix like/windows)



1. **GUI** written by VV.
   1. **Overview**

Despite the excellent command line interface, we still offer a friendly GUI interface to give our users a better experience. It will be easy for our users to get the expected information of register, program status, memory and cache, for special. For more details to install it and run it, refer to our readme file.

* 1. **Design**

The Graphic interface has an amazing hand-drawn background, which takes lots of efforts. It is a sketch drawing that presents all the information well. Sometimes it may be messy and unendurable for some person, especially when you run some extremely complicated programs. In such cases, you can consider to use our command line version. Our design servers to display more beautiful interface at the stand of art and gives the user a more comfortable enjoyment.

* 1. **Realization Toolkit**

Considering all the toolkits for python GUI designing, we choose PyQT4 to construct our Graphic interface due to its convenience and good compatibility to all platforms including windows, linux and MacOS. QT itself is such a powerful and widely applied library with an impeccable reference that can decrease our work to a great extent.

* 1. **Functions**

The GUI version supports both .yo and .ys files, which is different to the command line. Once a .ys file is imported, the y86 assembler will be called automatically to assemble the codes to binary ones that can be executed by our simulator. Once an invalid .ys file is loaded, the program will automatically call the GUI version of the assembler to help the user fix the problems. The basic functions of GUI are to be listed as following:

·display all the information you need including register, program status, cycles, source code, memory and cache

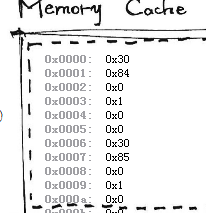
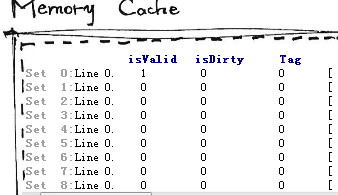
* support run, pause, step ,back and reset instructions
* change the frequency of the simulator dynamically at any time
* debug your program in our assembler
* a good handle of exceptions
* a special function to express our honor to D.Jin
  1. **Technical implementation**

Due to our Maximize Cohesion and Minimize Coupling designing schema, the GUI part is quiet separate from our simulator and assembler/disassembler, which is quiet a great advantage for use to implement all of our functions.

The GUI get all the details of every cycle and every stage by creating a Simulator class as its own. Then it calls function simulator.load() to convey the input file and output route to the simulator class and then calls simulator.run() to start the simulation. All the information it needs to present can be fetched in the private data of the simulator class. In GUI designing, we have no need at all to consider the details of the kernel design.

To compile the .ys input file, the GUI import assemble.py and simply use the assemble function. The assembly code are stored in an output file with a same prefix-name. Then our GUI import this output file and run it normally.

The highlight of our codes uses the QSyntaxHighLighter class in highlighter.py proposed by official docs of QT, which is quiet cool. The memory window only displays all changed memory address since the program runs. Our cache window displays all the cache.



We also use qss layout tools to help beautify our GUI.

1. **Kernel** written by rockyRocky
   1. **Structure**

y86-simulator/kernel/

|----Sim.py // status of simulator

|----Simulator.py // extends Sim; implements main functions

|----Memory.py // manages the memory system

|----Cache.py // under management of Memory,

| // and manages the Cache

|----History.py // records history for stepping back

|----constants.py // constants for y86 instruction set

|----utils.py // useful binary converting functions

* 1. **Usage**

**Only requires the class Simulator**

* + 1. **new an instance**

*from kernel import \**

*simulator = Simulator()*

* + 1. **load a .yo**

*simulator.load(fin, [fout])*

If fout is missing, a default logfile would be created with the same name of input. Any input error would throw an IO exception.

* + 1. **step, back, run**

*simulator.step()*

*simulator.back()*

*simulator.run()*

* + 1. **get status**

Since python doesn’t have public and private, I didn’t write setters and getters. READ ONLY. Be CAREFUL.

// to highlight where the program is going

simulator.cycle // get the current cycle

simulator.F\_currentPC // get the current PC of stage F

simulator.D\_currentPC // get the current PC of stage D

simulator.E\_currentPC // get the current PC of stage E

simulator.M\_currentPC // get the current PC of stage M

simulator.W\_currentPC // get the current PC of stage W

// to debug/display

simulator.getMemory(addr) // get memory at addr

simulator.getCache(si, li) // get cache at set si, line li.

simulator.isTerminated // .yo file finished execution or not

simulator.isGoing // whether simulator is in running mode

// others are the same as the figure at CSAPP2

* + 1. **set params**

simulator.isSecond // default as 1st edition

simulator.setCacheParams(S, E, B, m) // set params for cache

// attend to set cache params BEFORE use,

// because doing so would reset the cache

* 1. **Implementation**
     1. **Simulator**
* Class Simulator supports basic functions such as step, back and run.
* It recognizes yo-codes in both 1st and 2nd edtion of CSAPP.
* Below are listed the instructions supported:

*nop, halt, rrmovl, irmovl, rmmovl, mrmovl, opl (i.e. addl, subl, andl, xorl), jxx (i.e. jmp, jle, jl, je, jne, jge, jg), call, ret, pushl, popl, iaddl, leave)*

* + - 1. **Load**

// Attend to filter the instruction stream using Regular Expression



// Separate addr and instr

* + - 1. **Step**

/\*

Each time it steps, first call the control logic to produce bubbles and stalls, second update the stage registers (the sequential logic), third update the combination logic and finally record the history.

\*/

/\*

Take care of the sequence of pipe-control, write, and stage. In write and stage, make sure to do in the reverse order (i.e. Write Back, Memory, Excecute, Decode, Fetch), considering we don’t get all variables in intermediate registers (i.e. F, D, E, M, B)

\*/



/\*

For details see the code. Simply translate the hcl at webaside, CSAPP, some parts referencing the source by Linus Yang.

\*/

* + - 1. **Back**



* + - 1. **run**

// throw exception wherever it goes wrong



* + 1. **Memory**

Main memory is stored as list (in python). Index is address and value is its value.

Each time try fetching data at cache first. If missing, handle cache miss (i.e. to fetch a new block) and try again.

Conflict policy is set a dirty flag, and only update the cache at conflict. When the line is to be replaced, update the memory.

Main functions:

handleCacheMiss(addr)

getByte(addr)

getWord(addr)

setByte(addr)

setWord(addr, word)

setByteThrough(addr, byte) // that is not to use cache

* + 1. **Cache**
* Cache is stored as list and index is setIndex\*entrySize+lineIndex
* Placement policy is addr mod E
* Main functions:

getByte(addr)

setByte(addr, byte)

setBlock(addr, block)

* + 1. **History**
* History is stored as list of Sim. Default size is 100.
* Since instance of History is owned by Simulator, however history takes down everything of simulator, that would cause a recursive referencing. To avoid this, I make a Sim out of Simulator, taking all the status, and make Simulator extend Sim. Finally record Sims in History.
  + 1. **Utils**

*utils.py* contains some common but useful binary converting functions like lab1. Attend the function *bytes\_to\_int*. Since python doesn’t has strict types, it would regard 0xffffffff as 4294967295 rather than -1. Hence, converting it manually is required at the end of function.

1. **Assembler/Dessembler** written by Lumig
   1. **To Get Start**

The assembling/disassembling tools follow the rules as described in CSAPP 1st editon and both have command line version and GUI version as well. More details are also offered in the readme file



* 1. **Assembler**

The assembler supports all the y86 instruction in CSAPP book and some even more features as follows:

· All the y86 instructions even

· .pos, .long, .word, .byte instruction

· Labels for jump and call

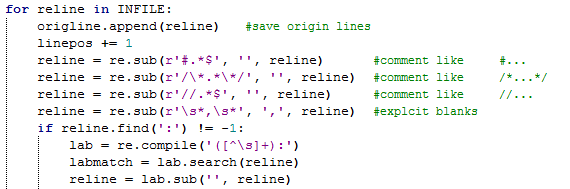
· Jump to a direct address

· Support widely used comment format # , /\*\*/ , //

· Retain all the original lines to help comprehension

· Show different error messages and the line position after assembling

We use regular expression, a recently widely applied technic to speed up the analysis as follows.



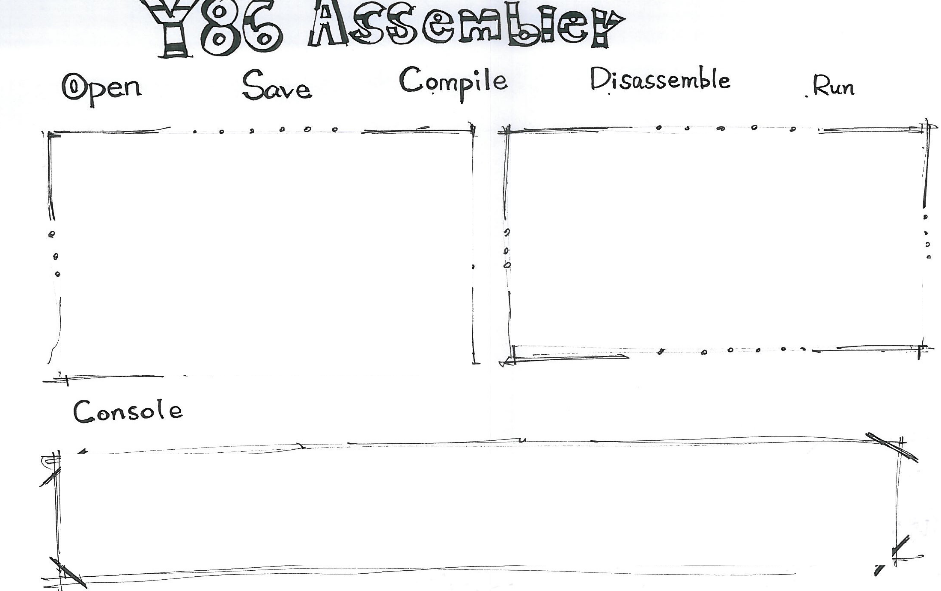
* 1. **Dissembler**

The disassembling tool does not support .long or .byte instruction. Every time it finds the address of an instruction not match its length, the .pos instruction will be applied to fix it. In the GUI mode, hit run to test your code in our simulator easily.

Apart from what is mentioned above, the disassembler enjoys as more functions and features as the assembler.

* 1. **GUI Mode (Text Editor)**

Another hand-drawn graphic interface with two code display window and a console. You can run immediately in our GUI simulator after you have modified/compiled your codes.



1. **Test**

For details see the source listed below.

*$Y86\_ROOT/y86-code/genTestYo.sh*

*$Y86\_ROOT/y86-code/test.sh*

Manually compare the results with those run by lab4.

Mainly compare the result of *asum.yo* with that given by TA.

**That’s all. Thank you!**