

HW-Z1-ZCU104 Evaluation Board

(XCZU7EV-2FFVC1156)

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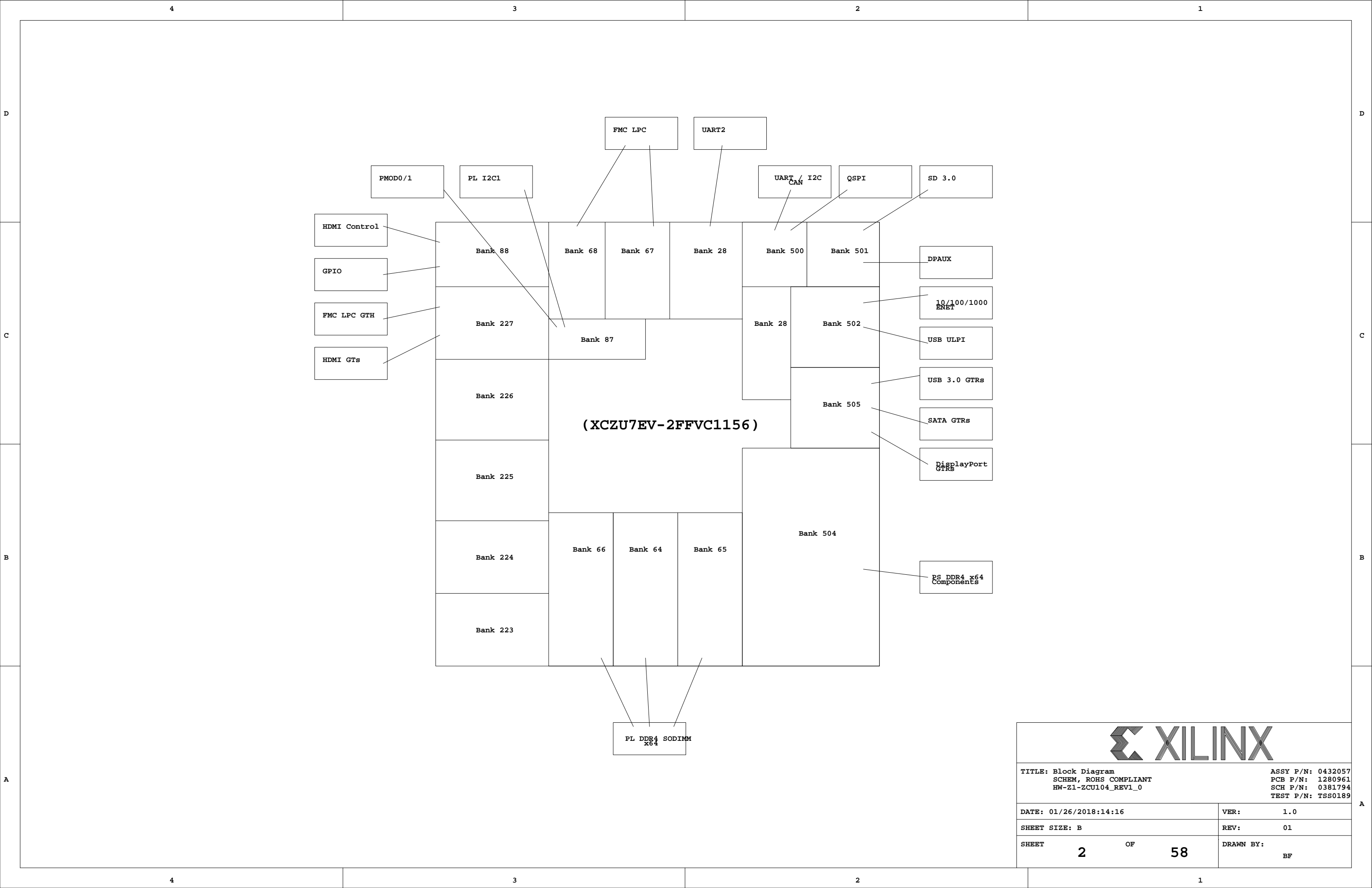
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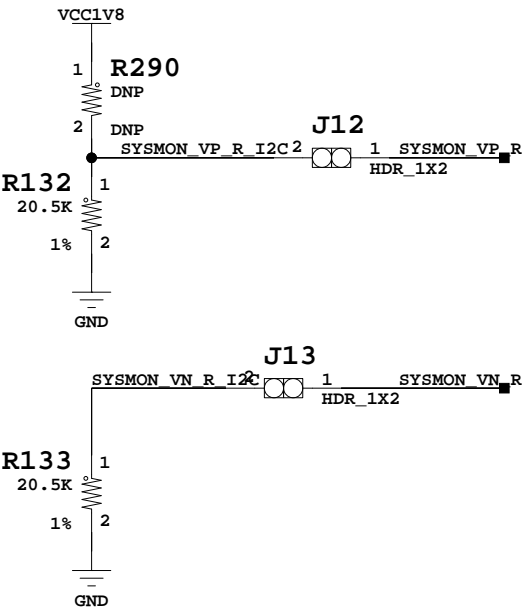
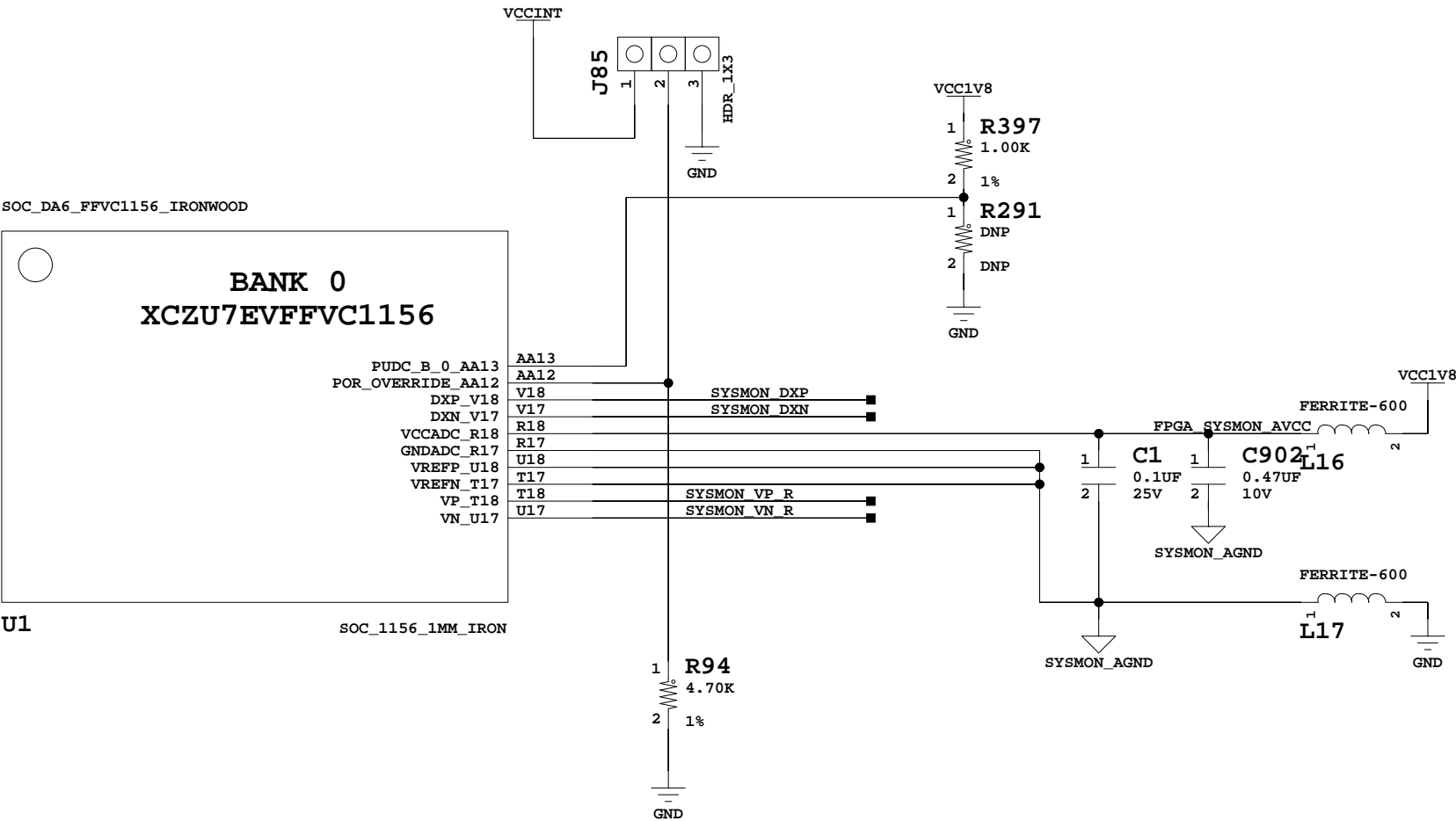
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TITLE: Title Page		ASSY P/N: 0432057	
SCHEM, ROHS COMPLIANT		PCB P/N: 1280961	
HW-Z1-ZCU104_REV1_0		SCH P/N: 0381794	
		TEST P/N: TSS0189	
DATE: 01/26/2018:14:16		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	1	OF	58
		DRAWN BY:	BF



			
TITLE: Block Diagram SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:16		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	2	OF	58
		DRAWN BY:	BF

POR_OVERRIDE select
Default: 2-3 GND



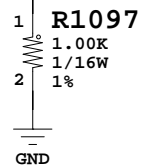
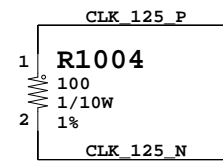
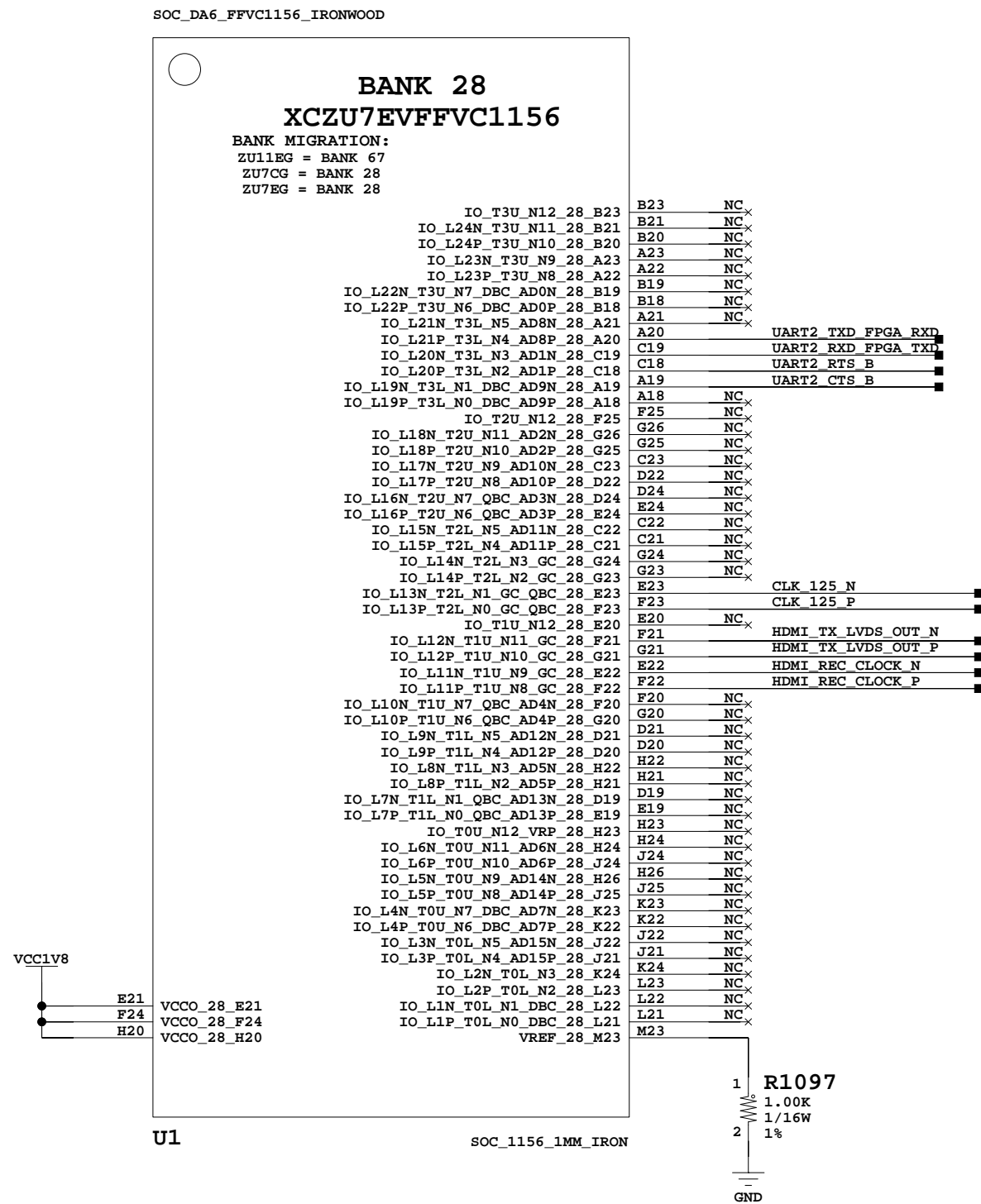
SYSMON I2C Address jumpers

Zynq Bank 0



TITLE: Zynq Bank 0
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0
ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 3 OF 58	DRAWN BY: BF



Zynq Banks 28



TITLE: Zynq Banks 28
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:16

VER:	1.0
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REV:	01
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SHEET	4	OF 58
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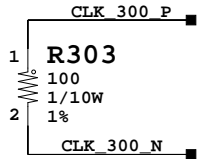
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SOC_DA6_FFVC1156_IRONWOOD

BANK 64
XCZU7EVFFVC1156

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IO_L24N_T3U_N11_64_AD16
IO_L24P_T3U_N10_64_AD17
IO_L23N_T3U_N9_64_AB14
IO_L23P_T3U_N8_64_AA14
IO_L22N_T3U_N7_DBC_AD0N_64_AA15
IO_L22P_T3U_N6_DBC_AD0P_64_AA16
IO_L21N_T3L_N5_AD8N_64_AB15
IO_L21P_T3L_N4_AD8P_64_AB16
IO_L20N_T3L_N3_AD1N_64_AC16
IO_L20P_T3L_N2_AD1P_64_AC17
IO_L19N_T3L_N1_DBC_AD9N_64_AE15
IO_L19P_T3L_N0_DBC_AD9P_64_AD15
IO_T2U_N12_64_AH16
IO_L18N_T2U_N11_AD2N_64_AG14
IO_L18P_T2U_N10_AD2P_64_AG15
IO_L17N_T2U_N9_AD10N_64_AF15
IO_L17P_T2U_N8_AD10P_64_AF16
IO_L16N_T2U_N7_QBC_AD3N_64_AJ14
IO_L16P_T2U_N6_QBC_AD3P_64_AH14
IO_L15N_T2L_N5_AD11N_64_AF17
IO_L15P_T2L_N4_AD11P_64_AE17
IO_L14N_T2L_N3_GC_64_AG18
IO_L14P_T2L_N2_GC_64_AF18
IO_L13N_T2L_N1_GC_QBC_64_AH17
IO_L13P_T2L_N0_GC_QBC_64_AH18
IO_T1U_N12_64_AL17
IO_L12N_T1U_N11_GC_64_AJ15
IO_L12P_T1U_N10_GC_64_AJ16
IO_L11N_T1U_N9_GC_64_AK17
IO_L11P_T1U_N8_GC_64_AJ17
IO_L10N_T1U_N7_QBC_AD4N_64_AK14
IO_L10P_T1U_N6_QBC_AD4P_64_AK15
IO_L9N_T1L_N5_AD12N_64_AL18
IO_L9P_T1L_N4_AD12P_64_AK18
IO_L8N_T1L_N3_AD5N_64_AL15
IO_L8P_T1L_N2_AD5P_64_AL16
IO_L7N_T1L_N1_QBC_AD13N_64_AM15
IO_L7P_T1L_N0_QBC_AD13P_64_AM16
IO_T0U_N12_VRP_64_AP14
IO_L6N_T0U_N11_AD6N_64_AN16
IO_L6P_T0U_N10_AD6P_64_AN17
IO_L5N_T0U_N9_AD14N_64_AP15
IO_L5P_T0U_N8_AD14P_64_AP16
IO_L4N_T0U_N7_DBC_AD7N_64_AM14
IO_L4P_T0U_N6_DBC_AD7P_64_AM14
IO_L3N_T0L_N5_AD15N_64_AN18
IO_L3P_T0L_N4_AD15P_64_AM18
IO_L2N_T0L_N3_64_AP13
IO_L2P_T0L_N2_64_AN13
IO_L1N_T0L_N1_DBC_64_AP17
IO_L1P_T0L_N0_DBC_64_AP18
VREF_64_AG16

AA17 NC
AD16 DDR4 SODIMM PARITY
AD17 DDR4 SODIMM CKE
AB14 DDR4 SODIMM RESET
AA14 DDR4 SODIMM CAS
AA15 DDR4 SODIMM CS0
AA16 DDR4 SODIMM WE
AB15 DDR4 SODIMM ALERT
AB16 DDR4 SODIMM BGI
AC16 DDR4 SODIMM BG0
AC17 DDR4 SODIMM ACT
AE15 DDR4 SODIMM ODT
AD15 DDR4 SODIMM RAS
AH16 DDR4 SODIMM A0
AG14 DDR4 SODIMM A1
AG15 DDR4 SODIMM A2
AF15 DDR4 SODIMM A3
AF16 DDR4 SODIMM A4
AJ14 DDR4 SODIMM A5
AH14 DDR4 SODIMM A6
AF17 DDR4 SODIMM A7
AE17 NC
AG18 DDR4 SODIMM CK0
AF18 DDR4 SODIMM CK0
AH17 CLK 300 N
AH18 CLK 300 P
AL17 DDR4 SODIMM CS1
AJ15 DDR4 SODIMM CK1
AJ16 DDR4 SODIMM CK1
AK17 DDR4 SODIMM A8
AJ17 DDR4 SODIMM A9
AK14 DDR4 SODIMM A10
AK15 DDR4 SODIMM A11
AL18 DDR4 SODIMM A12
AK18 DDR4 SODIMM A13
AL15 DDR4 SODIMM BA0
AL16 DDR4 SODIMM BA1
AM15 DDR4 SODIMM CKEL
AM16 DDR4 SODIMM ODT
AP14 VRP 64
AN16 DDR4 SODIMM CS3
AN17 DDR4 SODIMM CS2
AP15 NC
AP16 NC
AN14 NC
AM14 NC
AN18 NC
AM18 NC
AP13 NC
AN13 NC
AP17 NC
AP18 NC
AG16



VCC1V2

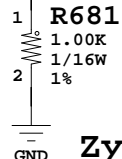
AF19
AG22
AK21

SOC_DA6_FFVC1156_IRONWOOD

BANK 65
XCZU7EVFFVC1156

IO_T3U_N12_65_AE22
IO_L24N_T3U_N11_PERSTN0_65_AA20
IO_L24P_T3U_N10_PERSTN1_I2C_SDA_65_AA19
IO_L23N_T3U_N9_65_AD19
IO_L23P_T3U_N8_I2C_SCLK_65_AC18
IO_L22N_T3U_N7_DBC_AD0N_65_AB18
IO_L22P_T3U_N6_DBC_AD0P_65_AA18
IO_L21N_T3L_N5_AD8N_65_AE20
IO_L21P_T3L_N4_AD8P_65_AD20
IO_L20N_T3L_N3_AD1N_65_AC19
IO_L20P_T3L_N2_AD1P_65_AB19
IO_L19N_T3L_N1_DBC_AD9N_65_AE19
IO_L19P_T3L_N0_DBC_AD9P_65_AE18
IO_T2U_N12_65_AF20
IO_L18N_T2U_N11_AD2N_65_AE24
IO_L18P_T2U_N10_AD2P_65_AE23
IO_L17N_T2U_N9_AD10N_65_AF22
IO_L17P_T2U_N8_AD10P_65_AF21
IO_L16N_T2U_N7_QBC_AD3N_65_AG23
IO_L16P_T2U_N6_QBC_AD3P_65_AF23
IO_L15N_T2L_N5_AD11N_65_AG20
IO_L15P_T2L_N4_AD11P_65_AG19
IO_L14N_T2L_N3_GC_65_AH21
IO_L14P_T2L_N2_GC_65_AG21
IO_L13N_T2L_N1_GC_QBC_65_AH23
IO_L13P_T2L_N0_GC_QBC_65_AH22
IO_T1U_N12_65_AH19
IO_L12N_T1U_N11_GC_65_AJ22
IO_L12P_T1U_N10_GC_65_AJ21
IO_L11N_T1U_N9_GC_65_AK20
IO_L11P_T1U_N8_GC_65_AJ20
IO_L10N_T1U_N7_QBC_AD4N_65_AK23
IO_L10P_T1U_N6_QBC_AD4P_65_AK22
IO_L9N_T1L_N5_AD12N_65_AK19
IO_L9P_T1L_N4_AD12P_65_AJ19
IO_L8N_T1L_N3_AD5N_65_AL23
IO_L8P_T1L_N2_AD5P_65_AL22
IO_L7N_T1L_N1_QBC_AD13N_65_AL21
IO_L7P_T1L_N0_QBC_AD13P_65_AL20
IO_T0U_N12_VRP_65_AM20
IO_L6N_T0U_N11_AD6N_65_AN23
IO_L6P_T0U_N10_AD6P_65_AM23
IO_L5N_T0U_N9_AD14N_65_AP23
IO_L5P_T0U_N8_AD14P_65_AN22
IO_L4N_T0U_N7_DBC_AD7N_65_AN21
IO_L4P_T0U_N6_DBC_AD7P_SMBALERT_65_AM21
IO_L3N_T0L_N5_AD15N_65_AP22
IO_L3P_T0L_N4_AD15P_65_AP21
IO_L2N_T0L_N3_65_AN19
IO_L2P_T0L_N2_65_AM19
IO_L1N_T0L_N1_DBC_65_AP20
IO_L1P_T0L_N0_DBC_65_AP19
VREF_65_AB20

AE22 NC
AA20 DDR4 SODIMM DQ8
AA19 DDR4 SODIMM DQ9
AD19 DDR4 SODIMM DQ10
AC18 DDR4 SODIMM DQ11
AB18 DDR4 SODIMM DQ81
AA18 DDR4 SODIMM DQ81
AE20 DDR4 SODIMM DQ12
AD20 DDR4 SODIMM DQ13
AC19 DDR4 SODIMM DQ14
AB19 DDR4 SODIMM DQ15
AE19 NC
AE18 DDR4 SODIMM DM1
AF20 NC
AE24 DDR4 SODIMM DQ0
AE23 DDR4 SODIMM DQ1
AF22 DDR4 SODIMM DQ2
AF21 DDR4 SODIMM DQ3
AG23 DDR4 SODIMM DQ80
AF23 DDR4 SODIMM DQ80
AG20 DDR4 SODIMM DQ4
AG19 DDR4 SODIMM DQ5
AH21 DDR4 SODIMM DQ6
AG21 DDR4 SODIMM DQ7
AH23 NC
AH22 DDR4 SODIMM DM0
AH19 NC
AJ22 DDR4 SODIMM DQ16
AJ21 DDR4 SODIMM DQ17
AK20 DDR4 SODIMM DQ18
AJ20 DDR4 SODIMM DQ19
AK23 DDR4 SODIMM DQ82
AK22 DDR4 SODIMM DQ82
AK19 DDR4 SODIMM DQ20
AJ19 DDR4 SODIMM DQ21
AL23 DDR4 SODIMM DQ22
AL22 DDR4 SODIMM DQ23
AL21 NC
AL20 DDR4 SODIMM DM2
AM20 VRP 65
AN23 DDR4 SODIMM DQ24
AM23 DDR4 SODIMM DQ25
AP23 DDR4 SODIMM DQ26
AN22 DDR4 SODIMM DQ27
AN21 DDR4 SODIMM DQ83
AM21 DDR4 SODIMM DQ83
AP22 DDR4 SODIMM DQ28
AP21 DDR4 SODIMM DQ29
AN19 DDR4 SODIMM DQ30
AM19 DDR4 SODIMM DQ31
AP20 NC
AP19 DDR4 SODIMM DM3
AB20



Zynq Banks 64 65



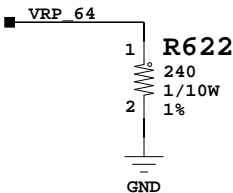
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SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N: 0432057
PCB P/N: 1280961
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DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 5 OF 58	DRAWN BY: BF

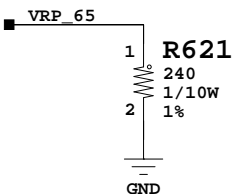
U1

SOC_1156_1MM_IRON



U1

SOC_1156_1MM_IRON



SOC_DA6_FFVC1156_IRONWOOD

BANK 66
XCZU7EVFFVC1156

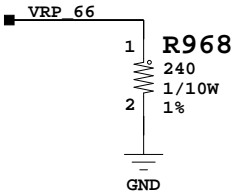
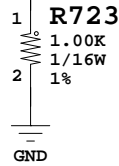
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IO_L24N_T3U_N11_66 AC13
IO_L24P_T3U_N10_66 AB13
IO_L23N_T3U_N9_66 AF12
IO_L23P_T3U_N8_66 AE12
IO_L22N_T3U_N7_DBC_AD0N_66 AD12
IO_L22P_T3U_N6_DBC_AD0P_66 AC12
IO_L21N_T3L_N5_AD8N_66 AF13
IO_L21P_T3L_N4_AD8P_66 AE13
IO_L20N_T3L_N3_AD1N_66 AE14
IO_L20P_T3L_N2_AD1P_66 AD14
IO_L19N_T3L_N1_DBC_AD9N_66 AF10
IO_L19P_T3L_N0_DBC_AD9P_66 AF11
IO_T2U_N12_66 AH8
IO_L18N_T2U_N11_AD2N_66 AG8
IO_L18P_T2U_N10_AD2P_66 AF8
IO_L17N_T2U_N9_AD10N_66 AG10
IO_L17P_T2U_N8_AD10P_66 AG11
IO_L16N_T2U_N7_QBC_AD3N_66 AH9
IO_L16P_T2U_N6_QBC_AD3P_66 AG9
IO_L15N_T2L_N5_AD11N_66 AH13
IO_L15P_T2L_N4_AD11P_66 AG13
IO_L14N_T2L_N3_GC_66 AJ11
IO_L14P_T2L_N2_GC_66 AH11
IO_L13N_T2L_N1_GC_QBC_66 AJ12
IO_L13P_T2L_N0_GC_QBC_66 AH12
IO_T1U_N12_66 AM13
IO_L12N_T1U_N11_GC_66 AK9
IO_L12P_T1U_N10_GC_66 AJ9
IO_L11N_T1U_N9_GC_66 AK10
IO_L11P_T1U_N8_GC_66 AJ10
IO_L10N_T1U_N7_QBC_AD4N_66 AL8
IO_L10P_T1U_N6_QBC_AD4P_66 AK8
IO_L9N_T1L_N5_AD12N_66 AL12
IO_L9P_T1L_N4_AD12P_66 AK12
IO_L8N_T1L_N3_AD5N_66 AL10
IO_L8P_T1L_N2_AD5P_66 AL11
IO_L7N_T1L_N1_QBC_AD13N_66 AL13
IO_L7P_T1L_N0_QBC_AD13P_66 AK13
IO_T0U_N12_VRP_66 AP8
IO_L6N_T0U_N11_AD6N_66 AM8
IO_L6P_T0U_N10_AD6P_66 AM9
IO_L5N_T0U_N9_AD14N_66 AM10
IO_L5P_T0U_N8_AD14P_66 AM11
IO_L4N_T0U_N7_DBC_AD7N_66 AN8
IO_L4P_T0U_N6_DBC_AD7P_66 AN9
IO_L3N_T0L_N5_AD15N_66 AP11
IO_L3P_T0L_N4_AD15P_66 AN11
IO_L2N_T0L_N3_66 AP9
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AF12
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AE14
AD14
AF10 NC
AF11
AH8 NC
AG8
AF8
AG10
AG11
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AH13
AG13
AJ11
AH11
AJ12 NC
AH12
AM13 NC
AK9
AJ9
AK10
AJ10
AL8
AK8
AL12
AK12
AL10
AL11
AL13 NC
AK13
AP8
AM8
AM9
AM10
AM11
AN8
AN9
AP11
AN11
AP9
AP10
AP12 NC
AN12
AB12

DDR4_SODIMM_DQ32
DDR4_SODIMM_DQ33
DDR4_SODIMM_DQ34
DDR4_SODIMM_DQ35
DDR4_SODIMM_DQ36
DDR4_SODIMM_DQ37
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VCC1V2
AF9
AG12
AK11
VCCO_66_AF9
VCCO_66_AG12
VCCO_66_AK11

U1 SOC_1156_1MM_IRON



SOC_DA6_FFVC1156_IRONWOOD

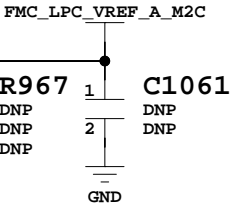
BANK 67
XCZU7EVFFVC1156

BANK MIGRATION:
ZU11EG = BANK 68
ZU7CG = BANK 67
ZU7EG = BANK 67

IO_T3U_N12_67 J20
IO_L24N_T3U_N11_67 L16
IO_L24P_T3U_N10_67 L17
IO_L23N_T3U_N9_67 K18
IO_L23P_T3U_N8_67 K19
IO_L22N_T3U_N7_DBC_AD0N_67 K15
IO_L22P_T3U_N6_DBC_AD0P_67 L15
IO_L21N_T3L_N5_AD8N_67 J17
IO_L21P_T3L_N4_AD8P_67 K17
IO_L20N_T3L_N3_AD1N_67 J15
IO_L20P_T3L_N2_AD1P_67 J16
IO_L19N_T3L_N1_DBC_AD9N_67 K20
IO_L19P_T3L_N0_DBC_AD9P_67 L20
IO_T2U_N12_67 J19
IO_L18N_T2U_N11_AD2N_67 G16
IO_L18P_T2U_N10_AD2P_67 H16
IO_L17N_T2U_N9_AD10N_67 F18
IO_L17P_T2U_N8_AD10P_67 G18
IO_L16N_T2U_N7_QBC_AD3N_67 H17
IO_L16P_T2U_N6_QBC_AD3P_67 H18
IO_L15N_T2L_N5_AD11N_67 G19
IO_L15P_T2L_N4_AD11P_67 H19
IO_L14N_T2L_N3_GC_67 F15
IO_L14P_T2L_N2_GC_67 G15
IO_L13N_T2L_N1_GC_QBC_67 F16
IO_L13P_T2L_N0_GC_QBC_67 F17
IO_T1U_N12_67 E13
IO_L12N_T1U_N11_GC_67 E14
IO_L12P_T1U_N10_GC_67 E15
IO_L11N_T1U_N9_GC_67 D14
IO_L11P_T1U_N8_GC_67 D15
IO_L10N_T1U_N7_QBC_AD4N_67 F13
IO_L10P_T1U_N6_QBC_AD4P_67 G14
IO_L9N_T1L_N5_AD12N_67 E17
IO_L9P_T1L_N4_AD12P_67 E18
IO_L8N_T1L_N3_AD5N_67 C17
IO_L8P_T1L_N2_AD5P_67 D17
IO_L7N_T1L_N1_QBC_AD13N_67 C16
IO_L7P_T1L_N0_QBC_AD13P_67 D16
IO_T0U_N12_VRP_67 C14
IO_L6N_T0U_N11_AD6N_67 C12
IO_L6P_T0U_N10_AD6P_67 C13
IO_L5N_T0U_N9_AD14N_67 A12
IO_L5P_T0U_N8_AD14P_67 A13
IO_L4N_T0U_N7_DBC_AD7N_67 B13
IO_L4P_T0U_N6_DBC_AD7P_67 B14
IO_L3N_T0L_N5_AD15N_67 A14
IO_L3P_T0L_N4_AD15P_67 A15
IO_L2N_T0L_N3_67 B15
IO_L2P_T0L_N2_67 B16
IO_L1N_T0L_N1_DBC_67 A16
IO_L1P_T0L_N0_DBC_67 A17
VREF_67_L18

VADJ_FMC
D13
E16
H15
VCCO_67_D13
VCCO_67_E16
VCCO_67_H15

U1 SOC_1156_1MM_IRON



J20 NC
L16
L17
K18
K19
K15
L15
J17
K17
J15
J16
K20
L20
J19 NC
G16
H16
F18
G18
H17
H18
G19
H19
F15
G15
F16
F17
E13 NC
E14
E15
D14 NC
D15 NC
F13 NC
G14 NC
E17
E18
C17
D17
C16
D16
C14 NC
C12
C13
A12
A13
B13 NC
B14 NC
A14 NC
A15 NC
B15 NC
B16 NC
A16 NC
A17 NC
L18

FMC LPC LA04 N
FMC LPC LA04 P
FMC LPC LA03 N
FMC LPC LA03 P
FMC LPC LA10 N
FMC LPC LA10 P
FMC LPC LA05 N
FMC LPC LA05 P
FMC LPC LA07 N
FMC LPC LA07 P
FMC LPC LA02 N
FMC LPC LA02 P
FMC LPC LA09 N
FMC LPC LA09 P
FMC LPC LA12 N
FMC LPC LA12 P
FMC LPC LA01 CC N
FMC LPC LA01 CC P
FMC LPC LA06 N
FMC LPC LA06 P
FMC LPC LA13 N
FMC LPC LA13 P
FMC LPC LA00 CC N
FMC LPC LA00 CC P
FMC LPC CLK0 M2C N
FMC LPC CLK0 M2C P
FMC LPC LA08 N
FMC LPC LA08 P
FMC LPC LA16 N
FMC LPC LA16 P
FMC LPC LA15 N
FMC LPC LA15 P
FMC LPC LA14 N
FMC LPC LA14 P
FMC LPC LA11 N
FMC LPC LA11 P

Zynq Banks 66 67



TITLE: Zynq Banks 66 67
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0
ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 6 OF 58	DRAWN BY: BF

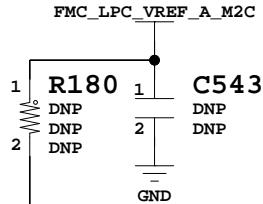
SOC_DA6_FFVC1156_IRONWOOD

BANK 68
XCZU7EVFFVC1156

BANK MIGRATION:
ZU11EG = BANK 69
ZU7CG = BANK 68
ZU7EG = BANK 68

IO_T3U_N12_68_A9
IO_L24N_T3U_N11_68_A11
IO_L24P_T3U_N10_68_B11
IO_L23N_T3U_N9_68_A7
IO_L23P_T3U_N8_68_A8
IO_L22N_T3U_N7_DBC_AD0N_68_A10
IO_L22P_T3U_N6_DBC_AD0P_68_B10
IO_L21N_T3L_N5_AD8N_68_A6
IO_L21P_T3L_N4_AD8P_68_B6
IO_L20N_T3L_N3_AD1N_68_B8
IO_L20P_T3L_N2_AD1P_68_B9
IO_L19N_T3L_N1_DBC_AD9N_68_C6
IO_L19P_T3L_N0_DBC_AD9P_68_C7
IO_T2U_N12_68_G13
IO_L18N_T2U_N11_AD2N_68_C11
IO_L18P_T2U_N10_AD2P_68_D12
IO_L17N_T2U_N9_AD10N_68_E12
IO_L17P_T2U_N8_AD10P_68_F12
IO_L16N_T2U_N7_QBC_AD3N_68_D10
IO_L16P_T2U_N6_QBC_AD3P_68_D11
IO_L15N_T2L_N5_AD11N_68_H12
IO_L15P_T2L_N4_AD11P_68_H13
IO_L14N_T2L_N3_GC_68_E10
IO_L14P_T2L_N2_GC_68_F11
IO_L13N_T2L_N1_GC_QBC_68_G11
IO_L13P_T2L_N0_GC_QBC_68_H11
IO_T1U_N12_68_D7
IO_L12N_T1U_N11_GC_68_F10
IO_L12P_T1U_N10_GC_68_G10
IO_L11N_T1U_N9_GC_68_G9
IO_L11P_T1U_N8_GC_68_H9
IO_L10N_T1U_N7_QBC_AD4N_68_D9
IO_L10P_T1U_N6_QBC_AD4P_68_E9
IO_L9N_T1L_N5_AD12N_68_E8
IO_L9P_T1L_N4_AD12P_68_F8
IO_L8N_T1L_N3_AD5N_68_C8
IO_L8P_T1L_N2_AD5P_68_C9
IO_L7N_T1L_N1_QBC_AD13N_68_E7
IO_L7P_T1L_N0_QBC_AD13P_68_F7
IO_T0U_N12_VRP_68_H14
IO_L6N_T0U_N11_AD6N_68_K13
IO_L6P_T0U_N10_AD6P_68_L14
IO_L5N_T0U_N9_AD14N_68_J14
IO_L5P_T0U_N8_AD14P_68_K14
IO_L4N_T0U_N7_DBC_AD7N_68_J11
IO_L4P_T0U_N6_DBC_AD7P_68_K12
IO_L3N_T0L_N5_AD15N_68_L11
IO_L3P_T0L_N4_AD15P_68_L12
IO_L2N_T0L_N3_68_J10
IO_L2P_T0L_N2_68_K10
IO_L1N_T0L_N1_DBC_68_L13
IO_L1P_T0L_N0_DBC_68_M13
VREF_68_J12

A9 NC
A11 FMC LPC LA23 N
B11 FMC LPC LA23 P
A7 FMC LPC LA27 N
A8 FMC LPC LA27 P
A10 FMC LPC LA21 N
B10 FMC LPC LA21 P
A6 FMC LPC LA24 N
B6 FMC LPC LA24 P
B8 FMC LPC LA26 N
B9 FMC LPC LA26 P
C6 FMC LPC LA25 N
C7 FMC LPC LA25 P
G13 NC
C11 FMC LPC LA19 N
D12 FMC LPC LA19 P
E12 FMC LPC LA20 N
F12 FMC LPC LA20 P
D10 FMC LPC LA18 CC N
D11 FMC LPC LA18 CC P
H12 FMC LPC LA22 N
H13 FMC LPC LA22 P
E10 FMC LPC LA17 CC N
F11 FMC LPC LA17 CC P
G11 NC
H11 NC
D7 NC
F10 FMC LPC CLK1 M2C N
G10 FMC LPC CLK1 M2C P
G9 NC
H9 NC
D9 FMC LPC LA30 N
E9 FMC LPC LA30 P
E8 FMC LPC LA32 N
F8 FMC LPC LA32 P
C8 FMC LPC LA33 N
C9 FMC LPC LA33 P
E7 FMC LPC LA31 N
F7 FMC LPC LA31 P
H14 VRP 68
K13 NC
L14 NC
J14 NC
K14 NC
J11 NC
K12 NC
L11 NC
L12 NC
J10 FMC LPC LA29 N
K10 FMC LPC LA29 P
L13 FMC LPC LA28 N
M13 FMC LPC LA28 P
J12

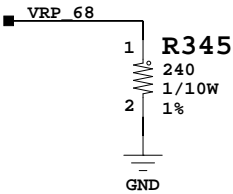


VADJ_FMC

F9 VCCO_68_F9
G12 VCCO_68_G12
K11 VCCO_68_K11

U1

SOC_1156_1MM_IRON



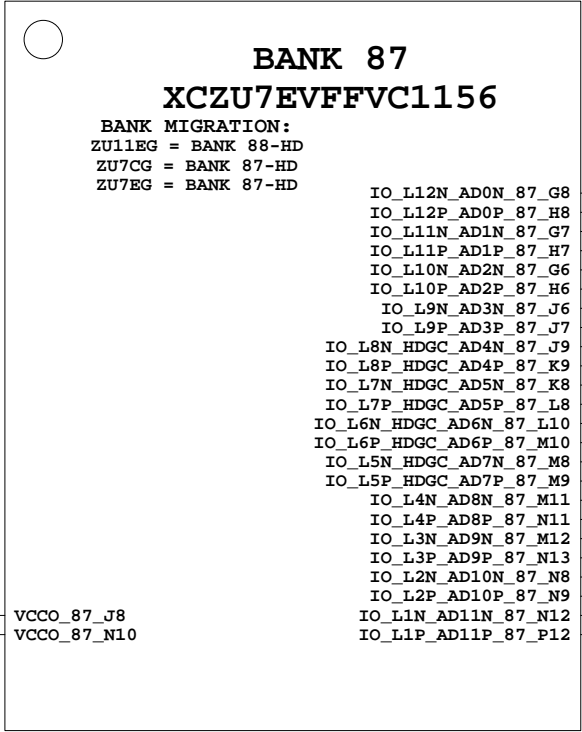
Zynq Banks 68



TITLE: Zynq Banks 68
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0
ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 7 OF 58	DRAWN BY: BF

SOC_DA6_FFVC1156_IRONWOOD

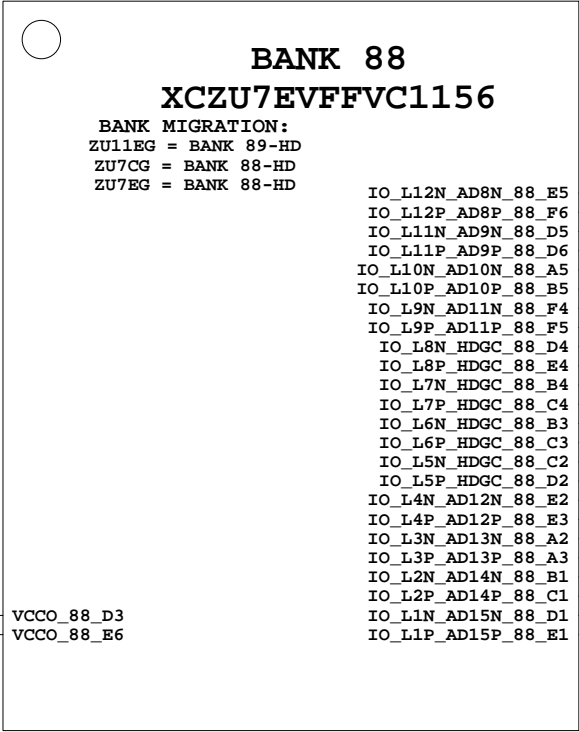


IO_L12N_AD0N_87_G8	G8	PMOD0_0	
IO_L12P_AD0P_87_H8	H8	PMOD0_1	
IO_L11N_AD1N_87_G7	G7	PMOD0_2	
IO_L11P_AD1P_87_H7	H7	PMOD0_3	
IO_L10N_AD2N_87_G6	G6	PMOD0_4	
IO_L10P_AD2P_87_H6	H6	PMOD0_5	
IO_L9N_AD3N_87_J6	J6	PMOD0_6	
IO_L9P_AD3P_87_J7	J7	PMOD0_7	
IO_L8N_HDGC_AD4N_87_J9	J9	PMOD1_0	
IO_L8P_HDGC_AD4P_87_K9	K9	PMOD1_1	
IO_L7N_HDGC_AD5N_87_K8	K8	PMOD1_2	
IO_L7P_HDGC_AD5P_87_L8	L8	PMOD1_3	
IO_L6N_HDGC_AD6N_87_L10	L10	PMOD1_4	
IO_L6P_HDGC_AD6P_87_M10	M10	PMOD1_5	
IO_L5N_HDGC_AD7N_87_M8	M8	PMOD1_6	
IO_L5P_HDGC_AD7P_87_M9	M9	PMOD1_7	
IO_L4N_AD8N_87_M11	M11	CPU RESET	
IO_L4P_AD8P_87_N11	N11	HDMI_8T49N241_L0L	
IO_L3N_AD9N_87_M12	M12	HDMI_8T49N241_RST	
IO_L3P_AD9P_87_N13	N13	HDMI_TX_LS_OE	
IO_L2N_AD10N_87_N8	N8	HDMI_TX_CT_HPD	
IO_L2P_AD10P_87_N9	N9	NC	
IO_L1N_AD11N_87_N12	N12	PL_I2C1_SCL_LS	
IO_L1P_AD11P_87_P12	P12	PL_I2C1_SDA_LS	

U1

SOC_1156_1MM_IRON

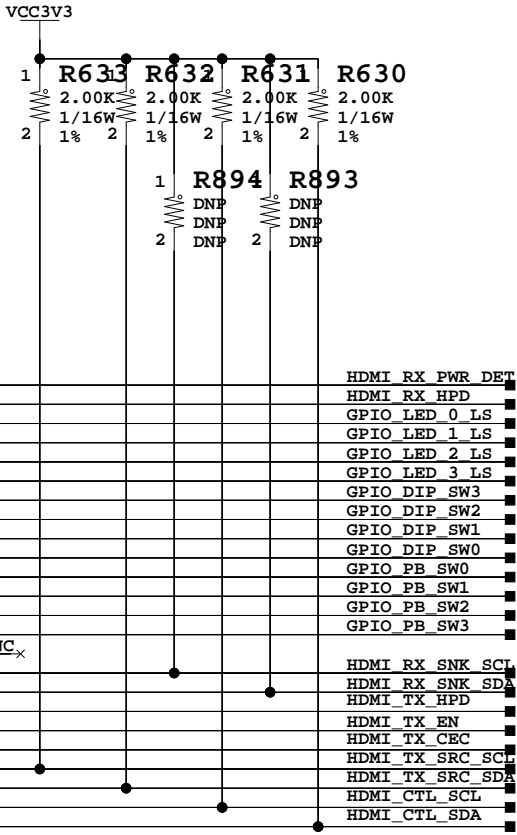
SOC_DA6_FFVC1156_IRONWOOD



IO_L12N_AD8N_88_E5	E5	HDMI_RX_PWR_DET	
IO_L12P_AD8P_88_F6	F6	HDMI_RX_HPD	
IO_L11N_AD9N_88_D5	D5	GPIO_LED_0_LS	
IO_L11P_AD9P_88_D6	D6	GPIO_LED_1_LS	
IO_L10N_AD10N_88_A5	A5	GPIO_LED_2_LS	
IO_L10P_AD10P_88_B5	B5	GPIO_LED_3_LS	
IO_L9N_AD11N_88_F4	F4	GPIO_DIP_SW3	
IO_L9P_AD11P_88_F5	F5	GPIO_DIP_SW2	
IO_L8N_HDGC_88_D4	D4	GPIO_DIP_SW1	
IO_L8P_HDGC_88_E4	E4	GPIO_DIP_SW0	
IO_L7N_HDGC_88_B4	B4	GPIO_PB_SW0	
IO_L7P_HDGC_88_C4	C4	GPIO_PB_SW1	
IO_L6N_HDGC_88_B3	B3	GPIO_PB_SW2	
IO_L6P_HDGC_88_C3	C3	GPIO_PB_SW3	
IO_L5N_HDGC_88_C2	C2	NC	
IO_L5P_HDGC_88_D2	D2	HDMI_RX_SNK_SCL	
IO_L4N_AD12N_88_E2	E2	HDMI_RX_SNK_SDA	
IO_L4P_AD12P_88_E3	E3	HDMI_TX_HPD	
IO_L3N_AD13N_88_A2	A2	HDMI_TX_EN	
IO_L3P_AD13P_88_A3	A3	HDMI_TX_CEC	
IO_L2N_AD14N_88_B1	B1	HDMI_TX_SRC_SCL	
IO_L2P_AD14P_88_C1	C1	HDMI_TX_SRC_SDA	
IO_L1N_AD15N_88_D1	D1	HDMI_CTL_SCL	
IO_L1P_AD15P_88_E1	E1	HDMI_CTL_SDA	

U1

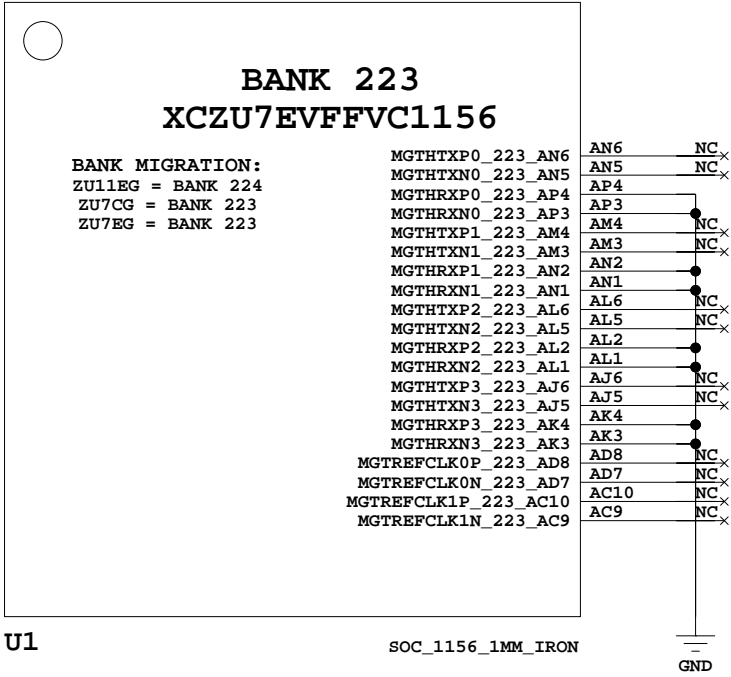
SOC_1156_1MM_IRON



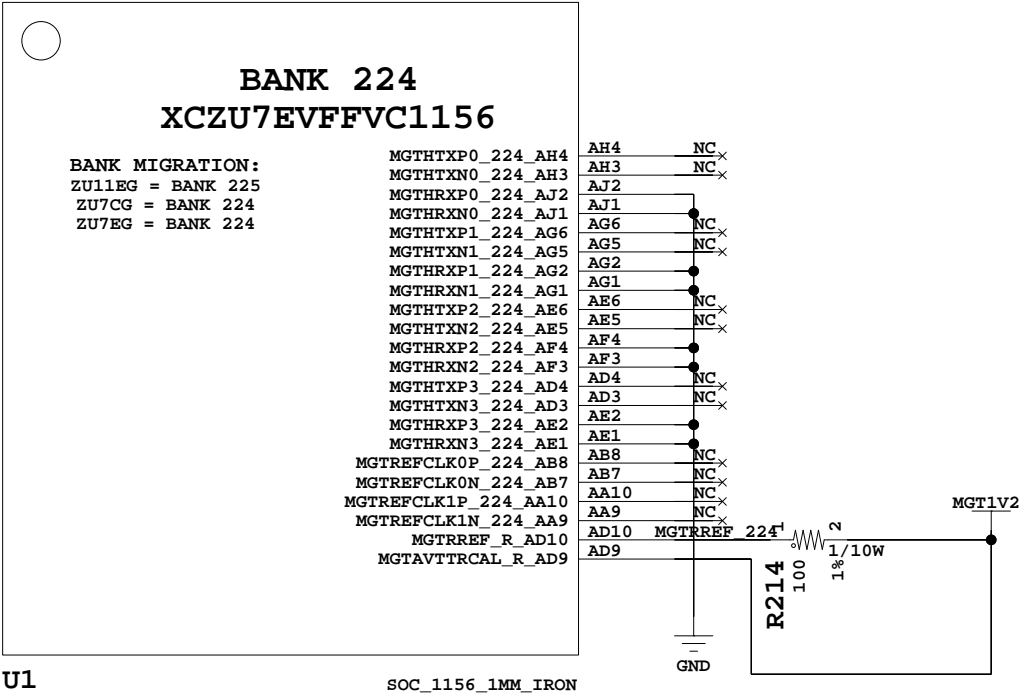
Zynq Banks 87 88

TITLE: Zynq Banks 87 88 SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0	
ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 8 OF 58	DRAWN BY: BF


SOC_DA6_FFVC1156_IRONWOOD

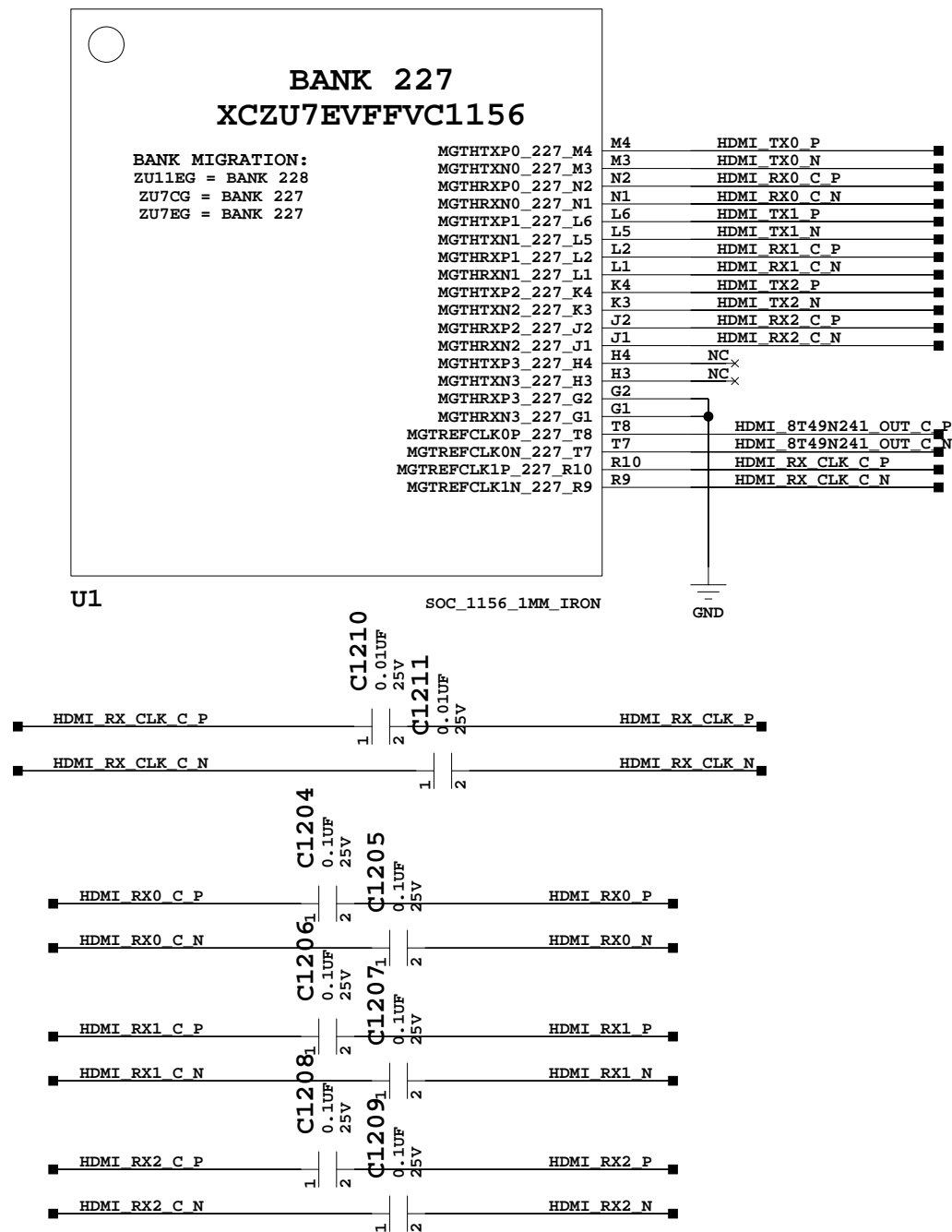
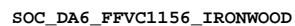
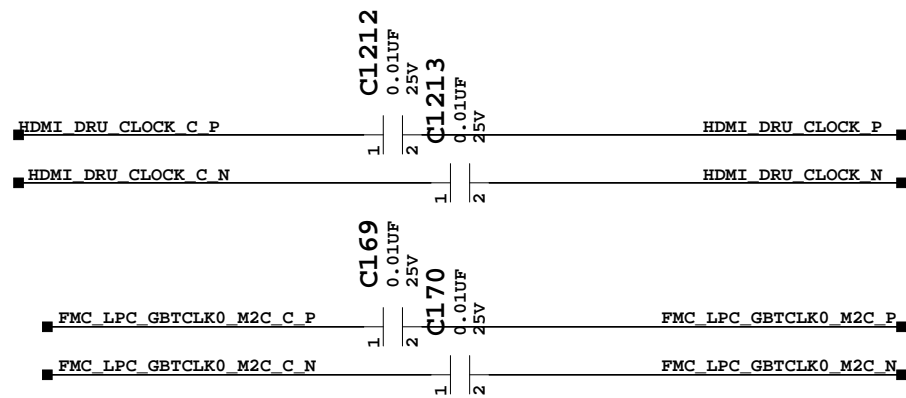
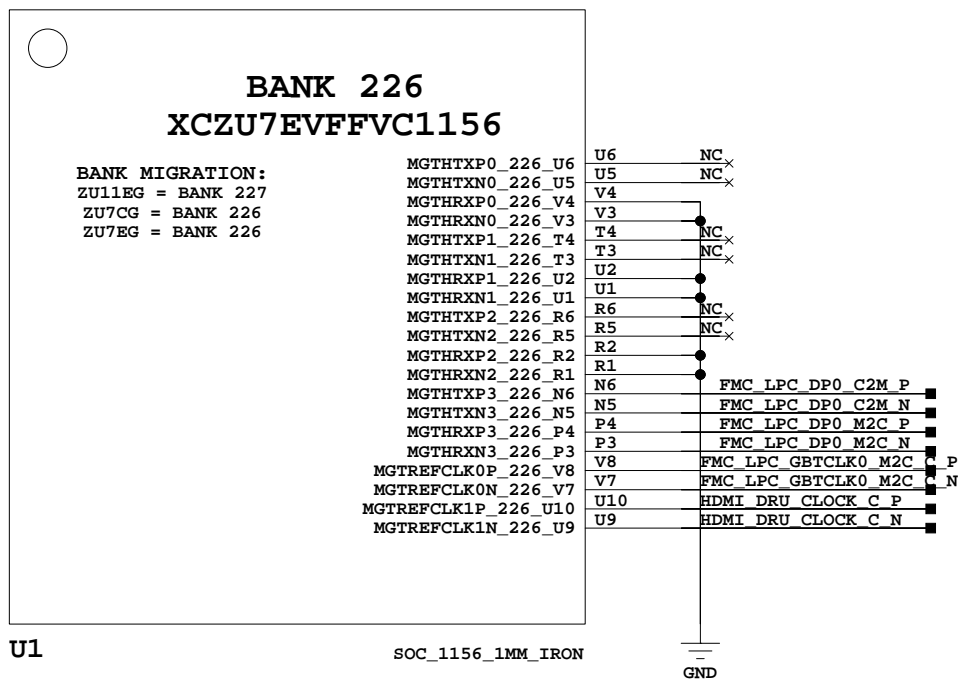
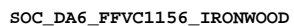
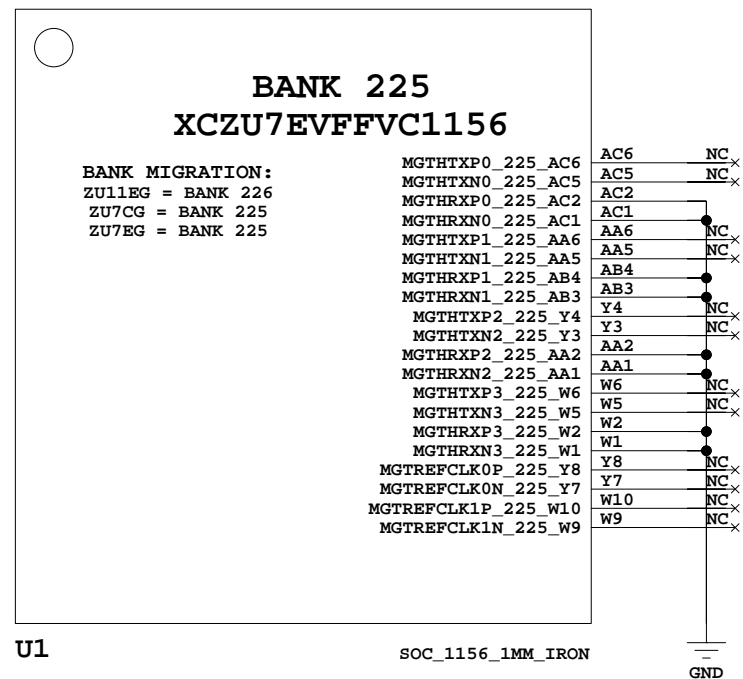
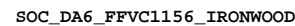


SOC_DA6_FFVC1156_IRONWOOD



Zynq Banks 223 224

			
TITLE: Zynq Banks 223 224 SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:16		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	9	OF	58
		DRAWN BY:	BF



Zynq Banks 225 226 227



TITLE: Zynq Banks 225 226 227
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:16

VER:	1.0
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SHEET SIZE: B

REV: 01

SHEET

OF

10

58

DRAWN BY:

BF

SOC_DA6_FFVC1156_IRONWOOD

BANK 500
XCZU7EVFFVC1156

PS_MIO25_D29	D29	MIO25 CAN RX	
PS_MIO24_E28	E28	MIO24 CAN TX	
PS_MIO23_B29	B29	NC	
PS_MIO22_F28	F28	NC	
PS_MIO21_C28	C28	UART1_TXD MIO21_RXD	
PS_MIO20_E29	E29	UART1_RXD MIO20_TXD	
PS_MIO19_B28	B28	UART0_RXD MIO19_TXD	
PS_MIO18_F27	F27	UART0_TXD MIO18_RXD	
PS_MIO17_C29	C29	MIO17_I2C1_SDA	
PS_MIO16_A28	A28	MIO16_I2C1_SCL	
PS_MIO15_E27	E27	NC	
PS_MIO14_A27	A27	NC	
PS_MIO13_D27	D27	NC	
PS_MIO12_C27	C27	NC	
PS_MIO11_B26	B26	NC	
PS_MIO10_F26	F26	NC	
PS_MIO9_C26	C26	NC	
PS_MIO8_D26	D26	NC	
PS_MIO7_B25	B25	NC	
PS_MIO6_A26	A26	NC	
PS_MIO5_D25	D25	MIO5_QSPI_LWR_CS_B	
PS_MIO4_A25	A25	MIO4_QSPI_LWR_DQ0	
PS_MIO3_E25	E25	MIO3_QSPI_LWR_DQ3	
PS_MIO2_B24	B24	MIO2_QSPI_LWR_DQ2	
PS_MIO1_C24	C24	MIO1_QSPI_LWR_DQ1	
PS_MIO0_A24	A24	MIO0_QSPI_LWR_CLK	

U1

SOC_1156_1MM_IRON

SOC_DA6_FFVC1156_IRONWOOD

BANK 502
XCZU7EVFFVC1156

PS_MIO77_L34	L34	MIO77 ENET MDIO	
PS_MIO76_L33	L33	MIO76 ENET MDC	
PS_MIO75_L30	L30	MIO75 ENET_RX_CTRL	
PS_MIO74_L29	L29	MIO74 ENET_RX_D3	
PS_MIO73_K34	K34	MIO73 ENET_RX_D2	
PS_MIO72_K33	K33	MIO72 ENET_RX_D1	
PS_MIO71_K32	K32	MIO71 ENET_RX_D0	
PS_MIO70_K31	K31	MIO70 ENET_RX_CLK	
PS_MIO69_K30	K30	MIO69 ENET_TX_CTRL	
PS_MIO68_K29	K29	MIO68 ENET_TX_D3	
PS_MIO67_K28	K28	MIO67 ENET_TX_D2	
PS_MIO66_J34	J34	MIO66 ENET_TX_D1	
PS_MIO65_J32	J32	MIO65 ENET_TX_D0	
PS_MIO64_J31	J31	MIO64 ENET_TX_CLK	
PS_MIO63_J30	J30	MIO63 USB_DATA7_R	
PS_MIO62_J29	J29	MIO62 USB_DATA6_R	
PS_MIO61_H34	H34	MIO61 USB_DATA5_R	
PS_MIO60_H33	H33	MIO60 USB_DATA4_R	
PS_MIO59_H32	H32	MIO59 USB_DATA3_R	
PS_MIO58_H31	H31	MIO58 USB_STP_R	
PS_MIO57_H29	H29	MIO57 USB_DATA1_R	
PS_MIO56_G34	G34	MIO56 USB_DATA0_R	
PS_MIO55_G33	G33	MIO55 USB_NXT	
PS_MIO54_G31	G31	MIO54 USB_DATA2_R	
PS_MIO53_G30	G30	MIO53 USB_DIR	
PS_MIO52_G29	G29	MIO52 USB_CLK	

U1

SOC_1156_1MM_IRON

SOC_DA6_FFVC1156_IRONWOOD

BANK 501
XCZU7EVFFVC1156

PS_MIO51_F34	F34	MIO51 SDIO_CLK_R	
PS_MIO50_F33	F33	MIO50 SDIO_CMD_R	
PS_MIO49_F32	F32	MIO49 SDIO_DAT3_R	
PS_MIO48_F31	F31	MIO48 SDIO_DAT2_R	
PS_MIO47_F30	F30	MIO47 SDIO_DAT1_R	
PS_MIO46_E34	E34	MIO46 SDIO_DAT0_R	
PS_MIO45_E33	E33	MIO45 SDIO_DETECT	
PS_MIO44_E32	E32	NC	
PS_MIO43_E30	E30	NC	
PS_MIO42_D34	D34	NC	
PS_MIO41_D32	D32	NC	
PS_MIO40_D31	D31	NC	
PS_MIO39_D30	D30	NC	
PS_MIO38_C34	C34	NC	
PS_MIO37_C33	C33	NC	
PS_MIO36_C32	C32	NC	
PS_MIO35_C31	C31	NC	
PS_MIO34_B34	B34	NC	
PS_MIO33_B33	B33	NC	
PS_MIO32_B31	B31	NC	
PS_MIO31_B30	B30	NC	
PS_MIO30_A33	A33	MIO30_DP_AUX_IN	
PS_MIO29_A32	A32	MIO29_DP_OE	
PS_MIO28_A31	A31	MIO28_DP_HPD	
PS_MIO27_A30	A30	MIO27_DP_AUX_OUT	
PS_MIO26_A29	A29	NC	

U1

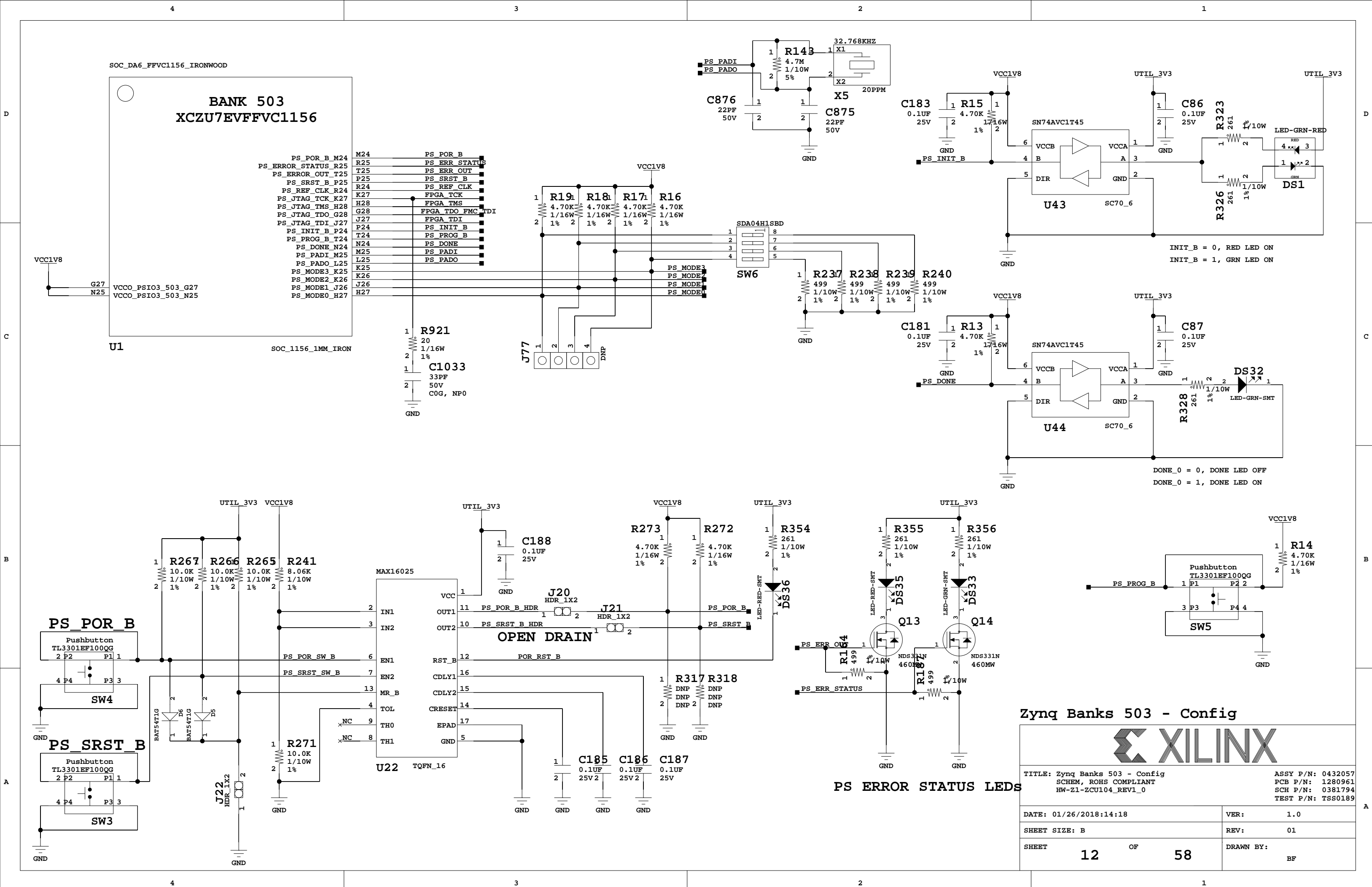
SOC_1156_1MM_IRON

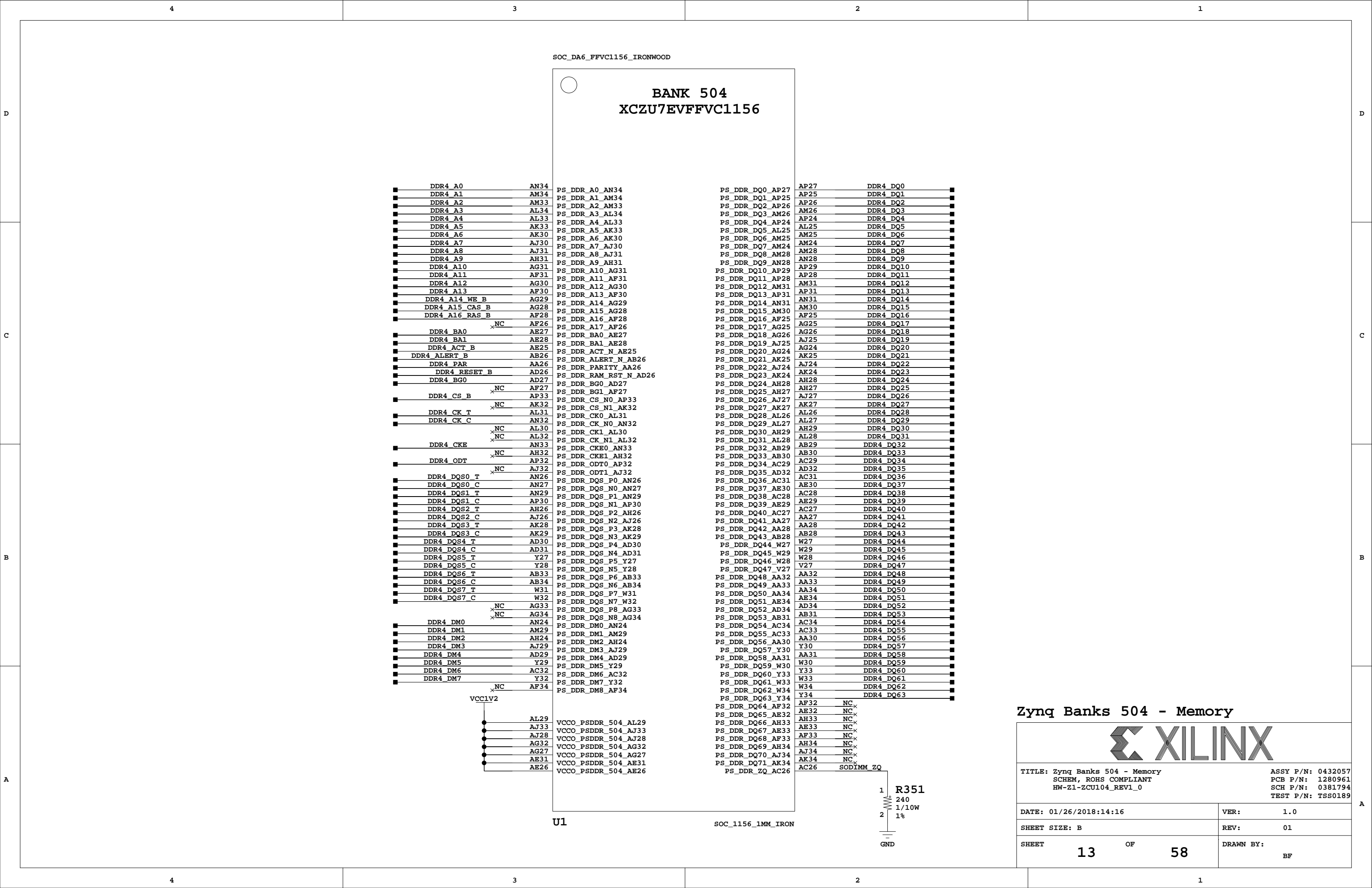
Zynq Banks 500 501 502 - MIO



TITLE: Zynq Banks 500 501 502 - MIO SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0	ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189
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DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 11 OF 58	DRAWN BY: BF

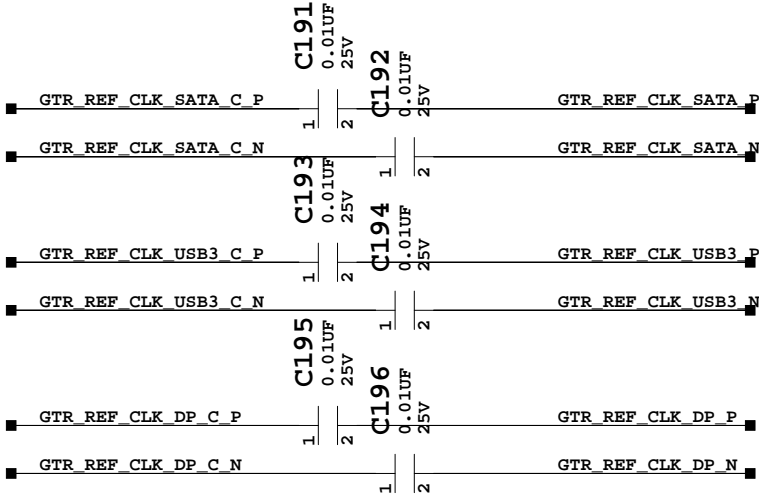
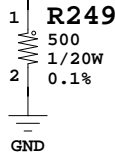




SOC_DA6_FFVC1156_IRONWOOD

BANK 505
XCZU7EVFFVC1156

PS_MGTRTXP0_505_U29	U29	GT0_DP_TX_P
PS_MGTRTXN0_505_U30	U30	GT0_DP_TX_N
PS_MGTRRXXP0_505_U33	U33	NC
PS_MGTRRXN0_505_U34	U34	NC
PS_MGTRTXP1_505_R29	R29	GT1_DP_TX_P
PS_MGTRTXN1_505_R30	R30	GT1_DP_TX_N
PS_MGTRRXXP1_505_T31	T31	NC
PS_MGTRRXN1_505_T32	T32	NC
PS_MGTRTXP2_505_P31	P31	GT2_USB0_TX_P
PS_MGTRTXN2_505_P32	P32	GT2_USB0_TX_N
PS_MGTRRXXP2_505_R33	R33	GT2_USB0_RX_P
PS_MGTRRXN2_505_R34	R34	GT2_USB0_RX_N
PS_MGTRTXP3_505_N29	N29	GT3_SATA1_TX_P
PS_MGTRTXN3_505_N30	N30	GT3_SATA1_TX_N
PS_MGTRRXXP3_505_N33	N33	GT3_SATA1_RX_P
PS_MGTRRXN3_505_N34	N34	GT3_SATA1_RX_N
PS_MGTREFCLK0P_505_T27	T27	NC
PS_MGTREFCLK0N_505_T28	T28	NC
PS_MGTREFCLK1P_505_P27	P27	GTR_REF_CLK_SATA_C_P
PS_MGTREFCLK1N_505_P28	P28	GTR_REF_CLK_SATA_C_N
PS_MGTREFCLK2P_505_M27	M27	GTR_REF_CLK_USB3_C_P
PS_MGTREFCLK2N_505_M28	M28	GTR_REF_CLK_USB3_C_N
PS_MGTREFCLK3P_505_M31	M31	GTR_REF_CLK_DP_C_P
PS_MGTREFCLK3N_505_M32	M32	GTR_REF_CLK_DP_C_N
PS_MGTRREF_505_U31	U31	

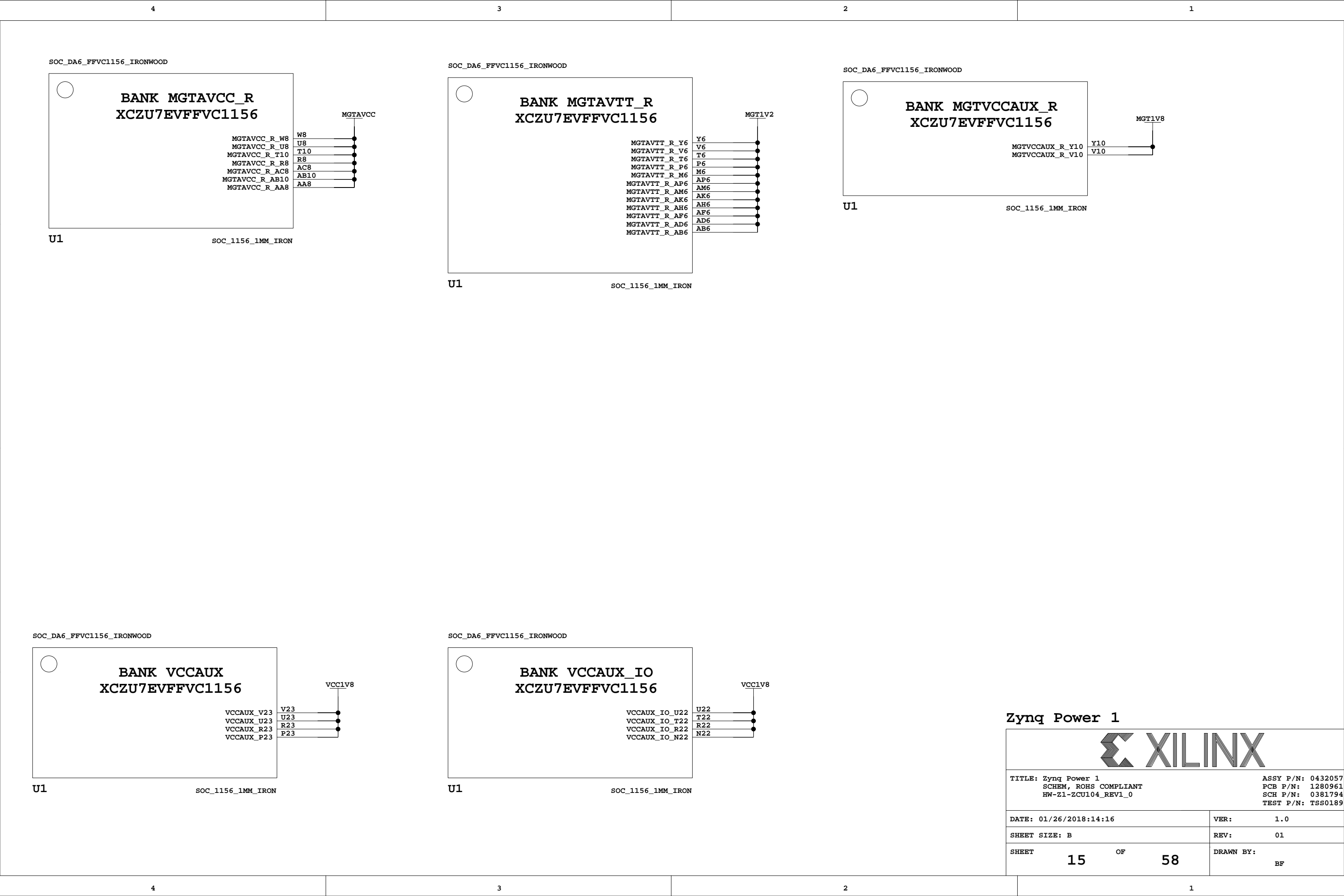


Zynq Banks 505 - GTR



TITLE: Zynq Banks 505 - GTR SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0	ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189
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DATE: 01/26/2018:14:17	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 14 OF 58	DRAWN BY: BF

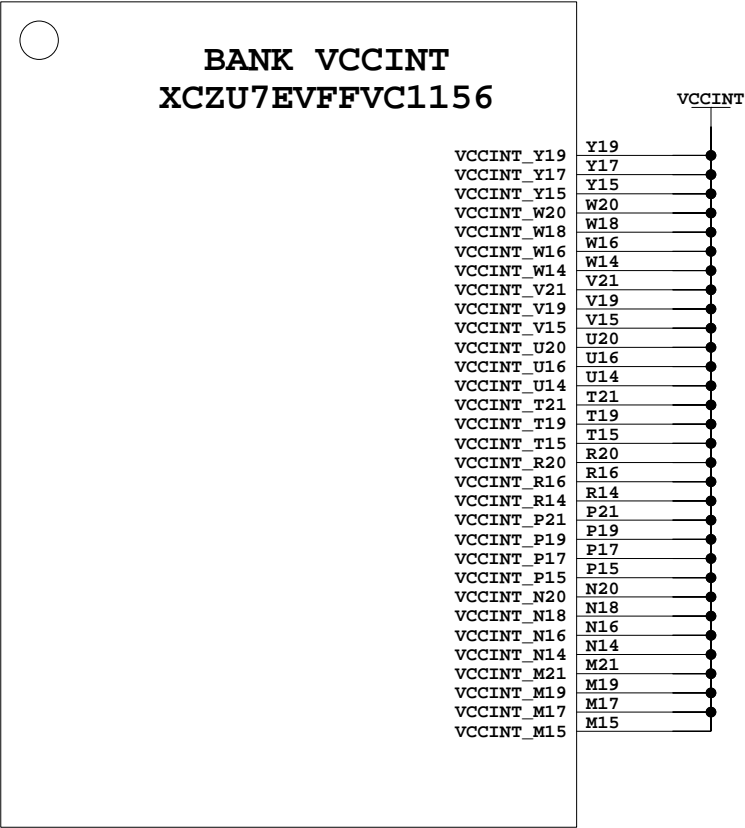


TITLE: Zynq Power 1
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

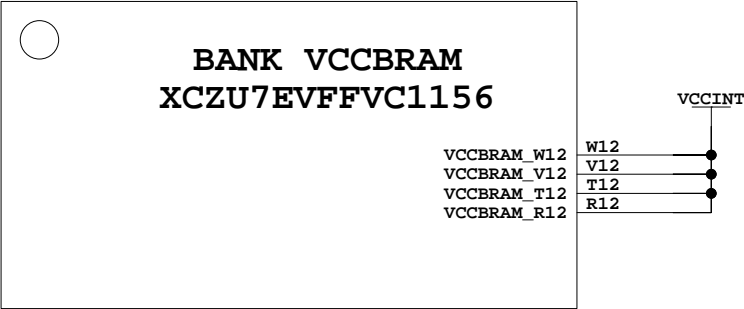
DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 15 OF 58	DRAWN BY: BF

SOC_DA6_FFVC1156_IRONWOOD



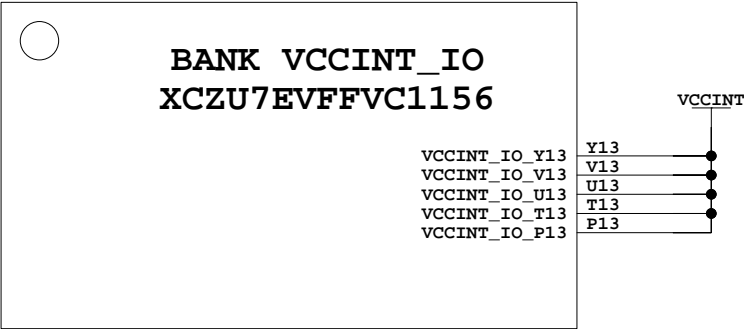
U1 SOC_1156_1MM_IRON

SOC_DA6_FFVC1156_IRONWOOD



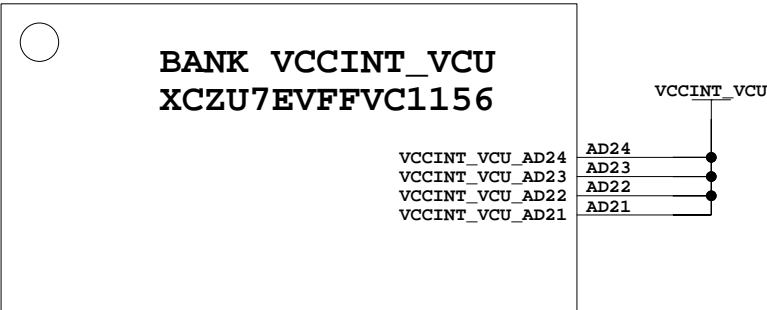
U1 SOC_1156_1MM_IRON

SOC_DA6_FFVC1156_IRONWOOD




U1 SOC_1156_1MM_IRON

SOC_DA6_FFVC1156_IRONWOOD

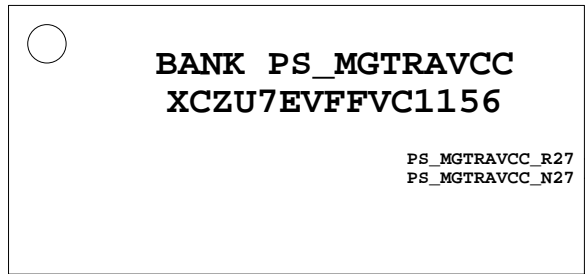


U1 SOC_1156_1MM_IRON

Zynq Power 2

			
TITLE: Zynq Power 2 SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:16		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	16	OF	58
		DRAWN BY:	BF

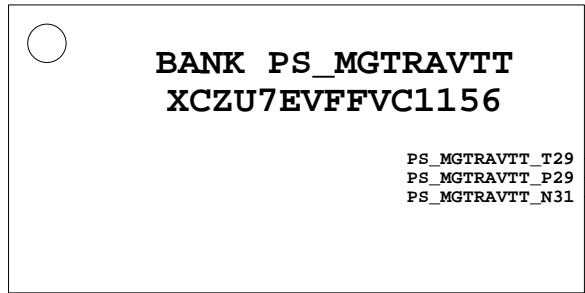
SOC_DA6_FFVC1156_IRONWOOD



U1

SOC_1156_1MM_IRON

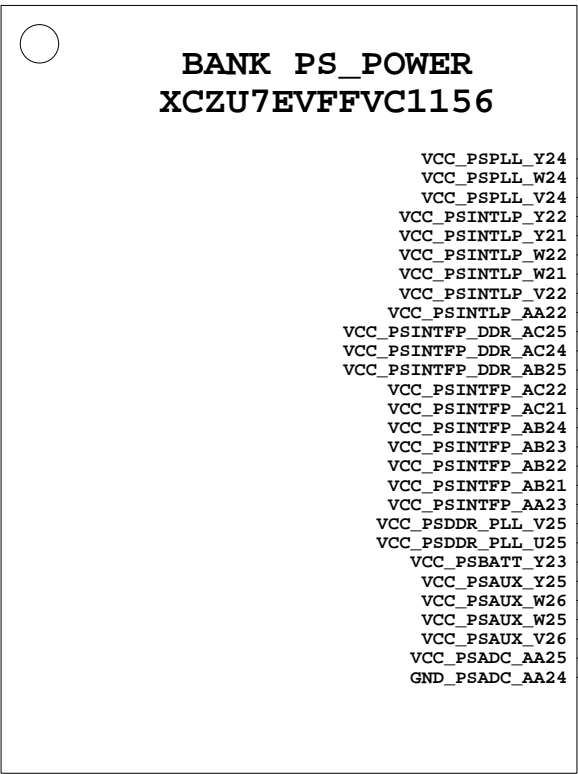
SOC_DA6_FFVC1156_IRONWOOD



U1

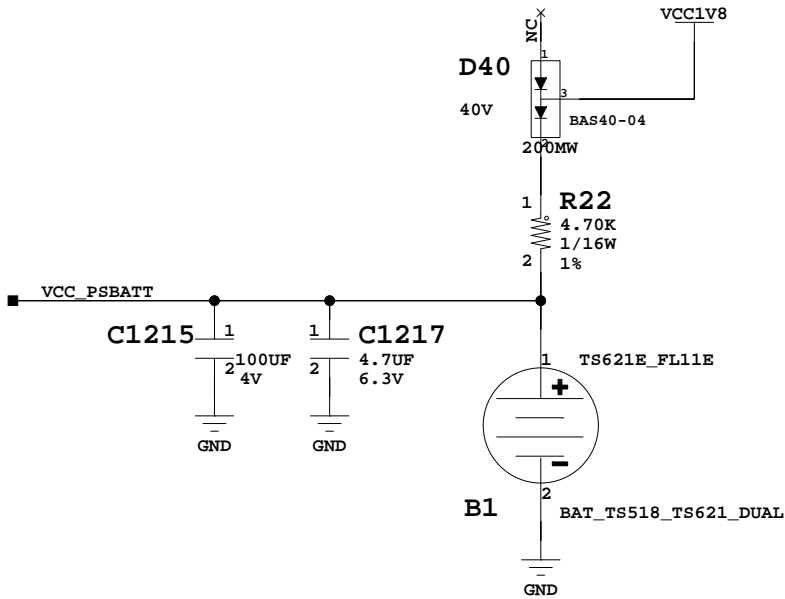
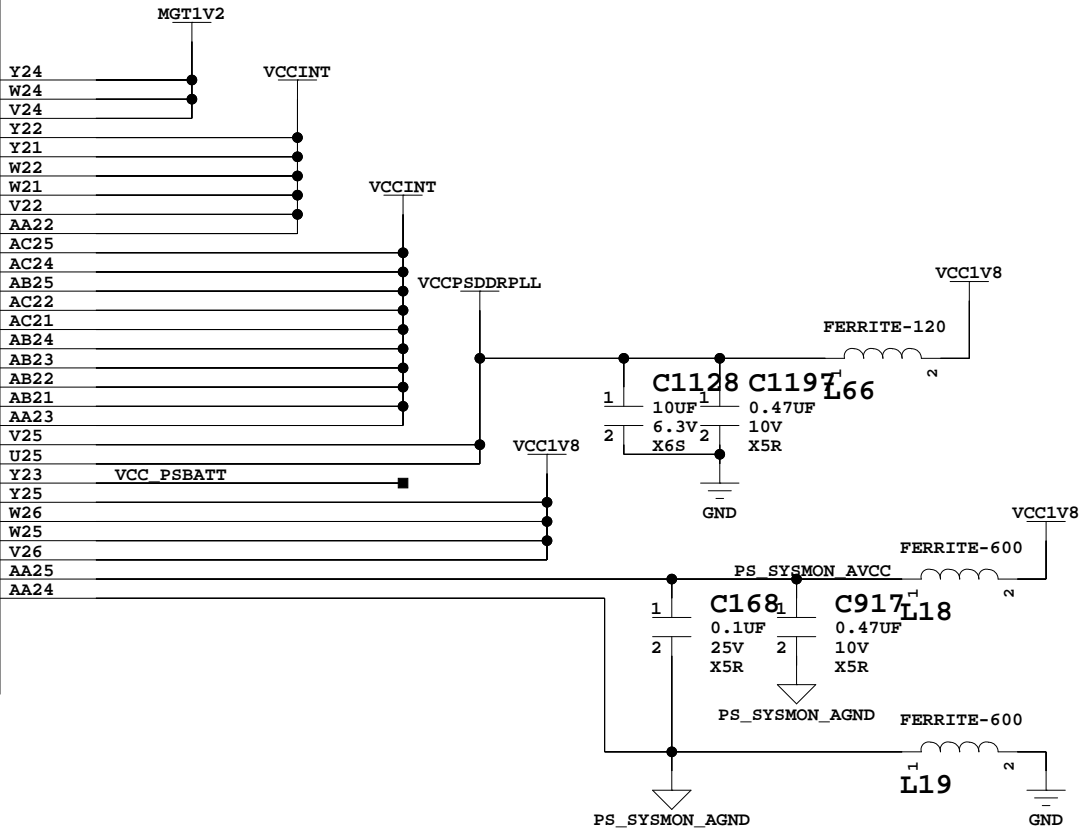
SOC_1156_1MM_IRON

SOC_DA6_FFVC1156_IRONWOOD



U1

SOC_1156_1MM_IRON



Zynq Power 3



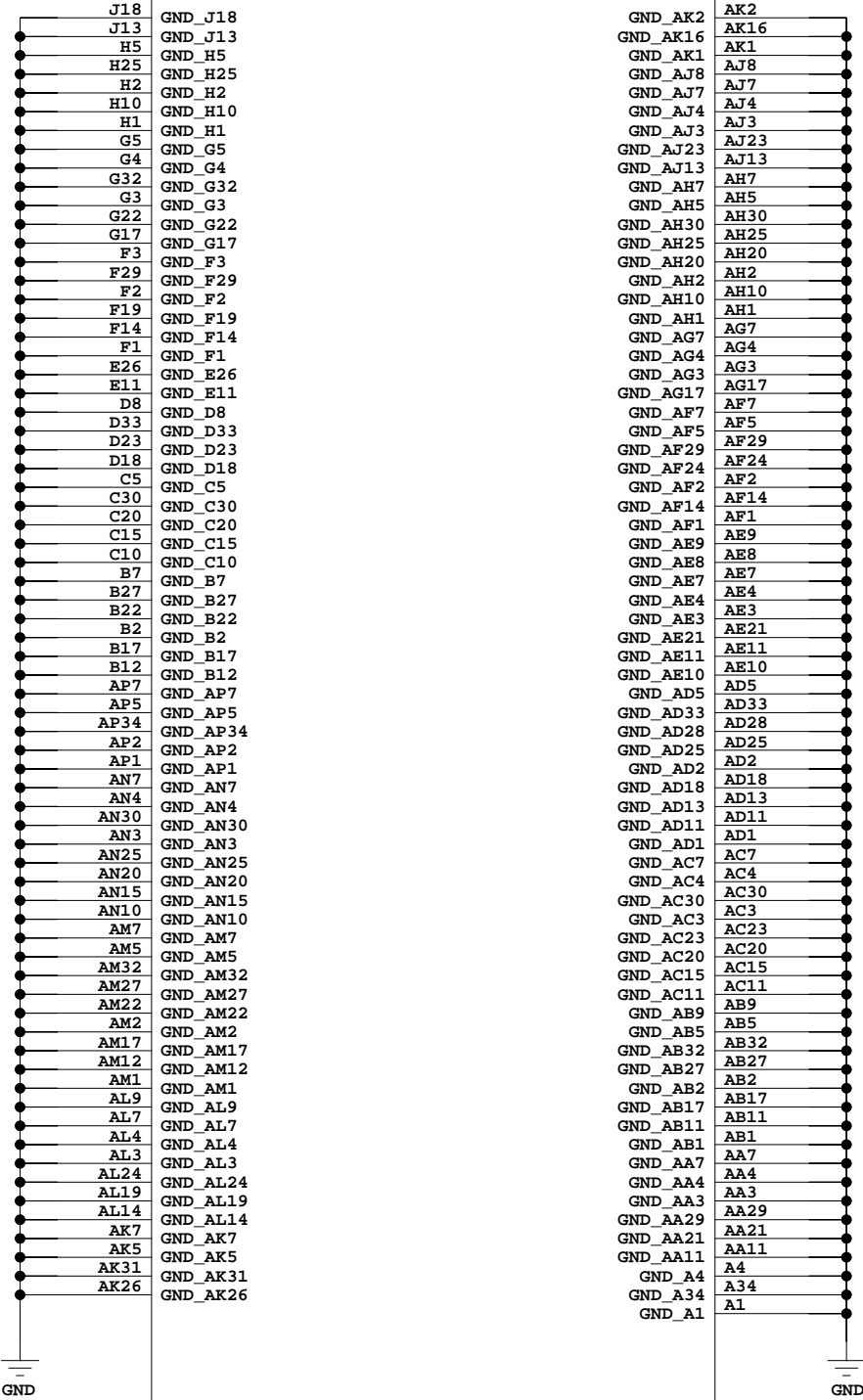
TITLE: Zynq Power 3
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 17 OF 58	DRAWN BY: BF

SOC_DA6_FFVC1156_IRONWOOD

BANK GND1
XCZU7EVFFVC1156

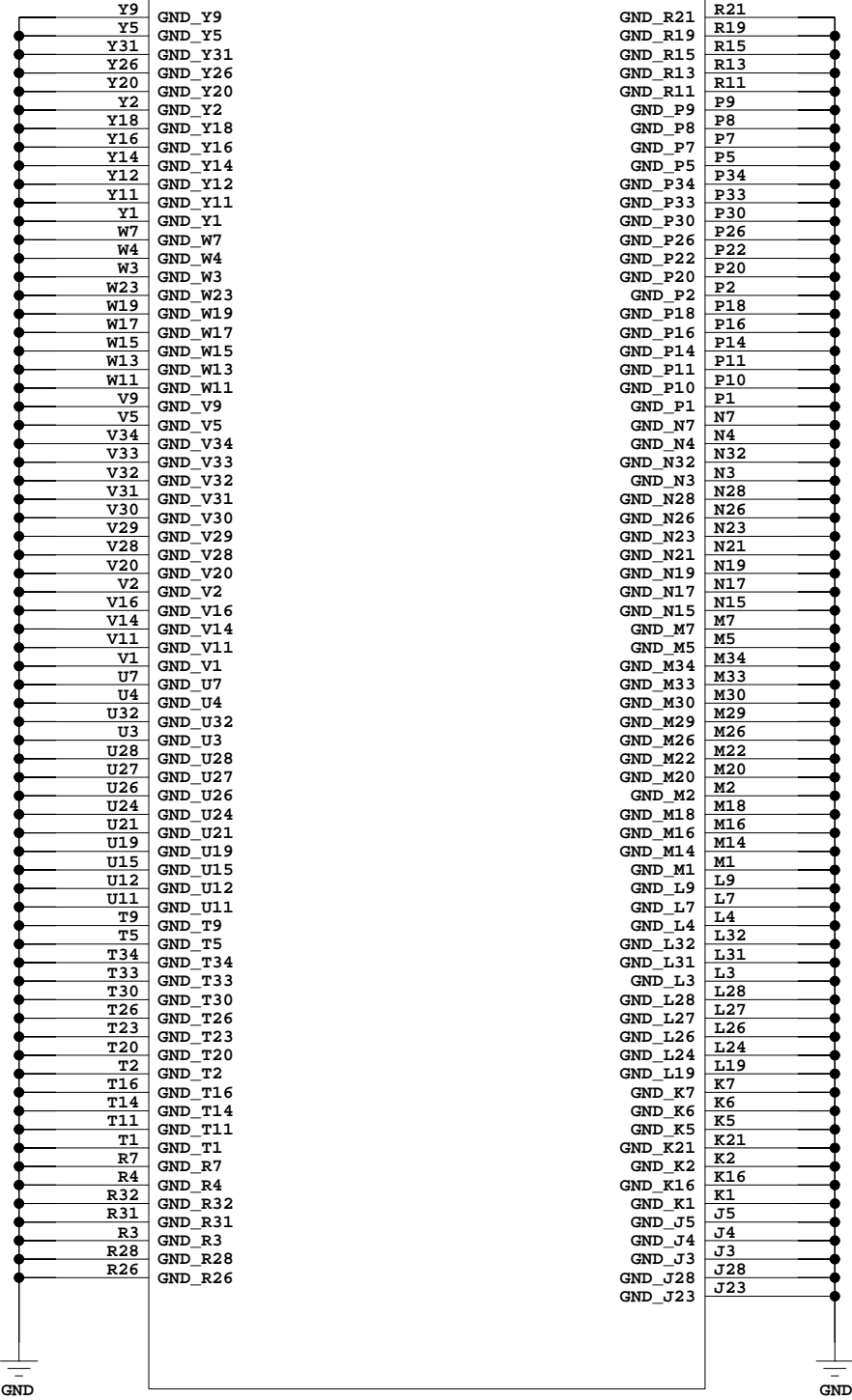


U1

SOC_1156_1MM_IRON

SOC_DA6_FFVC1156_IRONWOOD

BANK GND2
XCZU7EVFFVC1156



U1

SOC_1156_1MM_IRON

Zynq GND

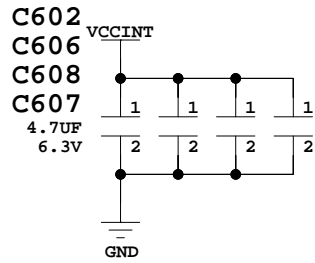
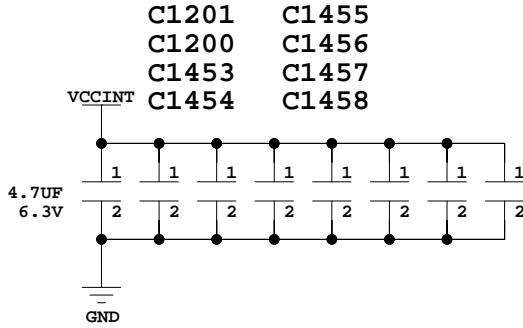
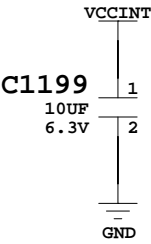
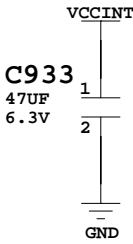
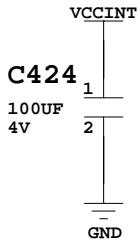
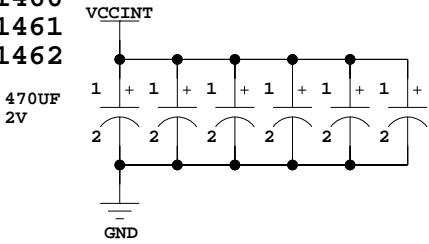


TITLE: Zynq GND SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0	ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189
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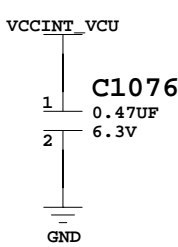
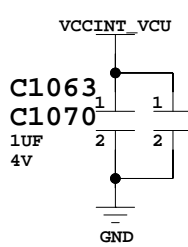
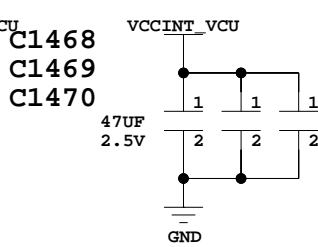
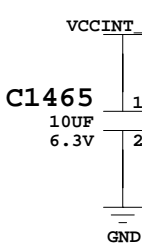
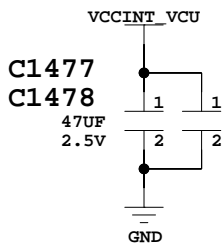
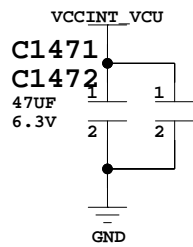
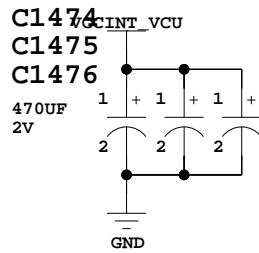
DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 18 OF 58	DRAWN BY: BF

C696 VCCINT / VCCBRAM

C697
C1459
C1460
C1461
C1462



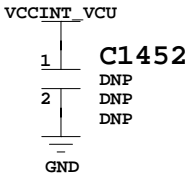
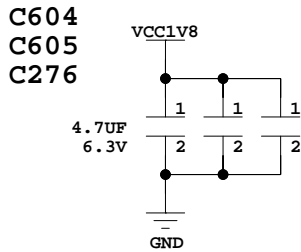
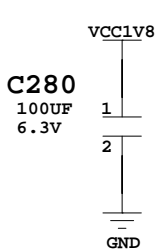
VCCINT_VCU



Place these 0201 capacitors directly under U1 near pins AD24/AD23/AD22/AD21

Provide a dual footprint to also support a 0402 cap

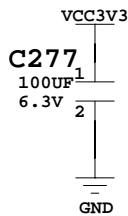
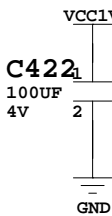
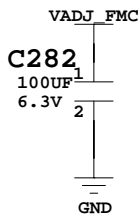
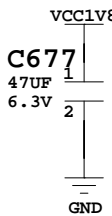
VCCAUX / VCCAUX_IO



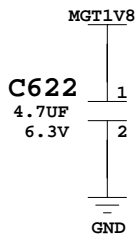
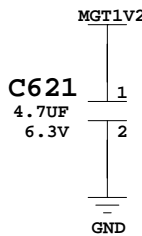
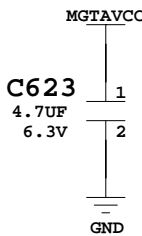
VCCINT_VCU Test Point

Place just outside U1 on bottomside

VCCO BANKS 0 28 64 65 66 67 68 87 88



MGT Power Rails



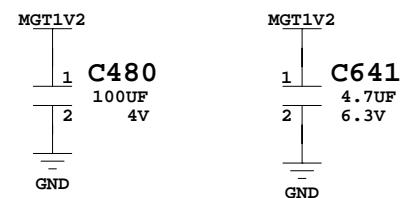
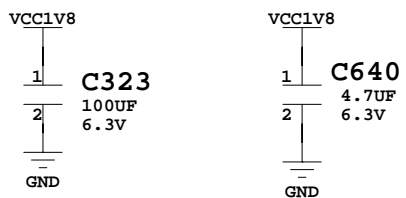
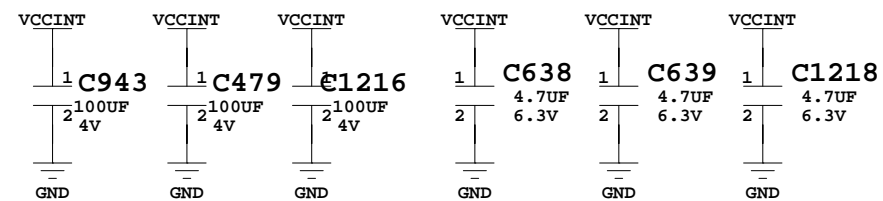
Zynq Decoupling 1



TITLE: Zynq Decoupling 1
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

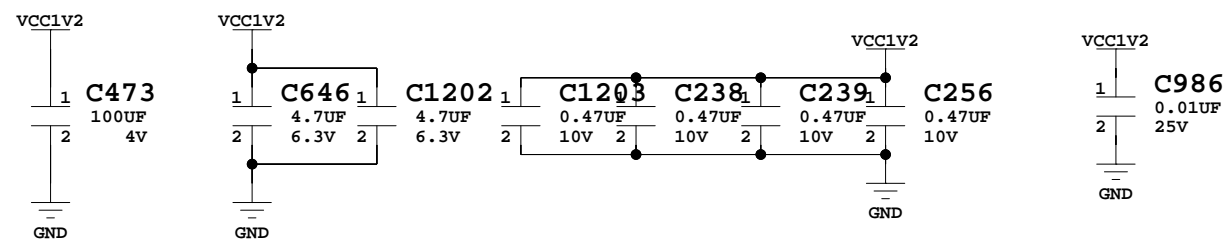
ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 19 OF 58	DRAWN BY: BF



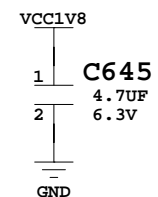
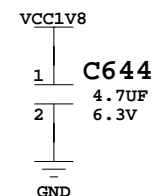
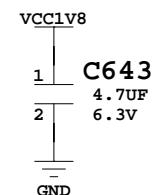
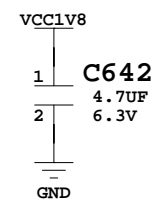
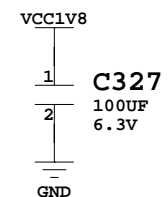
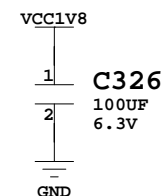
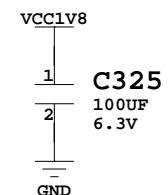
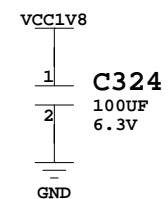
PSAUX

PSPLL



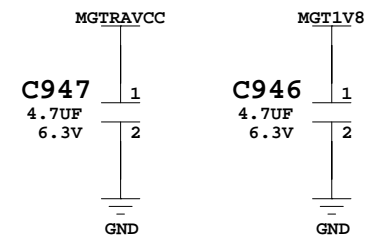
Place this round pad
capacitor directly under U1

PSINTFP_DDR/PSINTFP/PSINTLP



PSIO banks

Bank 505



Zynq Decoupling 2



TITLE: Zynq Decoupling 2
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:17

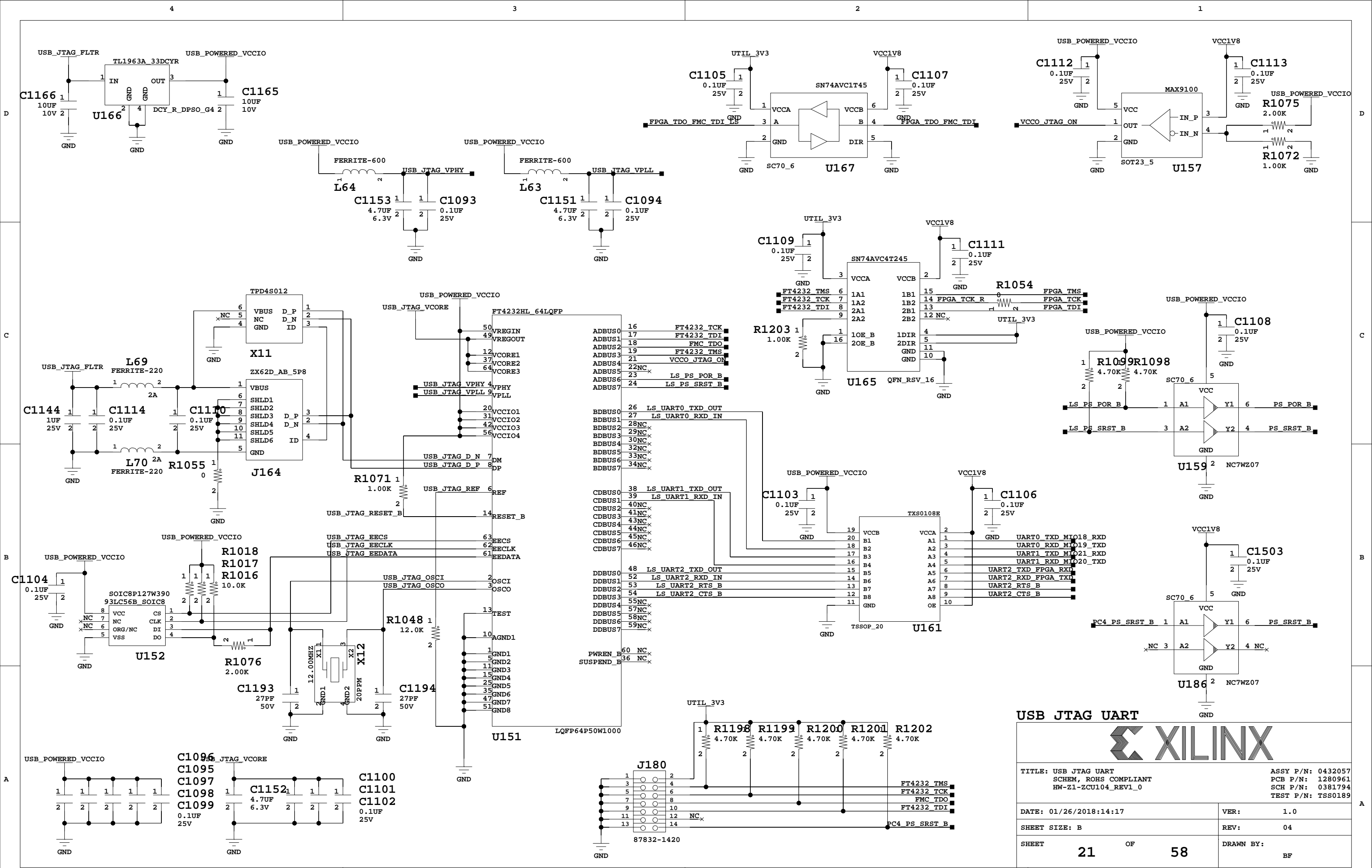
VER:	1.0
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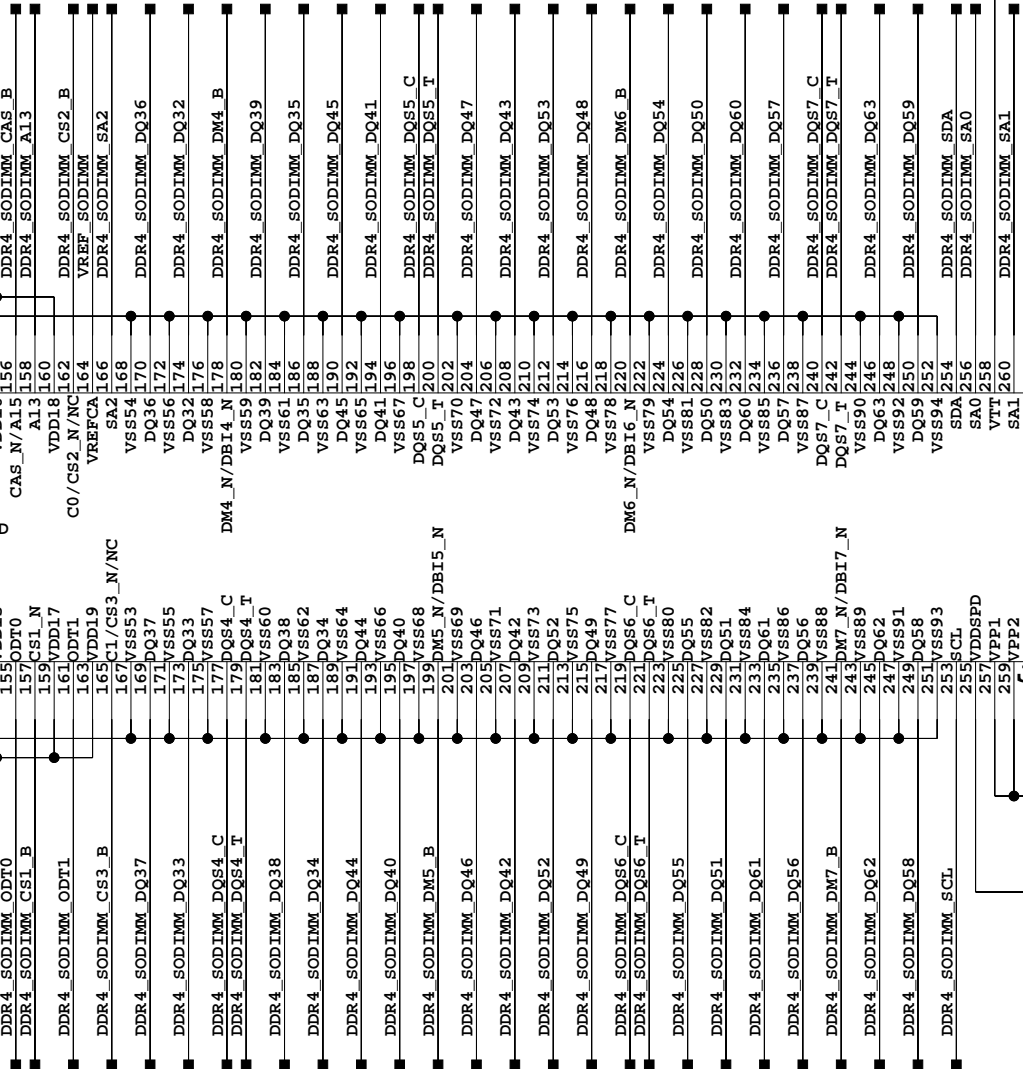
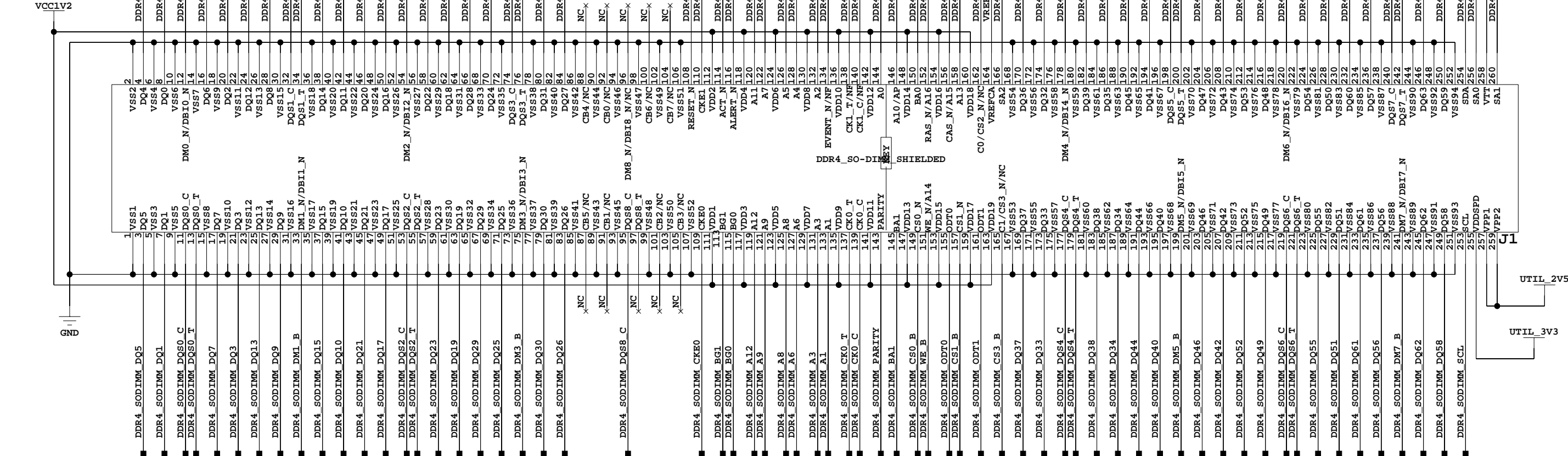
SHEET SIZE: B


REV:	01
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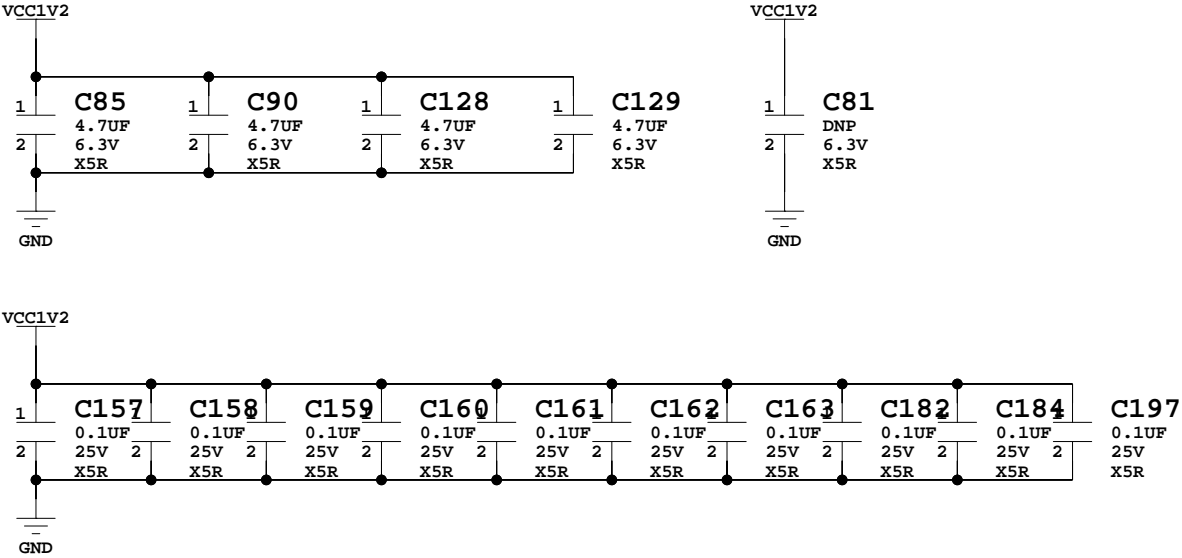
SHEET	20	OF	58
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DRAWN BY: BF





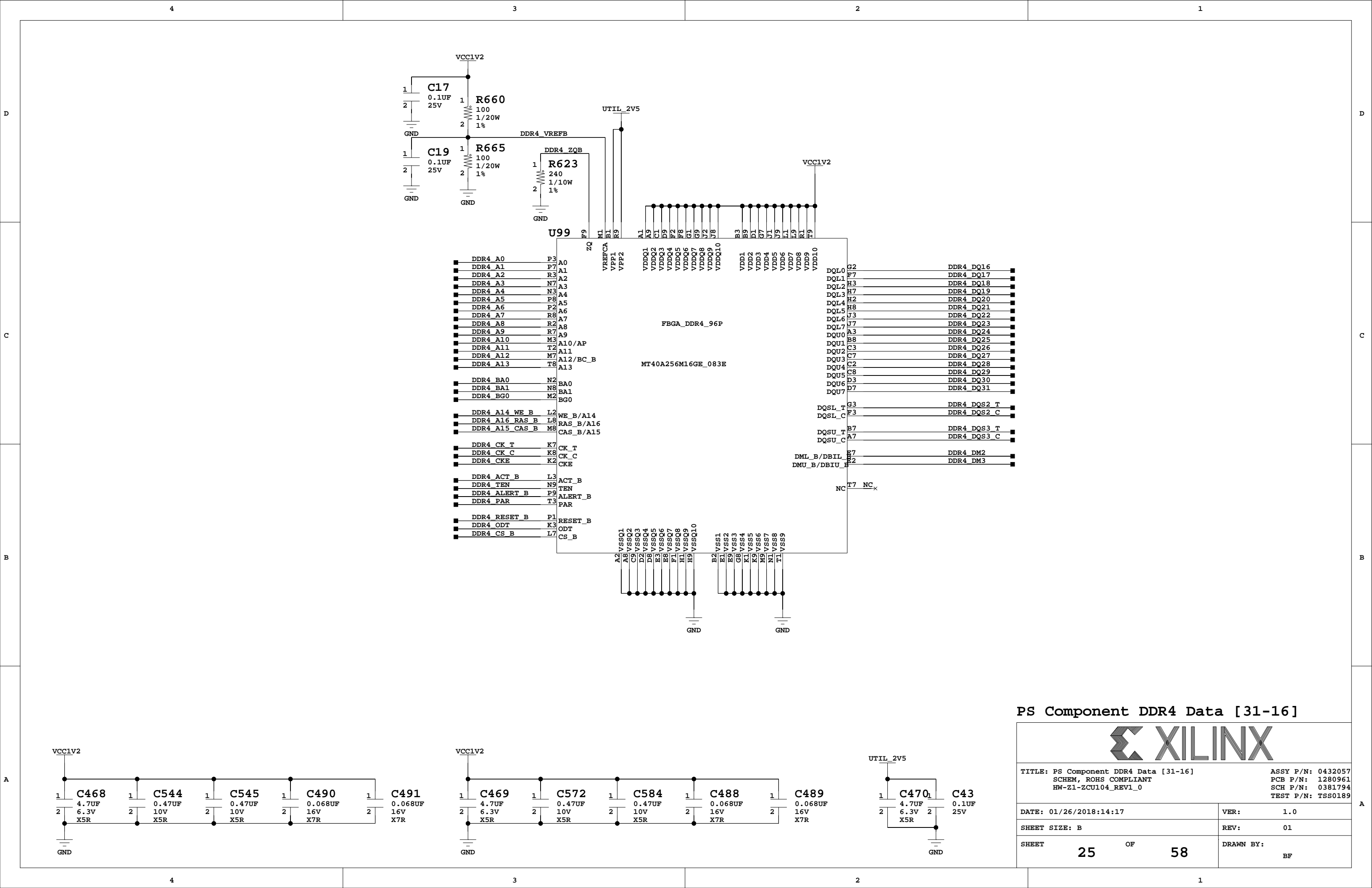
	
TITLE: PL DDR4 SODIMM 64 bit SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0	ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189
DATE: 01/26/2018:14:17	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET <div style="display: flex; justify-content: space-around; align-items: center;"> 22 OF 58 </div>	DRAWN BY: <div style="text-align: right; padding-right: 50px;">BF</div>

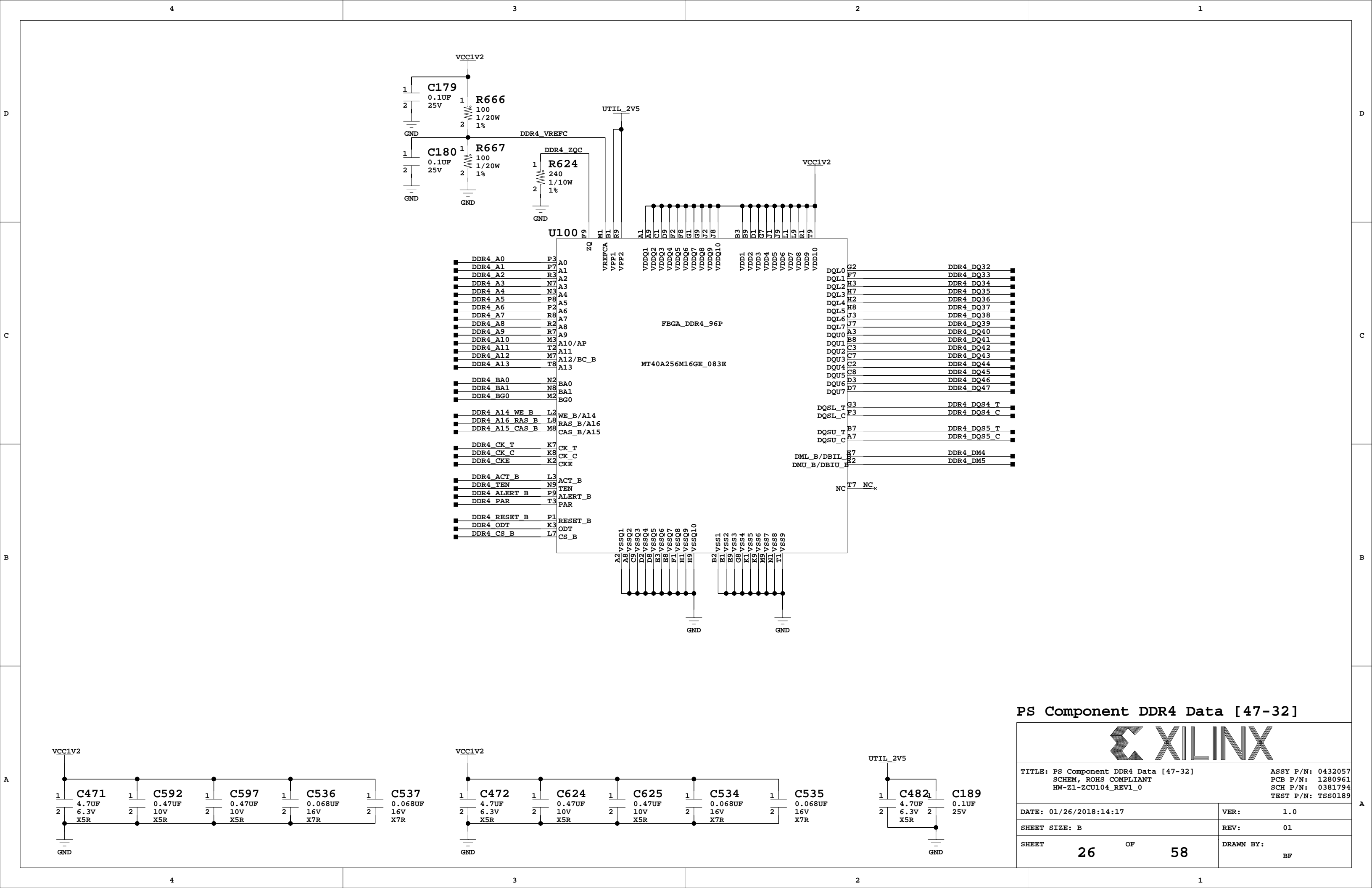


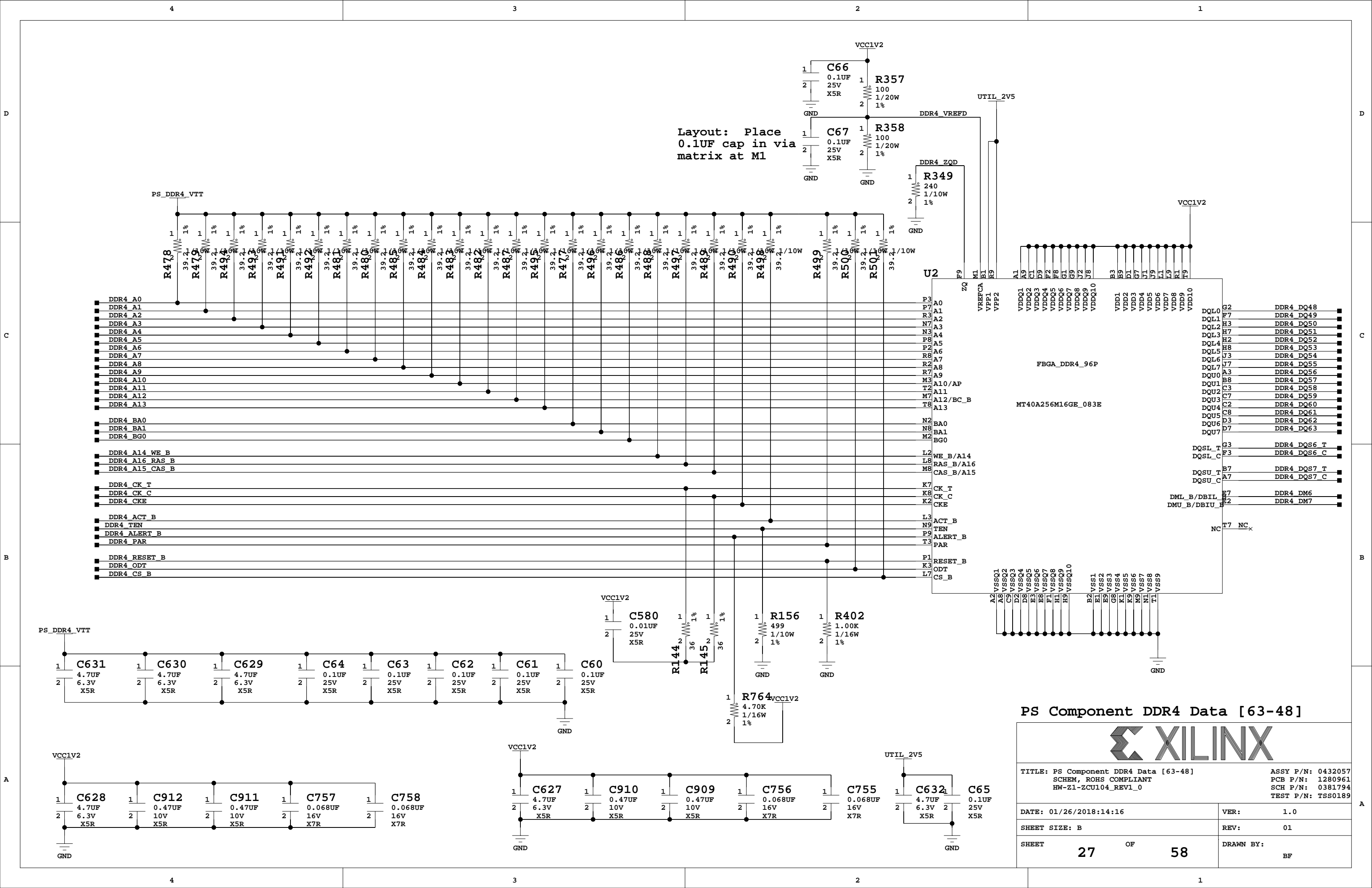
PLACE ABOVE CAPS BENEATH/CLOSE TO DIMM SOCKET

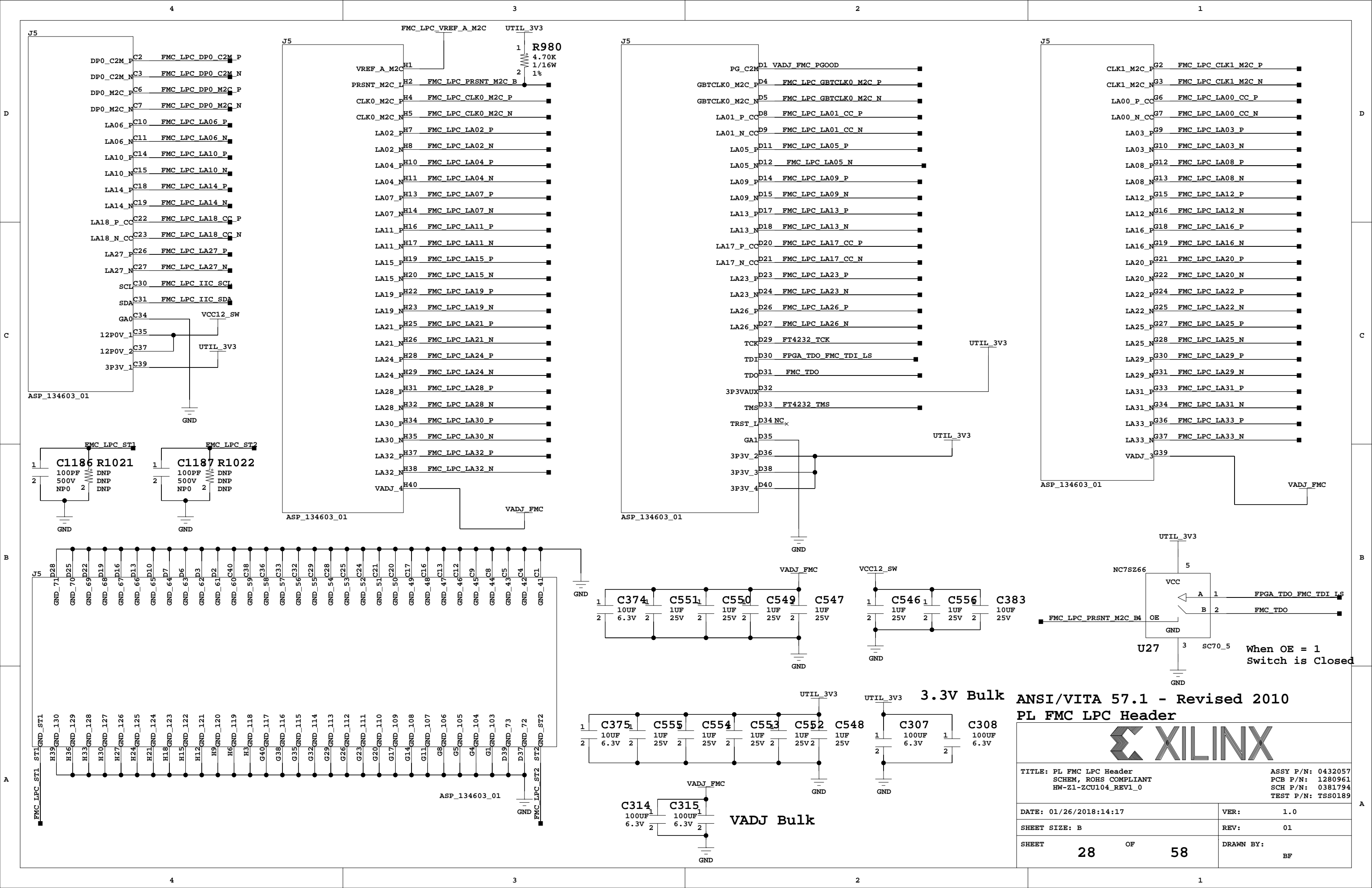
PL DDR4 SODIMM Decoupling

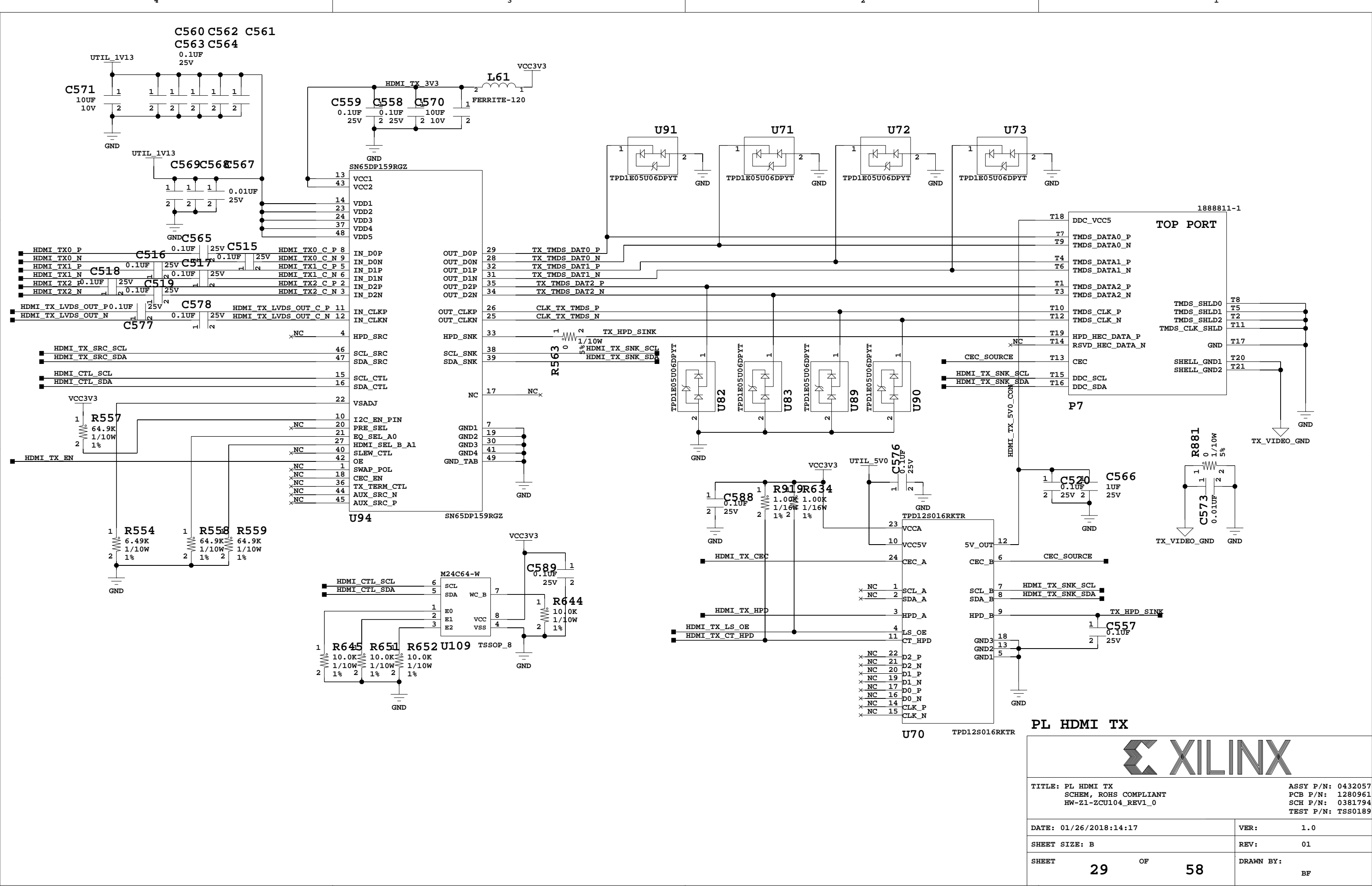
TITLE: PL DDR4 SODIMM Decoupling SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:17		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	23	OF	58
		DRAWN BY:	BF



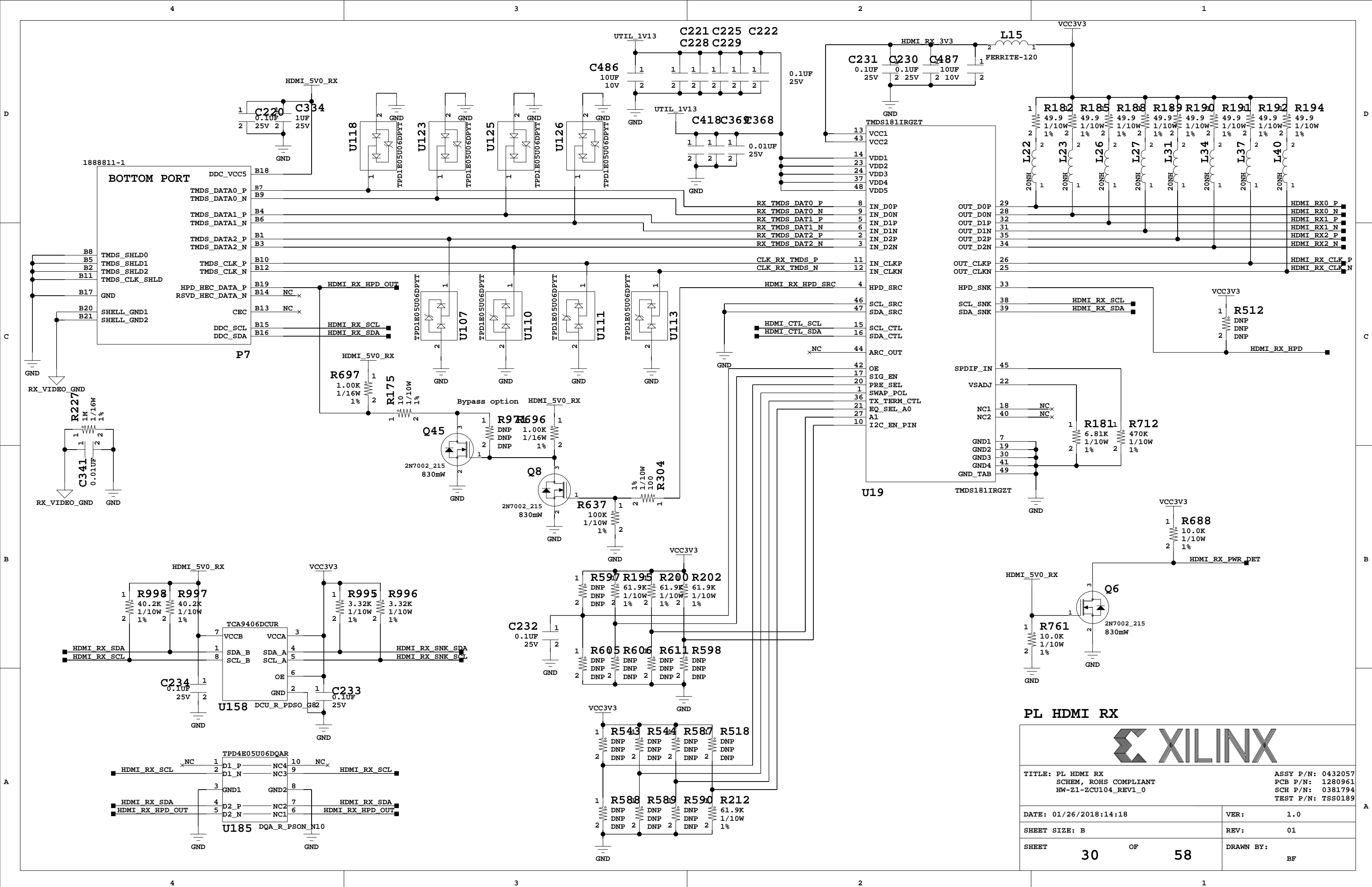


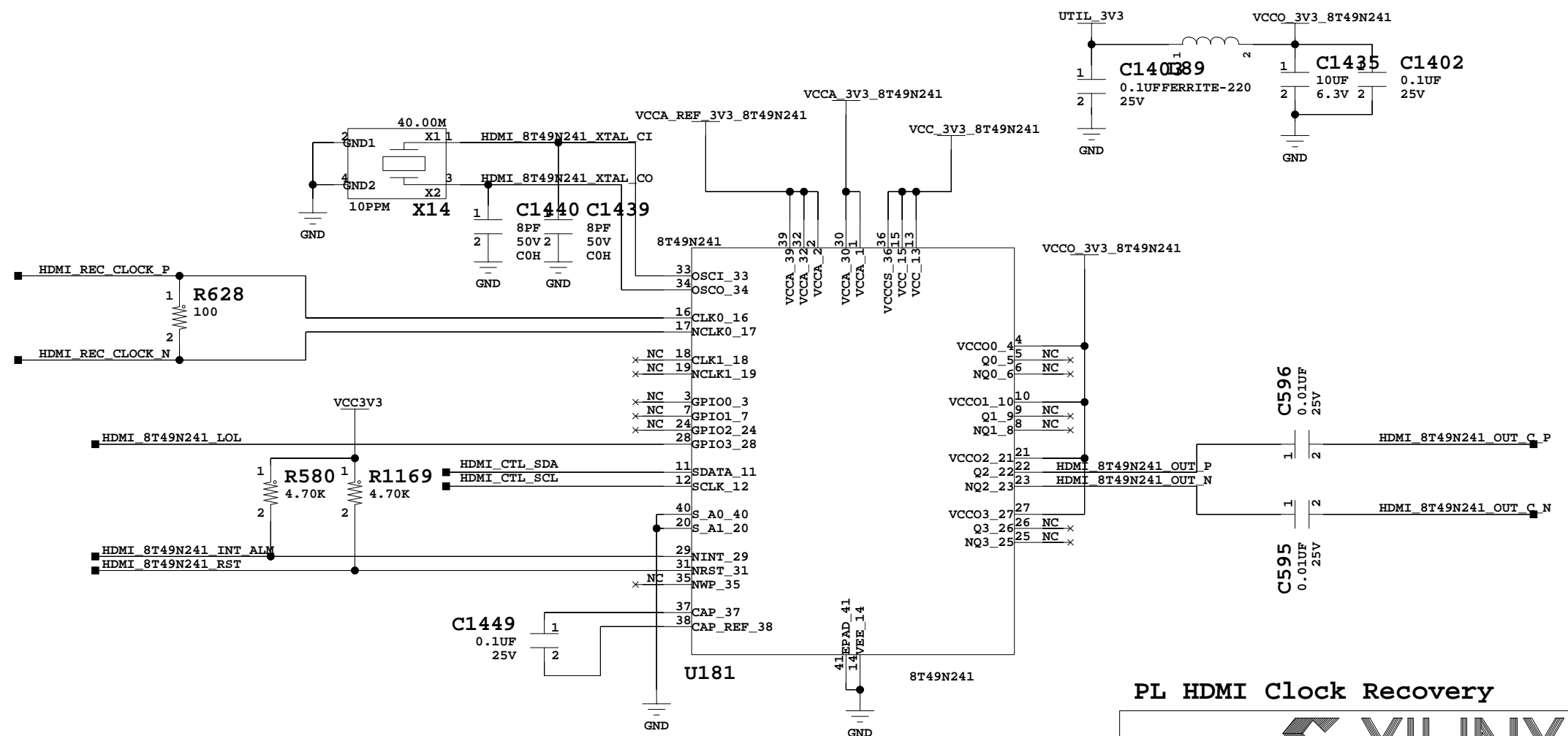
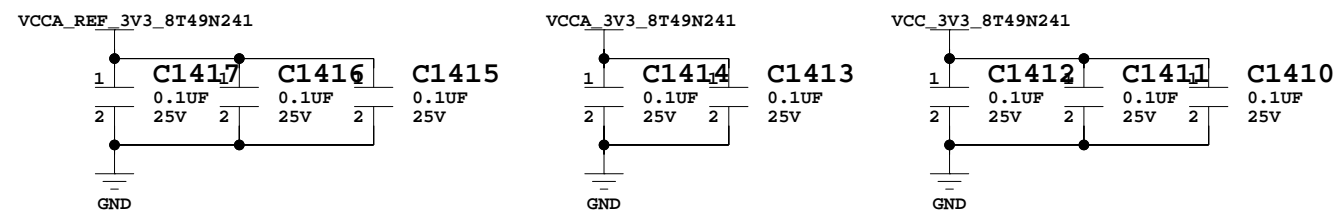
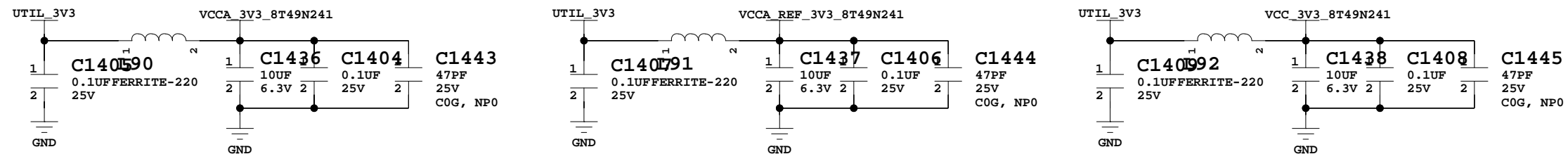






TITLE: PL HDMI TX SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:17		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	29 OF 58	DRAWN BY:	BF





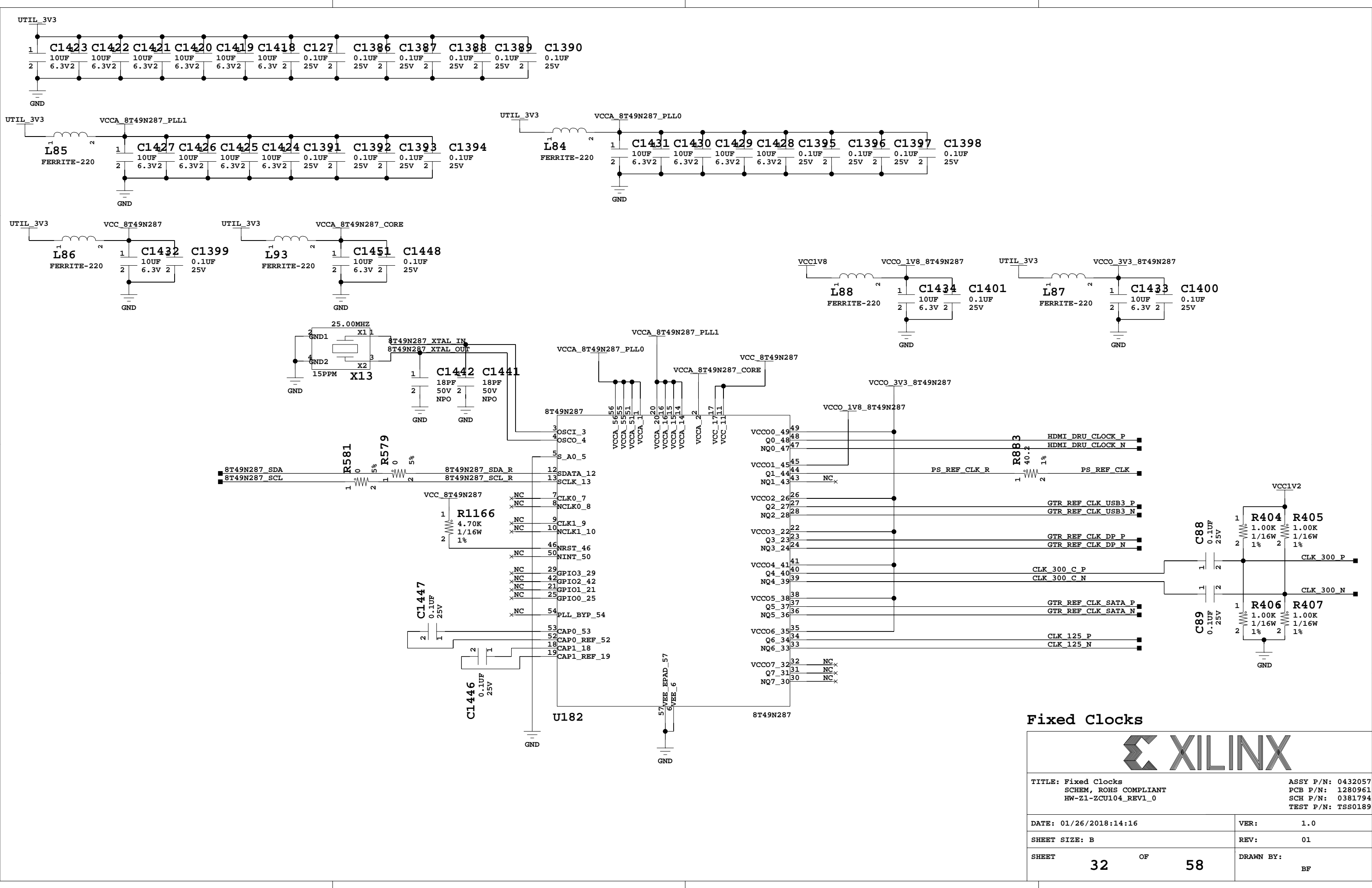
PL HDMI Clock Recovery



TITLE: PL HDMI Clock Recovery
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

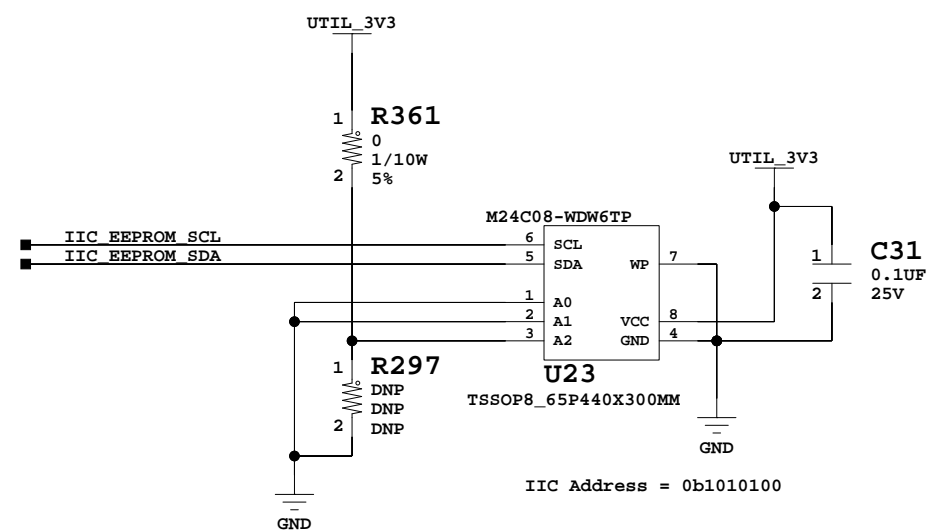
ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:21	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 31 OF 58	DRAWN BY: BF



Fixed Clocks

TITLE: Fixed Clocks SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:16	VER:	1.0	
SHEET SIZE: B	REV:	01	
SHEET	32	OF	58
DRAWN BY:		BF	



EEPROM

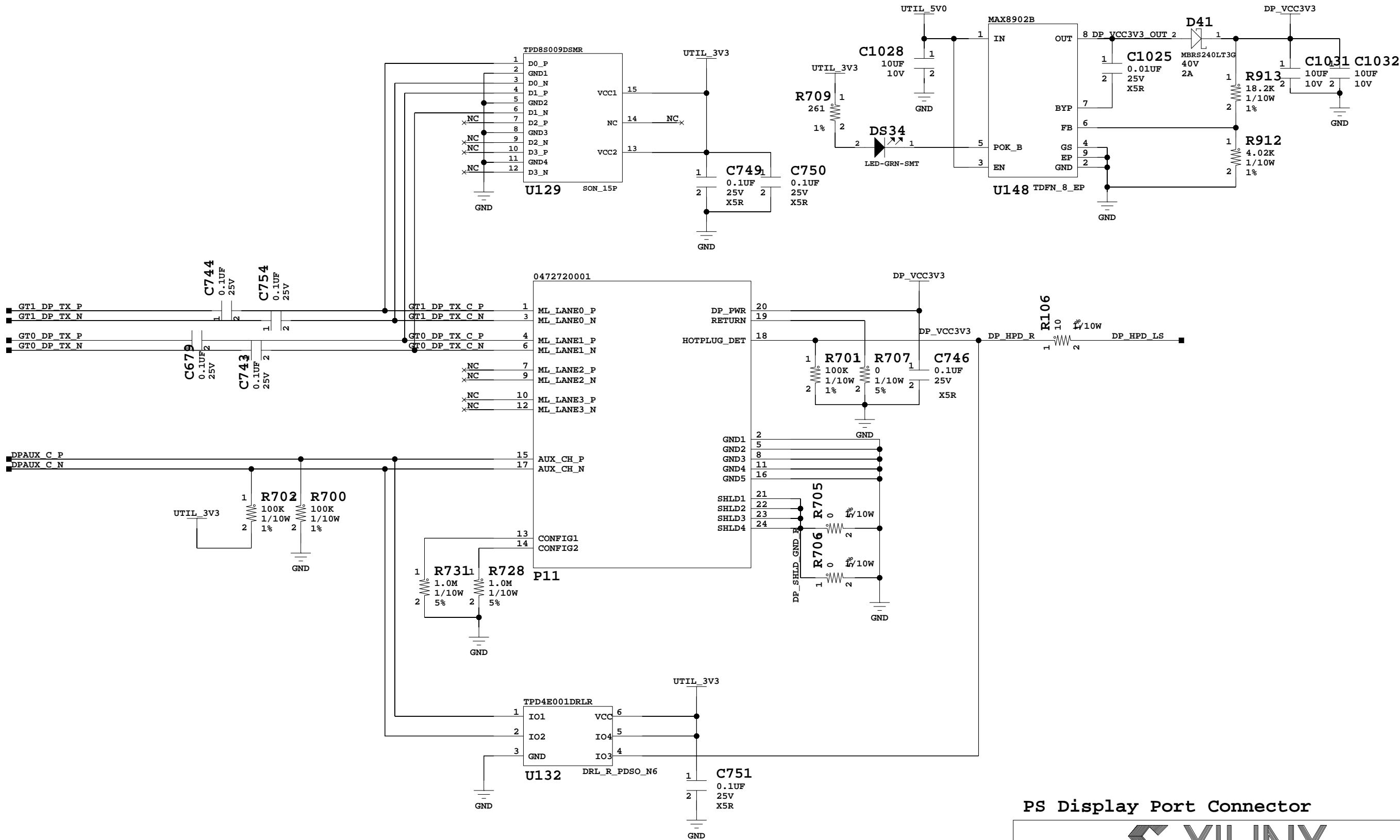


TITLE: EEPROM	ASSY P/N: 0432057
SCHEM, ROHS COMPLIANT	PCB P/N: 1280961
HW-Z1-ZCU104_REV1_0	SCH P/N: 0381794
	TEST P/N: TSS0189


DATE: 01/26/2018:14:16	VER: 1.0
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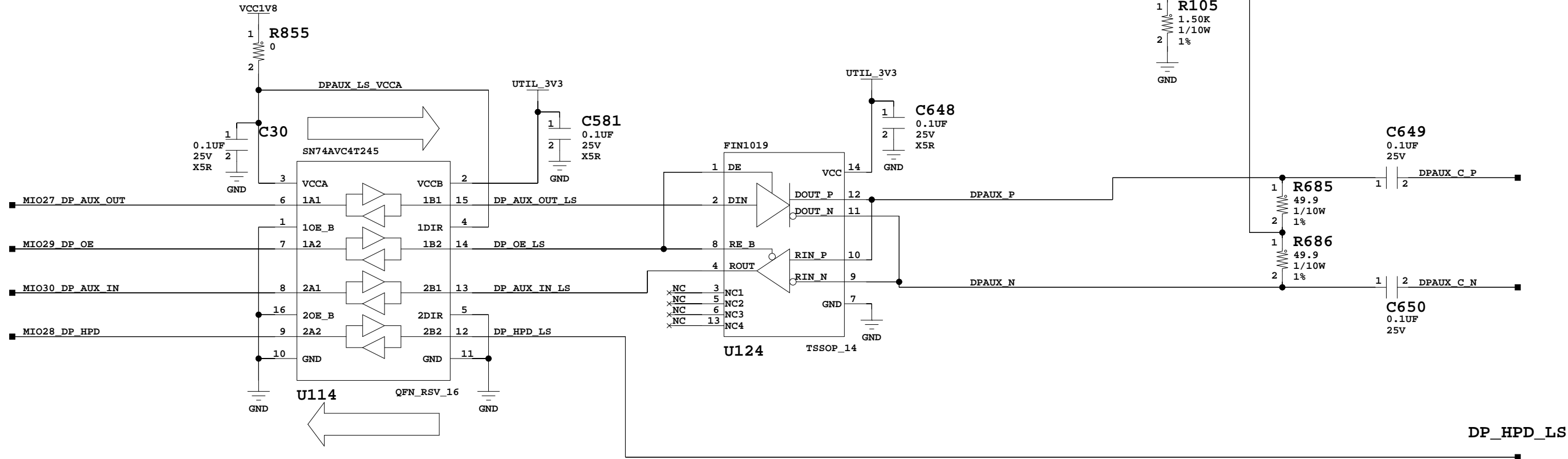
SHEET SIZE: B	REV: 01
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SHEET 33	OF 58	DRAWN BY: BF
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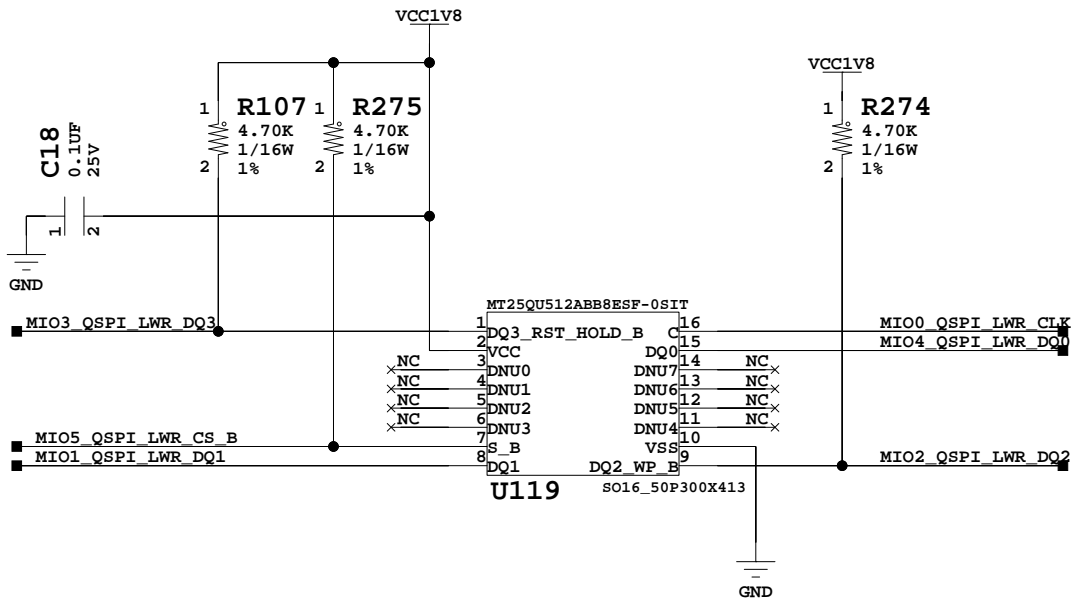
PS Display Port Connector

	
TITLE: PS Display Port Connector SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0	
ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:17	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 34 OF 58	DRAWN BY: BF

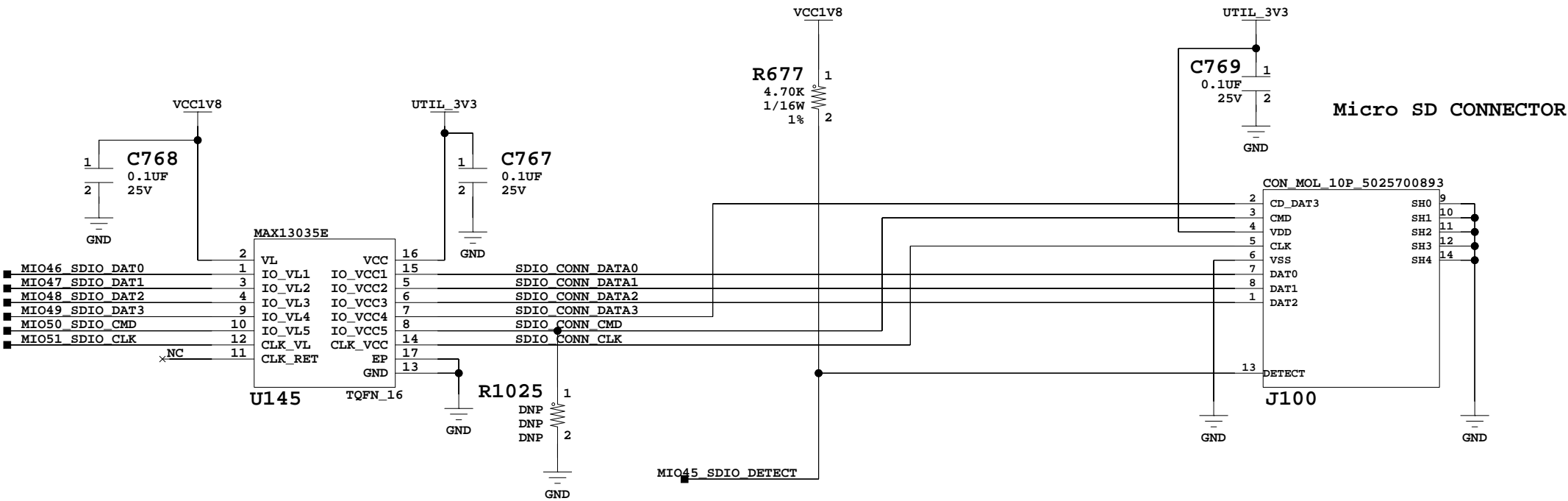


PS Display Port IO

TITLE: PS Display Port IO SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:17		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	35	OF	58
		DRAWN BY:	BF

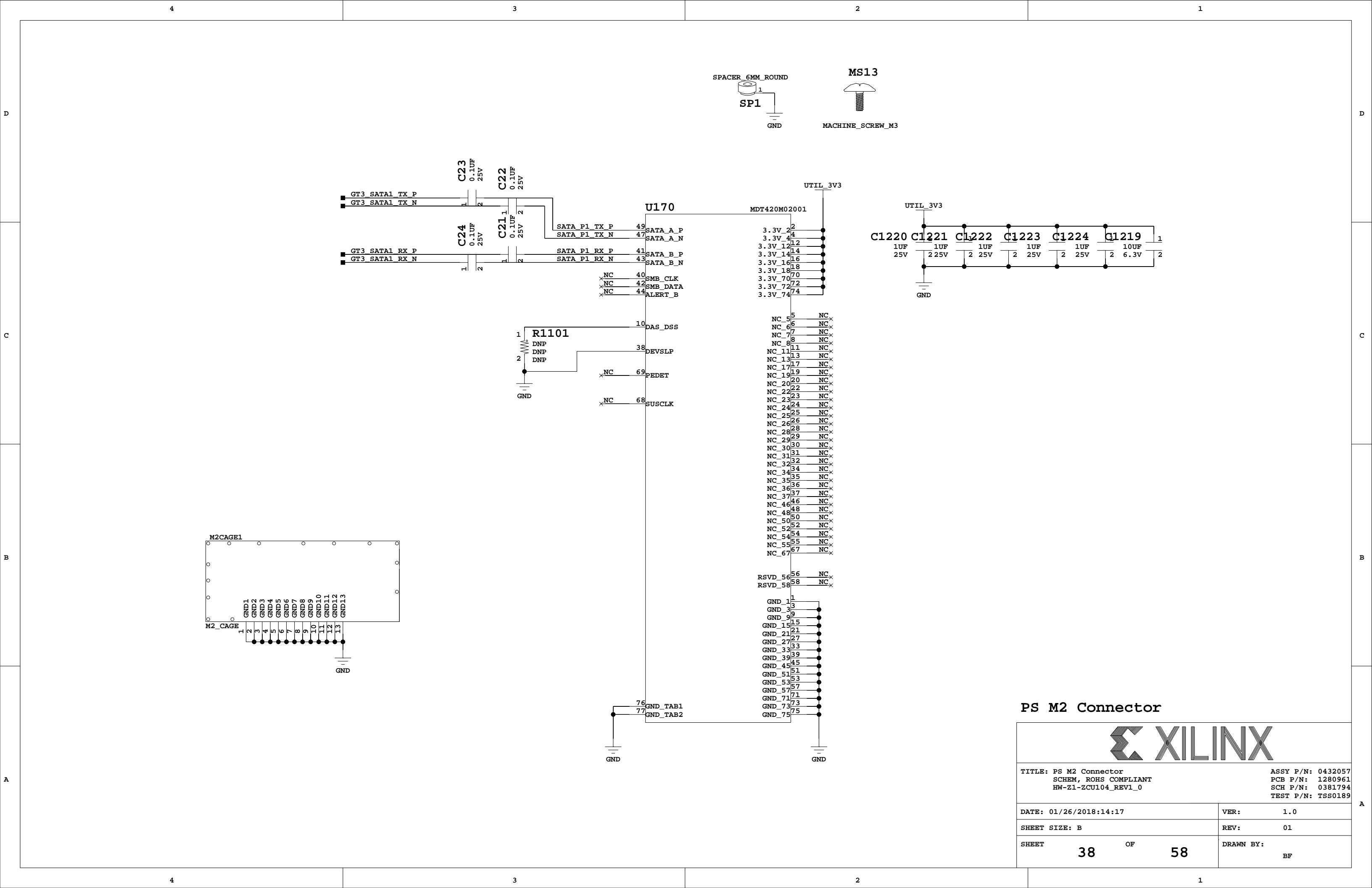


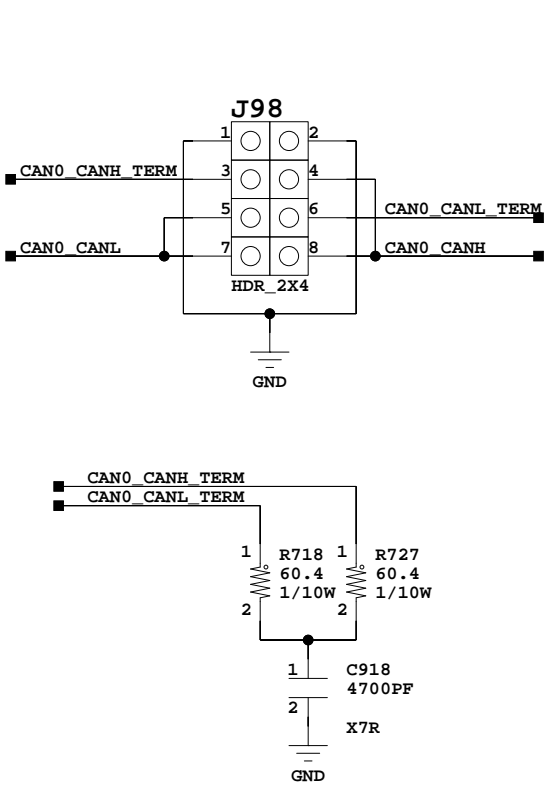
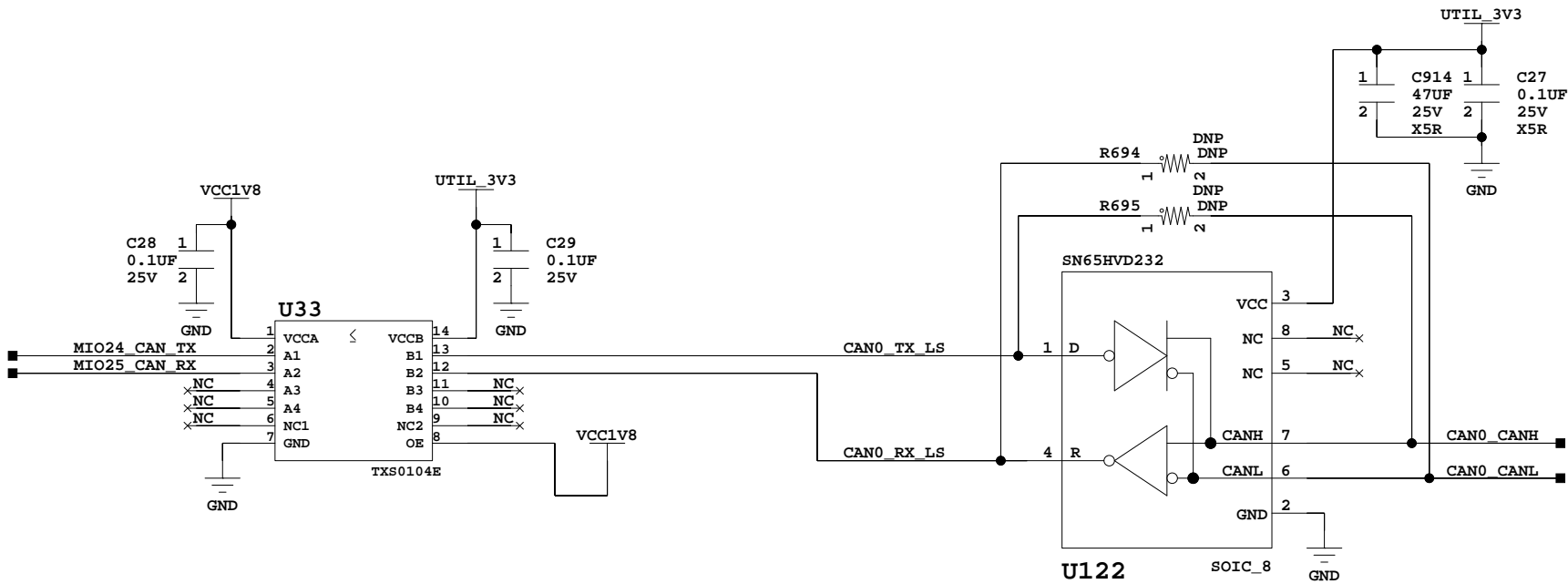
<div>XILINX</div>			
TITLE: PS QSPI SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:17		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	36	OF	58
		DRAWN BY:	BF



PS SD Card Connector

TITLE: PS SD Card Connector		ASSY P/N: 0432057	
SCHEM, ROHS COMPLIANT		PCB P/N: 1280961	
HW-Z1-ZCU104_REV1_0		SCH P/N: 0381794	
		TEST P/N: TSS0189	
DATE: 01/26/2018:14:17		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	37	OF	58
		DRAWN BY:	BF



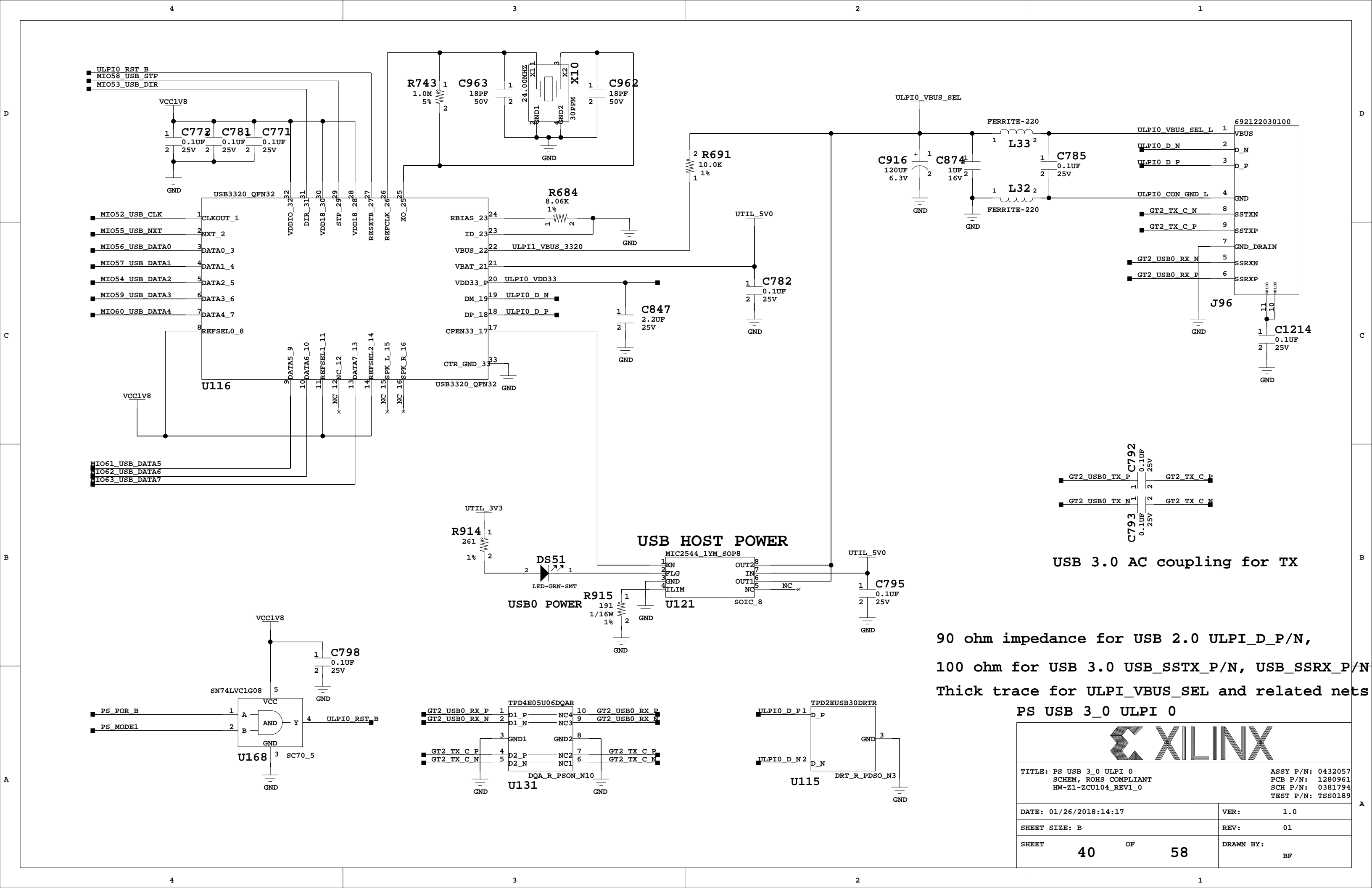


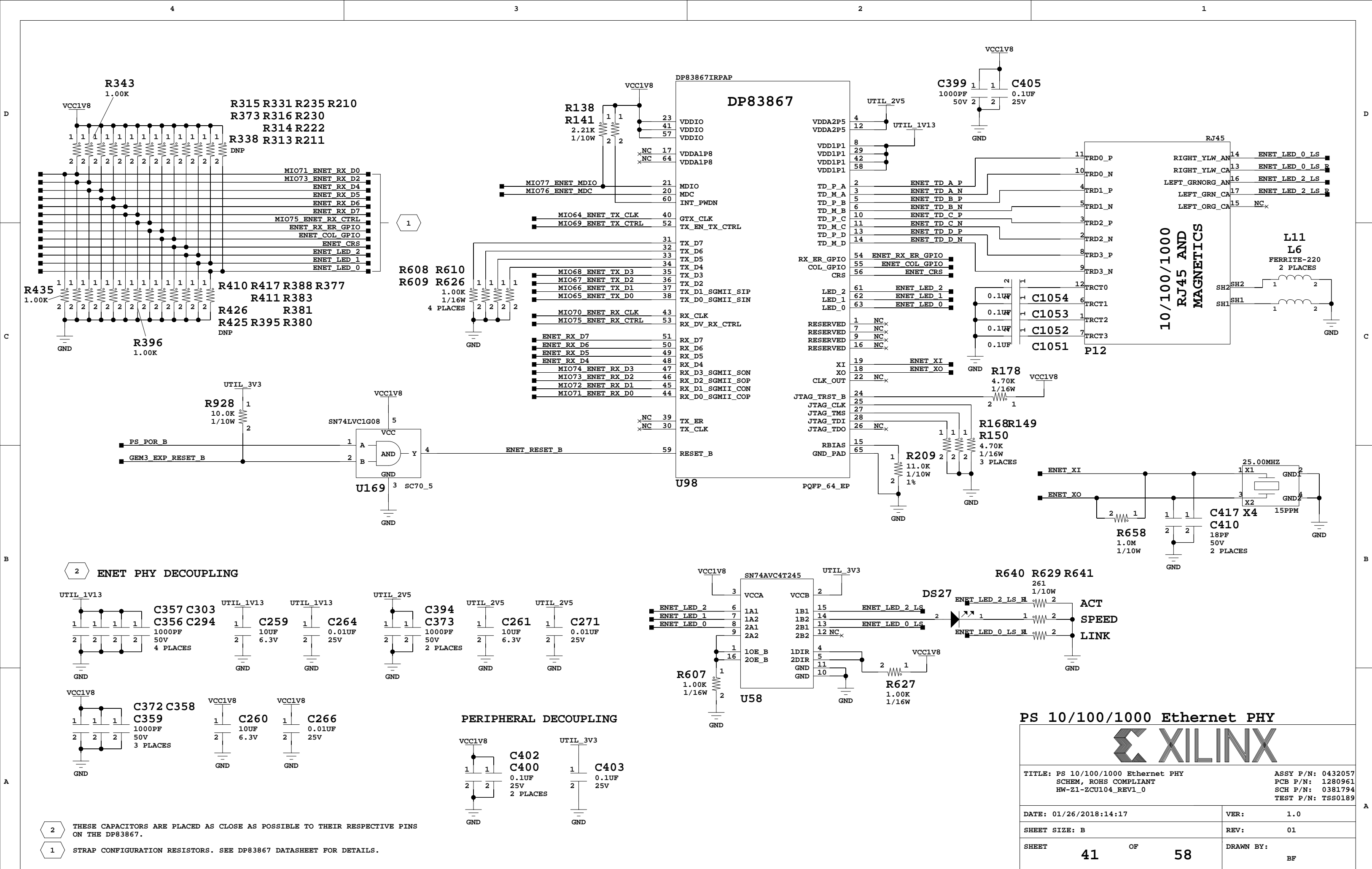
PS CAN Bus

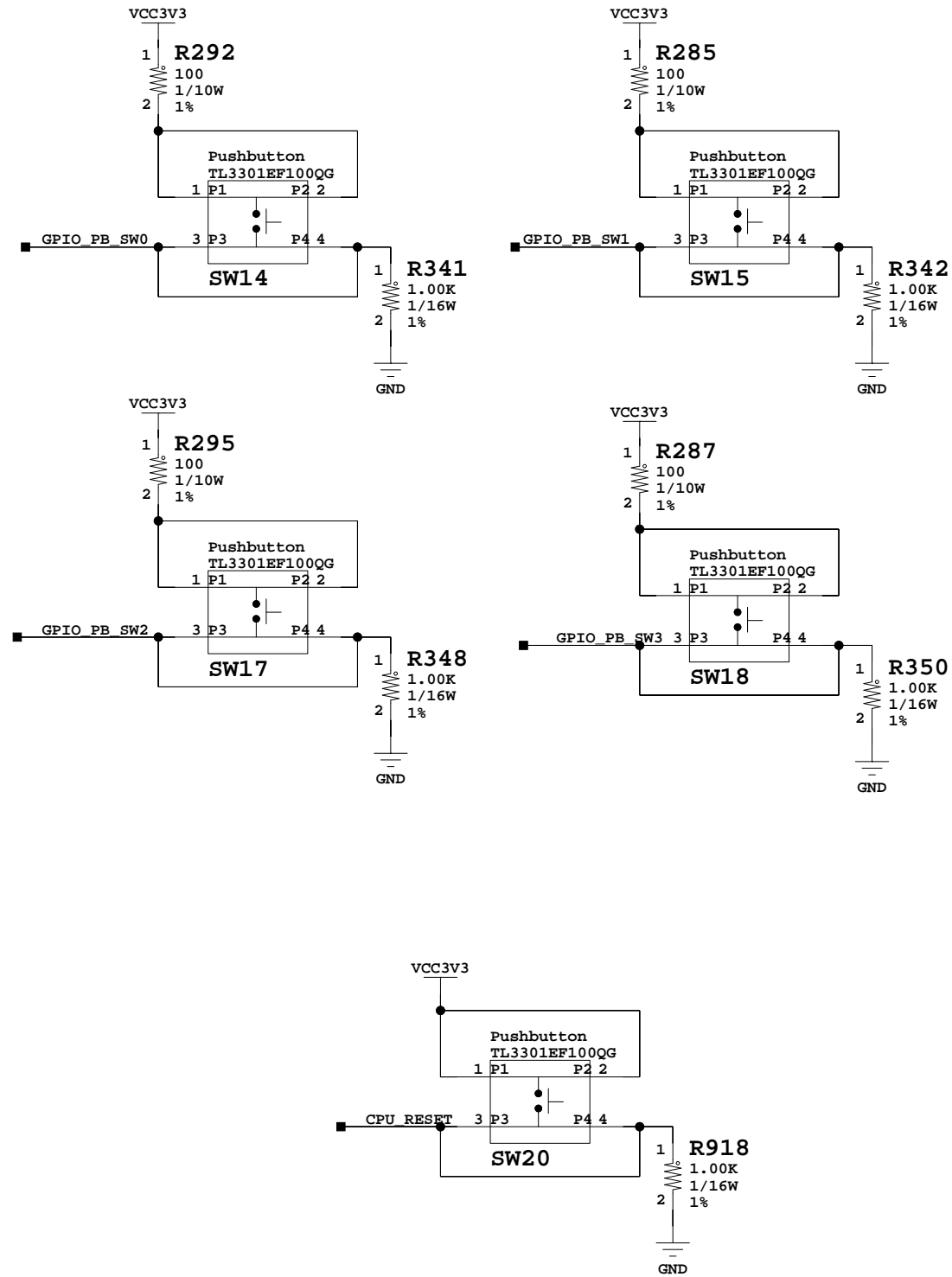
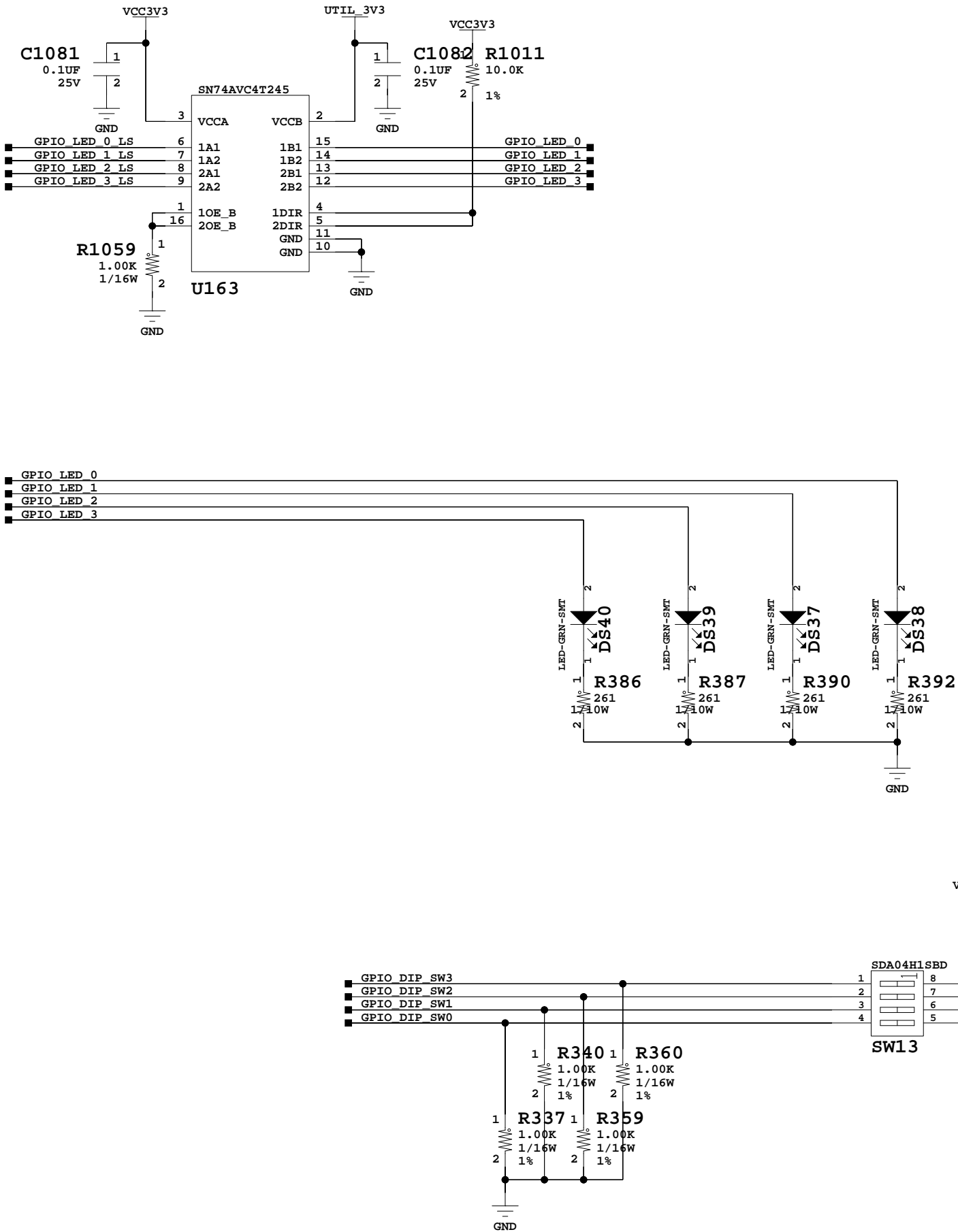


TITLE: PS CAN Bus SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0	ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189
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DATE: 01/26/2018:14:17	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 39 OF 58	DRAWN BY: BF







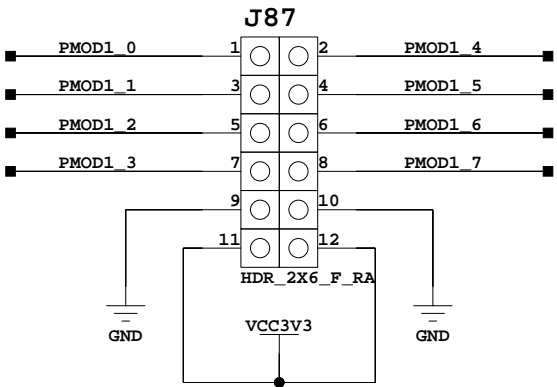
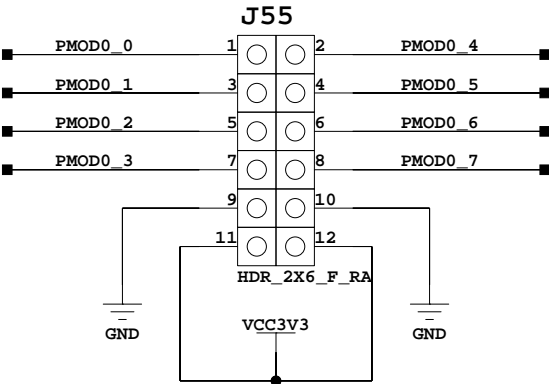
PL Buttons - Switches - LEDs



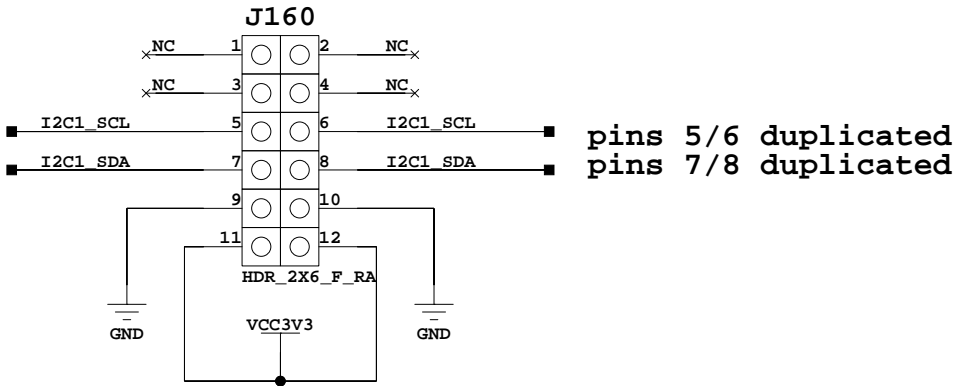
TITLE: PL Buttons - Switches - LEDs
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:17	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 42 OF 58	DRAWN BY: BF

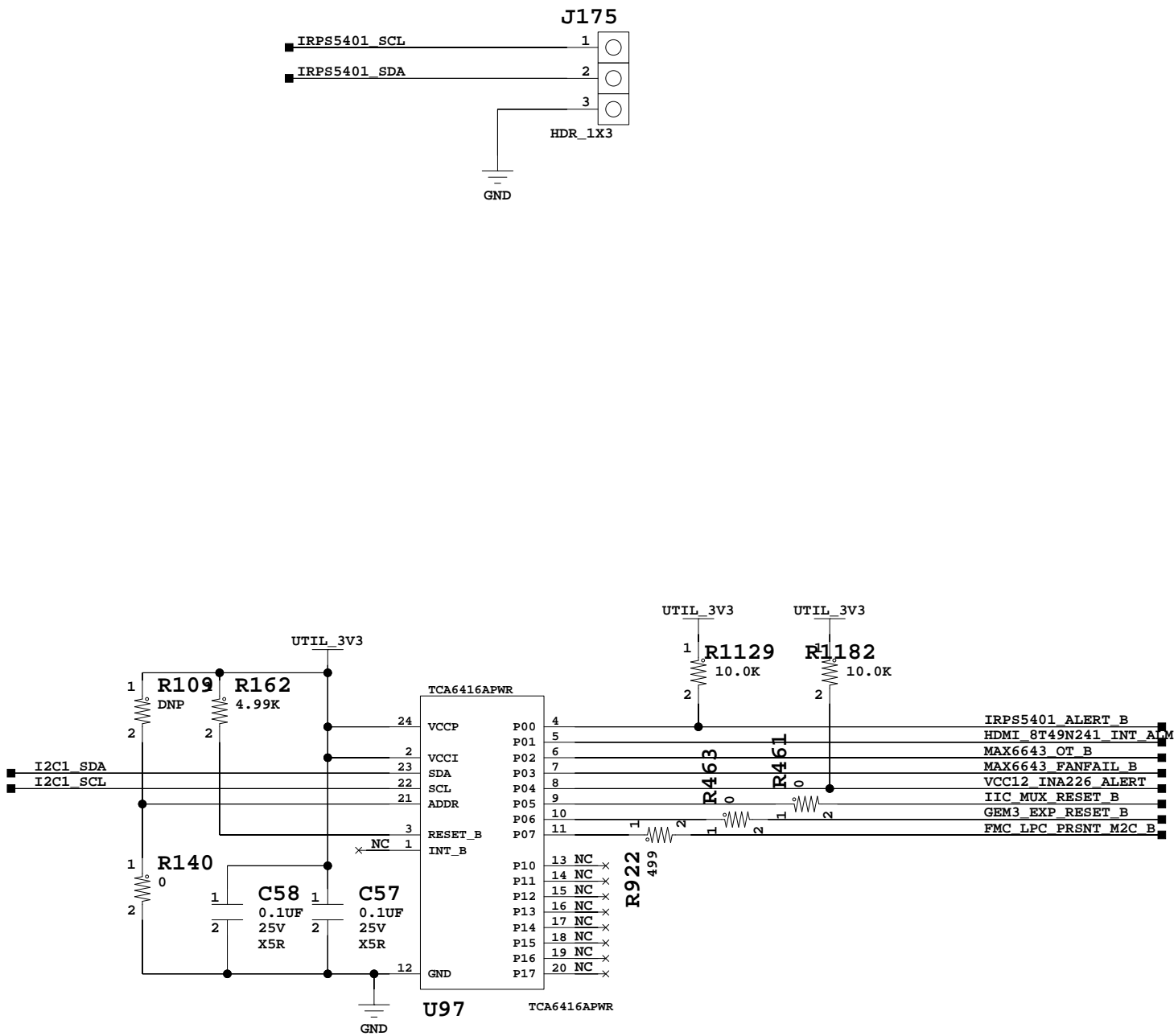


Connects I2C to R.A. Female 2x6 PMOD-style receptacle



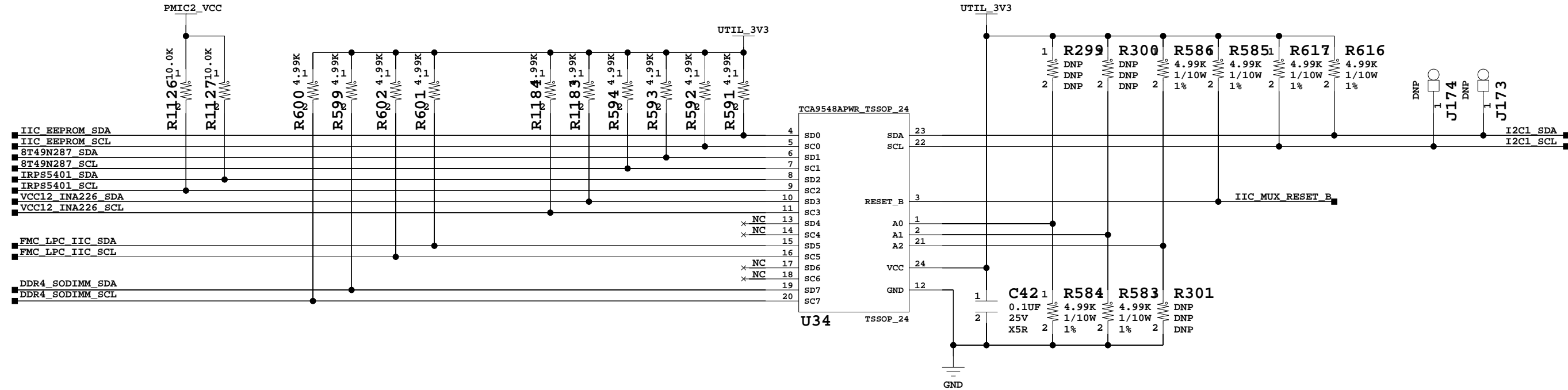
PL PMODs

TITLE: PL PMODs SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:17		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	43	OF	58
		DRAWN BY:	BF

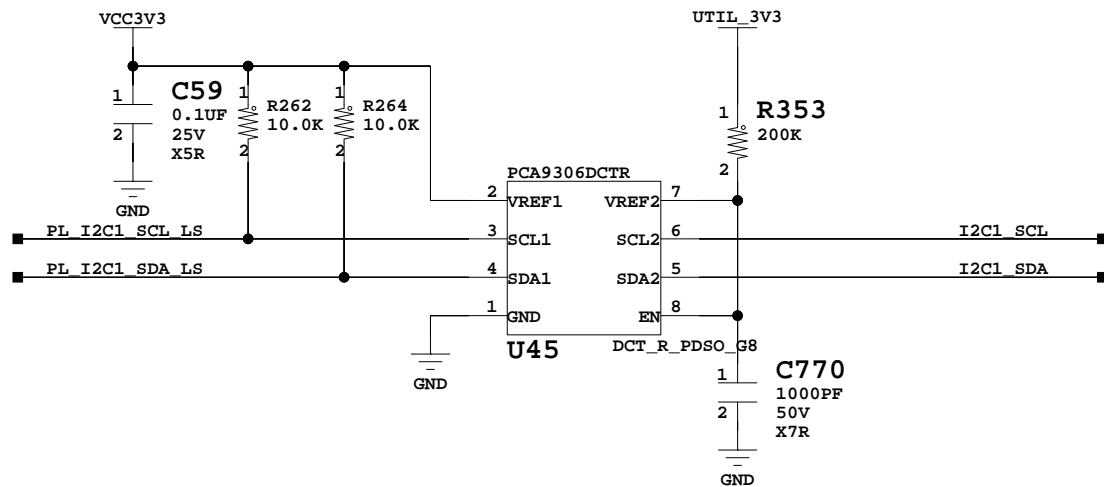


IO Expander PMBUS Header

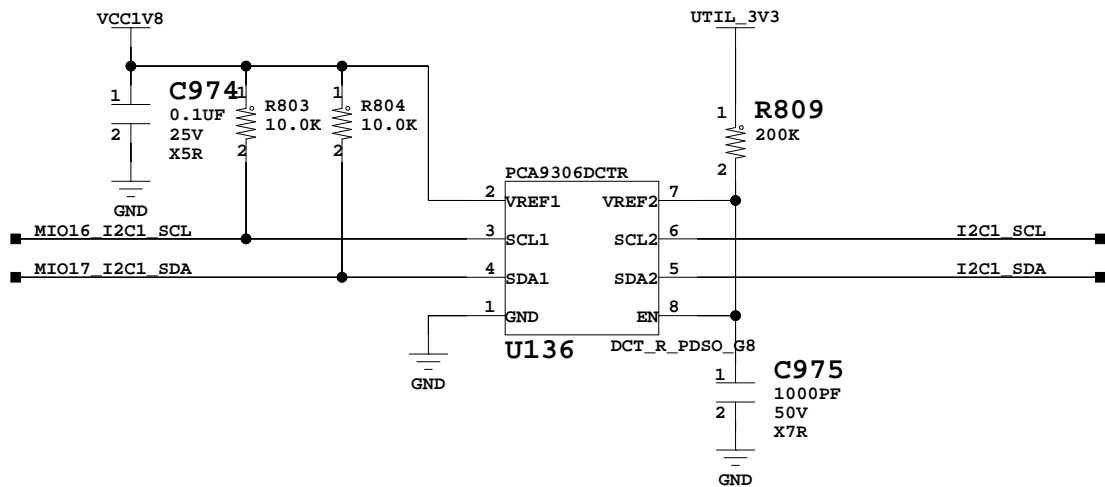
TITLE: IO Expander PMBUS Header SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:16		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	44	OF	58
DRAWN BY:		BF	



PL I2C Level Shifter (disabled if VREF1 is off)



PS I2C Level Shifter (I2C1)



I2C1 MUX and Level Shifters

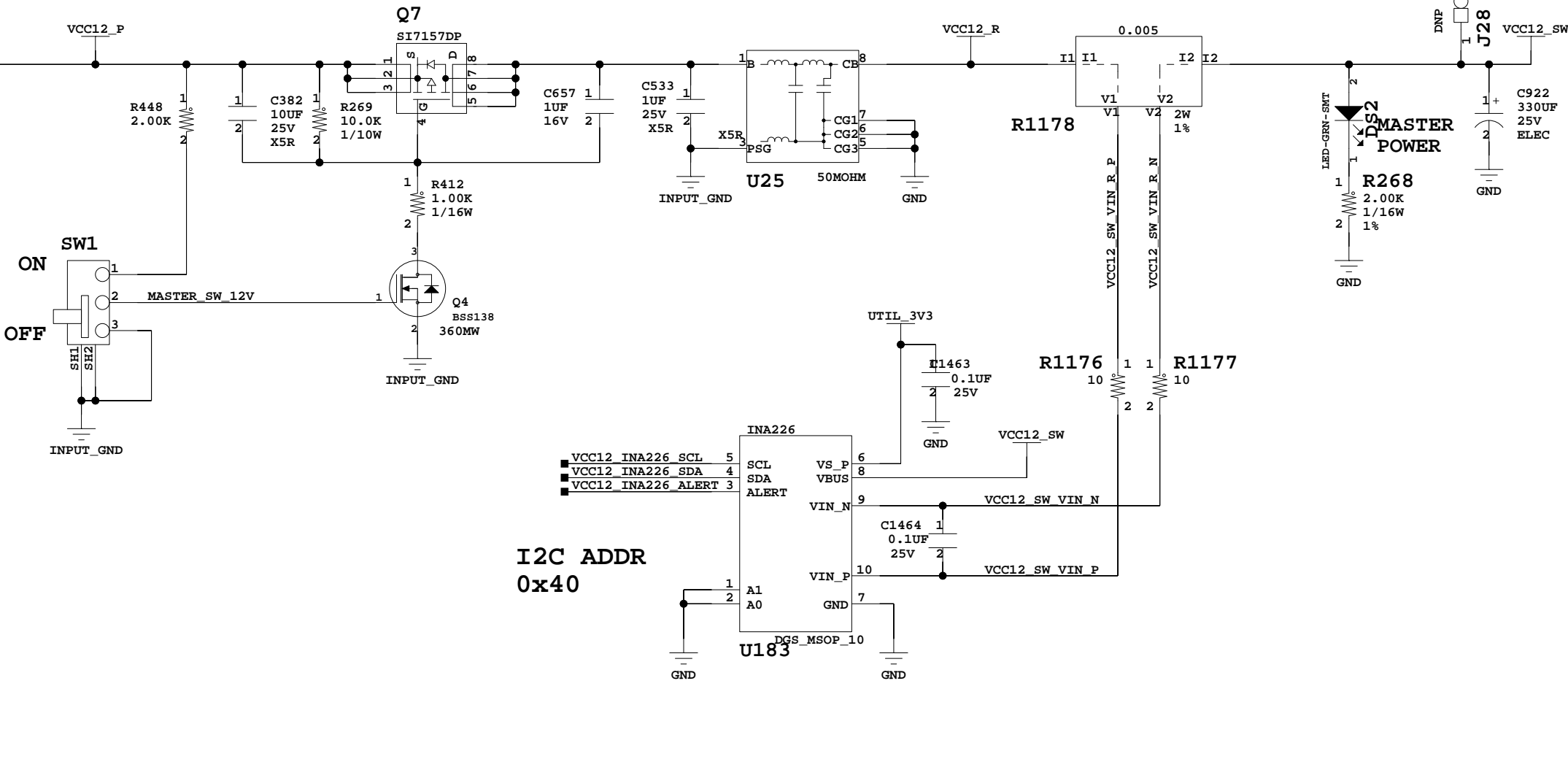
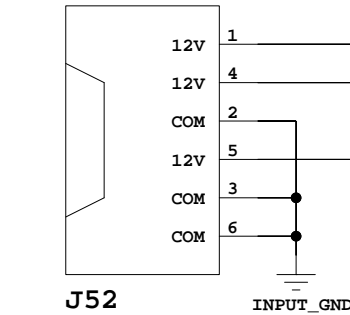


TITLE: I2C1 MUX and Level Shifters
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

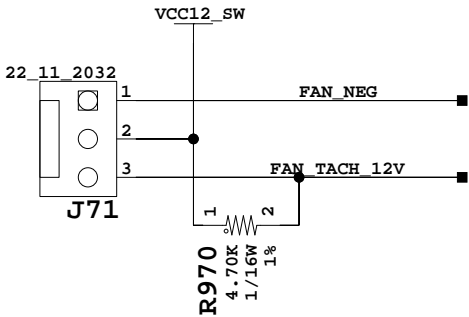
DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 45 OF 58	DRAWN BY: BF

6-PIN MINI-FIT
AC ADAPTER (BRICK)



GND Test points

Keyed Fan Header



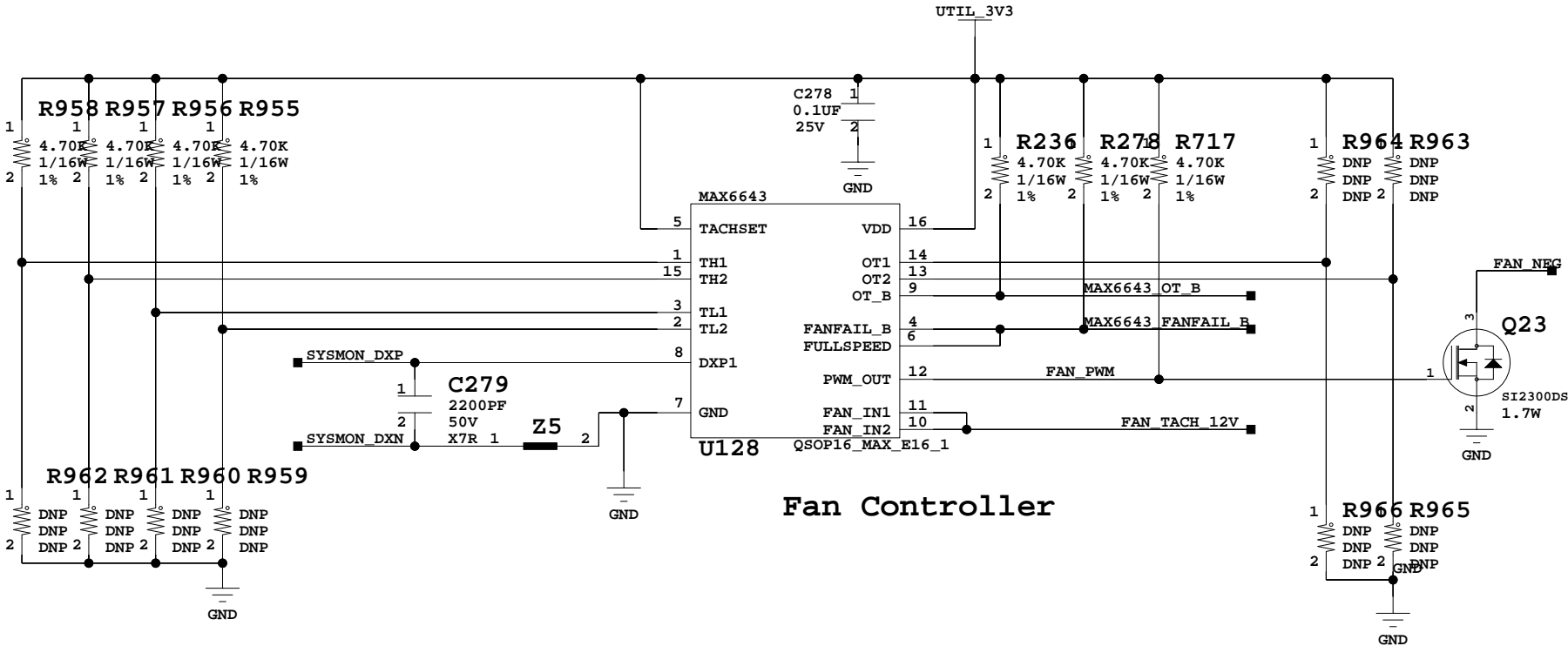
12V Power Connectors Switch



TITLE: 12V Power Connectors Switch
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0
ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 46 OF 58	DRAWN BY: BF

Fan Controller



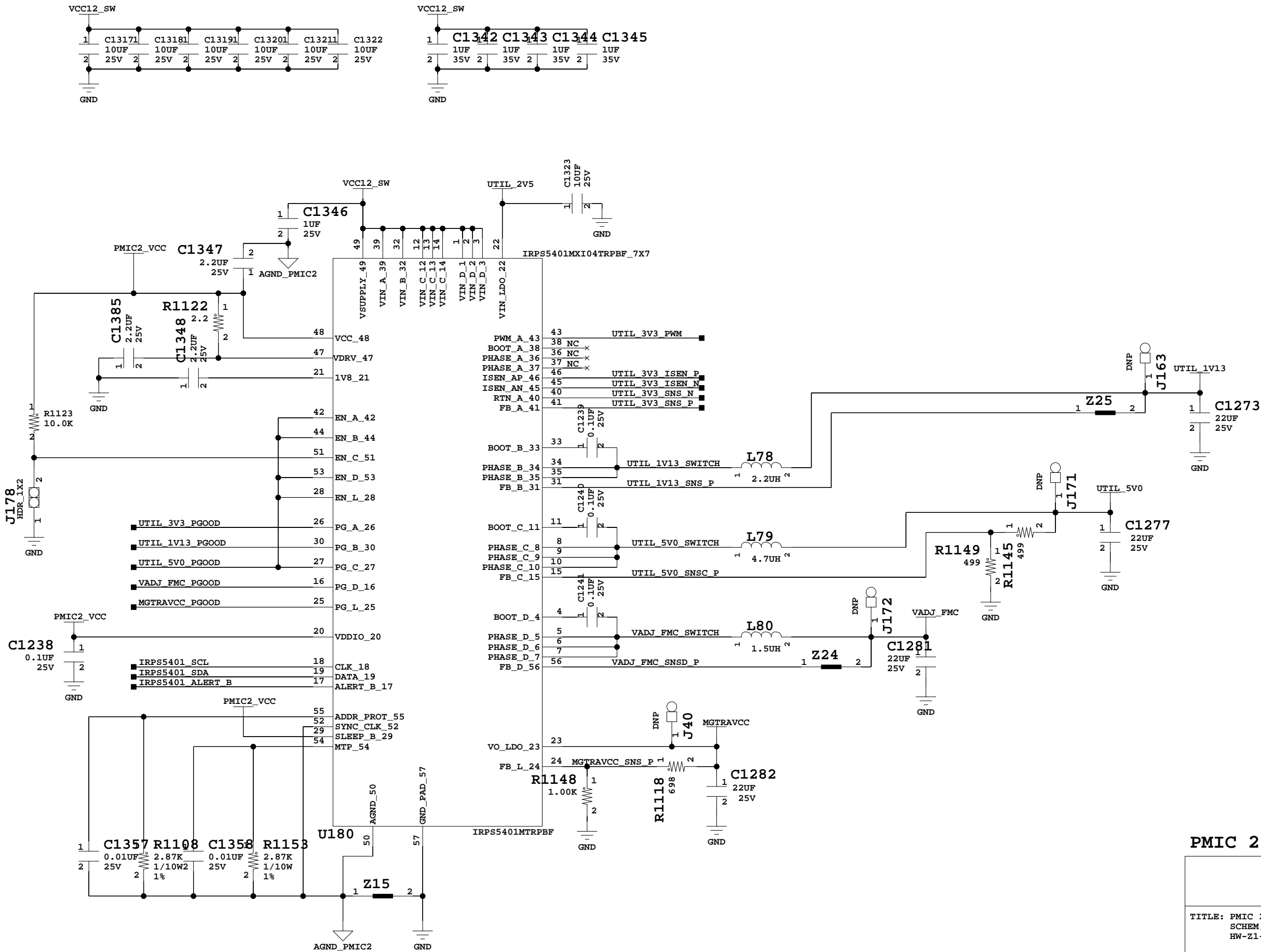
7-BIT PMBUS ADDR: 0x43
7-BIT I2C ADDR: 0x13



TITLE: PMIC 1
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:19	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 47 OF 58	DRAWN BY: BF



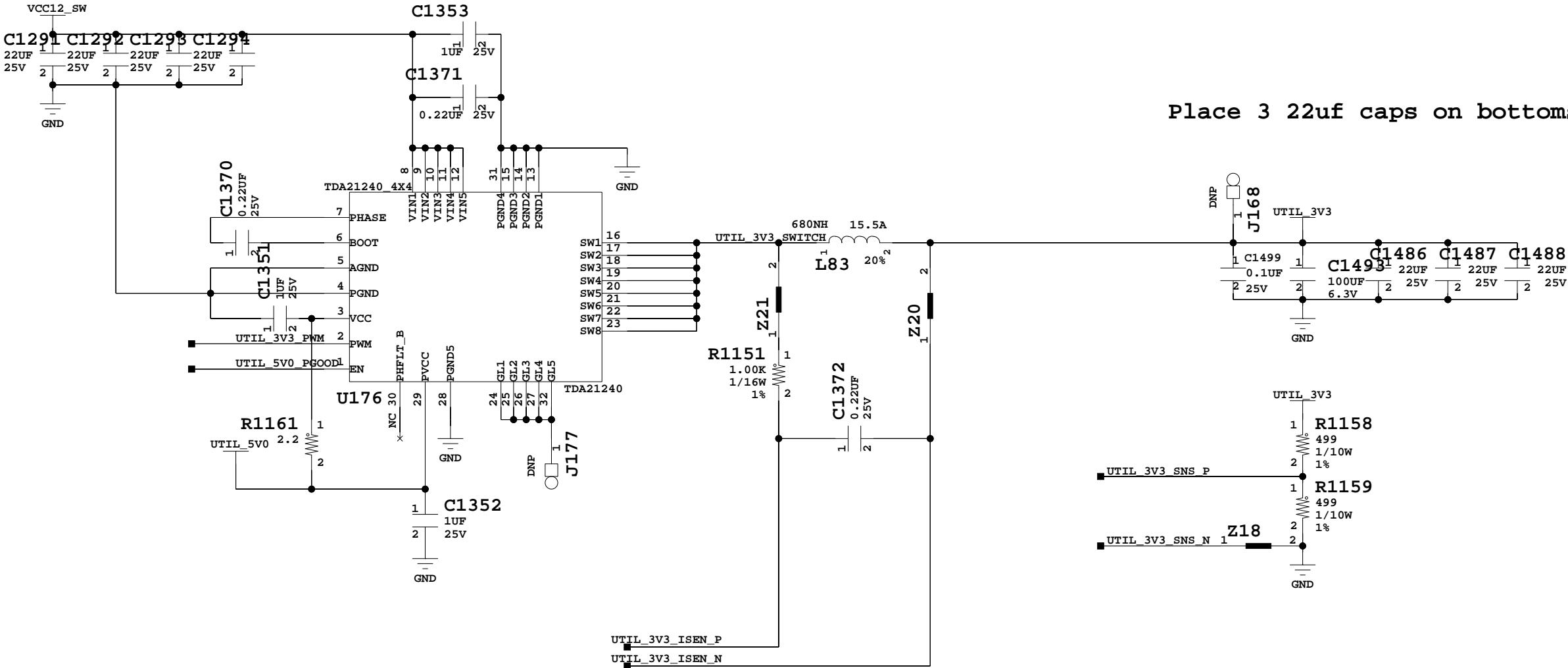
PMIC 2



TITLE: PMIC 2 SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0	ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189
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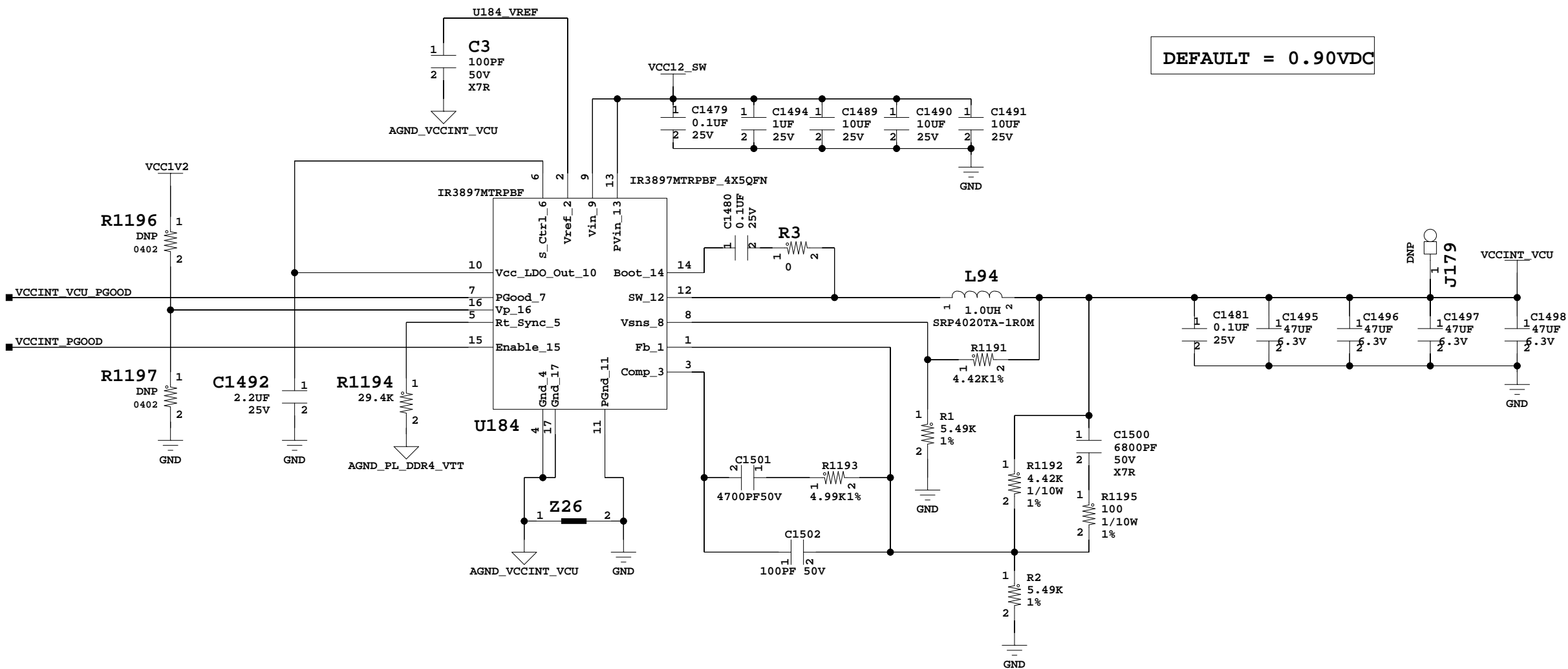
DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 49 OF 58	DRAWN BY: BF

7-BIT PMBUS ADDR: 0x44
I2C ADDR: 0x14



PMIC 2 Power Stage

TITLE: PMIC 2 Power Stage SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:17		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	50	OF	58
		DRAWN BY:	BF



These VALUES need to change for 0.9V
and voltage sense line needs to be identified

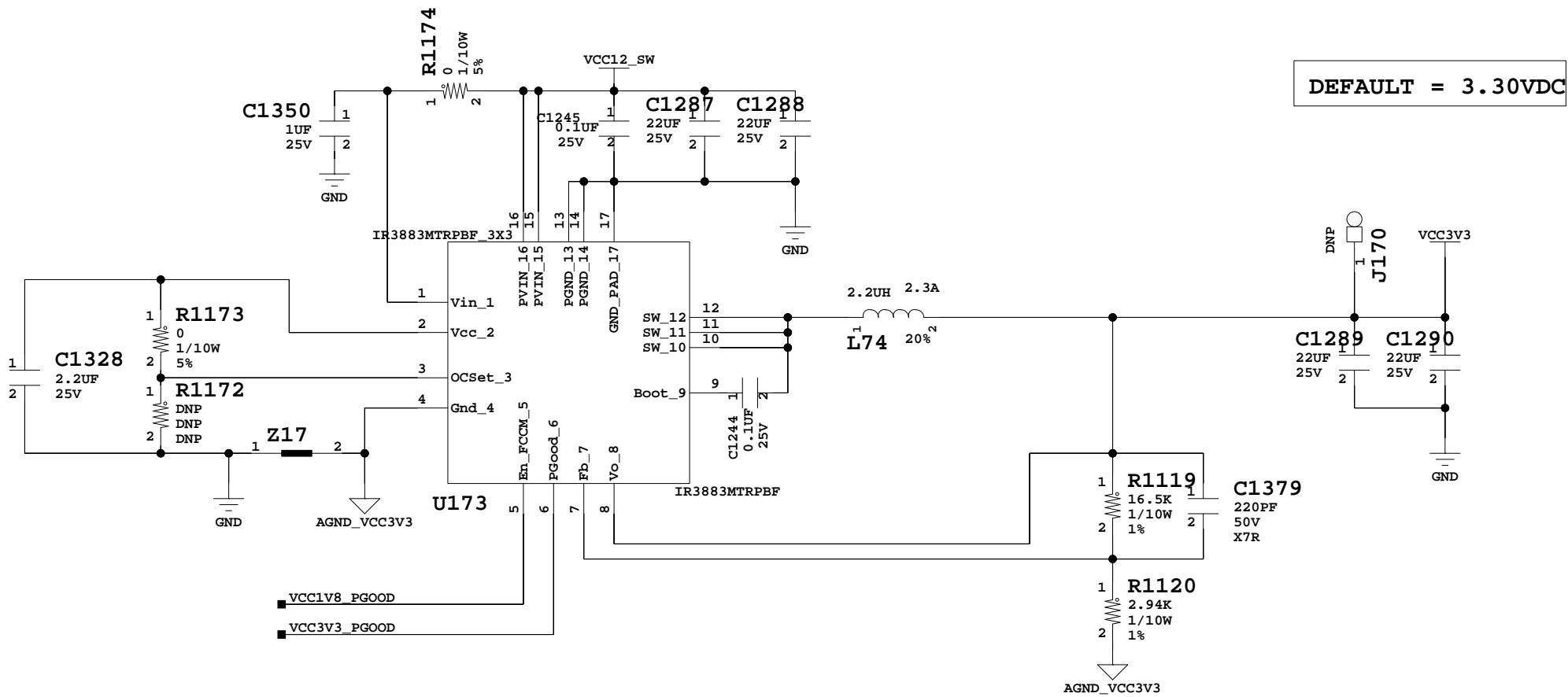
DN update 8-11-17 per Rick L
VCCINT_VCU 3A Regulator



TITLE: VCCINT_VCU 3A Regulator
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

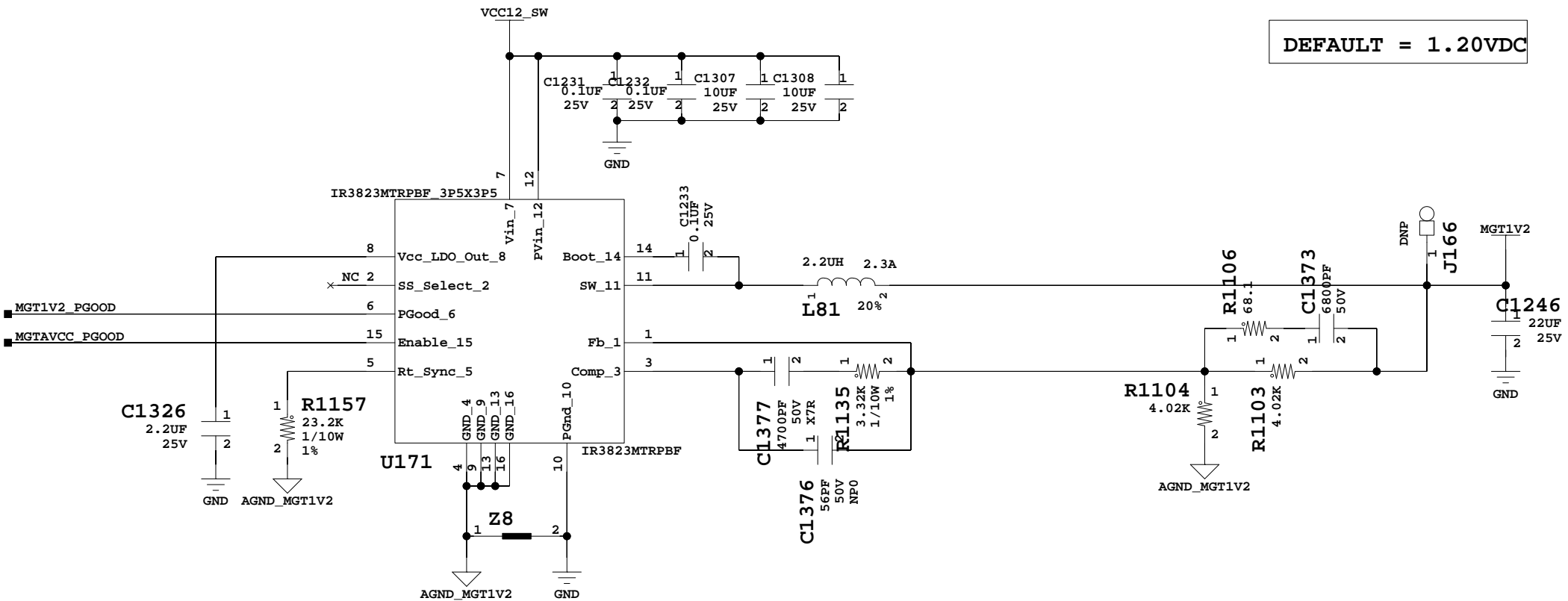
ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:17	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 51 OF 58	DRAWN BY: BF



VCC3V3 500mA Regulator

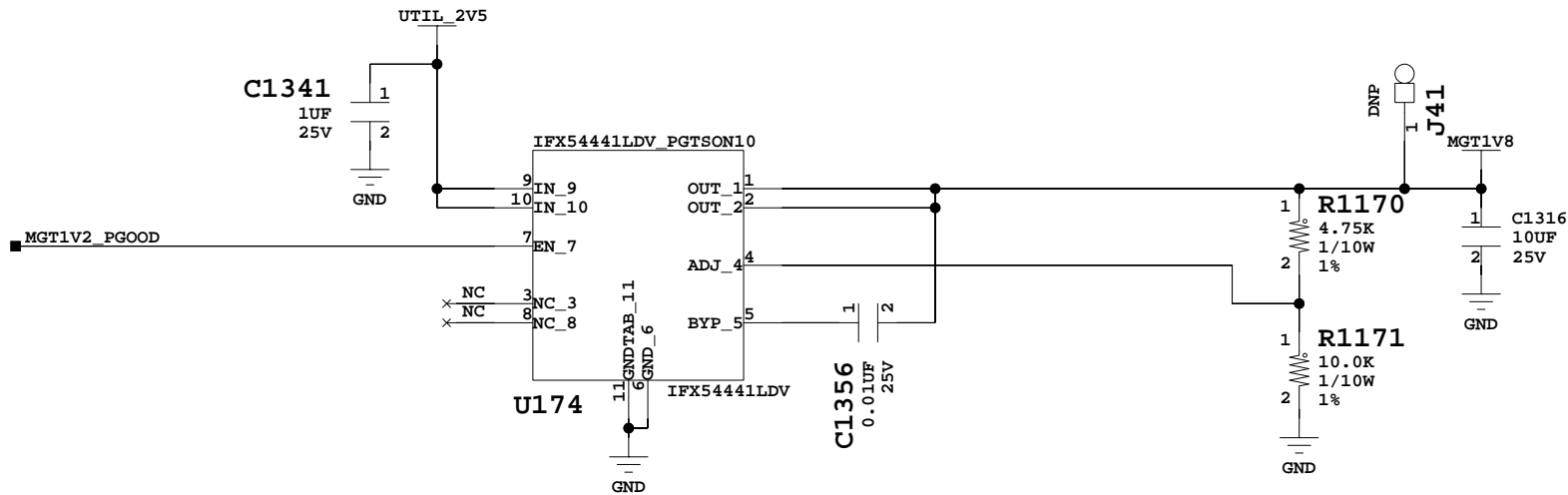
TITLE: VCC3V3 500mA Regulator SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:17		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	52	OF	58
		DRAWN BY:	BF



MGT1V2 1A Regulator

TITLE: MGT1V2 1A Regulator SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0		ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:16		VER:	1.0
SHEET SIZE: B		REV:	01
SHEET	53	OF	58
DRAWN BY:		BF	

DEFAULT = 1.81VDC

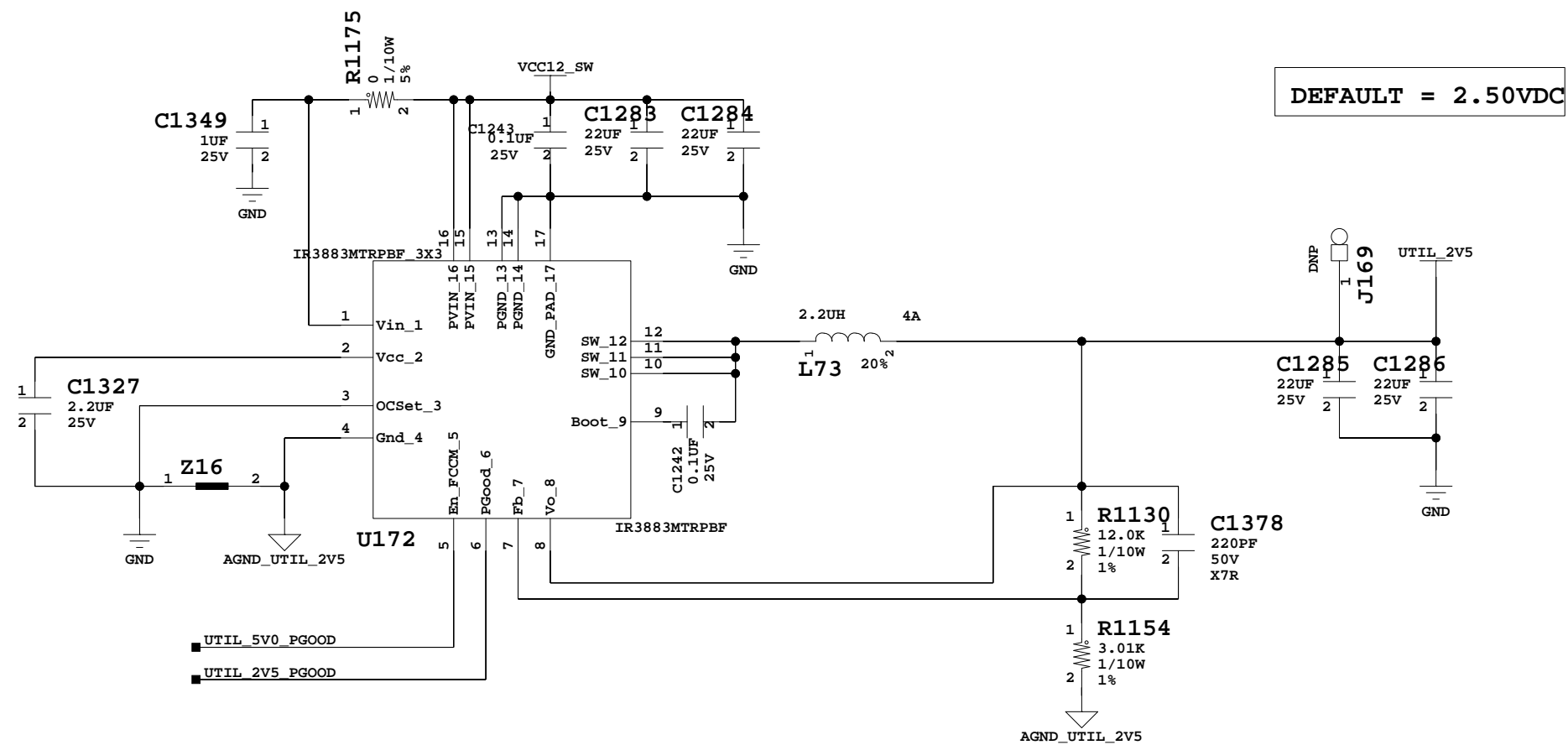


MGT1V8 100mA Regulator



TITLE: MGT1V8 100mA Regulator
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0
ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 54 OF 58	DRAWN BY: BF



UTIL_2V5 2A Regulator



TITLE: UTIL_2V5 2A Regulator
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N:	0432057
PCB P/N:	1280961
SCH P/N:	0381794
TEST P/N:	TSS0189

DATE: 01/26/2018:14:17

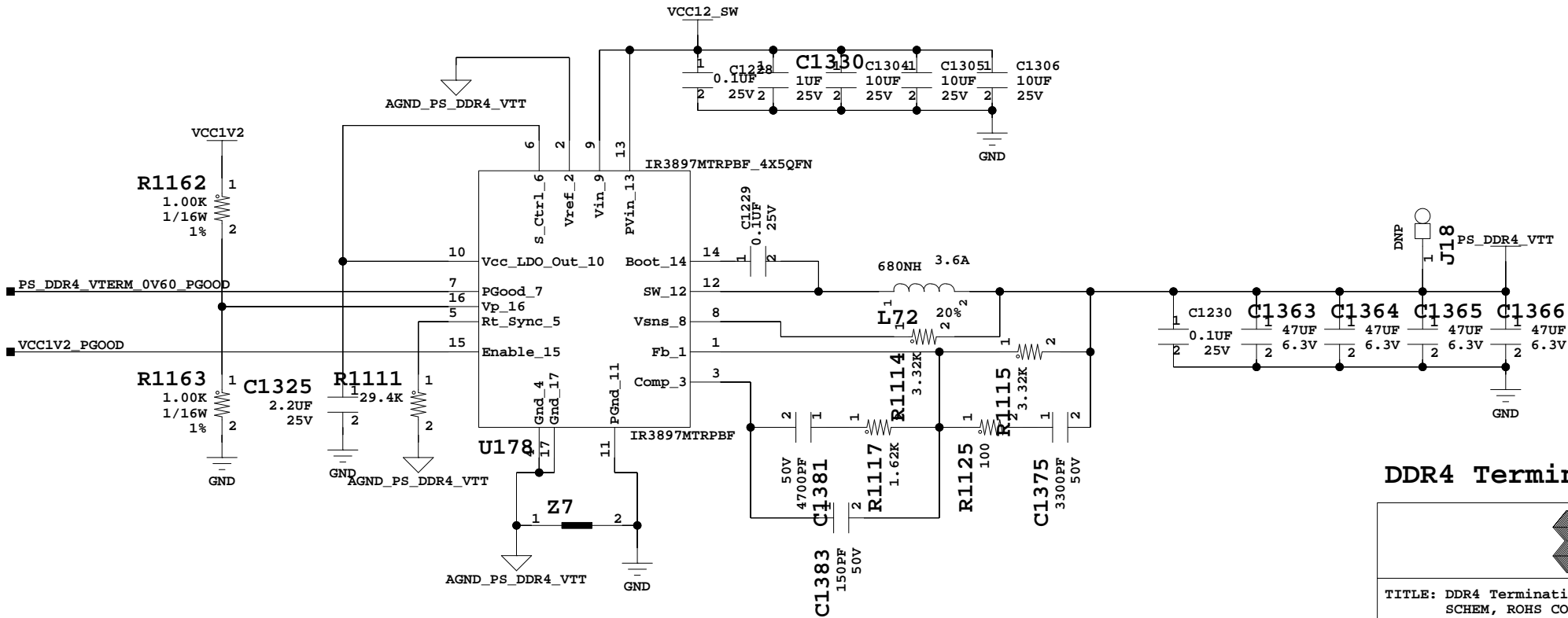
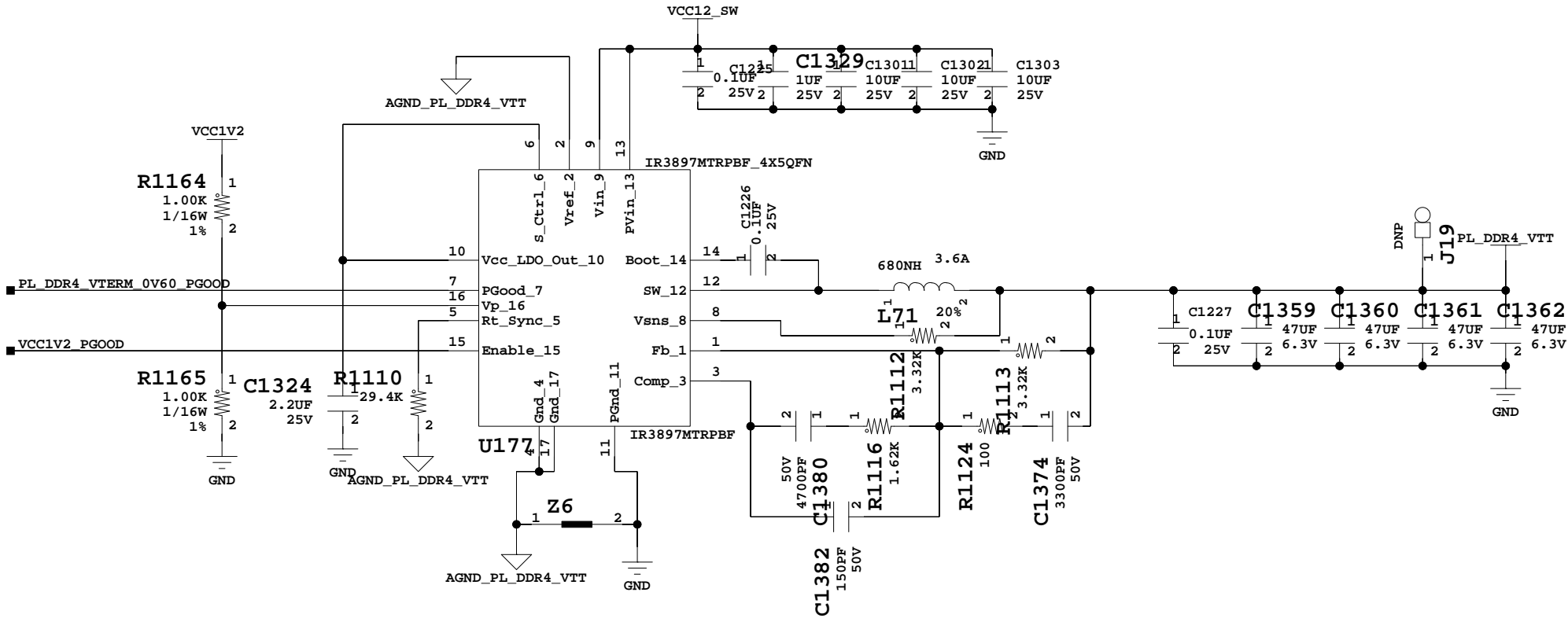
VER:	1.0
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SHEET SIZE: B


REV:	01
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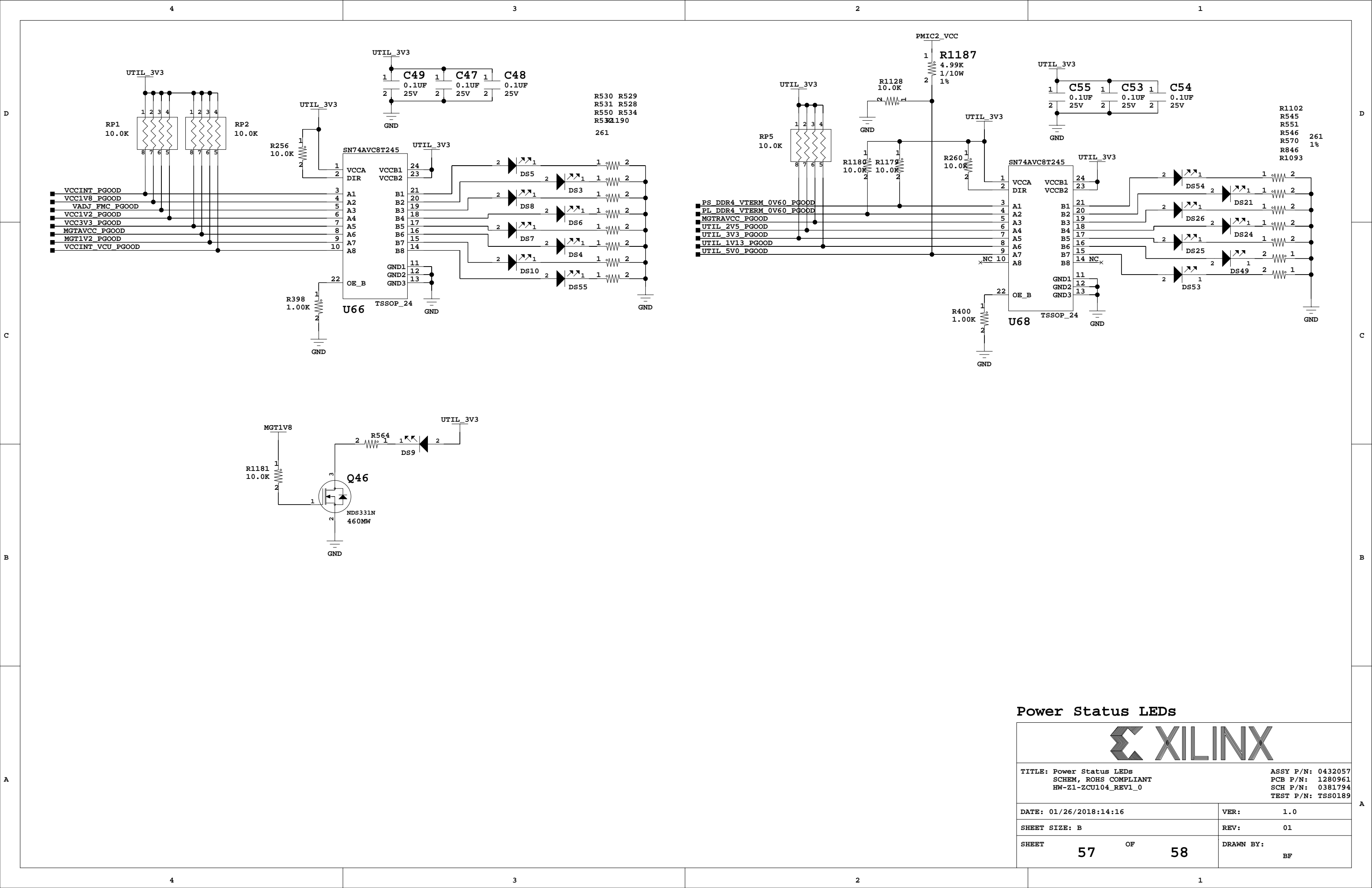
SHEET	55	OF	58
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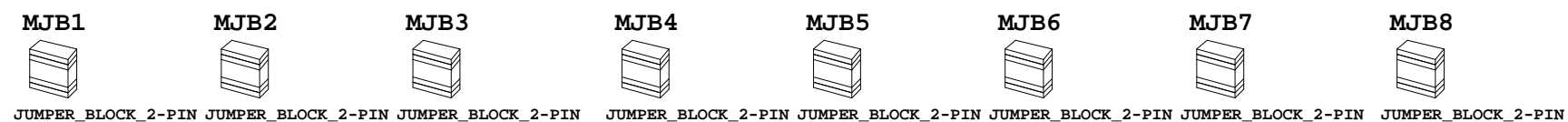
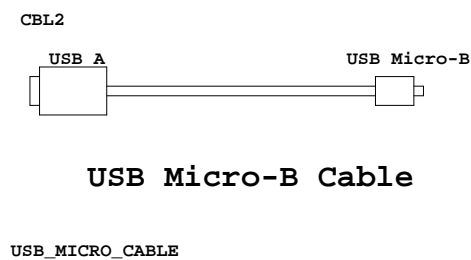
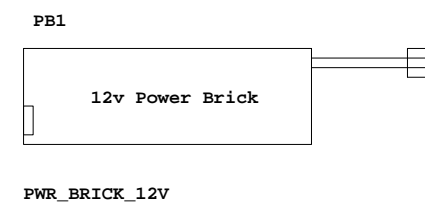
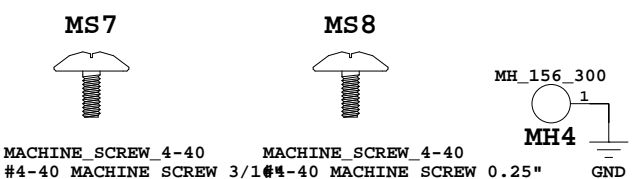
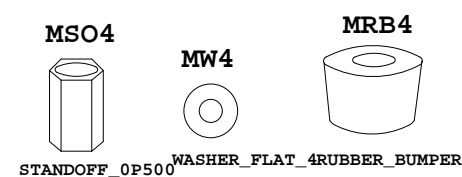
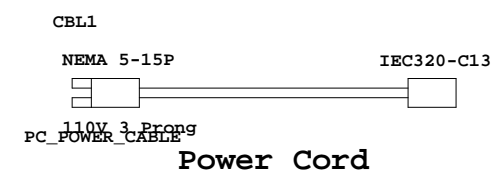
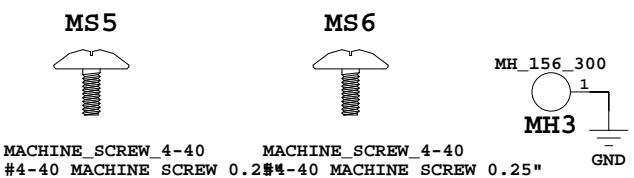
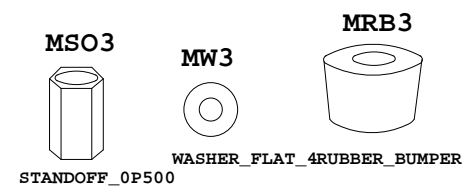
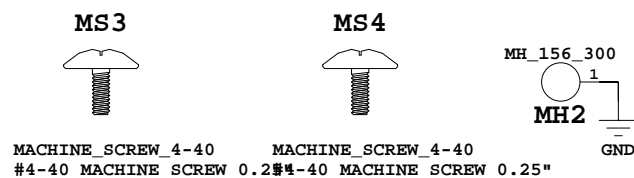
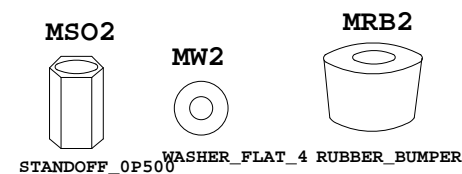
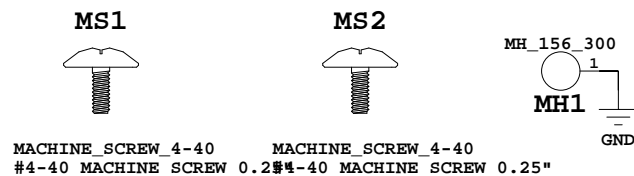
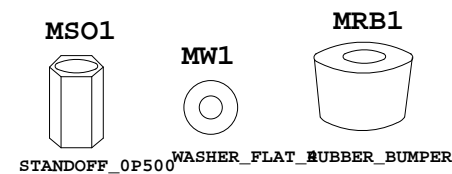
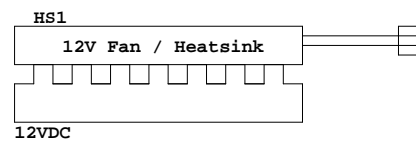
DRAWN BY:	BF
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DDR4 Termination Supplies

	
TITLE: DDR4 Termination Supplies SCHEM, ROHS COMPLIANT HW-Z1-ZCU104_REV1_0	
ASSY P/N: 0432057 PCB P/N: 1280961 SCH P/N: 0381794 TEST P/N: TSS0189	
DATE: 01/26/2018:14:21	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 56 OF 58	DRAWN BY: BF





Mechanical Components



TITLE: Mechanical Components
SCHEM, ROHS COMPLIANT
HW-Z1-ZCU104_REV1_0

ASSY P/N: 0432057
PCB P/N: 1280961
SCH P/N: 0381794
TEST P/N: TSS0189

DATE: 01/26/2018:14:16

VER:	1.0
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SHEET SIZE: B

REV:	01
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SHEET **58** OF **58**

DRAWN BY: BF