

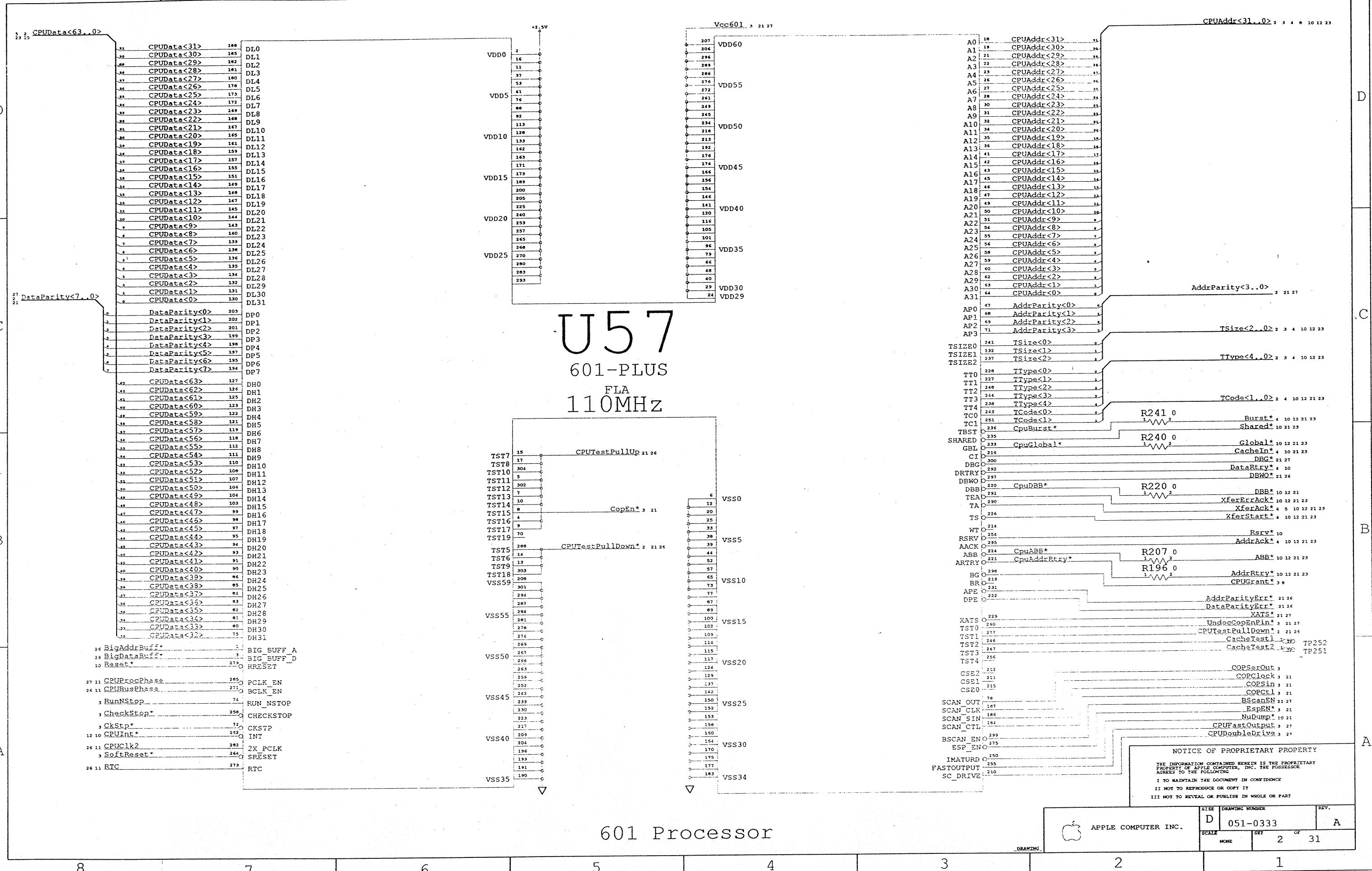
1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

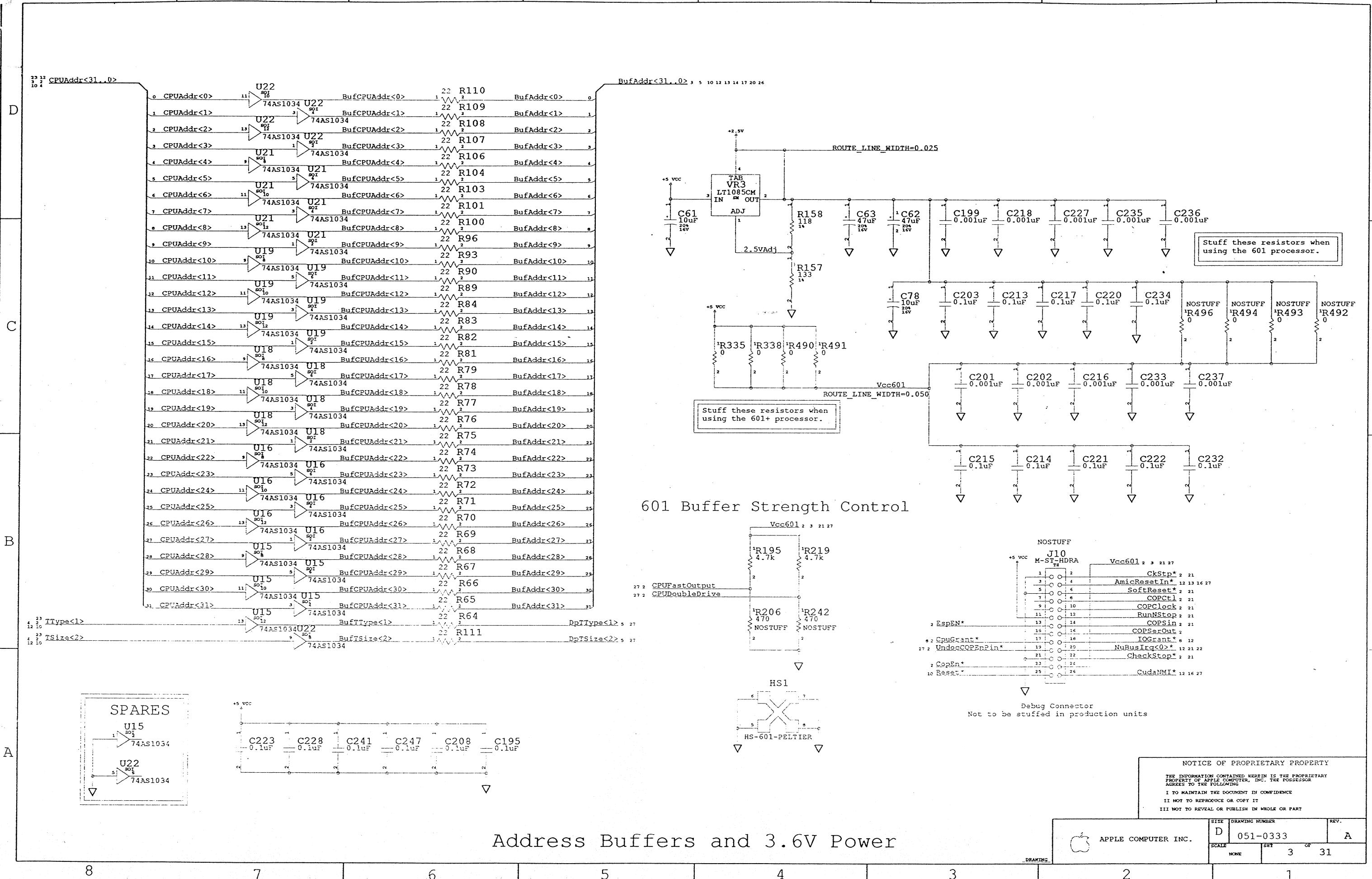
DESCRIPTION OF CHANGE			CK APPD	ENG APPD
REV	ZONE	ECN	DATE	DATE

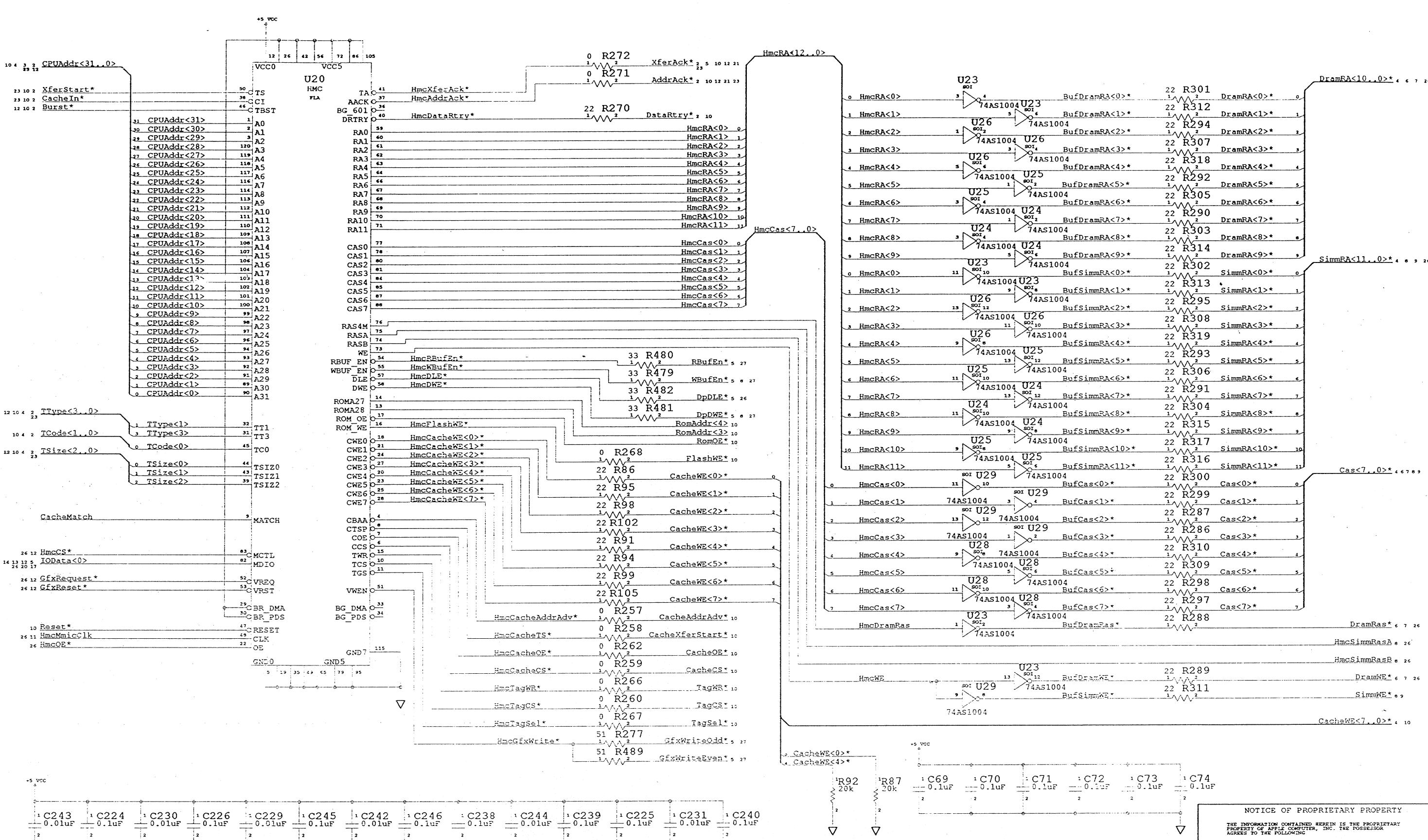
Power Macintosh 8100/110

- 1 Table of Contents
- 2 601 Processor
- 3 Address Buffers and 2.5V Power
- 4 HMC Memory Controller
- 5 Data Path Chips
- 6 8MB Motherboard DRAM - 1
- 7 8MB Motherboard DRAM - 2
- 8 DRAM SIMM Sockets - 1 & Memexp
- 9 DRAM SIMM Sockets - 2
- 10 PDS, Cache, ROM Connectors
- 11 Clocks, Lots of Clocks
- 12 IO Controller
- 13 Curio
- 14 Fast SCSI Controller
- 15 Serial (or Geo) Ports
- 16 Cuda, Power Monitor & ADB
- 17 Graphics DAC
- 18 Monitor & ADB Connectors
- 19 AWACS Sound Circuit
- 20 Floppy Disk
- 21 Terminators, Resistors & Stuff
- 22 NuBus Connectors
- 23 BART NuBus Controller
- 24 Ethernet & GeoPort Wakeup
- 25 Memory Data Termination
- 26 Test Points 1
- 27 Test Points 2
- 28-31 Cross Reference Tables

DIMENSIONS ARE IN MILLIMETERS		METRIC		Apple Computer Inc.	
XX	XX	DESIGN CK	XX	NOTICE OF PROPRIETARY PROPERTY	
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ANGLES :		ENG APPD	HMC APPD	TITLE	
DO NOT SCALE DRAWING		GA APPD	DESIGNER	Schematic, Main Logic Board	
RELEASE		/	SCALE	Power Macintosh 8100/110	
DRAWING		NONE		DRAWING NUMBER	051-0333
THIRD ANGLE PROJECTION		MATERIAL/FINISH NOTED AS APPLICABLE	SIZE	REV.	A
			D	SHT	1 OF 31







Half A Memory Controller

23 10 5 2 CPUData<63..0>

D

D

C

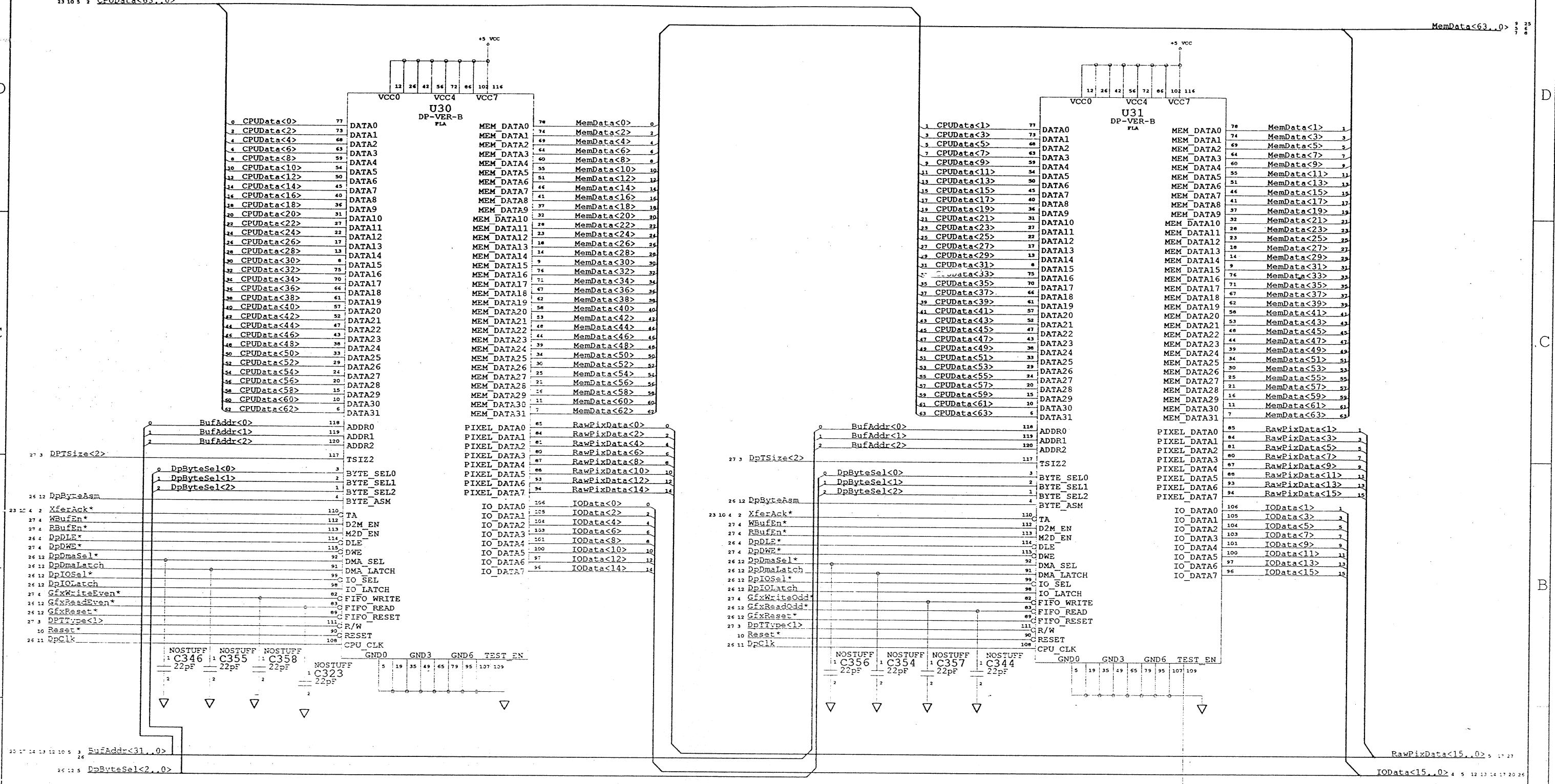
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Data Path Chips

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DRAWING NUMBER	SIZE		REV.
	D	5051-0333	
SCALE	None	5	31
SHR	OF		



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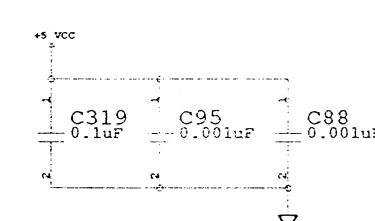
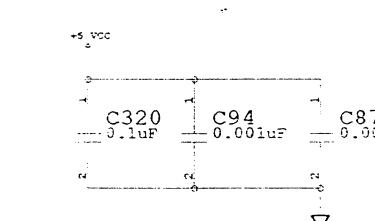
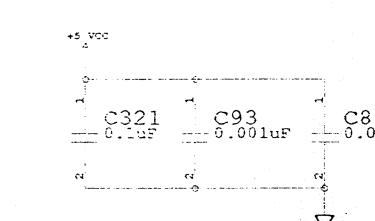
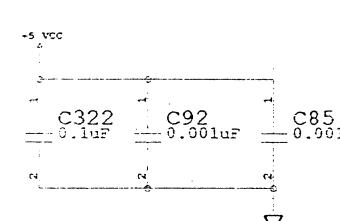
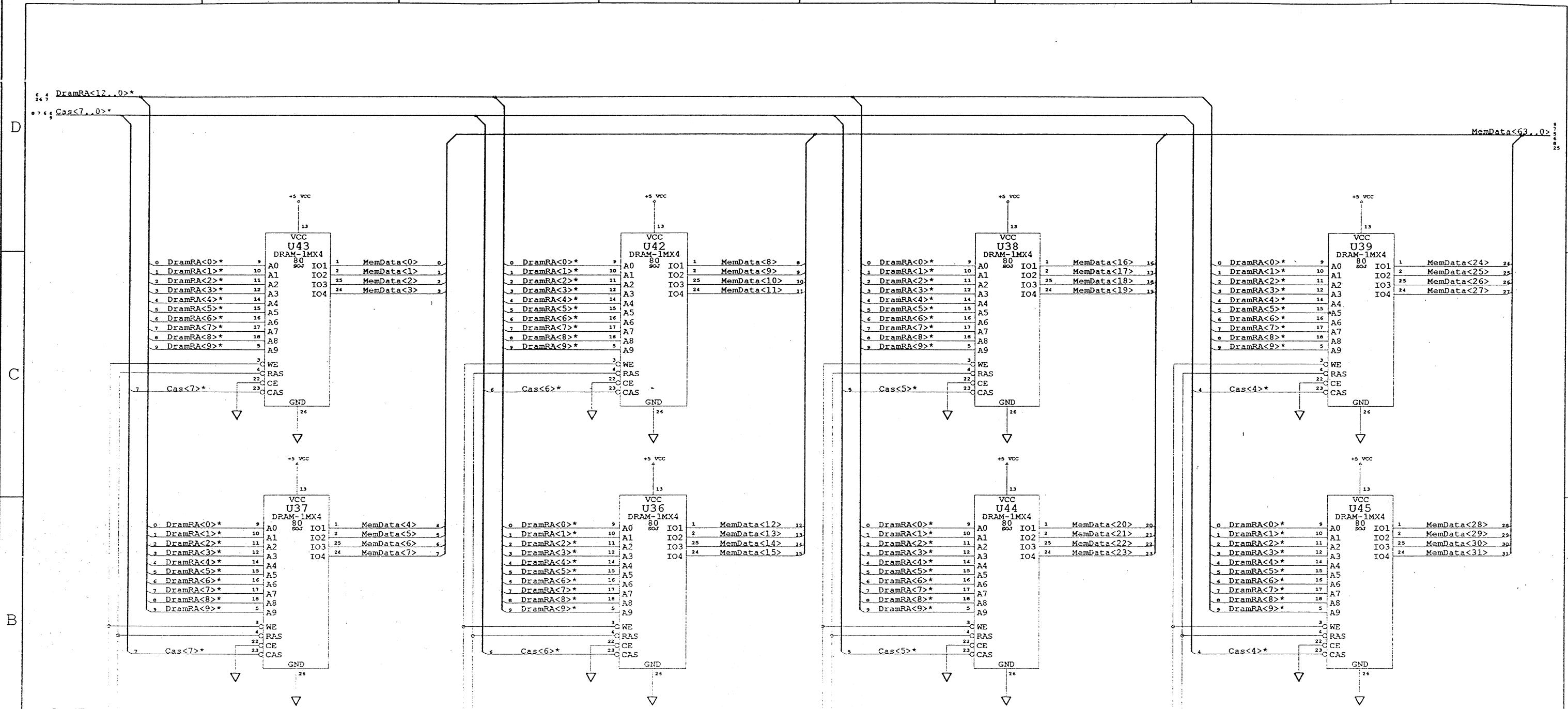
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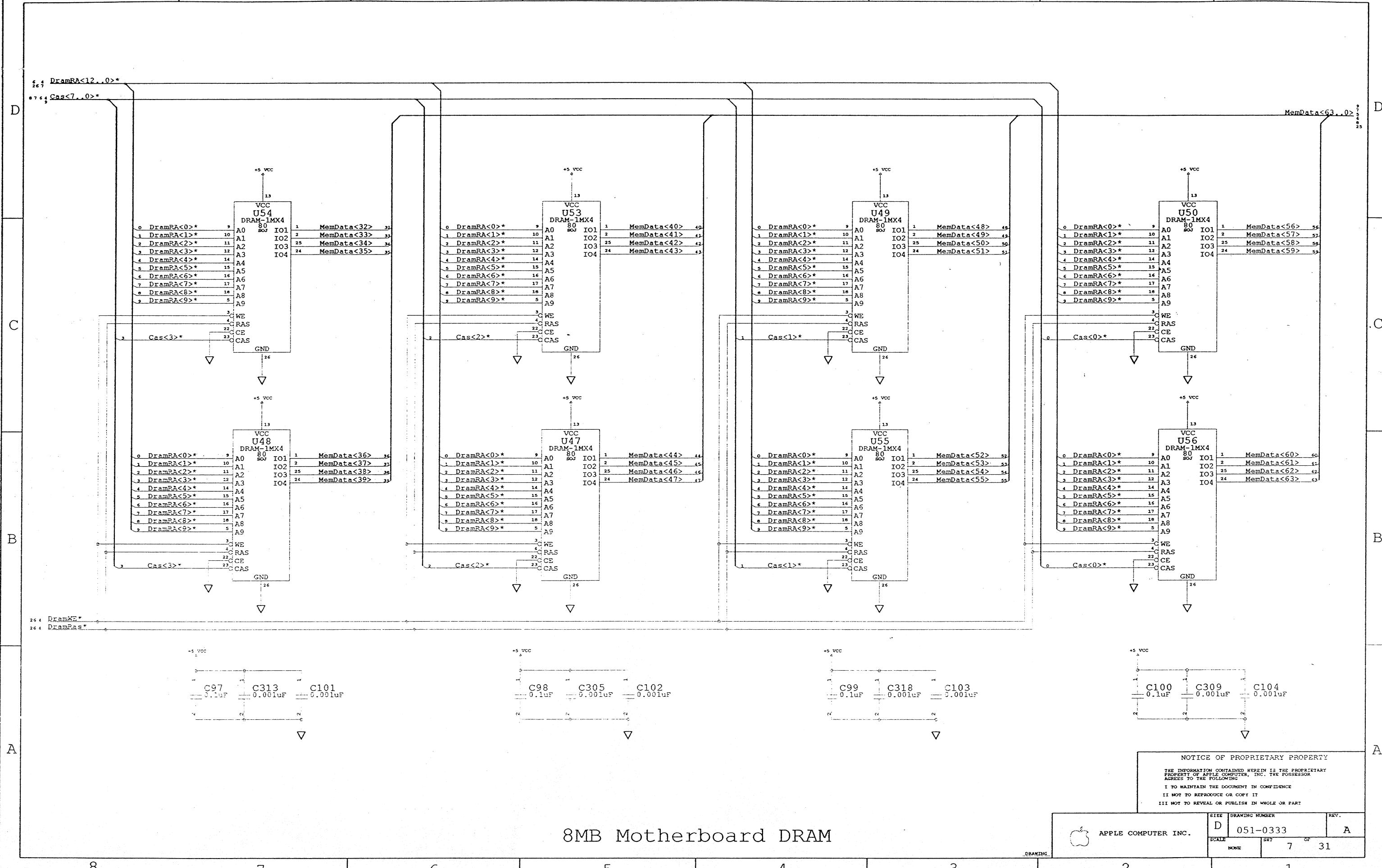
1



8MB Motherboard DRAM

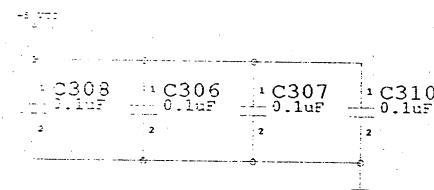
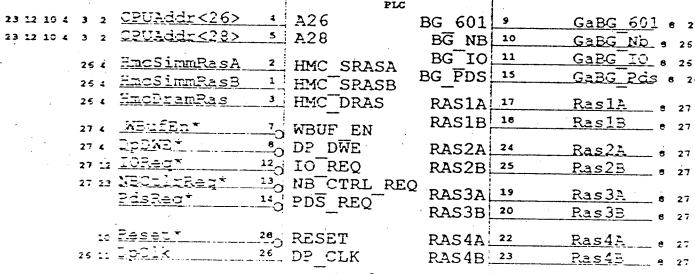
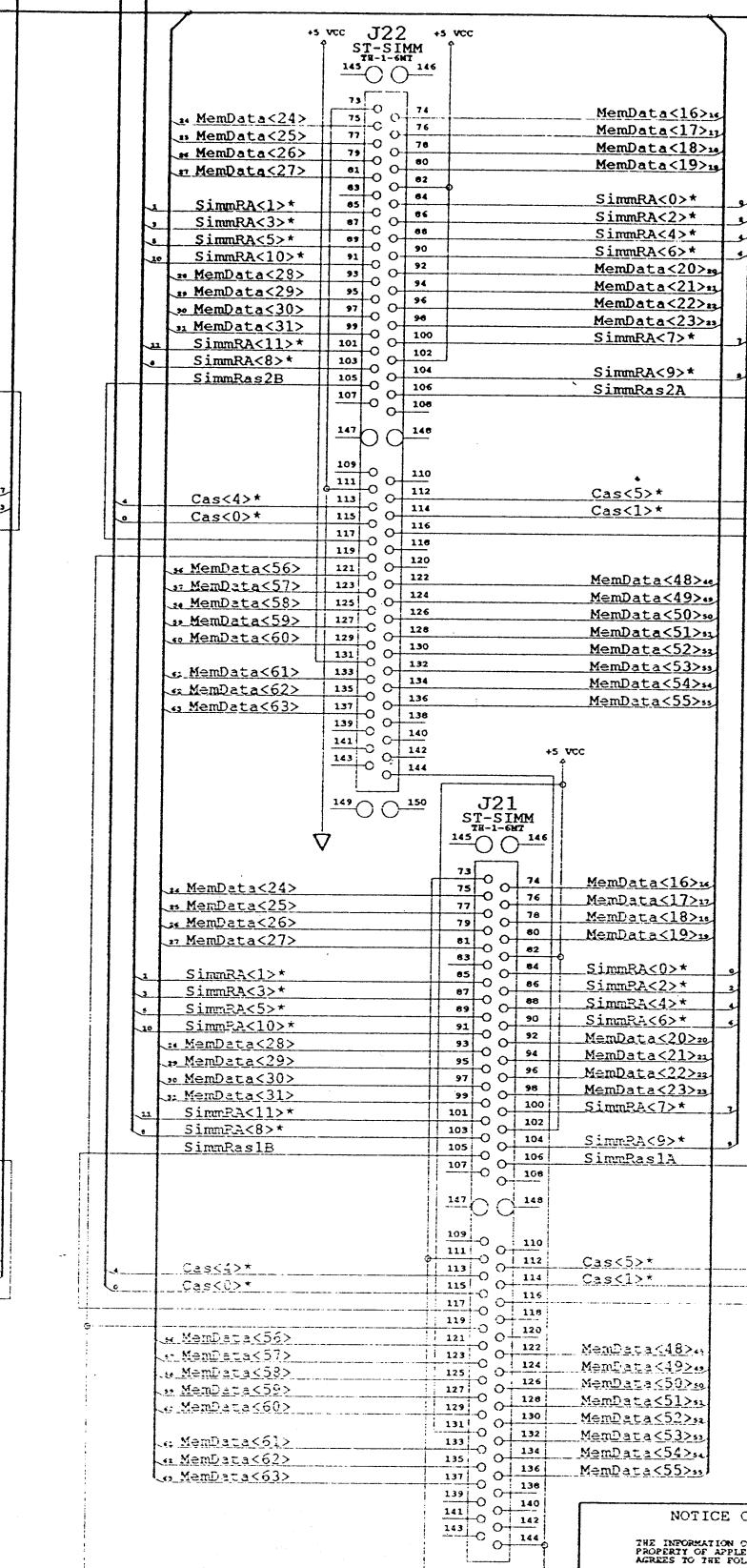
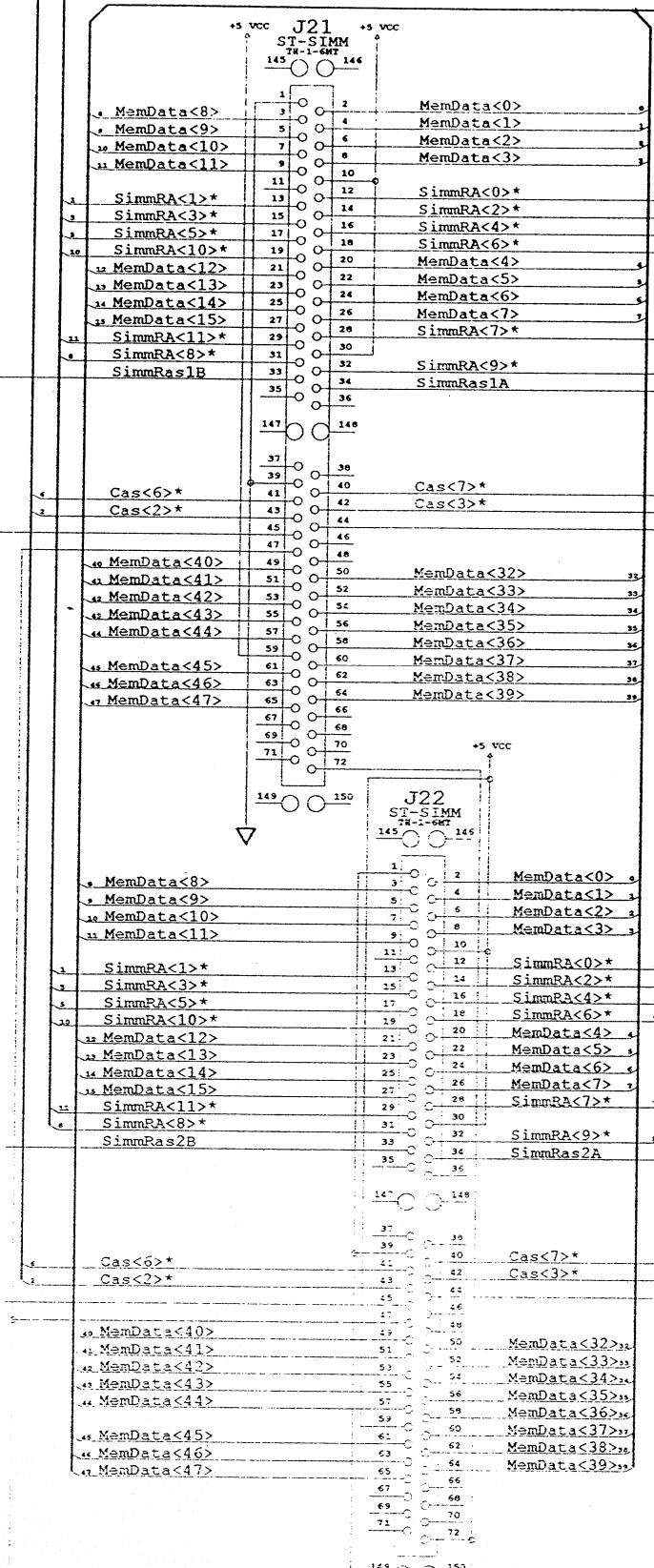
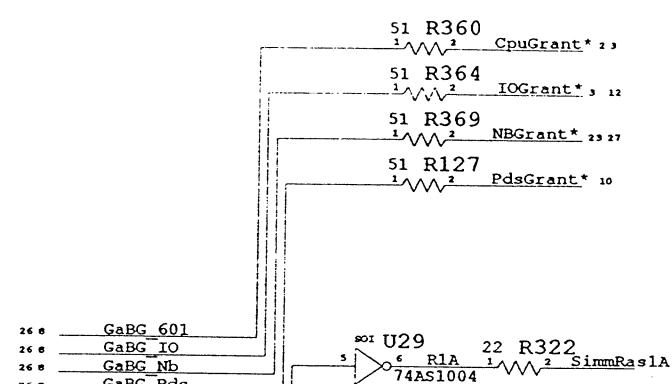
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DRAWING	SIZE	DRAWING NUMBER	REV.
D	051-0333		A
SCALE	SHY	6	31



26 9 6 4 SimmRA<12..0>*

9 8 7 6 4 Cas<7..0>*



4 SimmWE*

DRAM SIMM Sockets

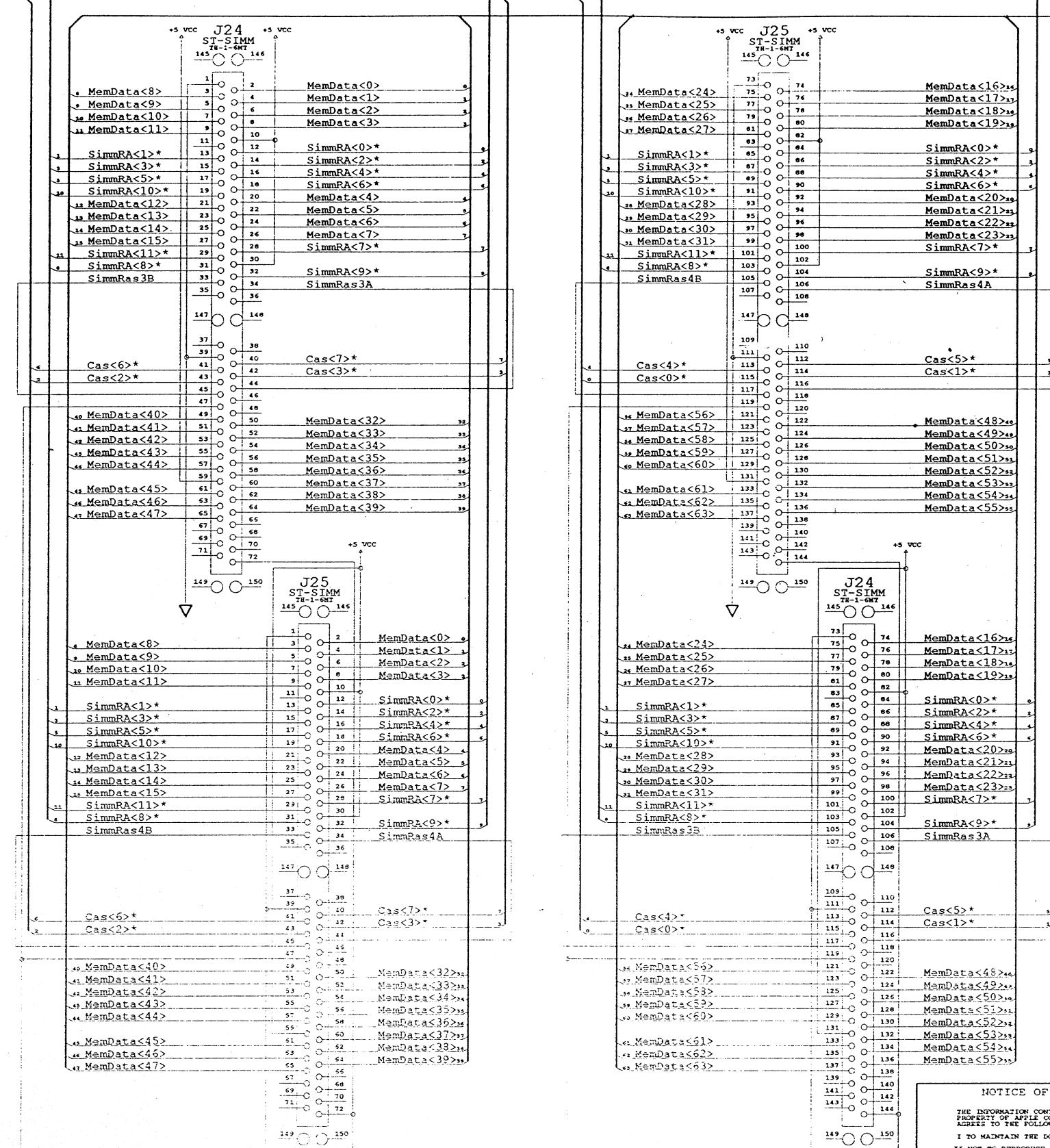
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DRAWING	SIZE	DRAWING NUMBER	REV.	DR		
				SCALE	SHT	ON
APPLE COMPUTER INC.	D	051-0333	A	NONE	8	31

269 * 4 SimmRA<12..0>*
 98764 Cas<7..0>*

4 SimmRA*

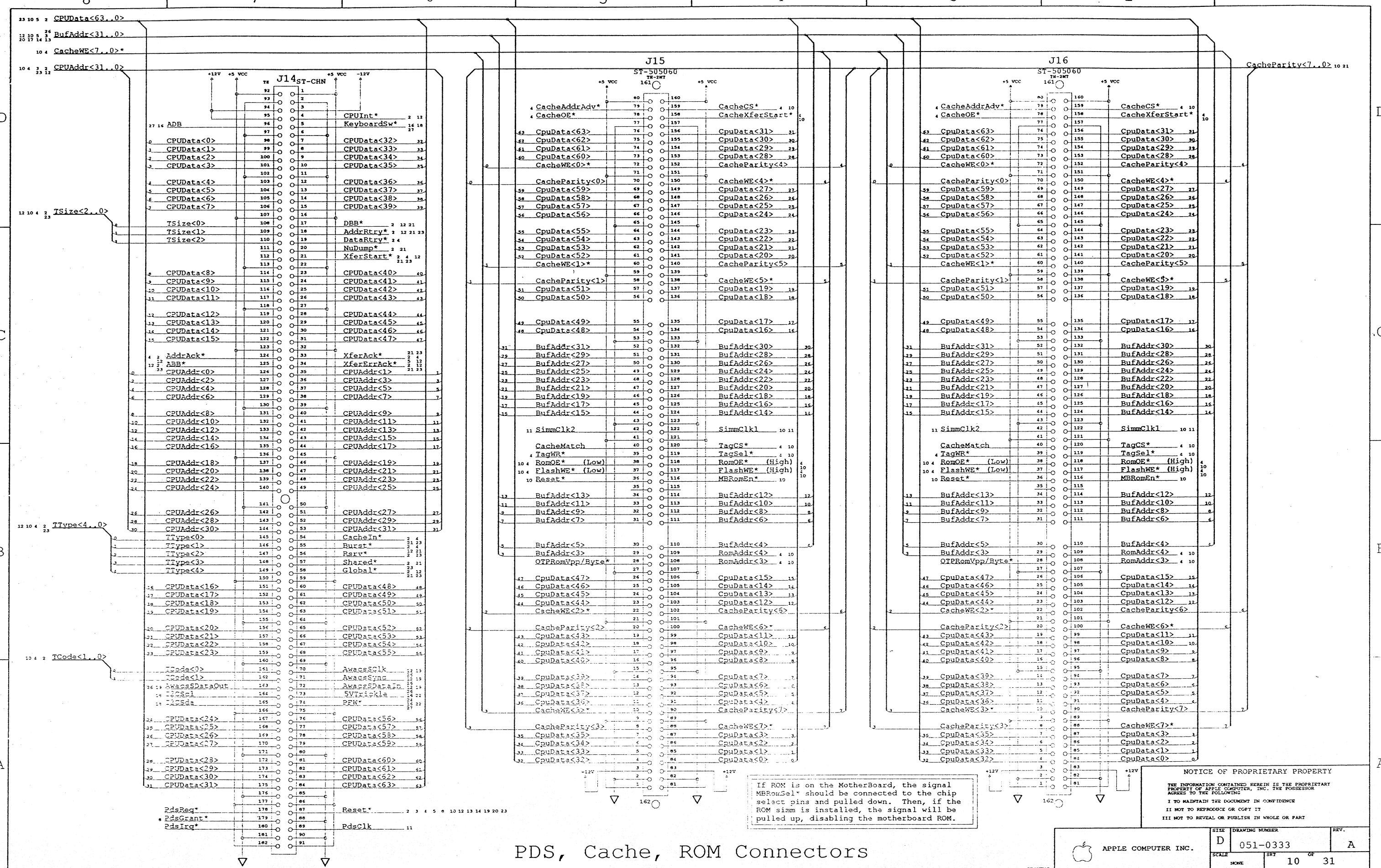
DRAM SIMM Sockets



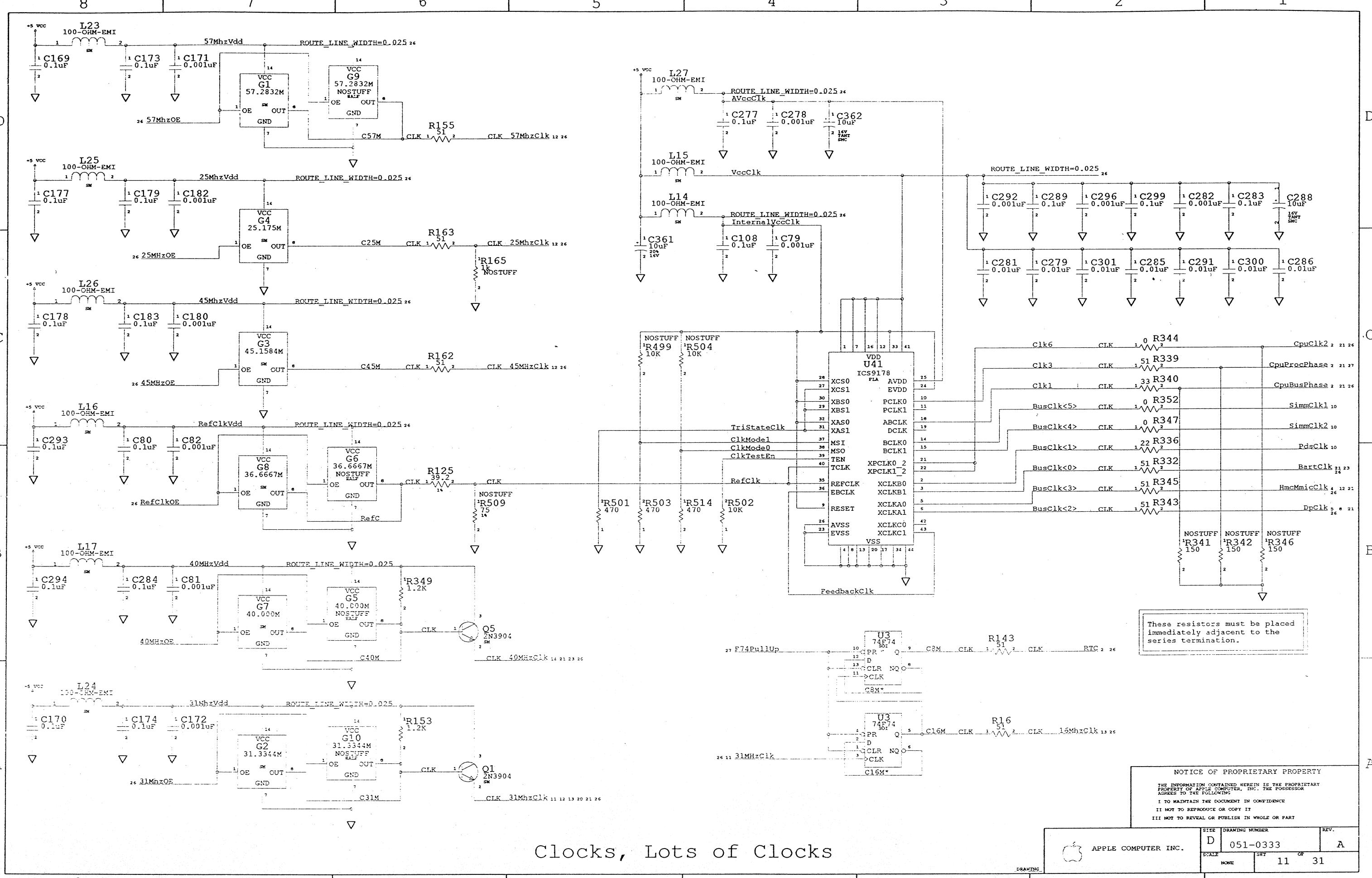
MemData<63..0>

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DRAWING	SIZE	DRAWING NUMBER	REV.
D		051-0333	A
SCALE	WT	OF	
NONE	9	31	



PDS, Cache, ROM Connectors



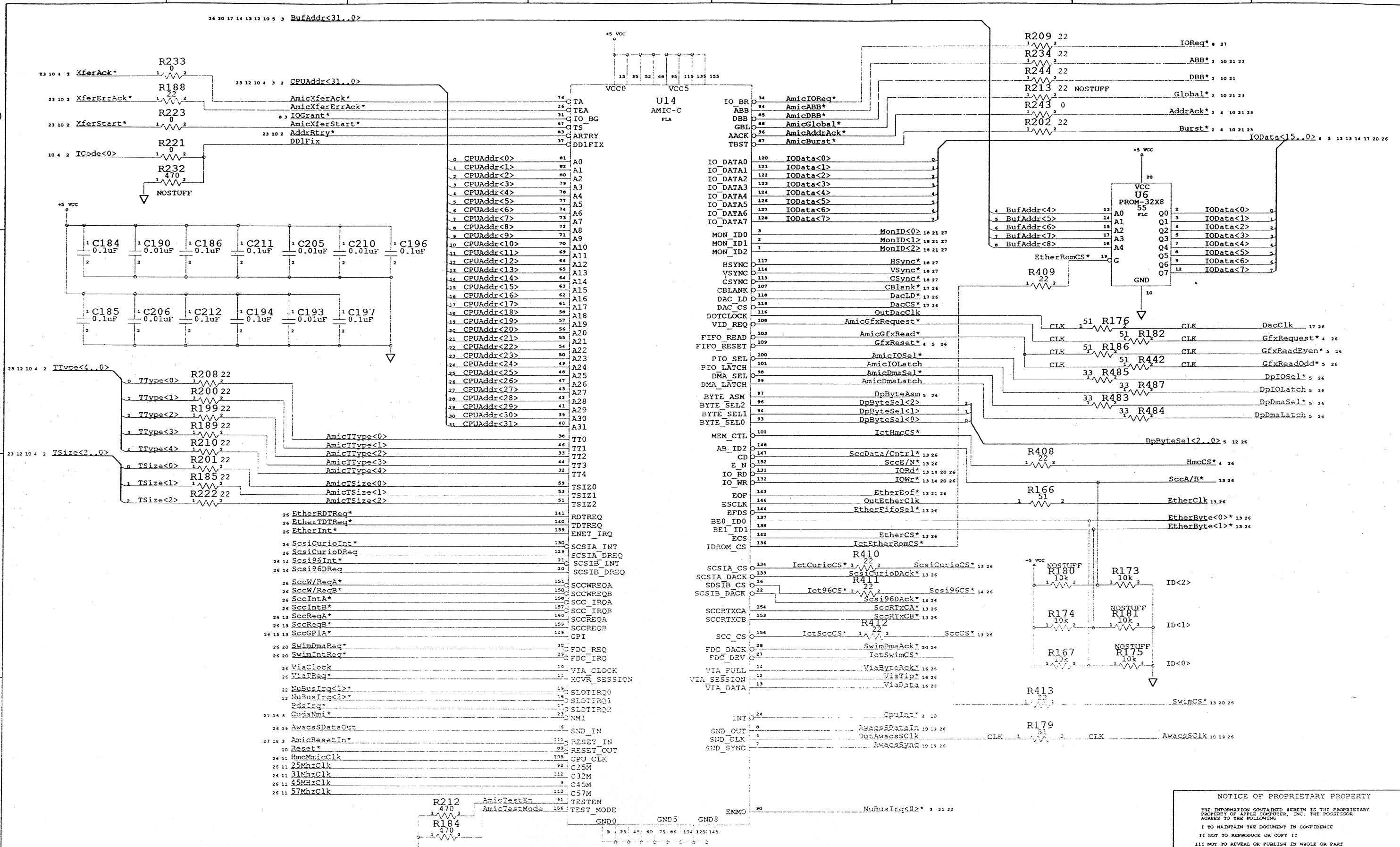
Clocks, Lots of Clocks

These resistors must be placed immediately adjacent to the series termination.

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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-0333	REV. A
	SCALE NONE	SHT 11 OF 31	

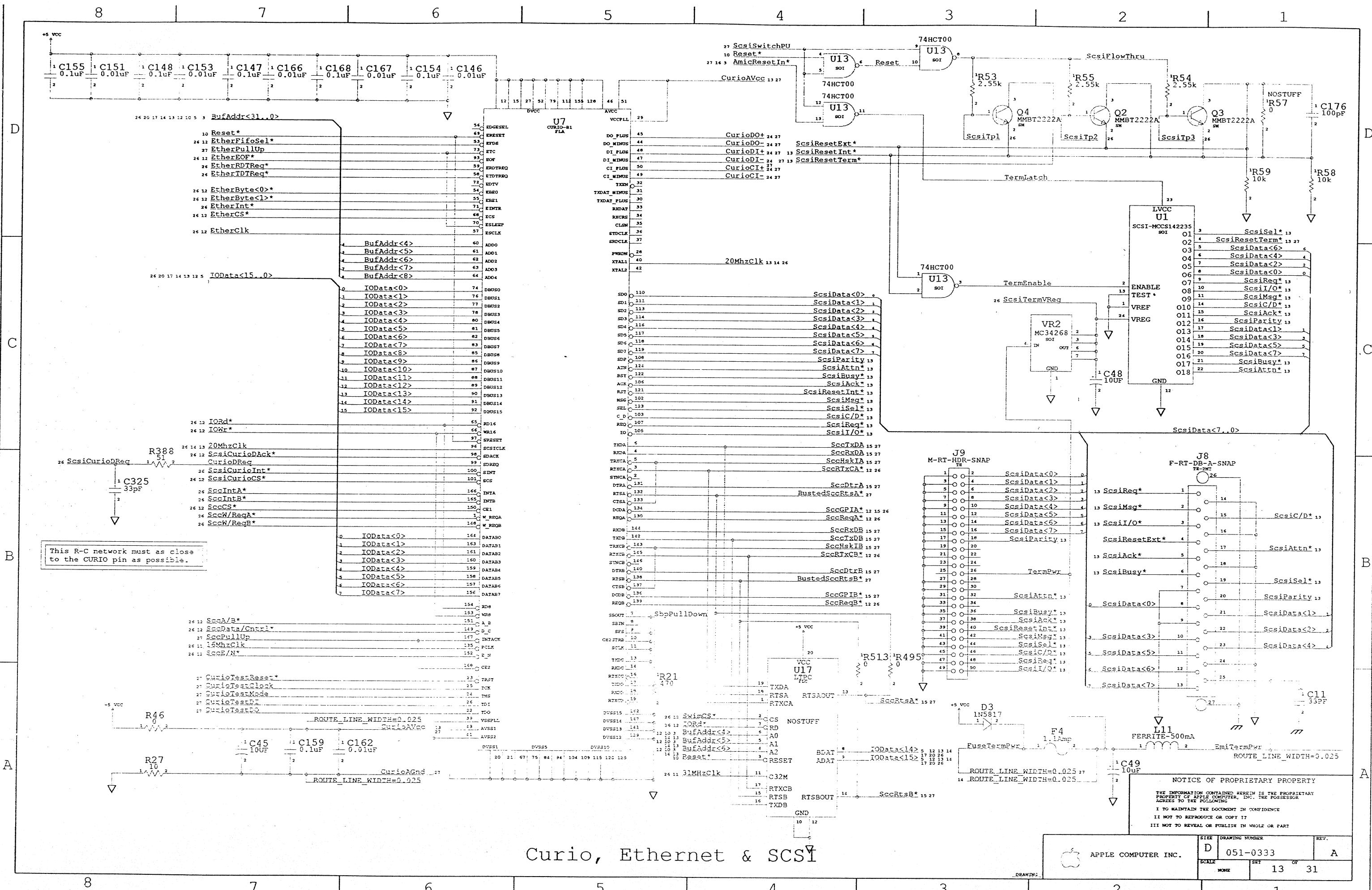
26 20 17 14 13 12 10 5 3 BufAddr<31..0>



Apple Miscellaneous Interface Chip & Ethernet ROM

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1	12	051-0333	A



Curio, Ethernet & SCSI

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SIZE	DRAWING NUMBER
------	----------------

D

B INC D 051-0333

SCALE SHT

SCALE SH1 13

	APPLE COMPUTER INC.	D	051-0333	A
		SCALE	SWT	OF
		NONE	13	31

D

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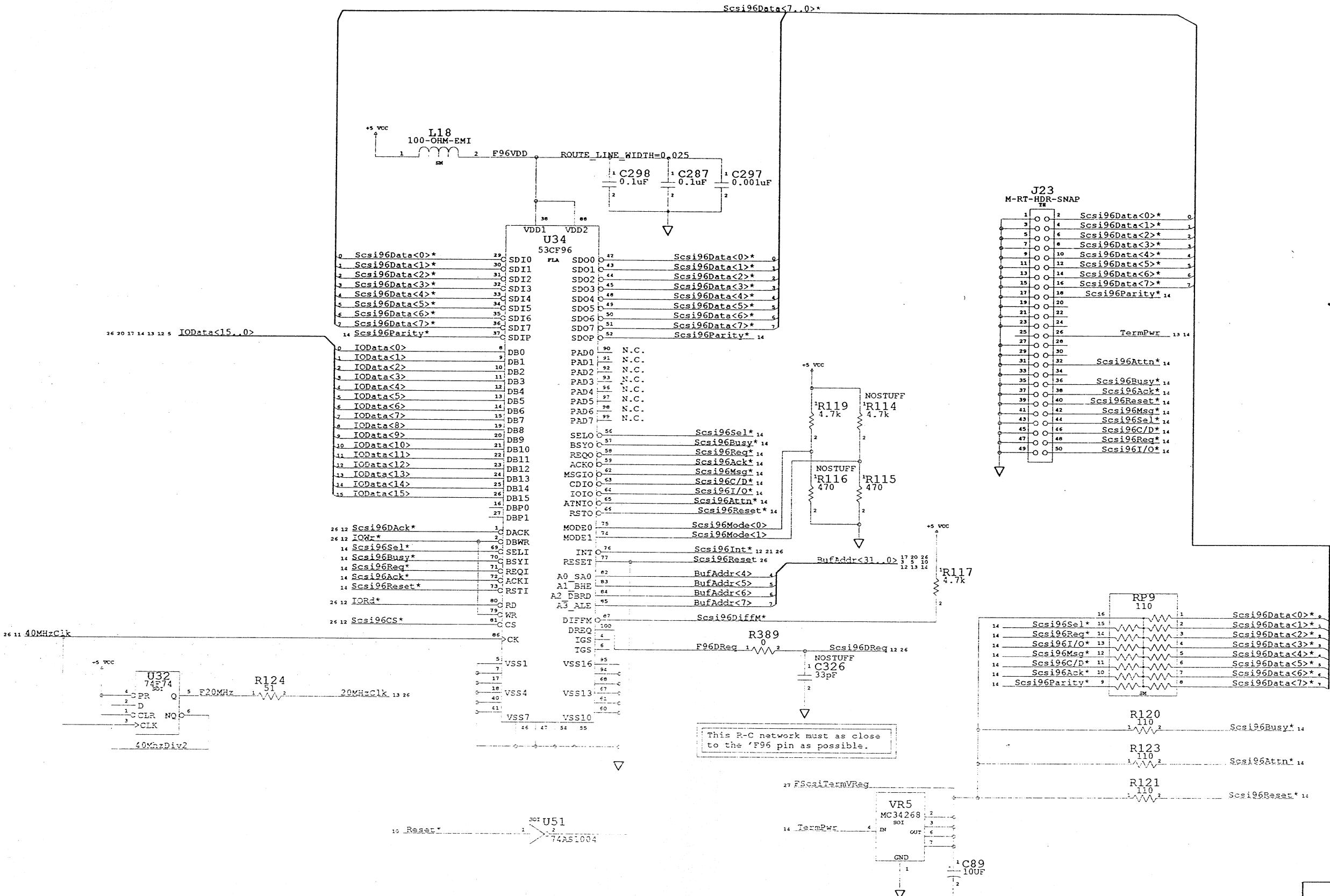
C

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A

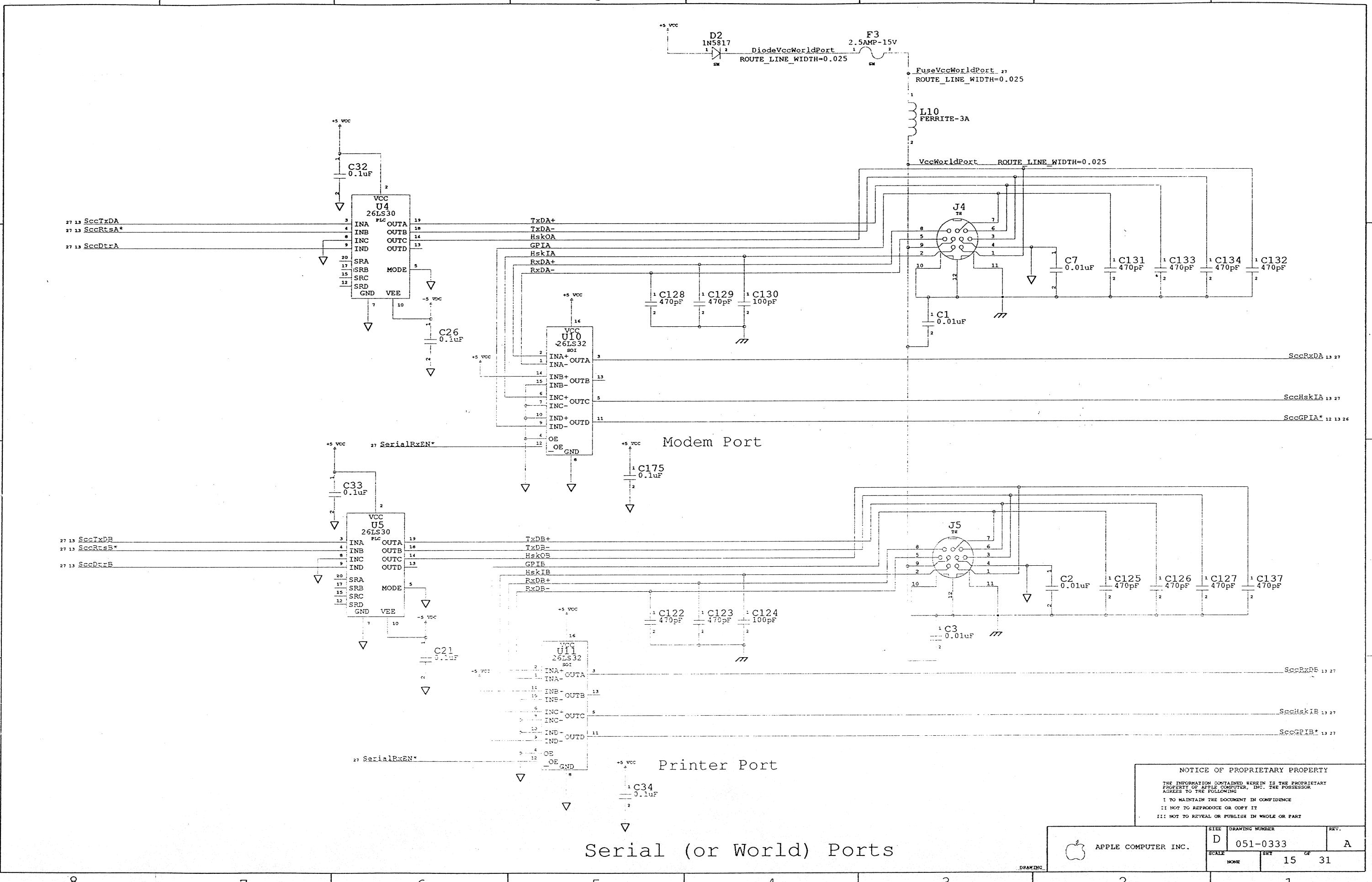


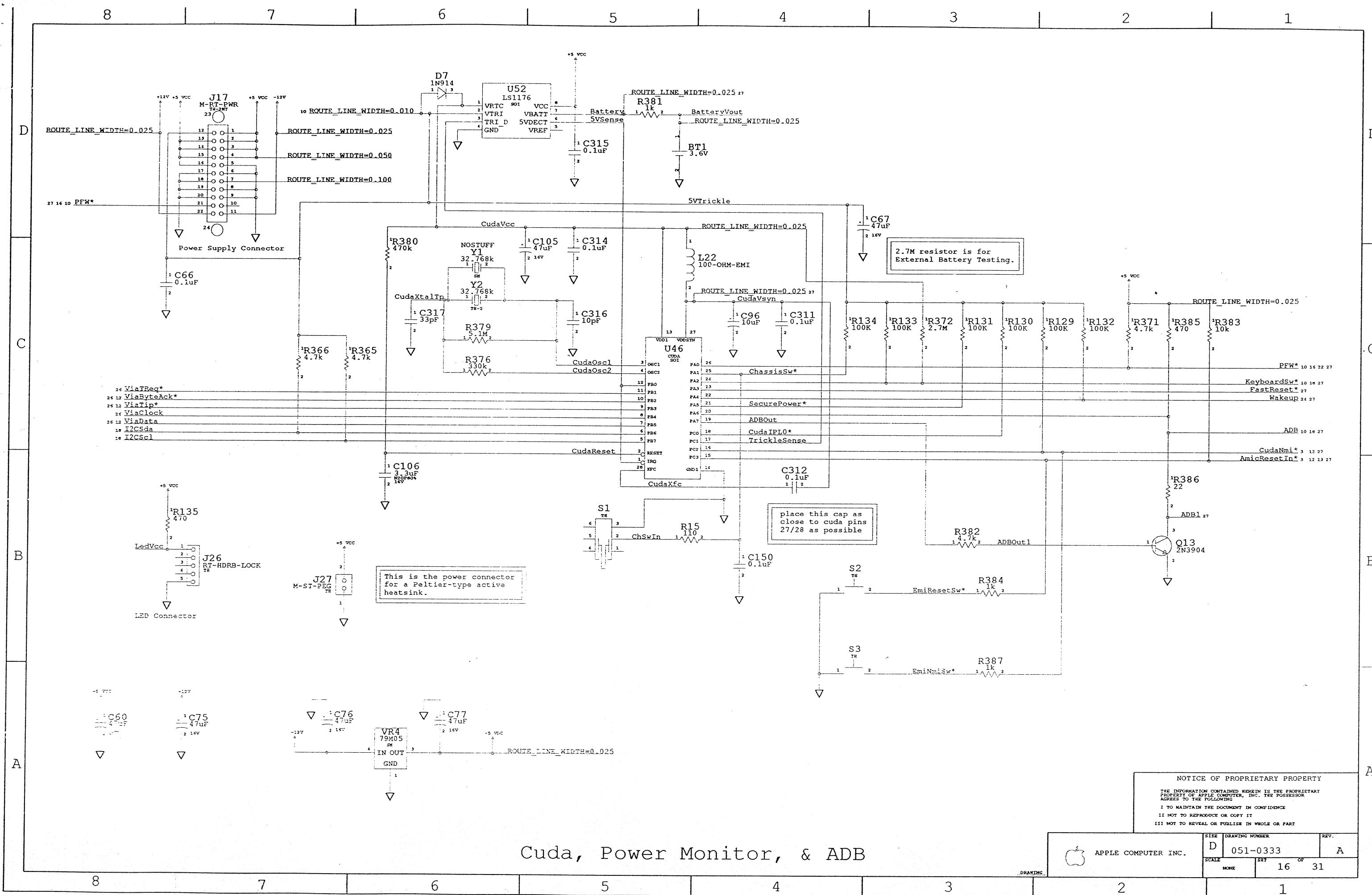
Fast Internal SCSI

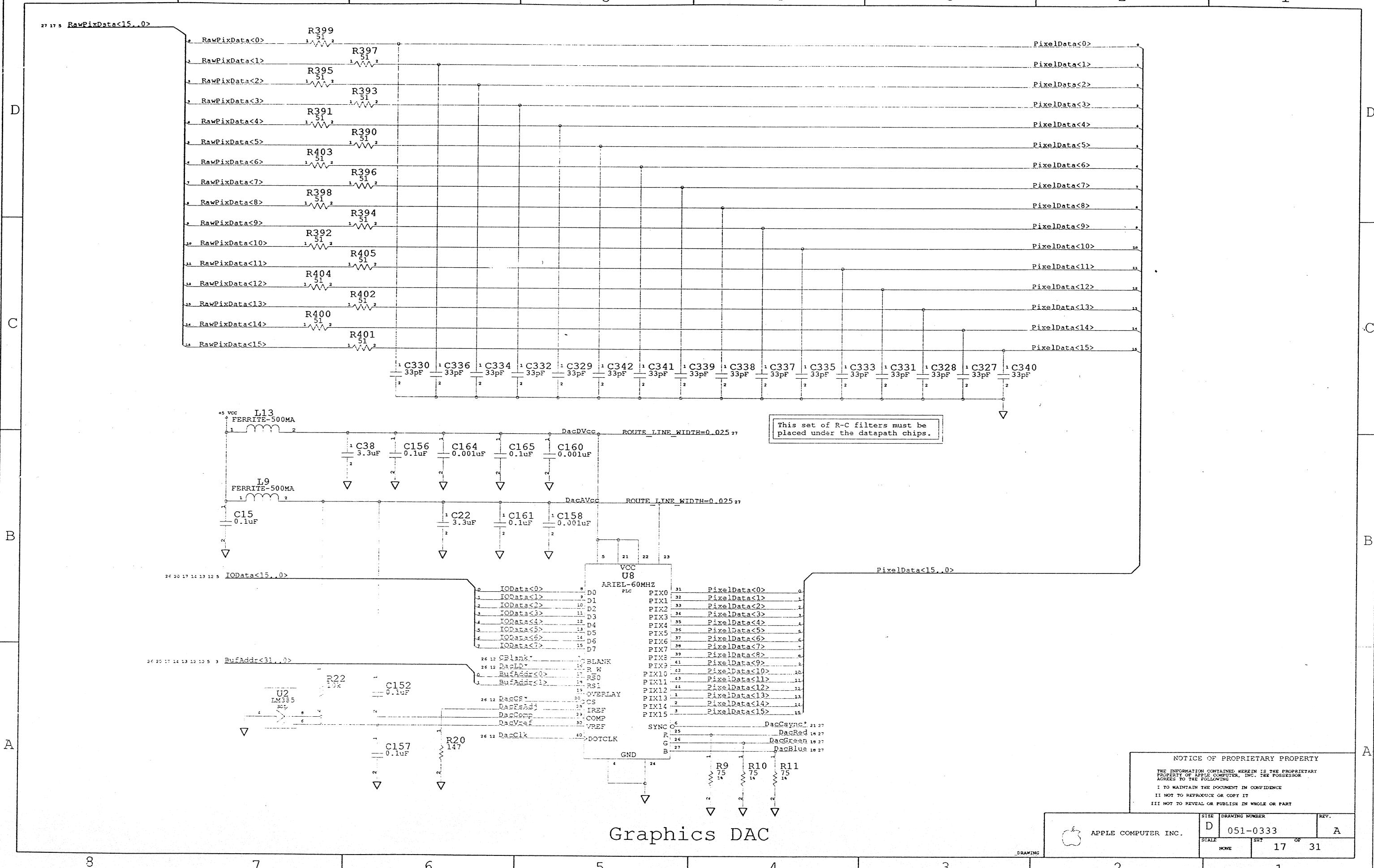
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	051-0333		A
SCALE	SHT	14	31
NONE			

8 | 7 | 6 | 5 | 4 | 3 | 2 | 1







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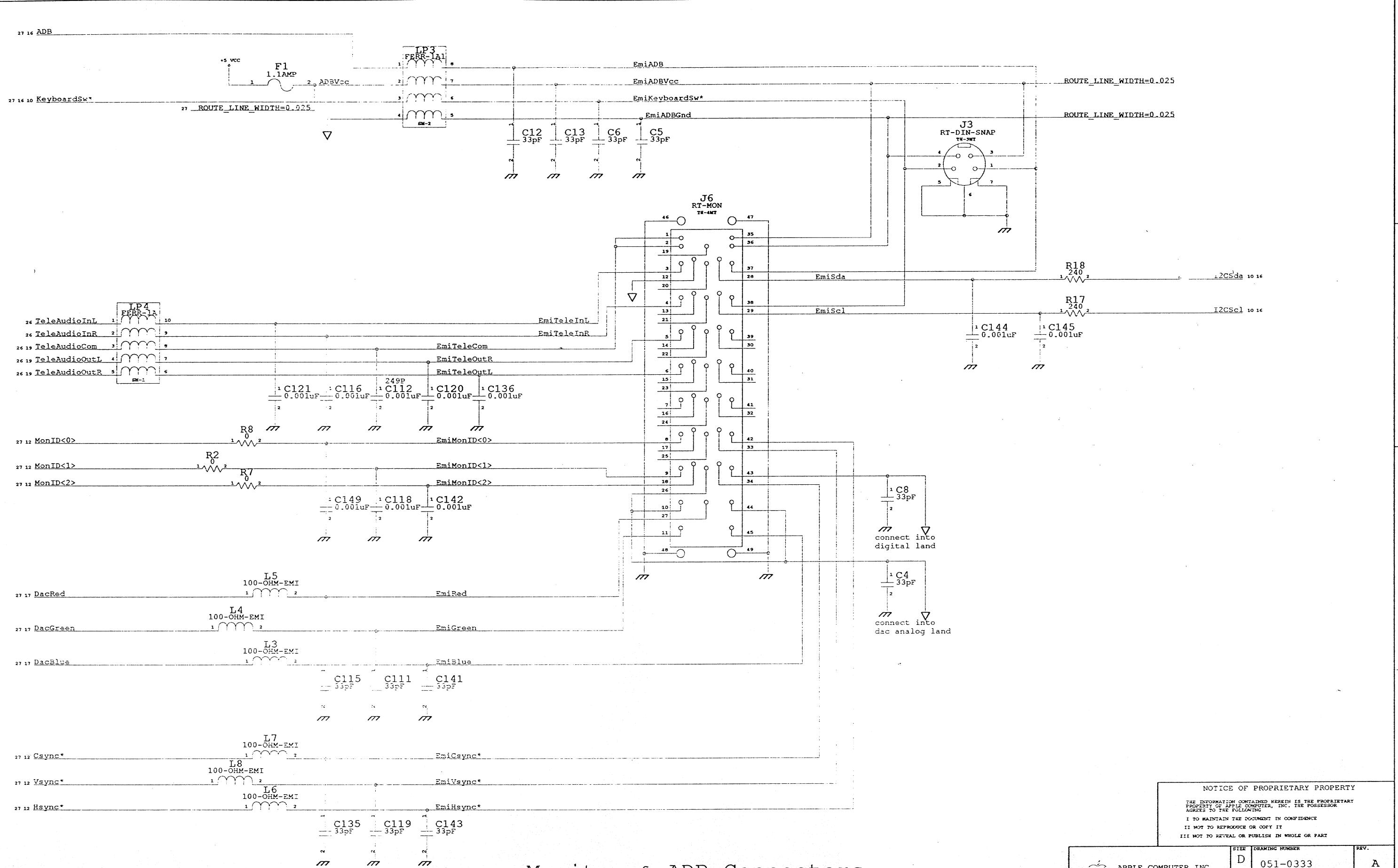
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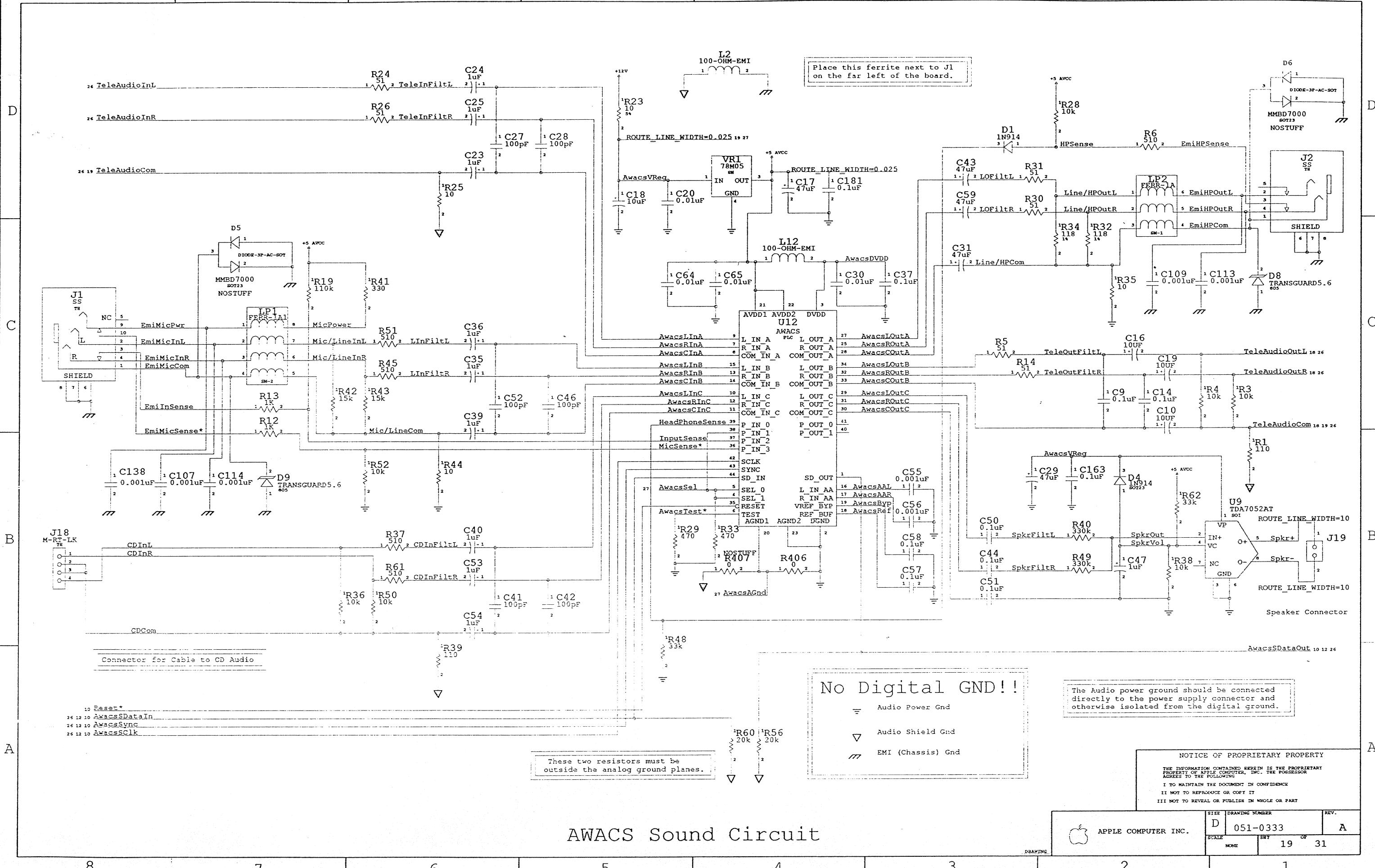
4

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1





8 | 7 | 6 | 5 | 4 | 3 | 2 | 1

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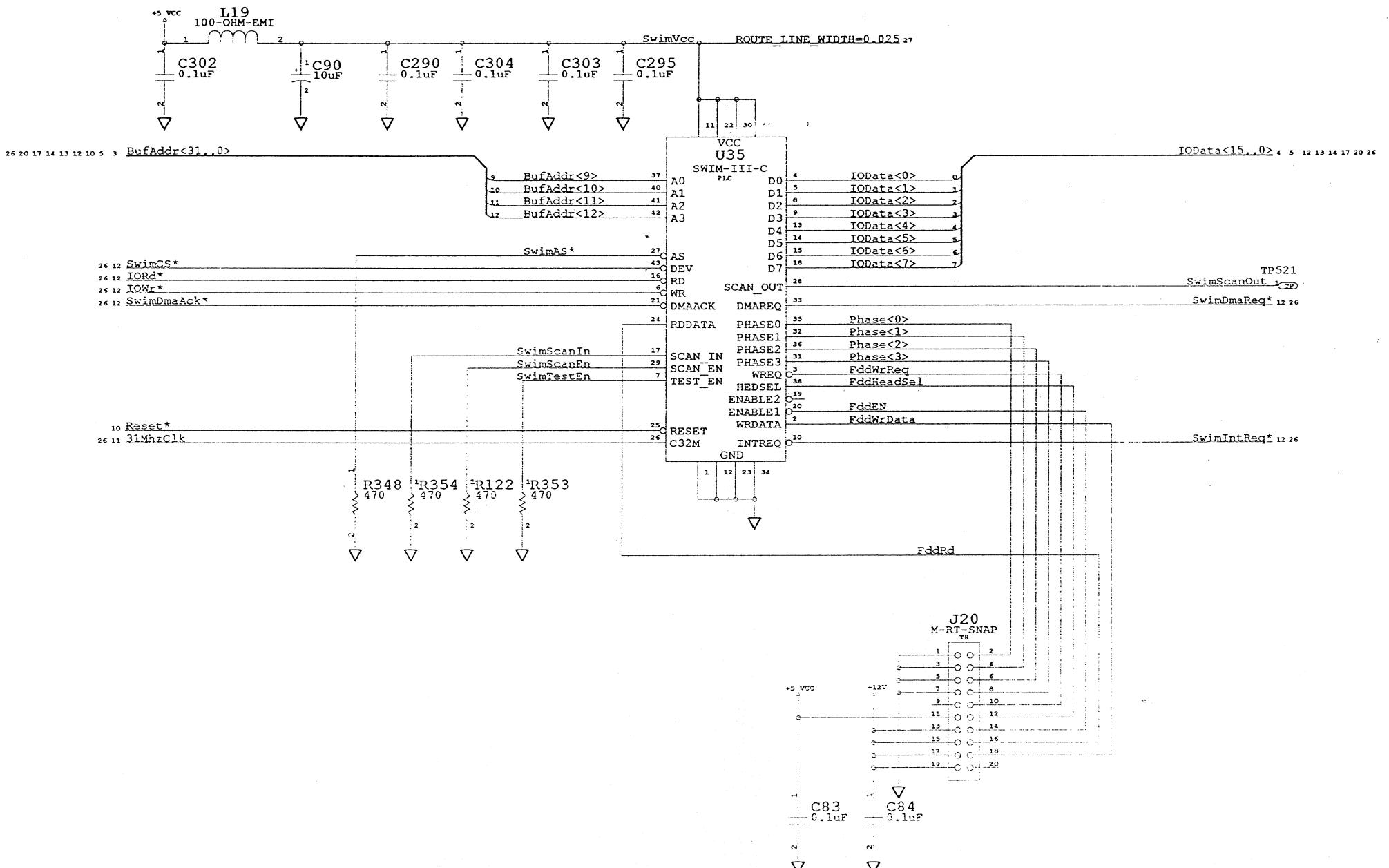
C

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Floppy Disk

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DRAWING	SIZE	DRAWING NUMBER		REV.
		051-0333	REV. A	
DRAWING	SCALE NONE	SAT 20	OF 31	

8 | 7 | 6 | 5 | 4 | 3 | 2 | 1

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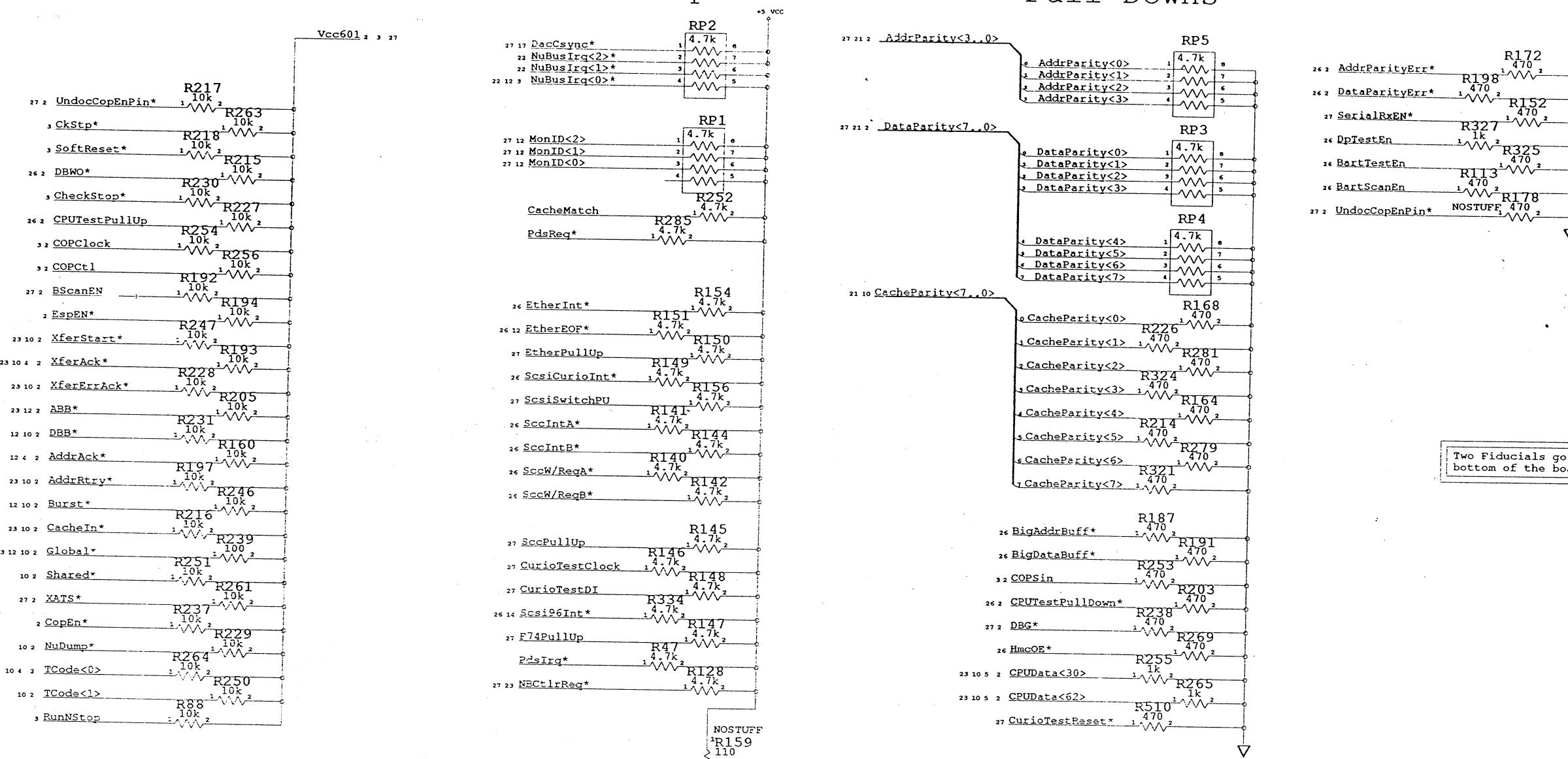
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6

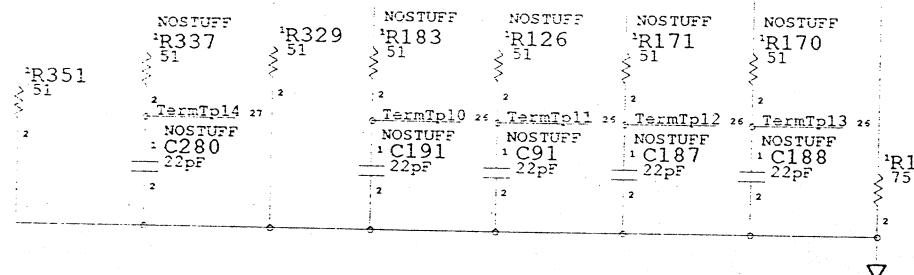
3.6V
Pull Ups

5V
Pull Ups

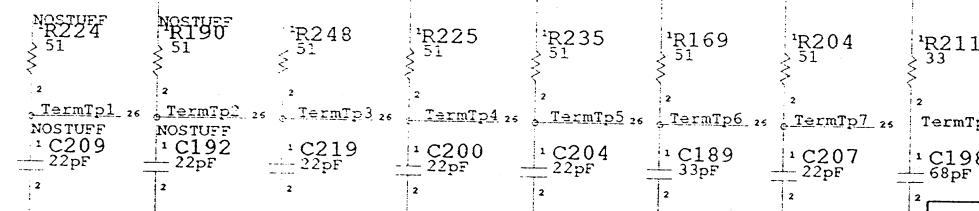
Pull Downs



```
26 11 CpuClk2      (Place_at_end_of_line_near_Cpu)
27 11 CpuProcPhase (Place_at_end_of_line_near_Cpu)
28 11 CpuBusPhase (Place_at_end_of_line_near_Cpu)
29 11 PcbClk       (Place_at_end_of_line_near_MEMORY)
30 11 HmcMmicClk  (Place_at_end_of_line_near_Amic)
31 11 40MHzClk    (Place_at_end_of_line_near_Bart)
32 11 BartClk      (Place_at_end_of_line_near_Bart)
33 11 31MHzClk    (Place_at_end_of_line_near_Swim)
```



```
23 10 2 XferStart* (Place at end of line near Am)
23 10 2 XferAck* (Place at end of line near 60)
12 4 AddrAck* (Place at end of line near 60)
23 10 2 AddrRetry* (Place at end of line near Am)
23 12 2 ARB* (Place at end of line near Am)
12 10 2 DBB1* (Place at end of line near Am)
12 10 2 Burst* (Place at end of line near Am)
23 12 10 2 Global* (Place at end of line near Am)
```



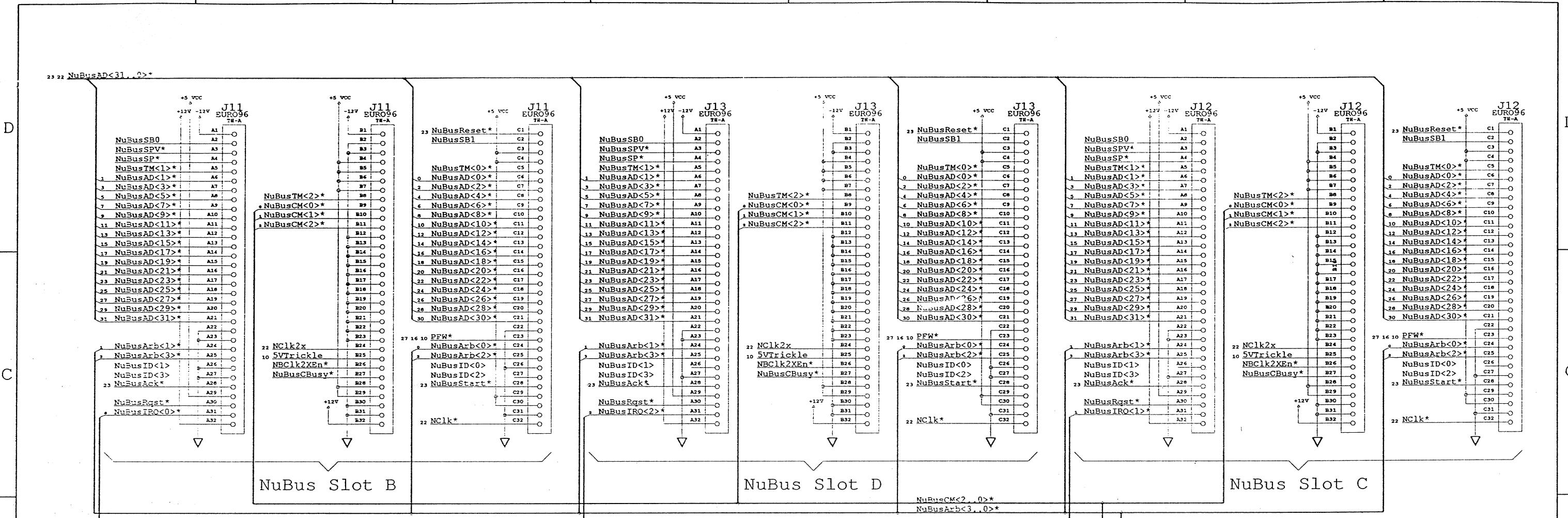
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Terminators, Resistors & Stuff



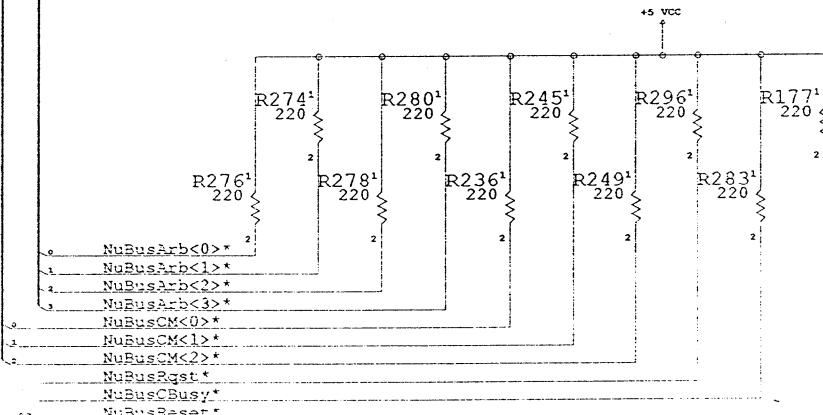
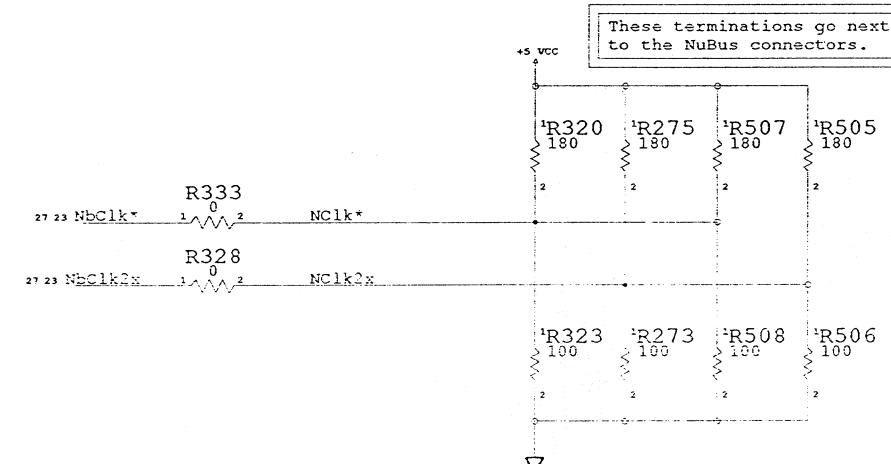
APPLE COMPUTER INC

SIZE D	DRAWING NUMBER 051-0333	REV. A
SCALE NONE	SHT 21	OF 31



22 12 3 NuBusIRQ<2..0>*

These terminations go next to the NuBus connectors.



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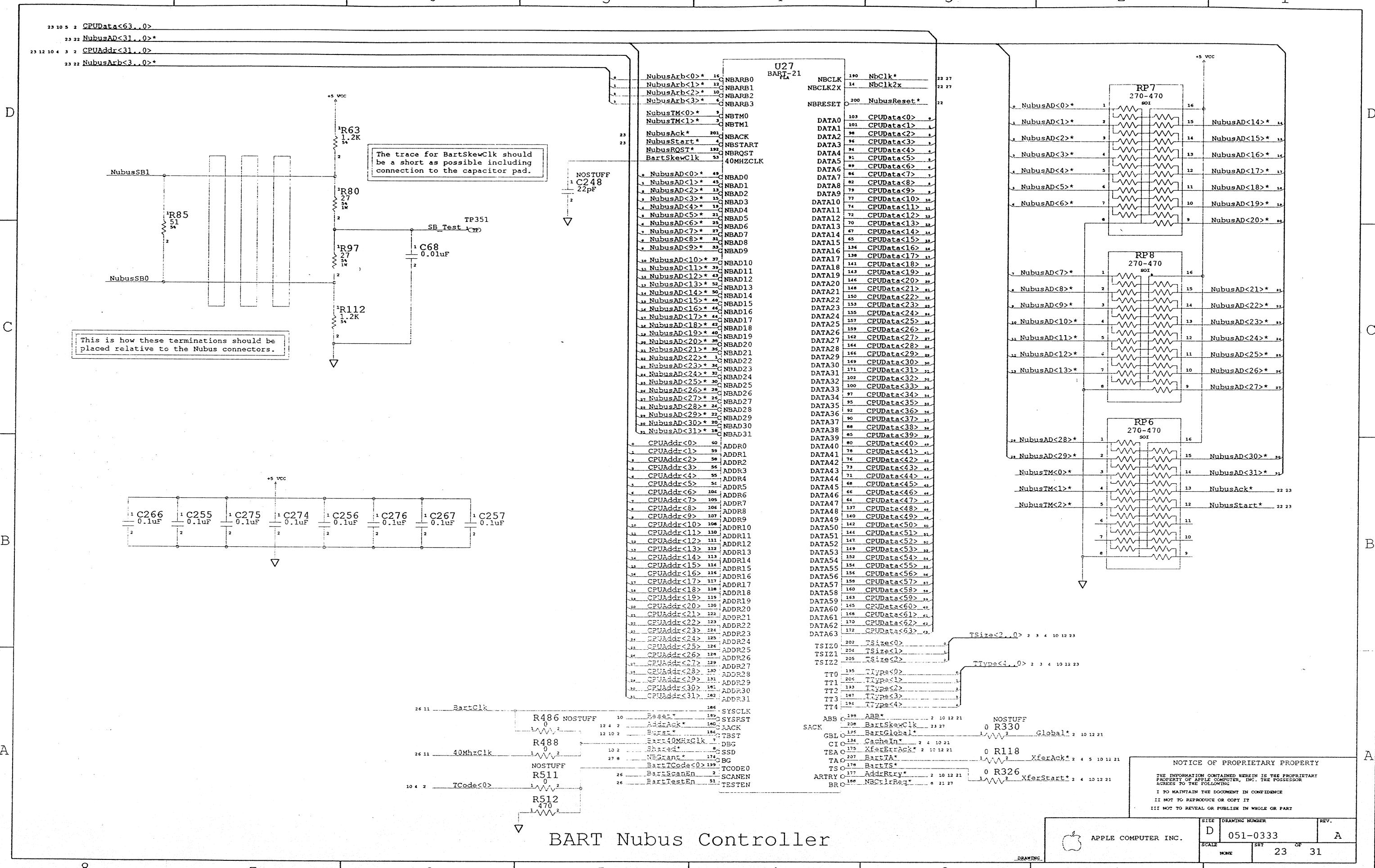
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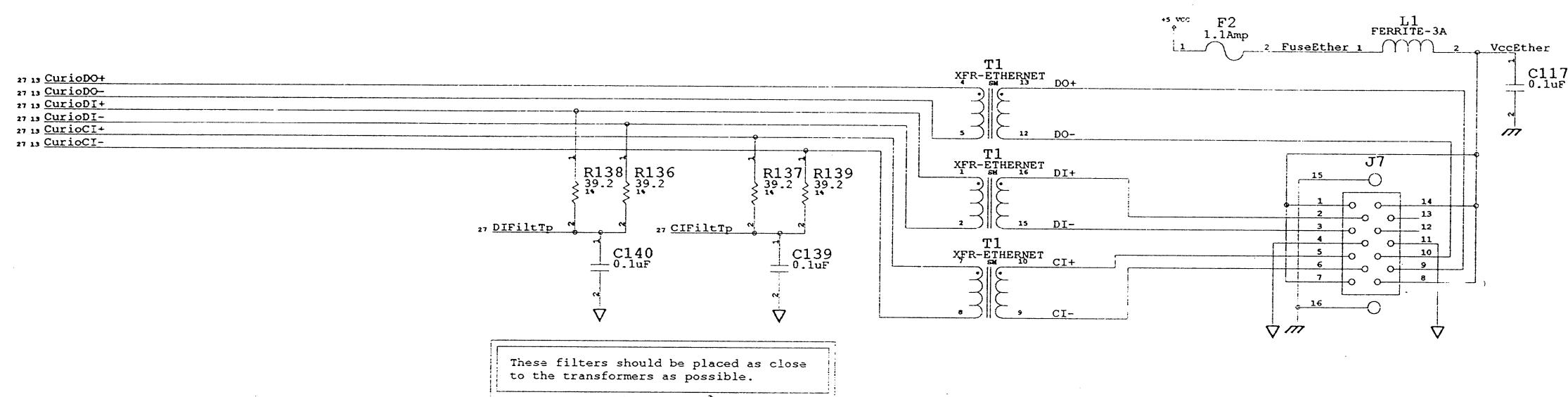
NuBus Connectors and Logic

DRAWING NUMBER		REV.
D	051-0333	A
SCALE	S42	OF
None	22	31



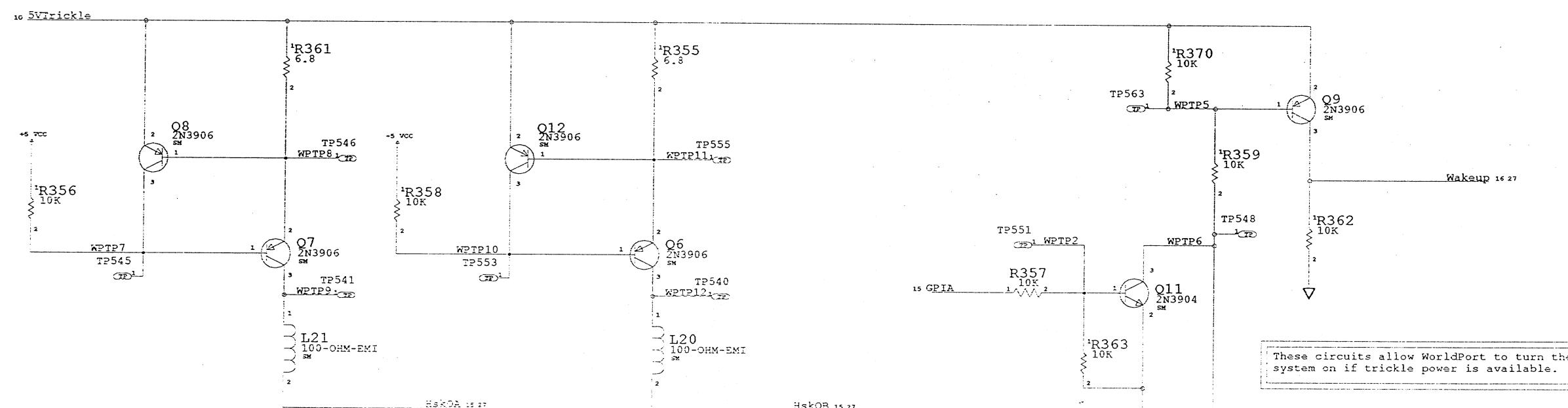
D

D



C

C



B

B

These circuits limit the trickle current to WorldPort when the power supply is off.

HskOB 15 27

HskOB 15 27

TP561 WPTP4

R367 10K Q10 2N3904

R368 10K

These circuits limit the trickle current to WorldPort when the power supply is off.

Ethernet & WorldPort Wakeup

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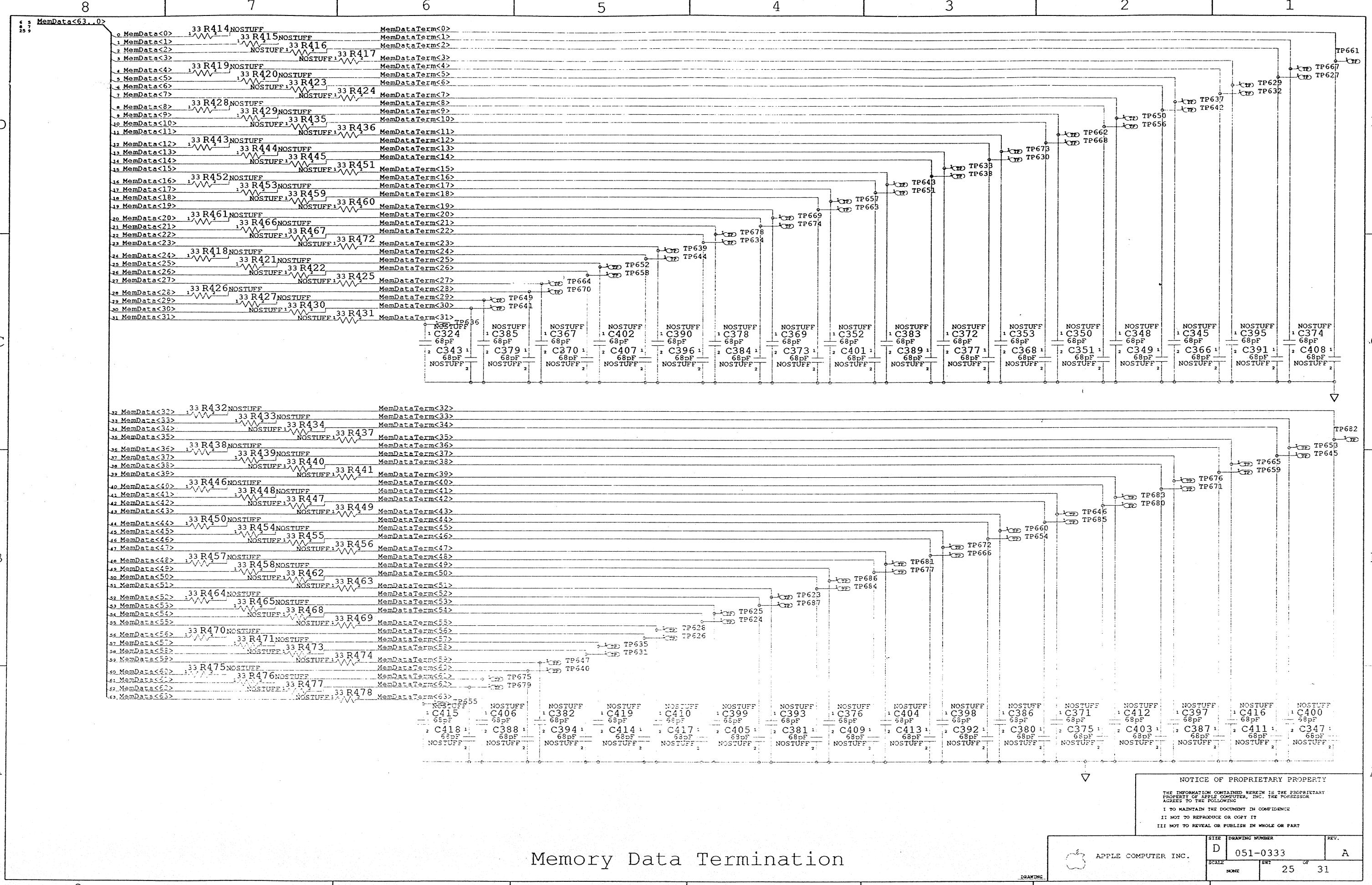
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DRAWING	SPEC.	DRAWING NUMBER	REV.
D	051-0333	A	
SCALE	NONE	WT	24 31



Memory Data Termination



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D 051-0333
SCALE SWT

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	SIZE	DRAWING NUMBER	
UTER INC.	D	051-0333	
SCALE	SHT	OF	
NONE		25	31

TP401 BufCPUAddr<0>,
 TP400 HmcRA<11> 4
 TP400 BufCPUAddr<1> 3
 TP399 HmcRA<10> 4
 TP396 BufCPUAddr<2> 3
 TP396 HmcRA<9> 4
 TP389 BufCPUAddr<3> 3
 TP389 HmcRA<8> 4
 TP388 BufCPUAddr<4> 3
 TP388 HmcRA<7> 4
 TP384 BufCPUAddr<5> 3
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 TP384 BufCPUAddr<6> 3
 TP384 HmcRA<5> 4
 TP377 BufCPUAddr<7> 3
 TP377 HmcRA<4> 4
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 TP373 HmcRA<2> 4
 TP366 BufCPUAddr<9> 3
 TP366 HmcRA<1> 4
 TP360 BufCPUAddr<10> 3
 TP360 HmcRA<0> 4
 TP361 BufCPUAddr<11> 3
 TP361 HmcWE 4
 TP357 BufCPUAddr<12> 3
 TP357 HmcDramRas 4 6
 TP353 BufCPUAddr<13> 3
 TP353 HmcSimmRasA 4 6
 TP355 BufCPUAddr<14> 3
 TP355 HmcSimmRasB 4 6
 TP352 BufCPUAddr<15> 3
 TP352 HmcWE 4
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 TP344 BufCPUAddr<17> 3
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 TP341 BigDataBuff 2 21
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 TP337 BufCPUAddr<21> 3
 TP337 DEWO 2 21
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 TP307 BufCPUAddr<27> 3
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 TP230 Scci96Mode<32> 14
 TP200 EtherRDReq 12 13
 TP200 EtherTDTReq 12 13
 TP214 EtherInt 12 13 21
 TP505 CpuBusPhase 2 11 21
 TP233 ScciCurioDReq 12 13
 TP197 SccW/ReqA 12 13 21
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 TP444 HmcRA<1> 4
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 TP577 Ras3B*
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 TP550 Ras3B*
 TP578 R4A*
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 TP181 AwacsROutC 19
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 TP218 AwacsROutA 19
 TP165 AwacsRInC 19
 TP186 AwacsRInB 19
 TP37 AwacsRInA 19
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 TP587 R2B*
 TP557 Ras2B*
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 TP379 HmcCacheWE<2>* 4
 TP367 HmcCacheWE<1>* 4
 TP368 HmcCacheWE<0>* 4
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 TP342 HmcCacheCS* 4
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MATERIAL	27	OF	31
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*** Signal Cross-Reference ***
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SVTRICKLE	10 16 22 24
16MHzCLK	11 13 26
20MHzCLK	13 14 24
25MHzCLK	11 12 26
25MHzOSC	11 26
25MHzVDO	11 26
31MHzCLK	11 12 13 20 21 26
31MHzOSC	11 26
31MHzVDO	11 26
40MHzCLK	11 14 21 23 24
40MHzDV2	14 26
40MHzOSC	11
40MHzVDO	11 26
45MHzCLK	11 12 26
45MHzOSC	11 26
45MHzVDO	11 26
57MHzCLK	11 12 26
57MHzOSC	11 26
57MHzVDO	11 26
ARB *	2 10 12 21 23
ADB	10 16 18 27
ABD1	16 27
ADPCOUT	16 27
ADPCOUT1	16 27
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ADORACK *	2 4 10 12 21 23
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ADDRPARITY *	2 21 26
ADDRTRM *	2 10 12 21 23
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CAM *	11 26
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C1GM *	11 26

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C40M	11
C45M	11 26
C57M	11 26
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*** Unit Cross-Reference ***
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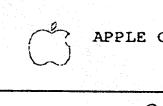
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