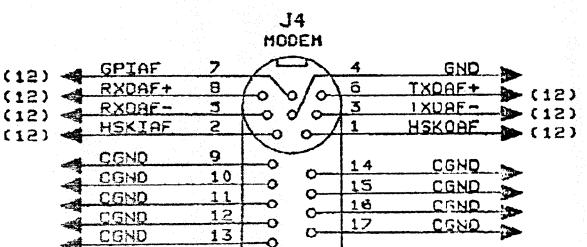
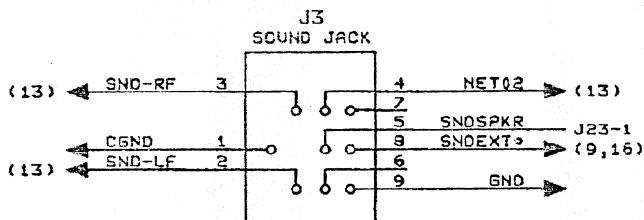
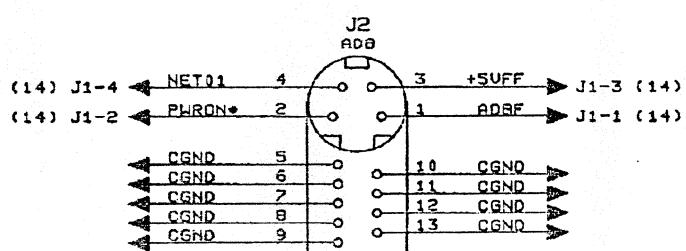
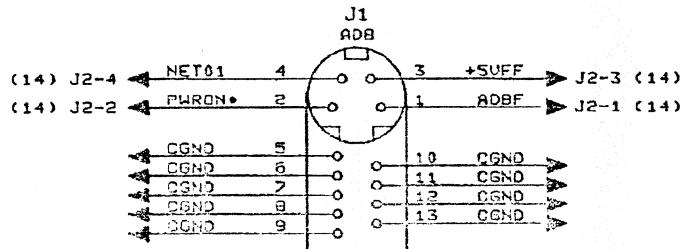


II Ci

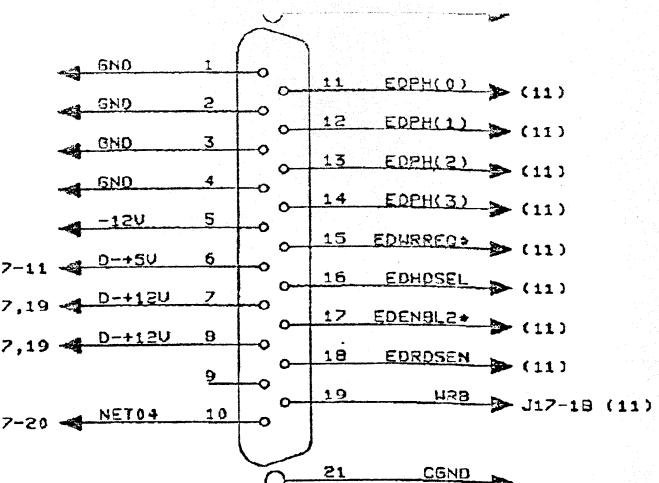
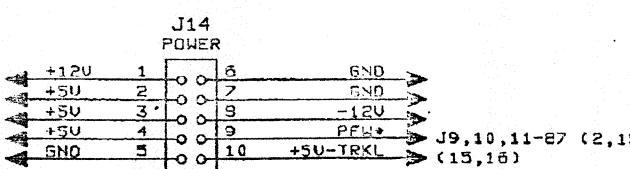
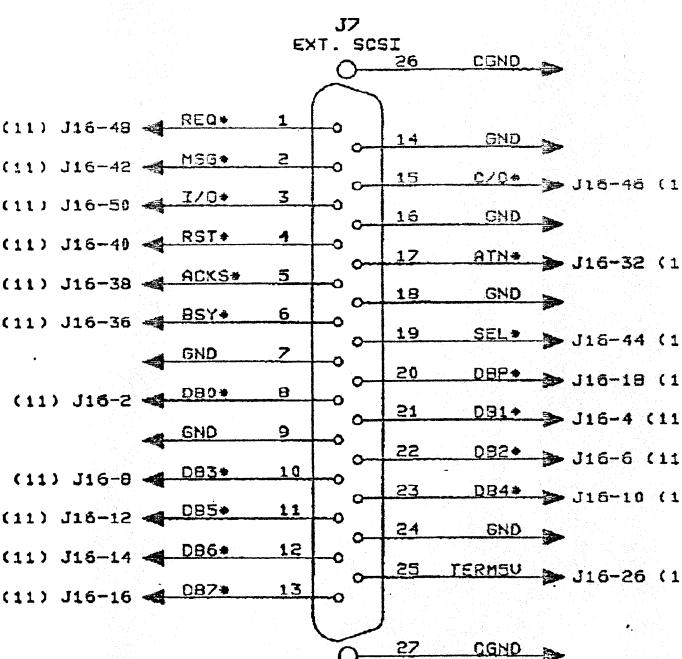
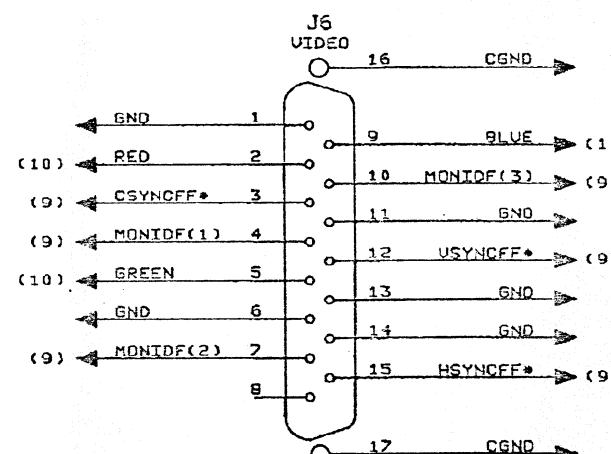
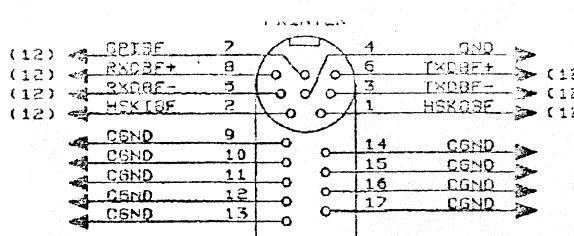
C | V286

1. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
2. ALL CRYSTAL VALUES ARE IN HERTZ.
3. () DENOTE SHEET TO SHEET REFERENCES.
4. ON J9, J10, & J11, -5.2V PINS ARE SHORTED TOGETHER, BUT -5.2V IS NOT SUPPLIED TO THEM.
5. () INDICATES SIGNALS WHICH GO TO A CONNECTOR.
6. (TP) INDICATES SIGNALS WHICH GO ONLY TO A TEST POINT.

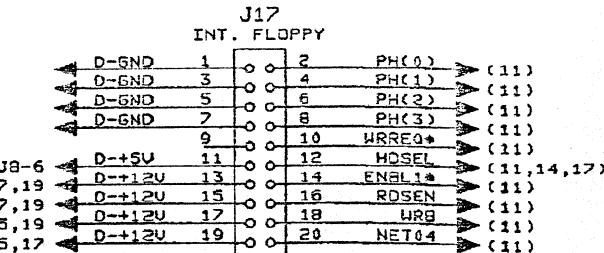


(9,16) 1: IRQ*(1)
 (9,16) 2: IRQ*(2)
 (9,16) 3: IRQ*(3)

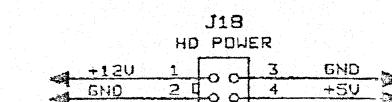
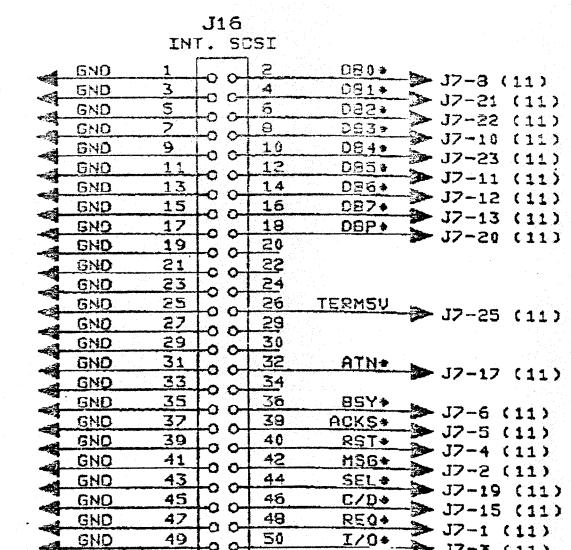
THREE EXTRA NUBUS INTERRUPTS



(11) J17-11 → D-+5V
 (11) J8-8;J17-13,15,17,19 ← D-+12U
 (11) J8-7;J17-13,15,17,19 ← D-+12U
 (11) J17-20 ← NET04 → J17-18 (11)



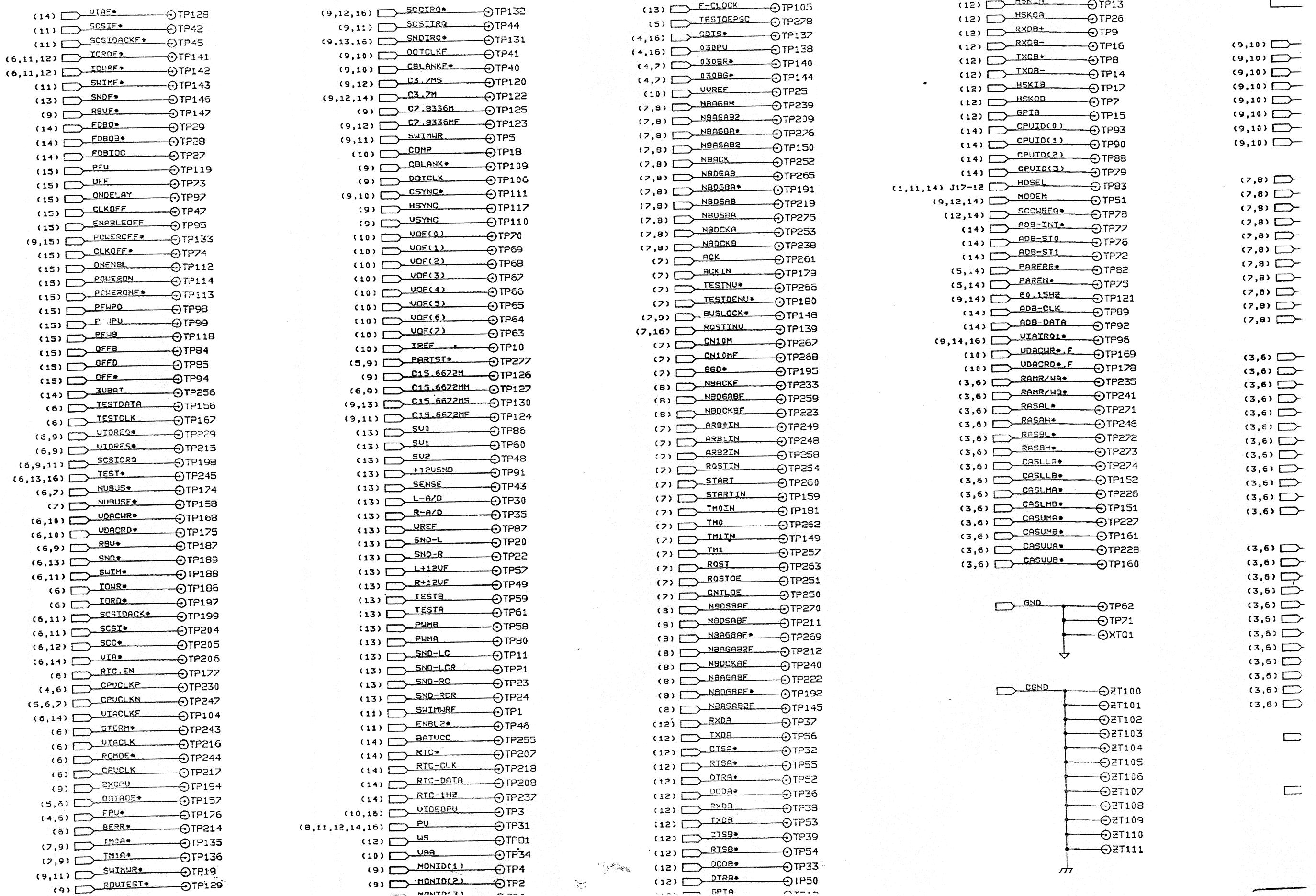
(11) J8-6 → D-+5U
 (11) J8-7,8;J17-15,17,19 → D-+12U
 (11) J8-7,8;J17-13,17,19 → D-+12U
 (11) J8-7,9;J17-13,15,19 → D-+12U
 (11) J8-7,8;J17-13,15,17 → D-+12U



J23 SPEAKER
 J3-5 SNDSPKR 1: GND 2: GND

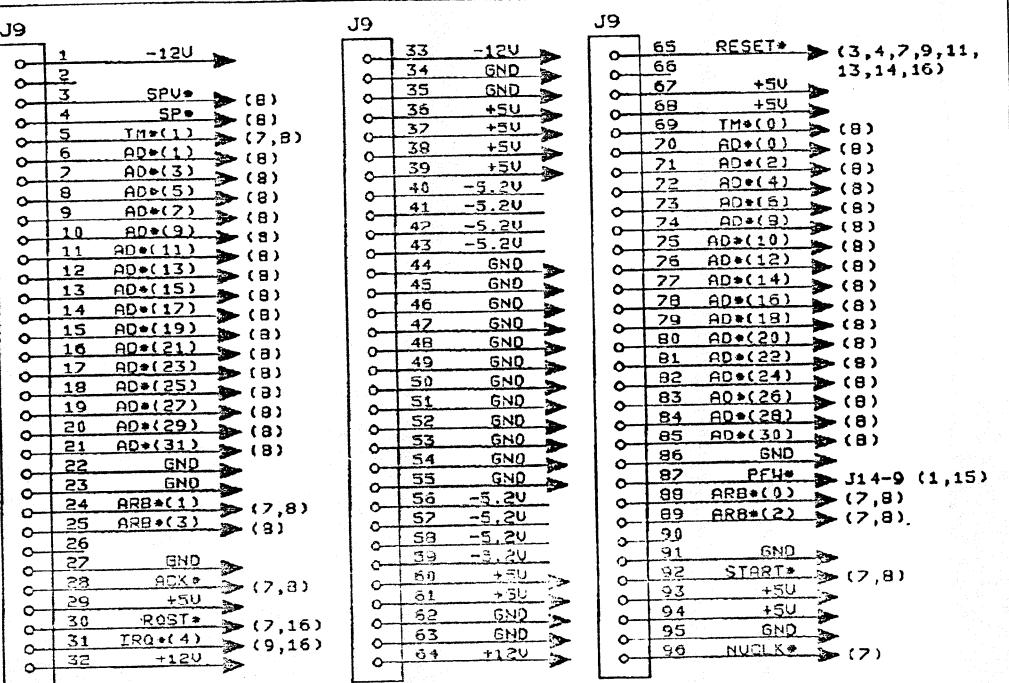
REFEREN
 LAST 1
 BT1, C158,
 DS1, F3, J
 Q4, R112,
 S3, IP2/8,

DIMENSIONS ARE IN MILLIMETERS		ME
TOLERANCES		
X...X =		DRAFTER B. RAULTON
X...X =		DES. APPROV.
X...X =		MANUFACTURER
ANGLES =		1
DO NOT SCALE DRAWINGS		RELEASE COPIES 10/10
MATERIAL		

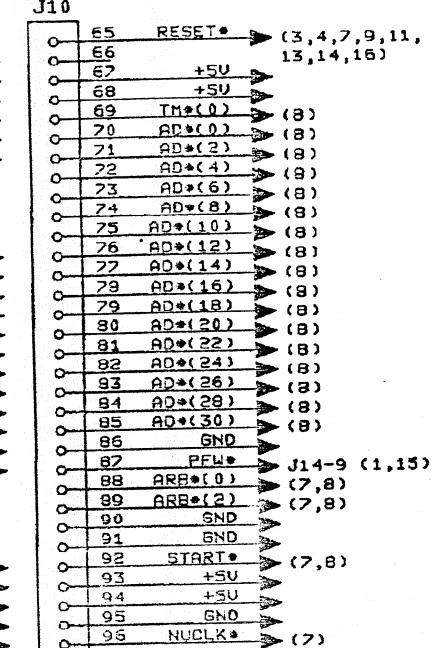
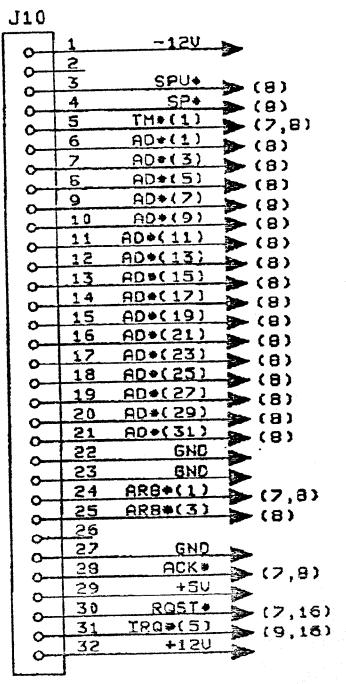


SLOT1=C

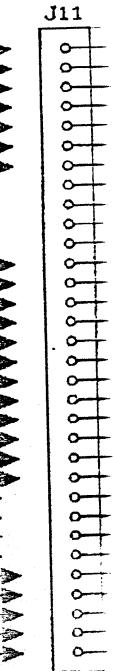
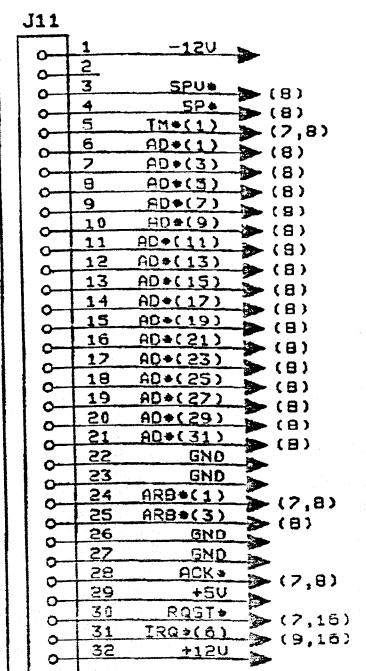
4



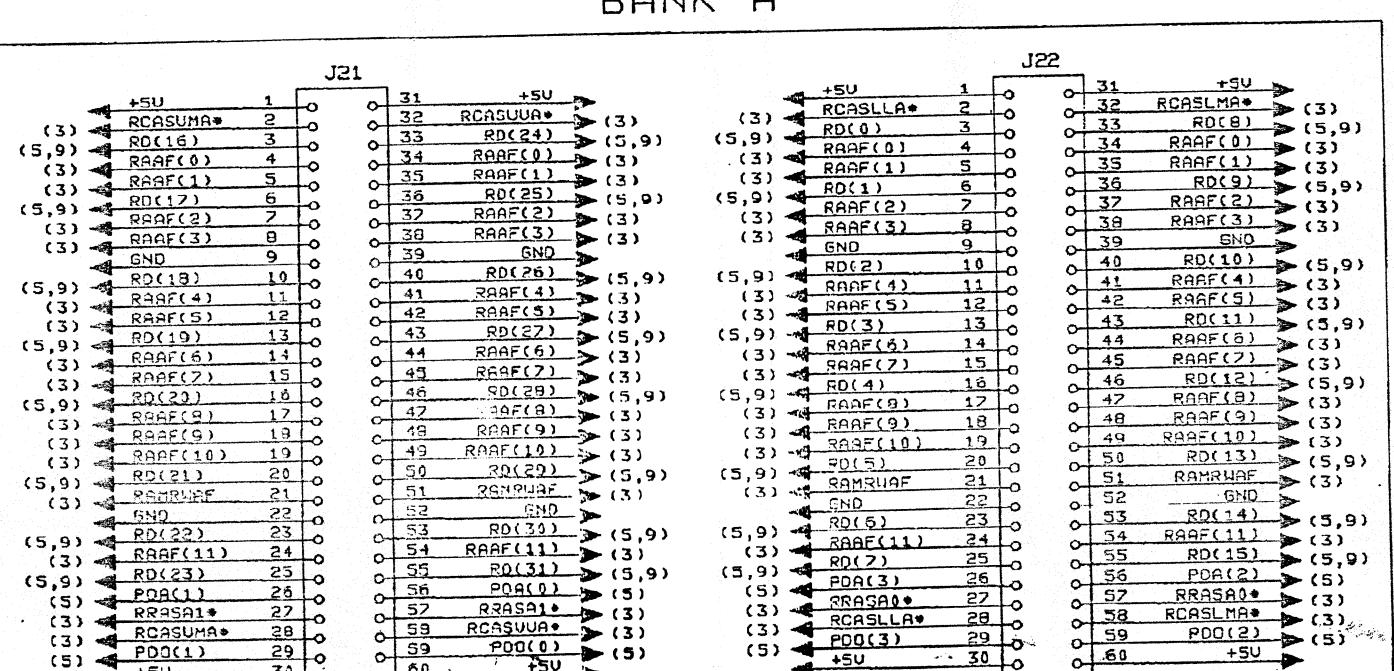
SLOT2=D

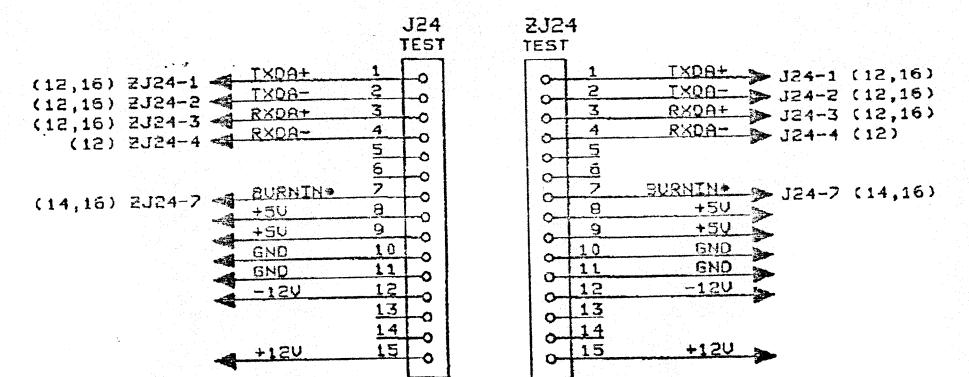
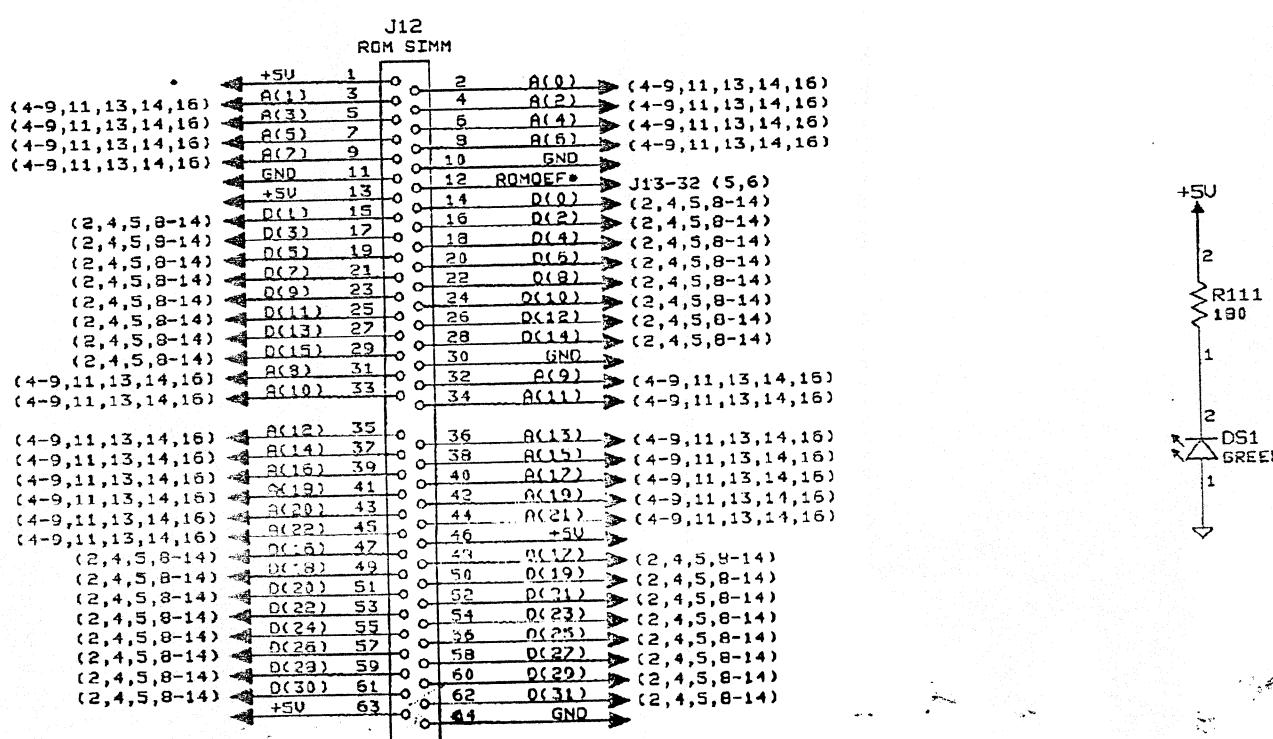
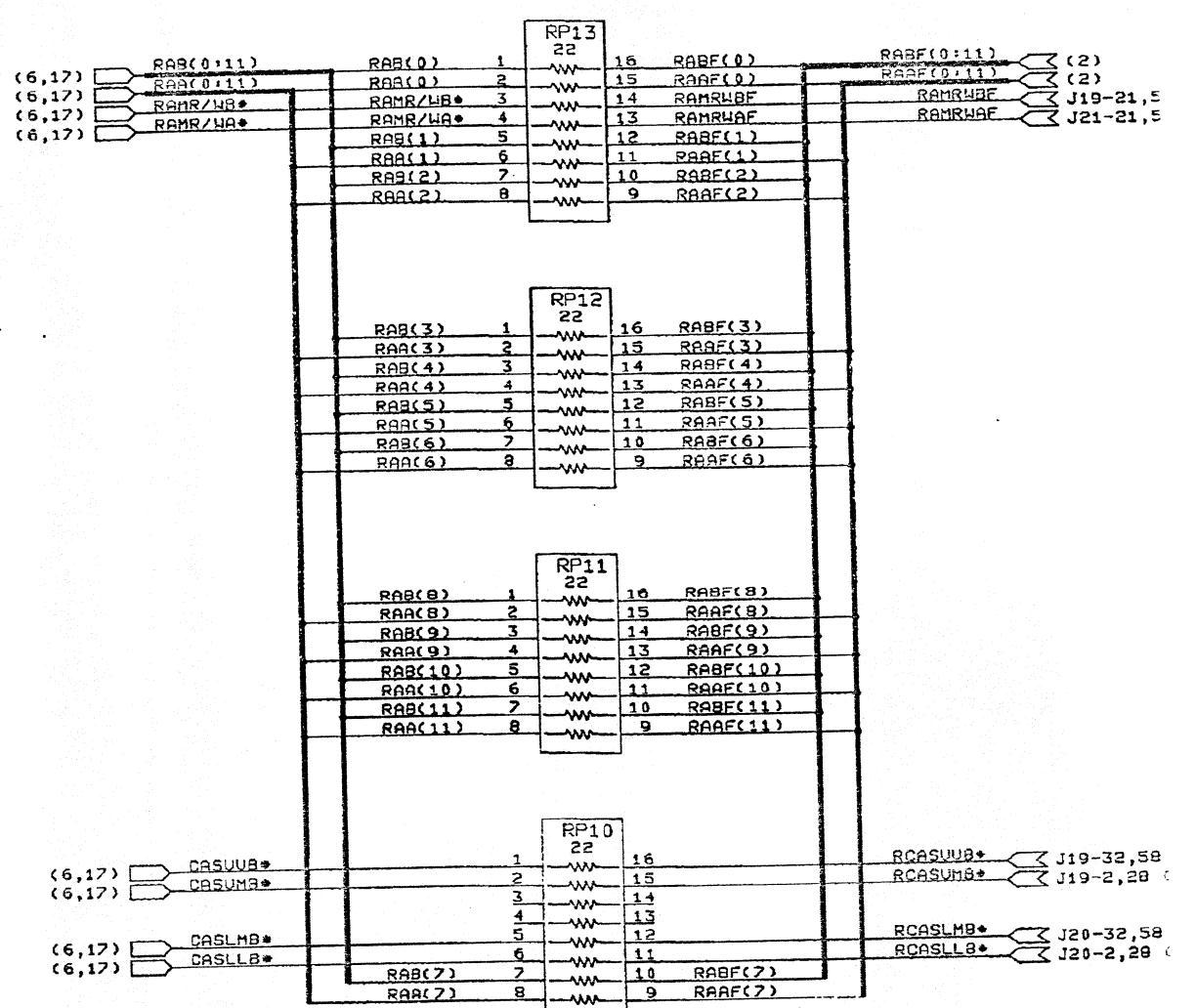
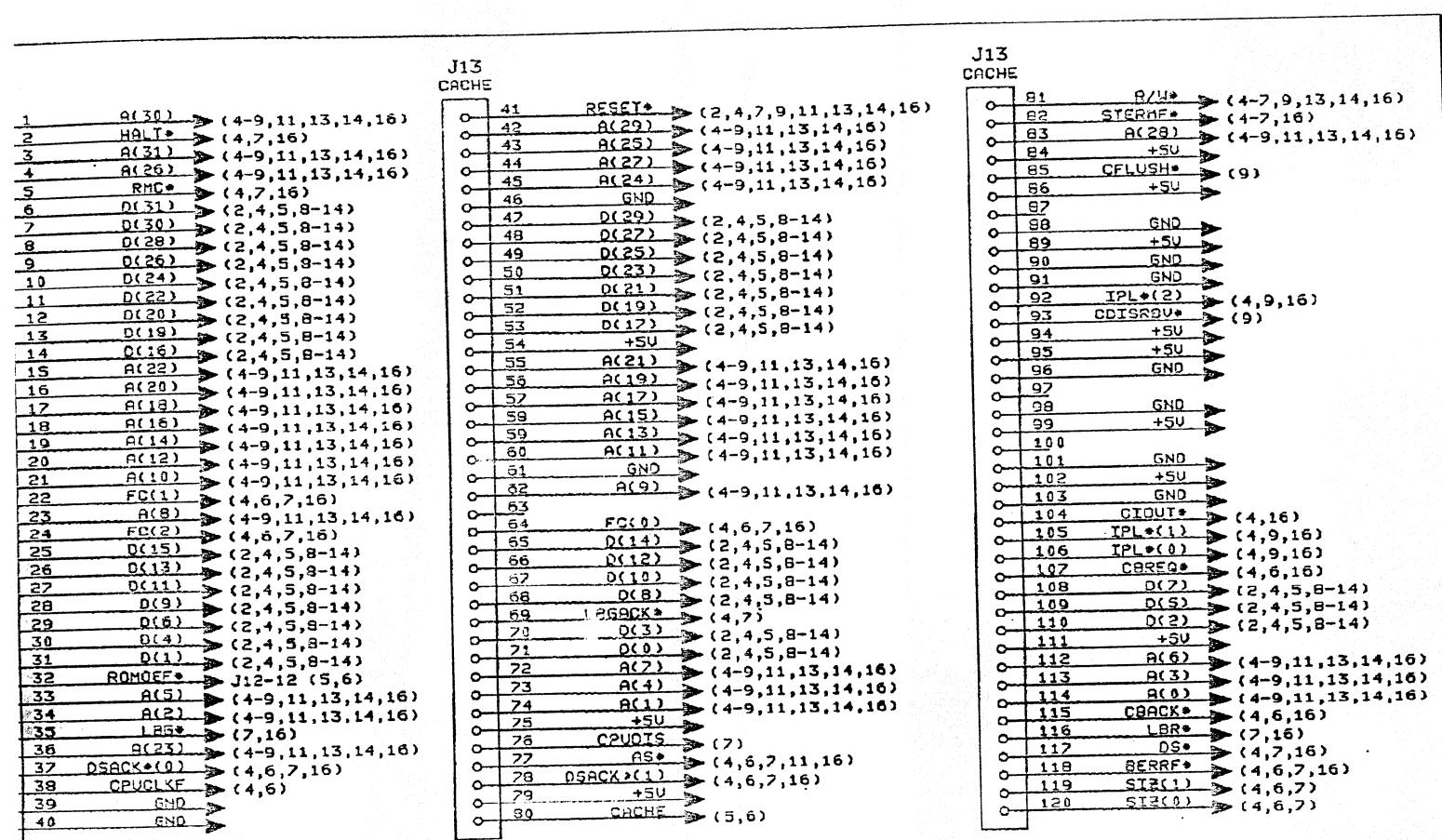


SLOT3=E

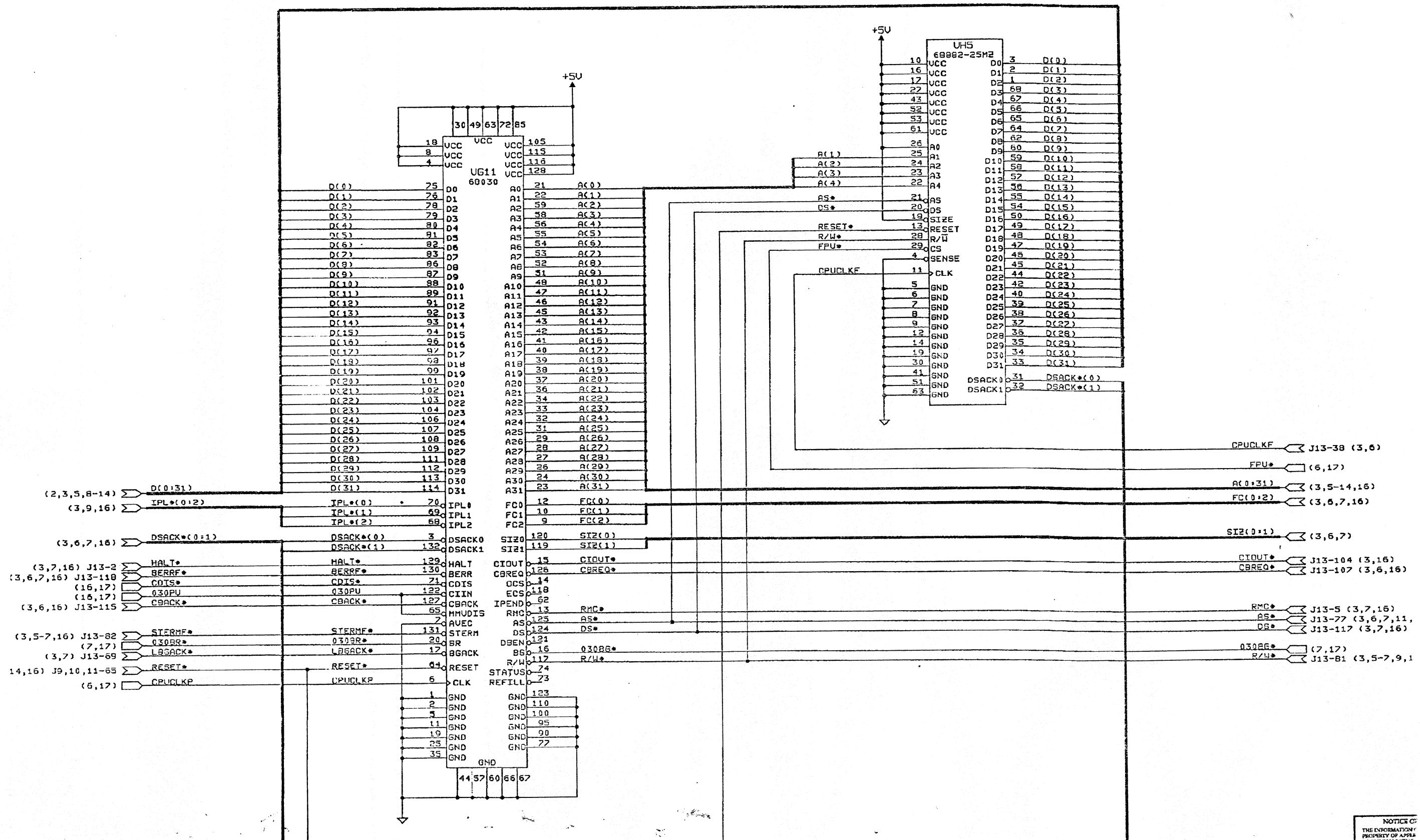


BANK A

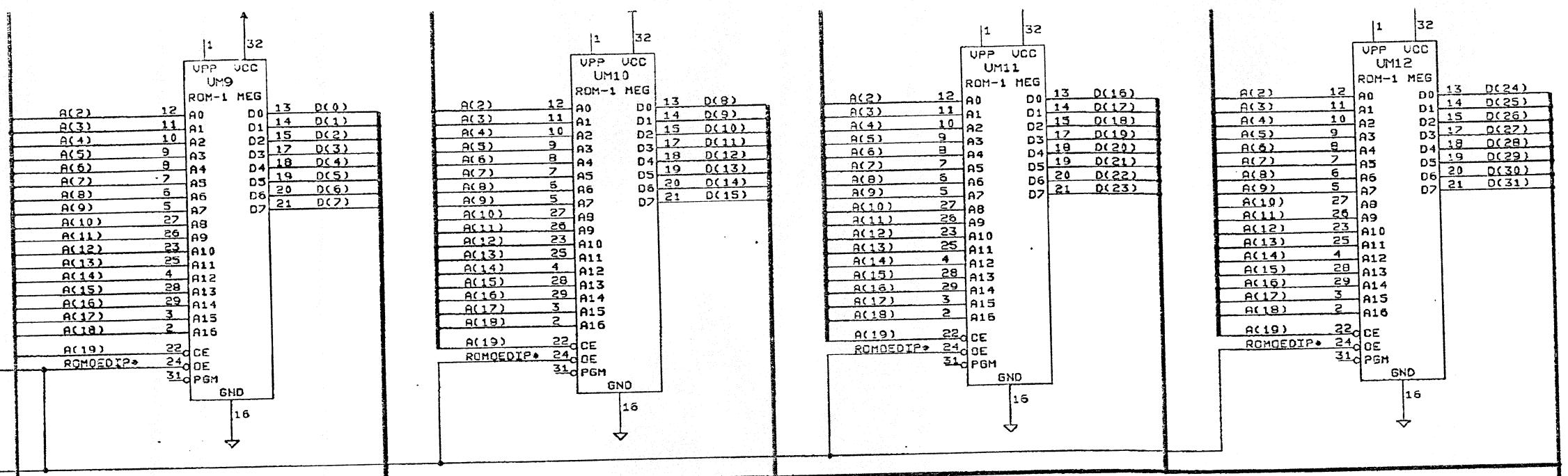




CPU (68030) & FPU (68882)

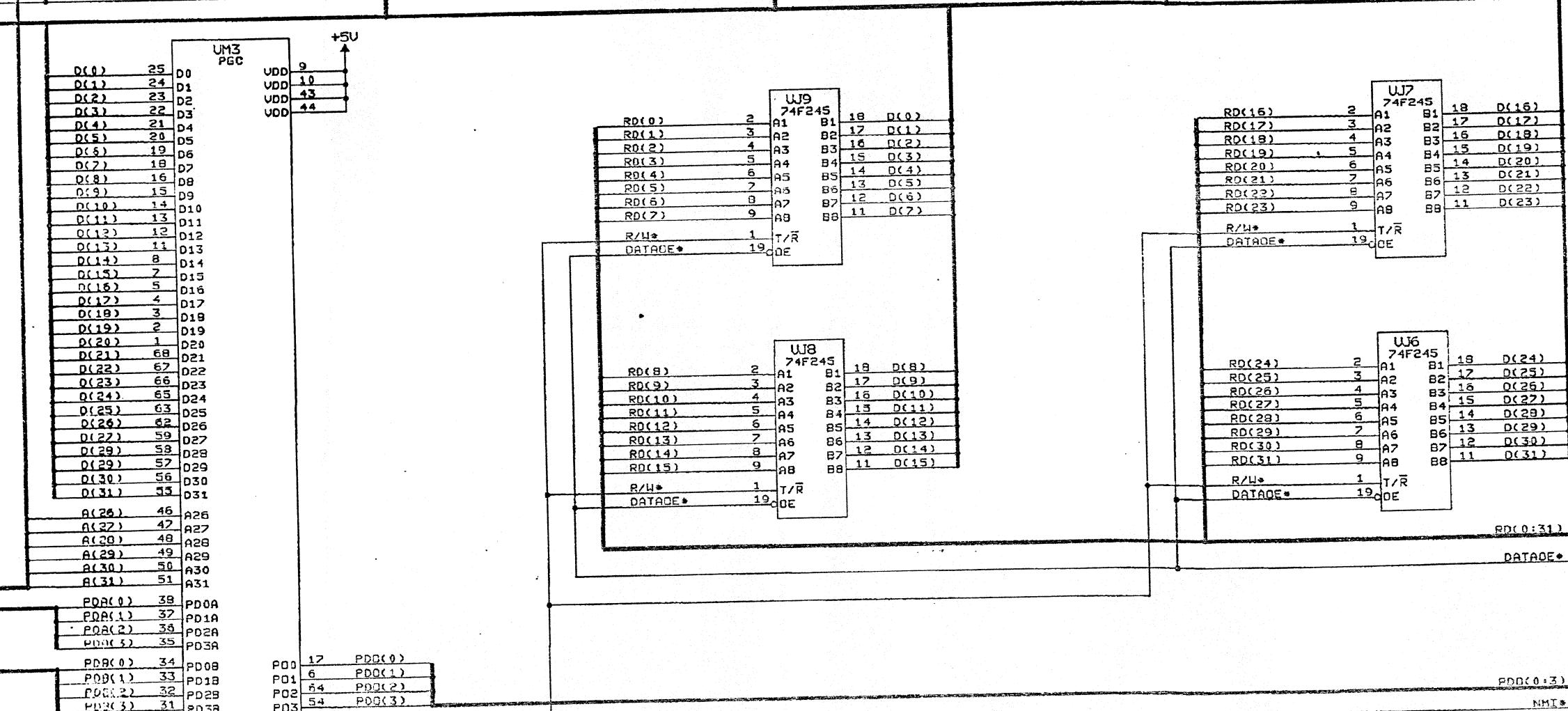


ROUT D415 5
PARITY CHIP



12-12:J13-32 > ROMDEF* 1 0 0 2

(2-4,8-14) >



(3,4,6-14,16) > A(0:31)

(2) > PDA(0:3)

(2) > PDB(0:3)

(3,6) J13-80 > CACHE

(14,17) J13-82 > PAREN*

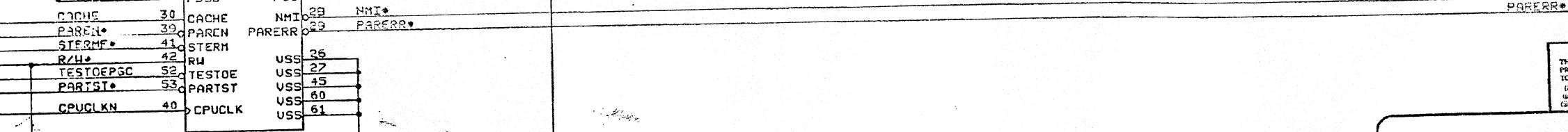
(14,16) J13-82 > STERMF*

(14,16) J13-81 > R/H*

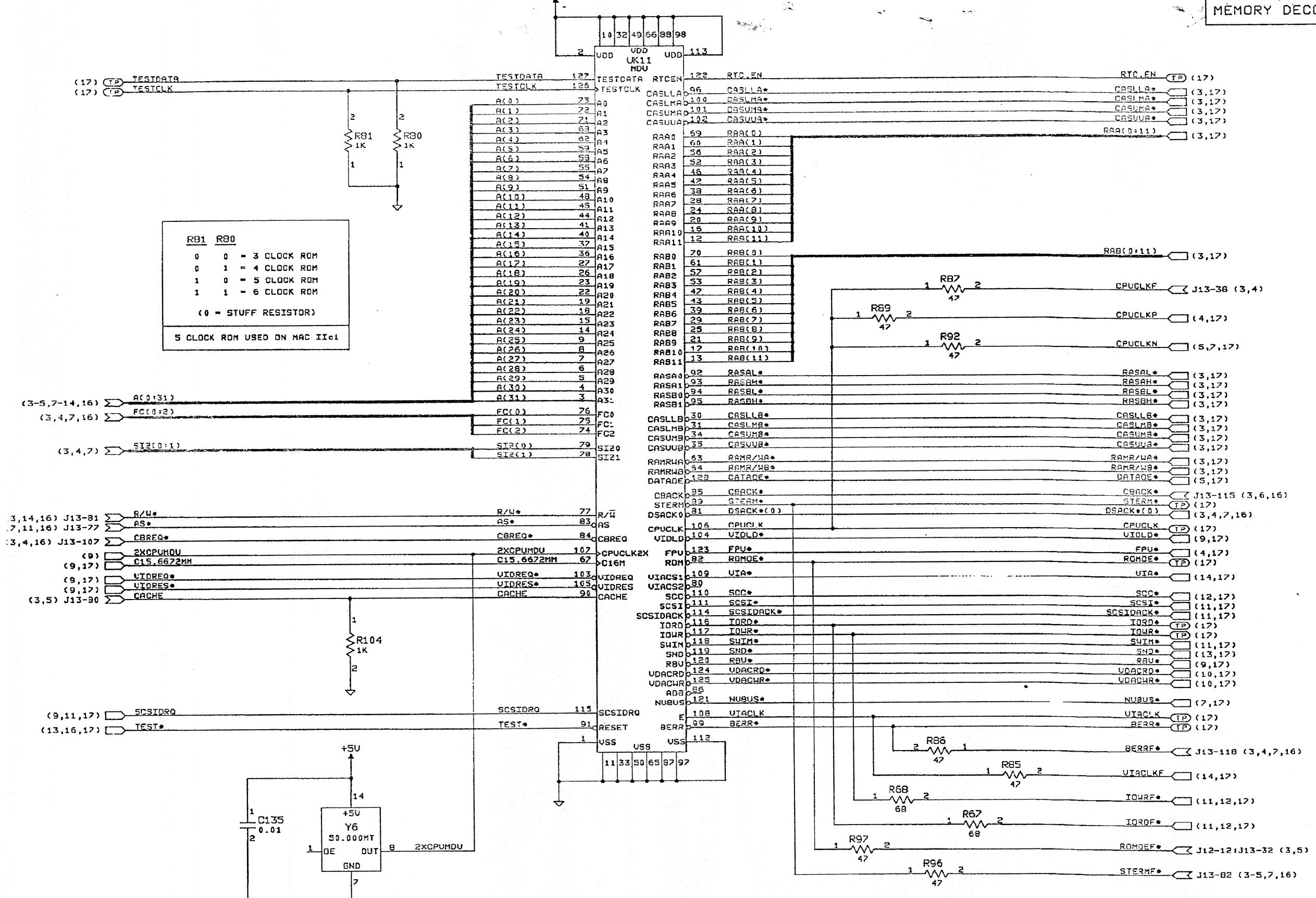
(17) J13-81 > TESTOEPGC

(9,17) J13-81 > PARTST*

(6,7,17) J13-81 > CPUCLKY

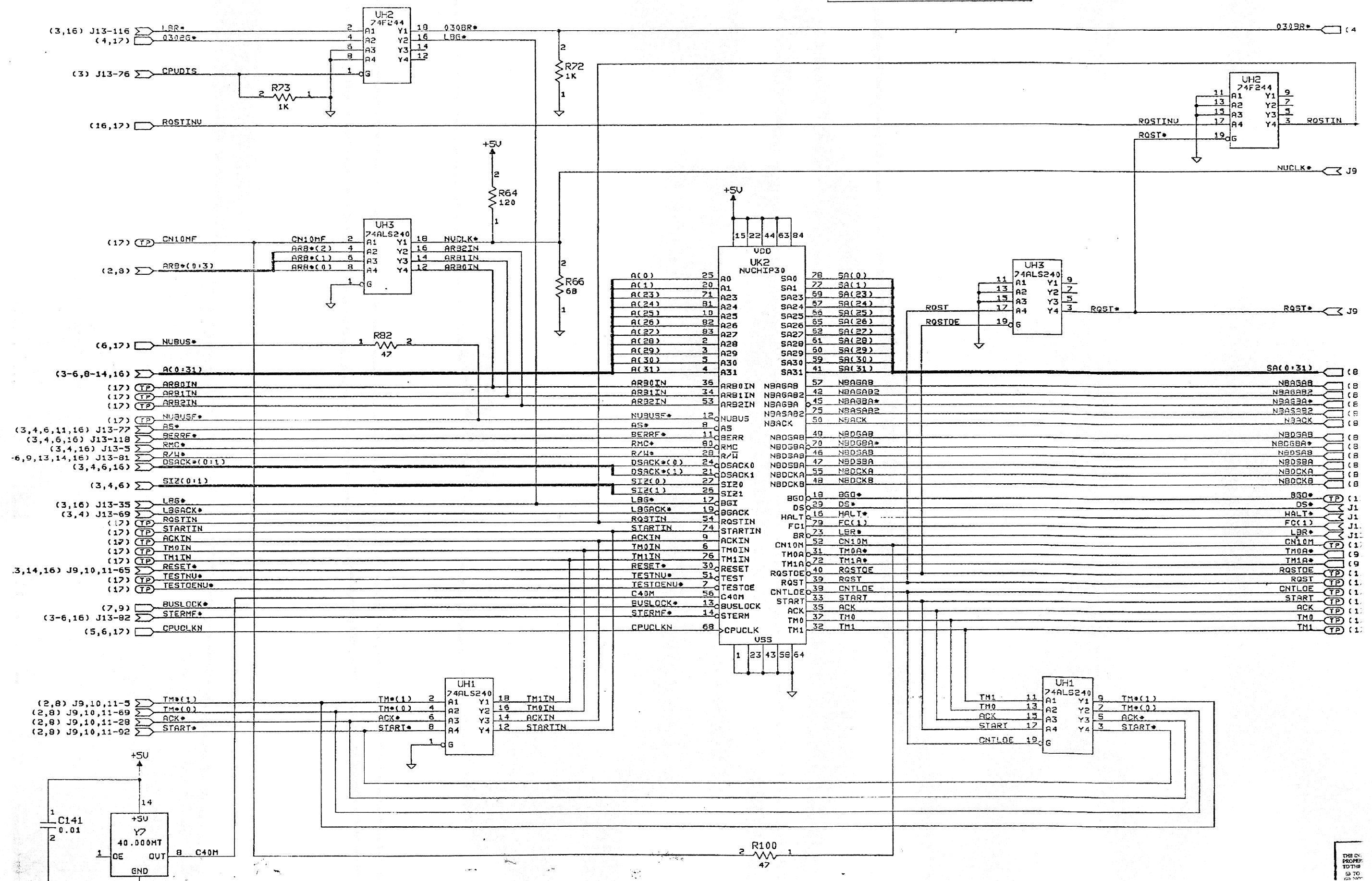


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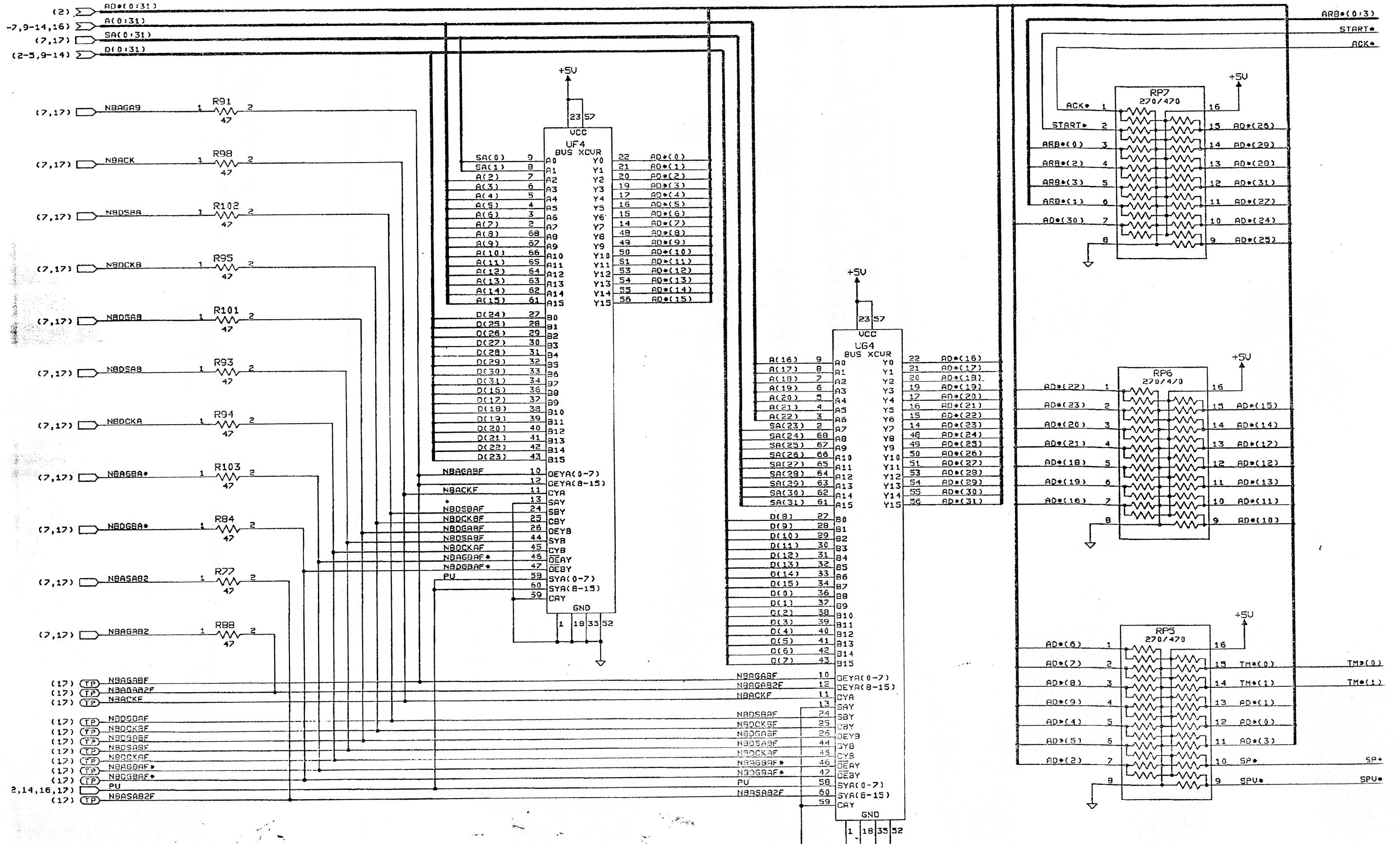


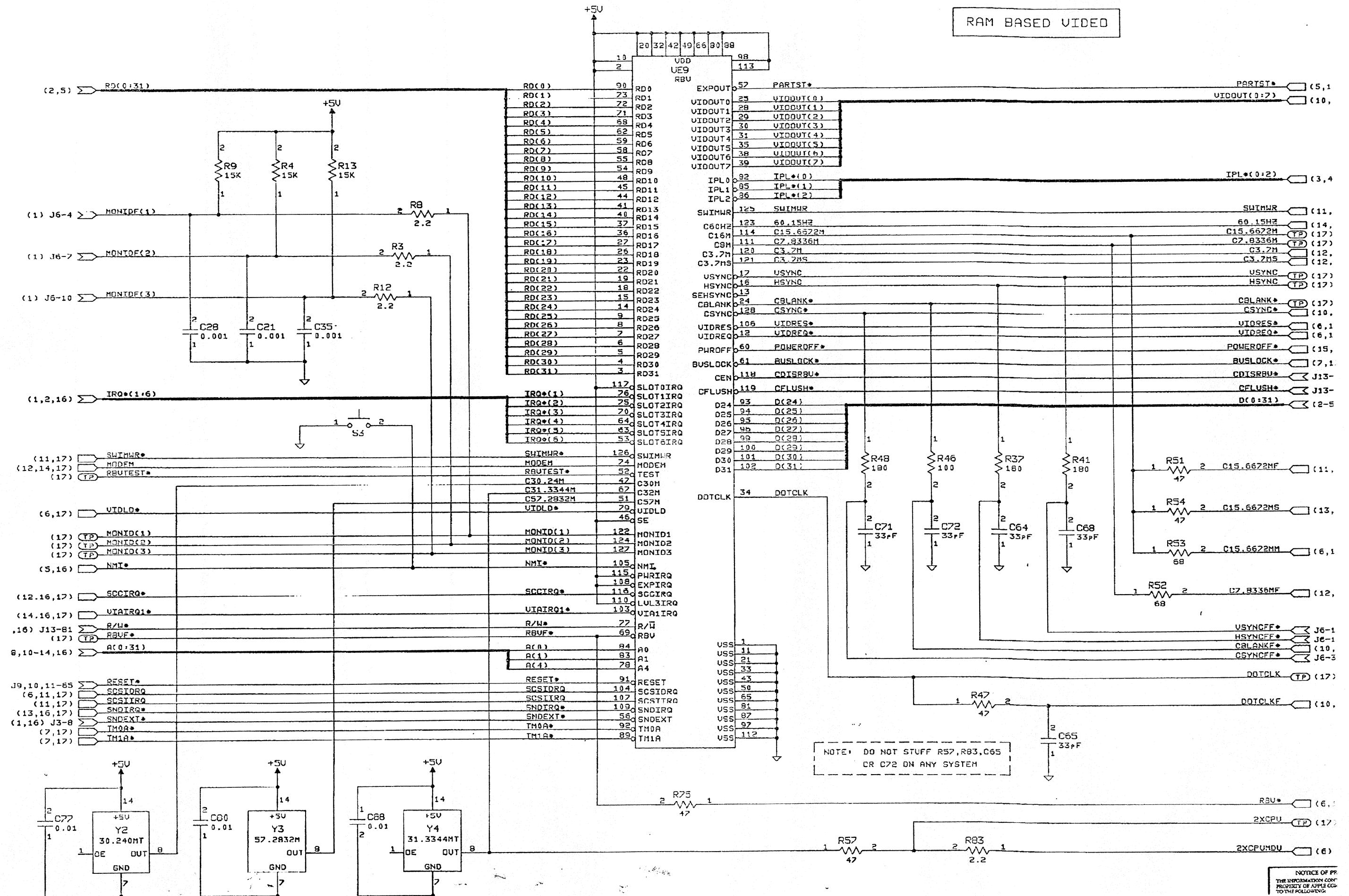
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NUBUS CONTROL LOGIC



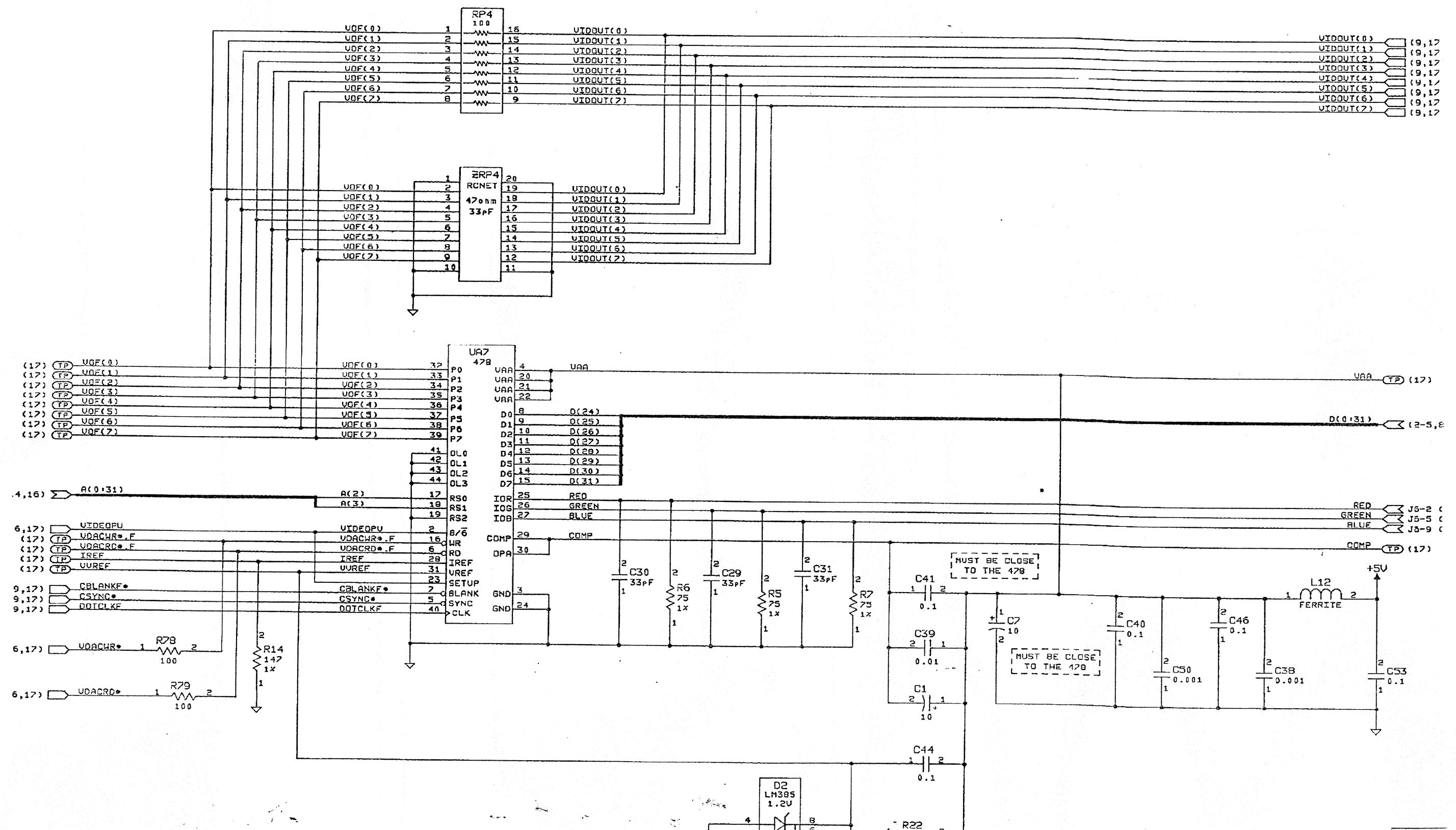
NUBUS ADDRESS & DATA BUFFERS

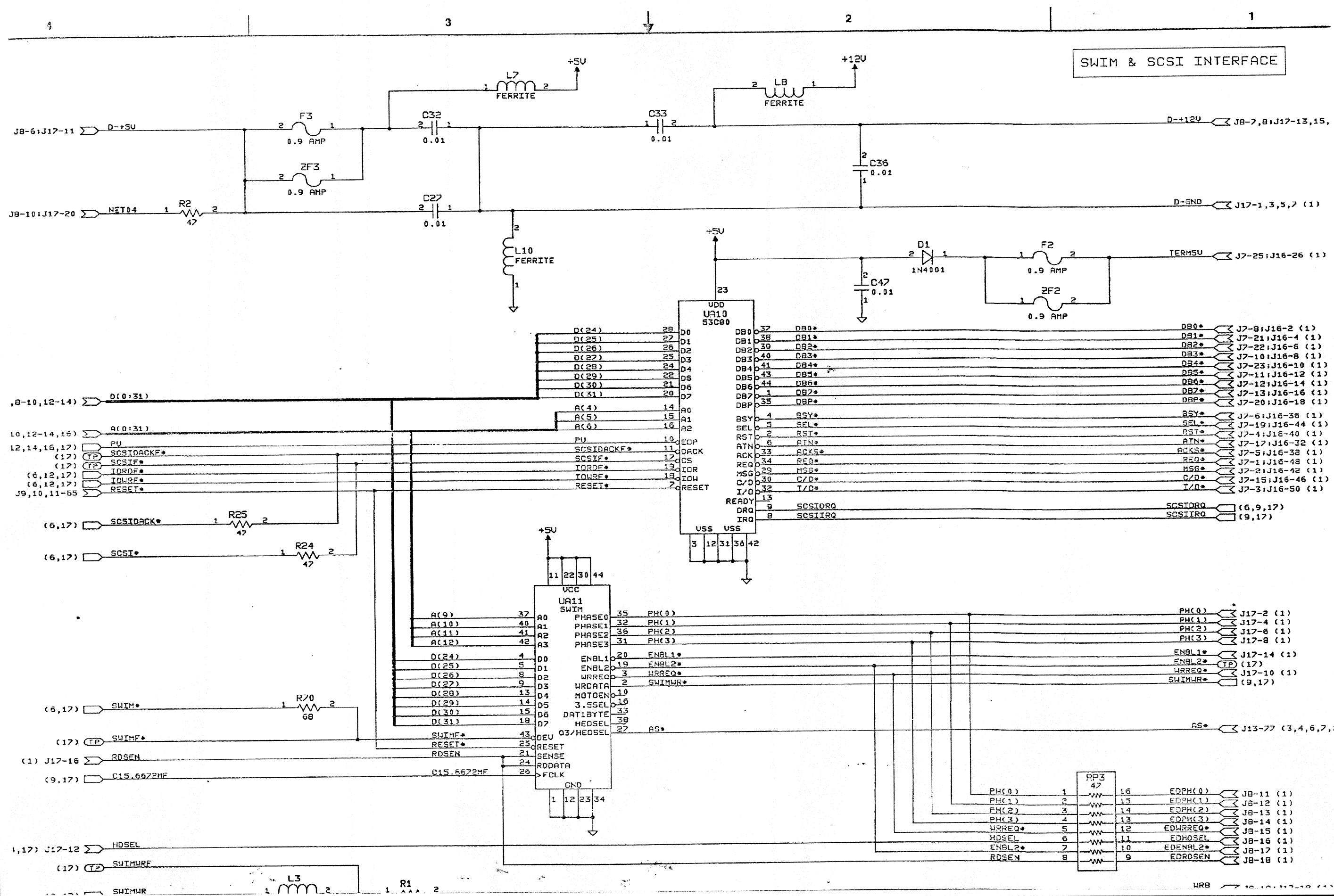




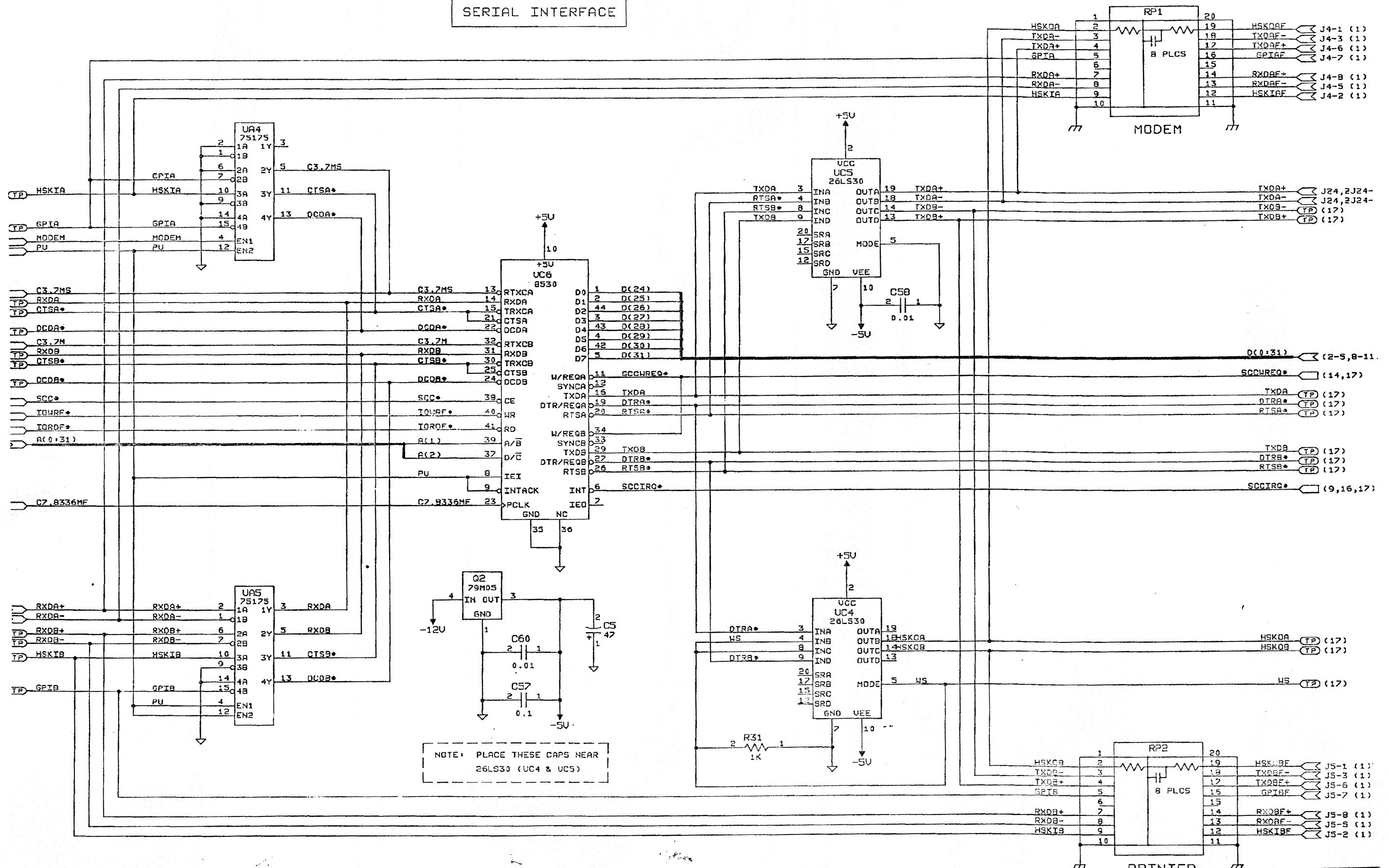
NOTE: DO NOT STUFF R57, R83, OR
CR C72 ON ANY SYSTEM

VIDEO DAC

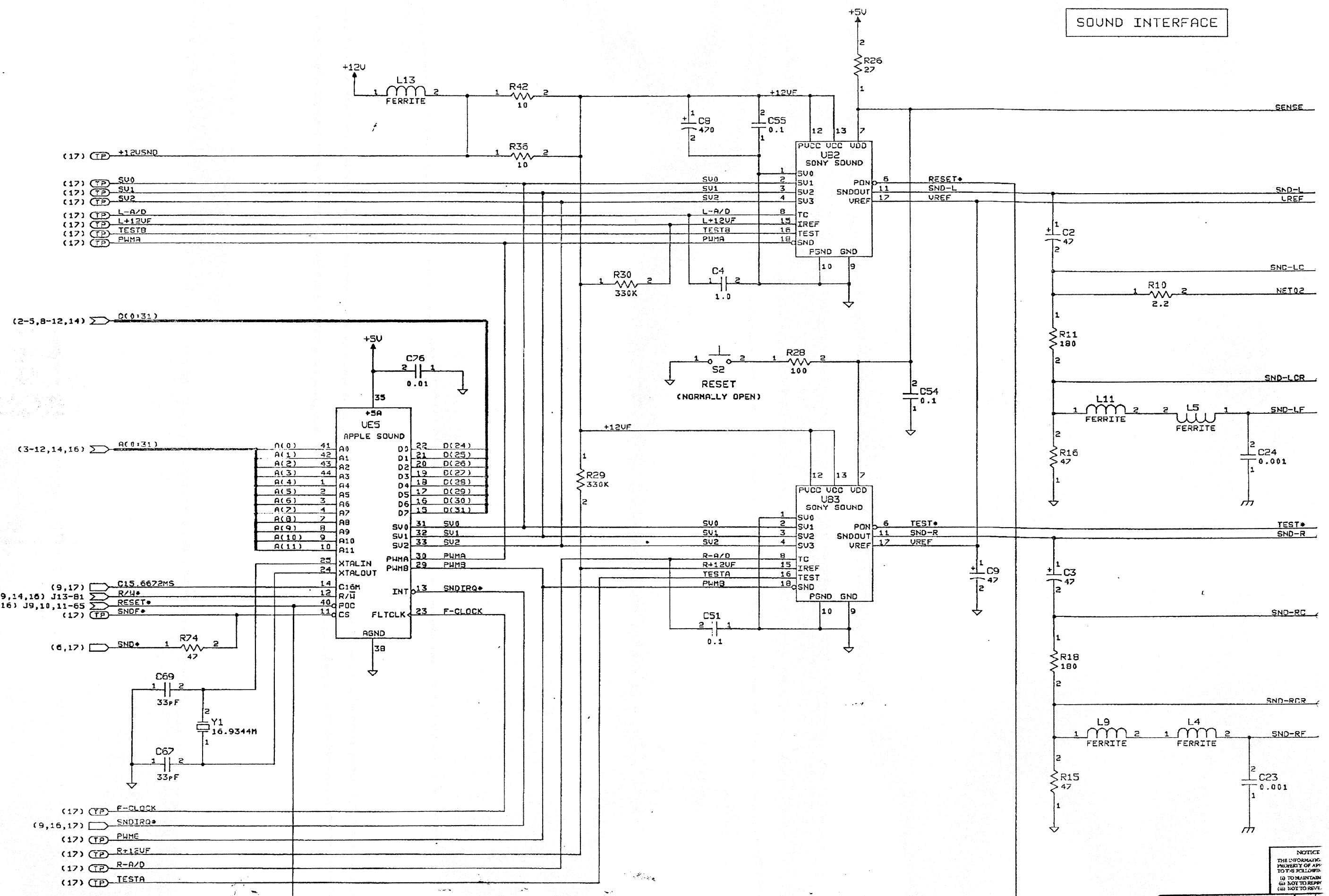




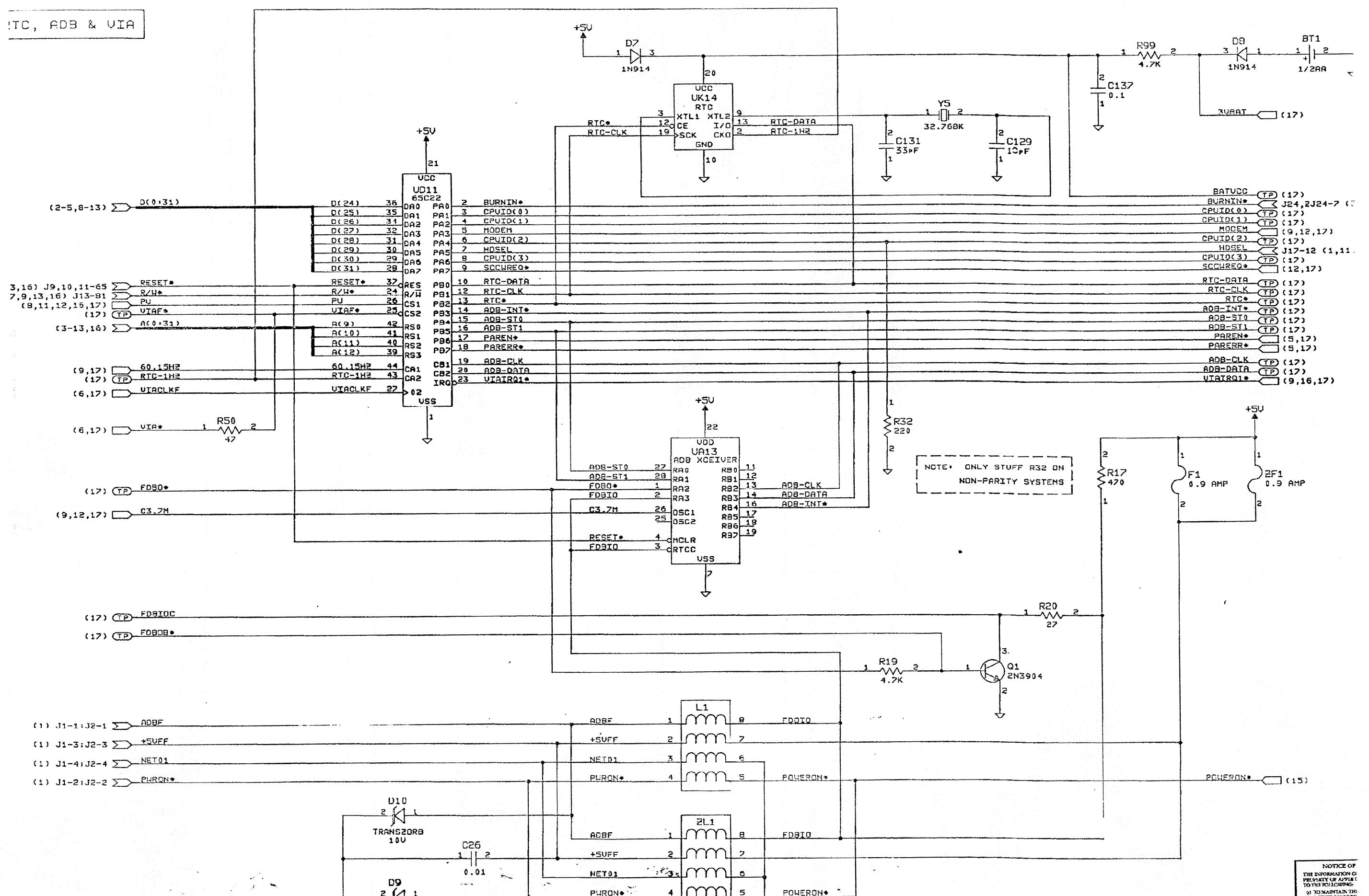
SERIAL INTERFACE



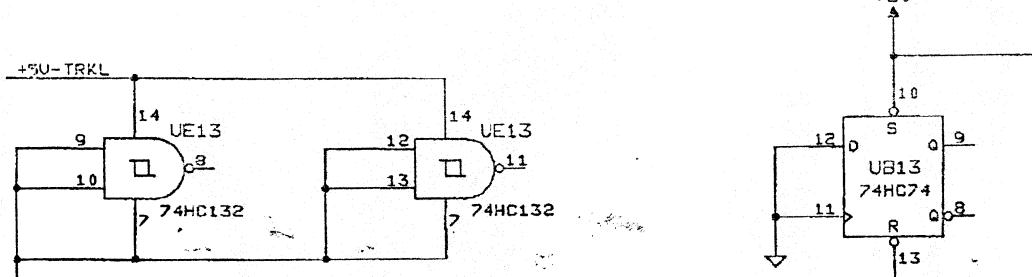
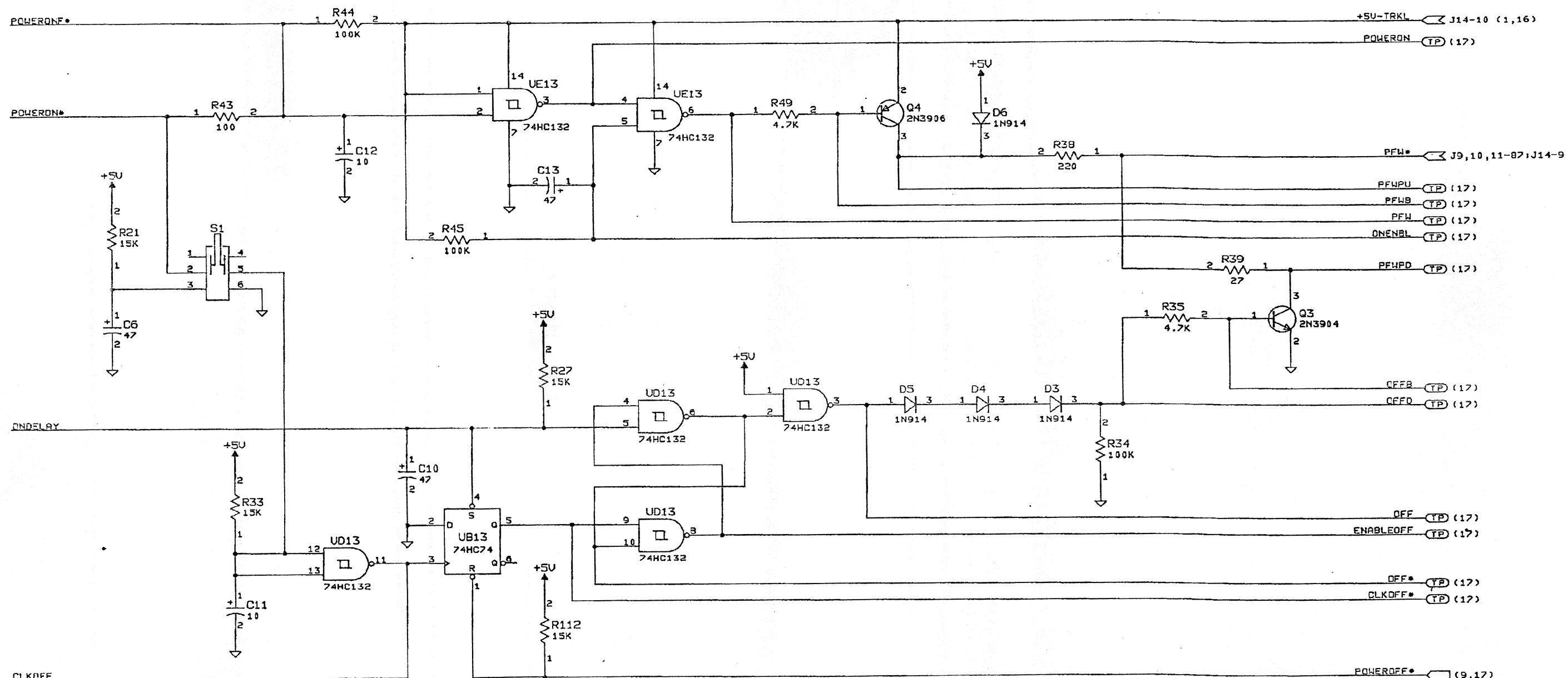
SOUND INTERFACE



!TC, ADB & VIA



POWER-ON CIRCUITRY



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