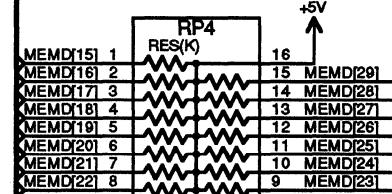
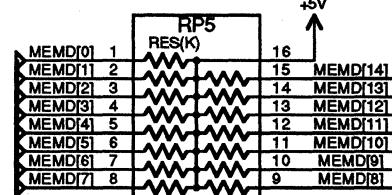
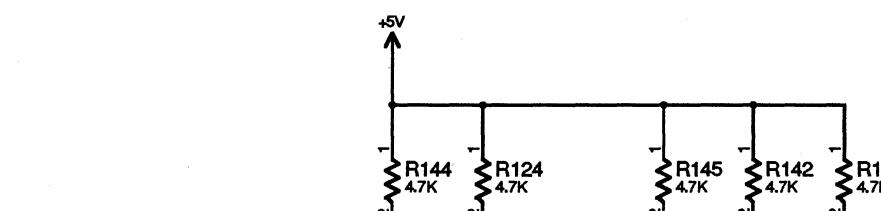


1 2 3 4 5 6 7 8

D[0:31] {2,12,26,34}
DIR245 {4,28}
EN245 {4,28}



MEMD[0:31] {5,6,9}



IPL*[0] {2,26}

IPL*[1] {2,26}

IPL*[2] {2,26}

BG* CPU {2,9}

BB* {2,11,27}

CDIS*

MDIS*

CPURESETIN* {26}

RW {2,4,9,11,26,27,32}

SNOOPCONTROL[0] {2,11,27}

SNOOPCONTROL[1] {2,11,27}

TT[0] {2,11,27}

TT[1] {2,4,11,27,32}

BS_CPUIN

BS_MODE {2}

TRISTATE_CPU* {2}

TEA* {2,4,11,26,27,32}

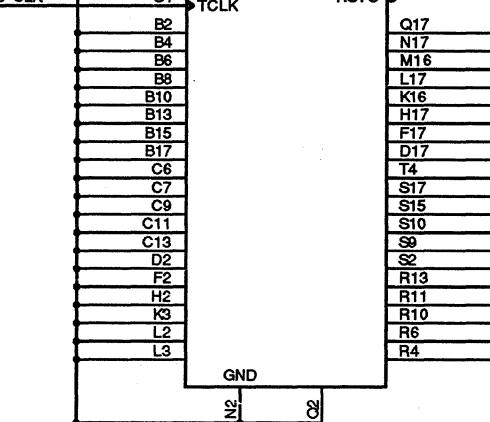
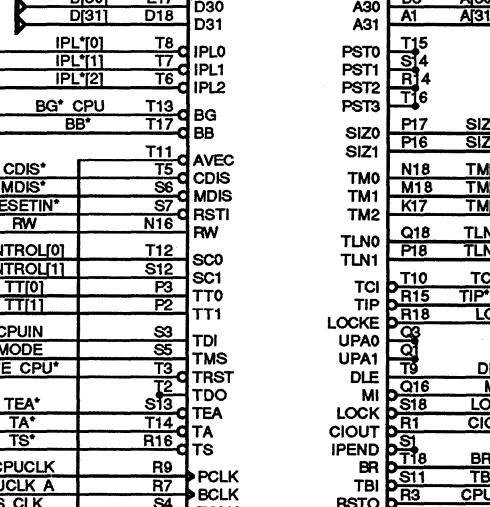
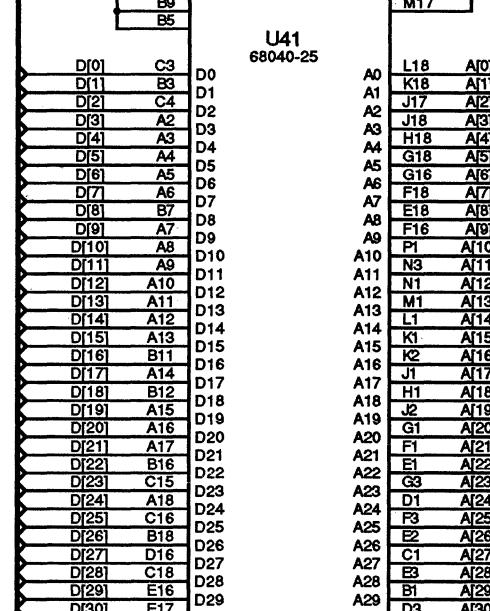
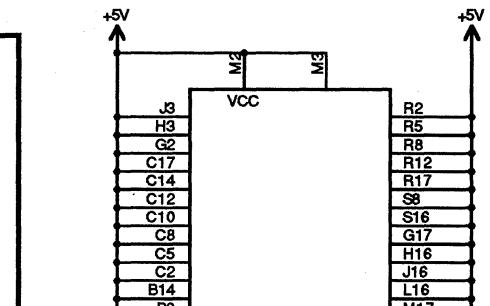
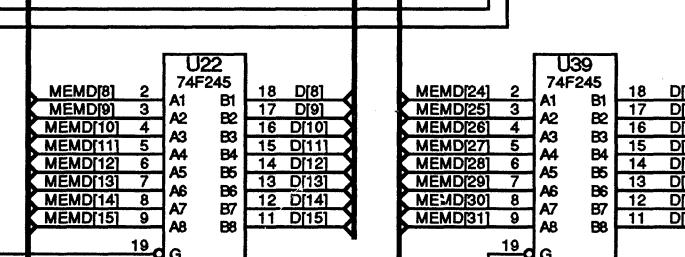
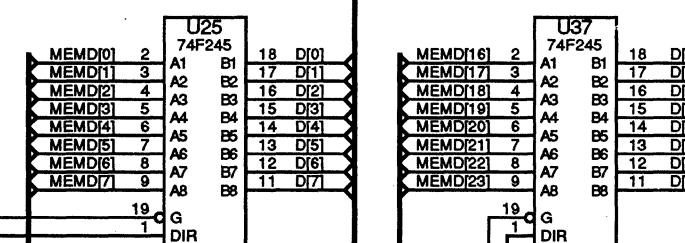
TA* {2,4,11,26,27,32}

TS* {2,4,9,11,26,27,32}

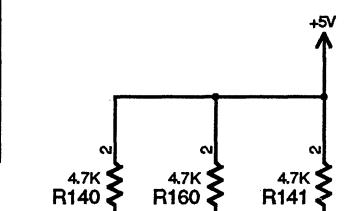
2XCPUCLK {10}

CPUCLK_A {4,10}

BS_CLK {2}



A[0:31] {2,4,9,11,12,32}



SIZ[0] {2,4,11,27,32}

SIZ[1] {2,4,11,27,32}

TM[0] {2}

TM[1] {2}

TM[2] {2}

TLN[0] {2}

TLN[1] {2}

TCI* {2}

TIP* CPU {2}

LOCKE* {2}

LOCK* {2,11,27}

CIOUT* {2}

DLE* {2}

M1* {2,4}

LOCK* {2,11,27}

CIOUT* {2}

BR* CPU {2,27}

TBI* {2,4,11,27,32}

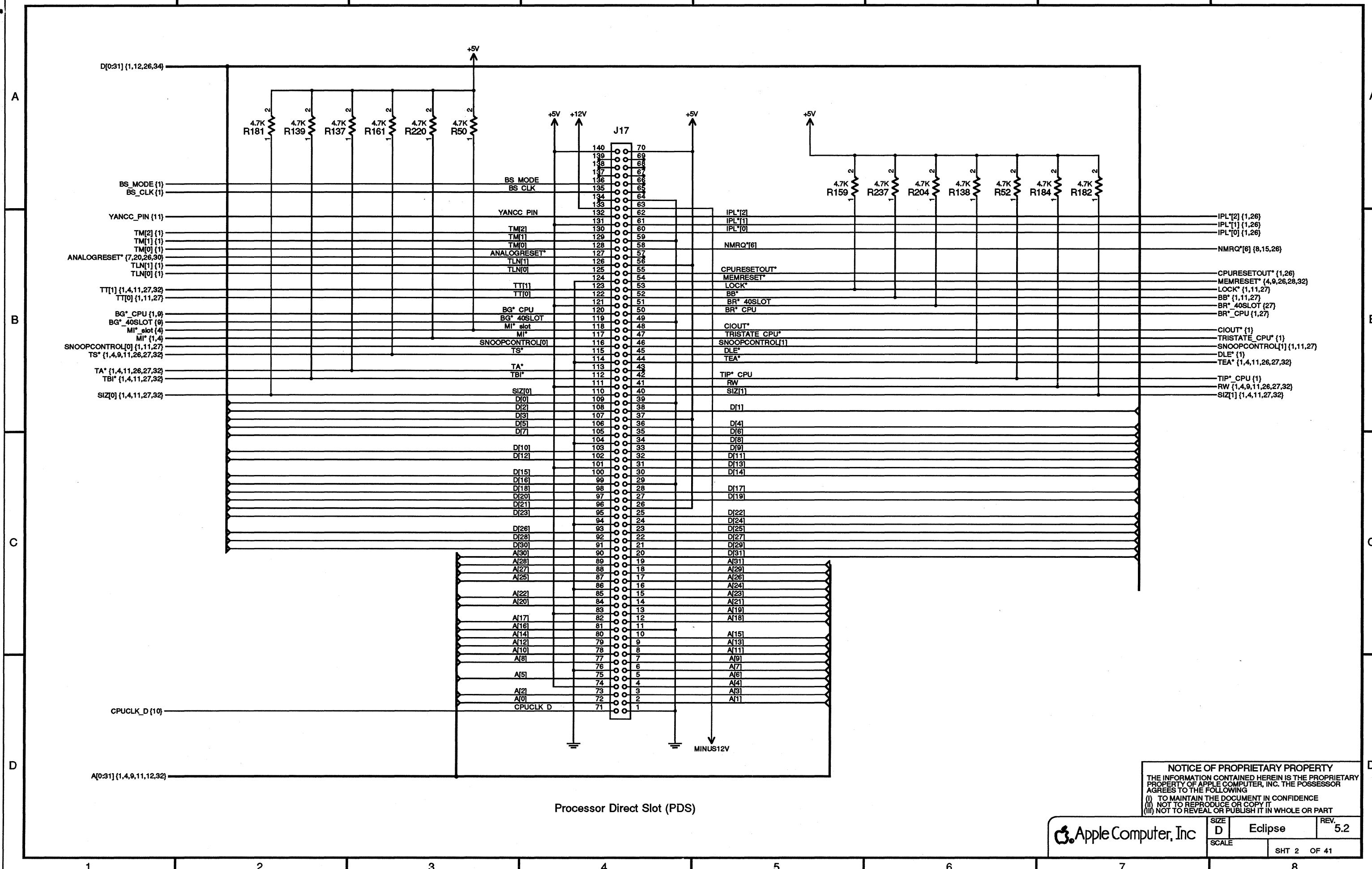
CPURESETOUT* {2,26}

ECLIPSE PVT
July 11, 1991

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Apple Computer, Inc.

SIZE	REV.
D	Eclipse
SCALE	V5.2
SHT 1	OF 41



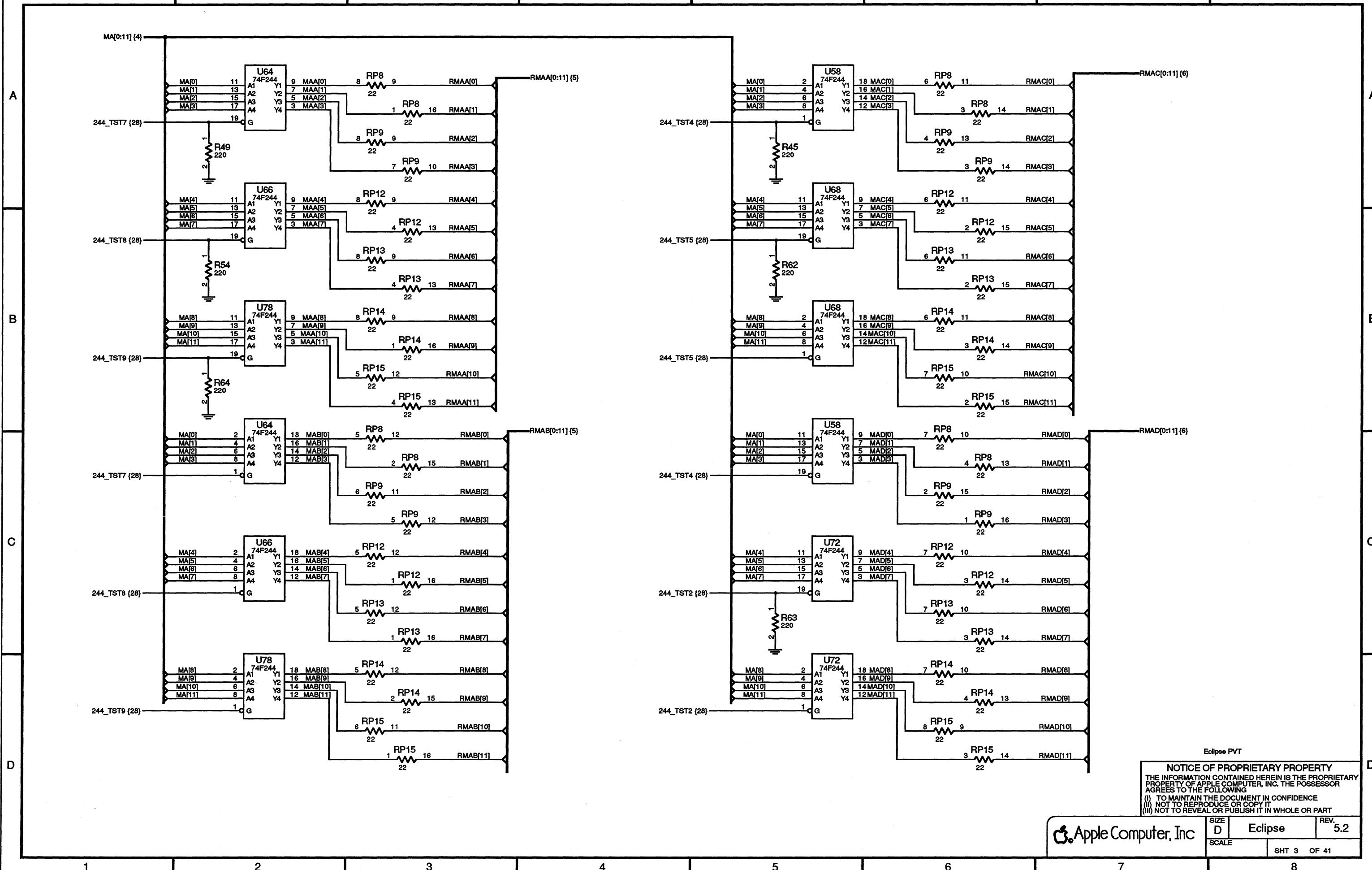
A[0:31] {1,4,9,11,12,32}

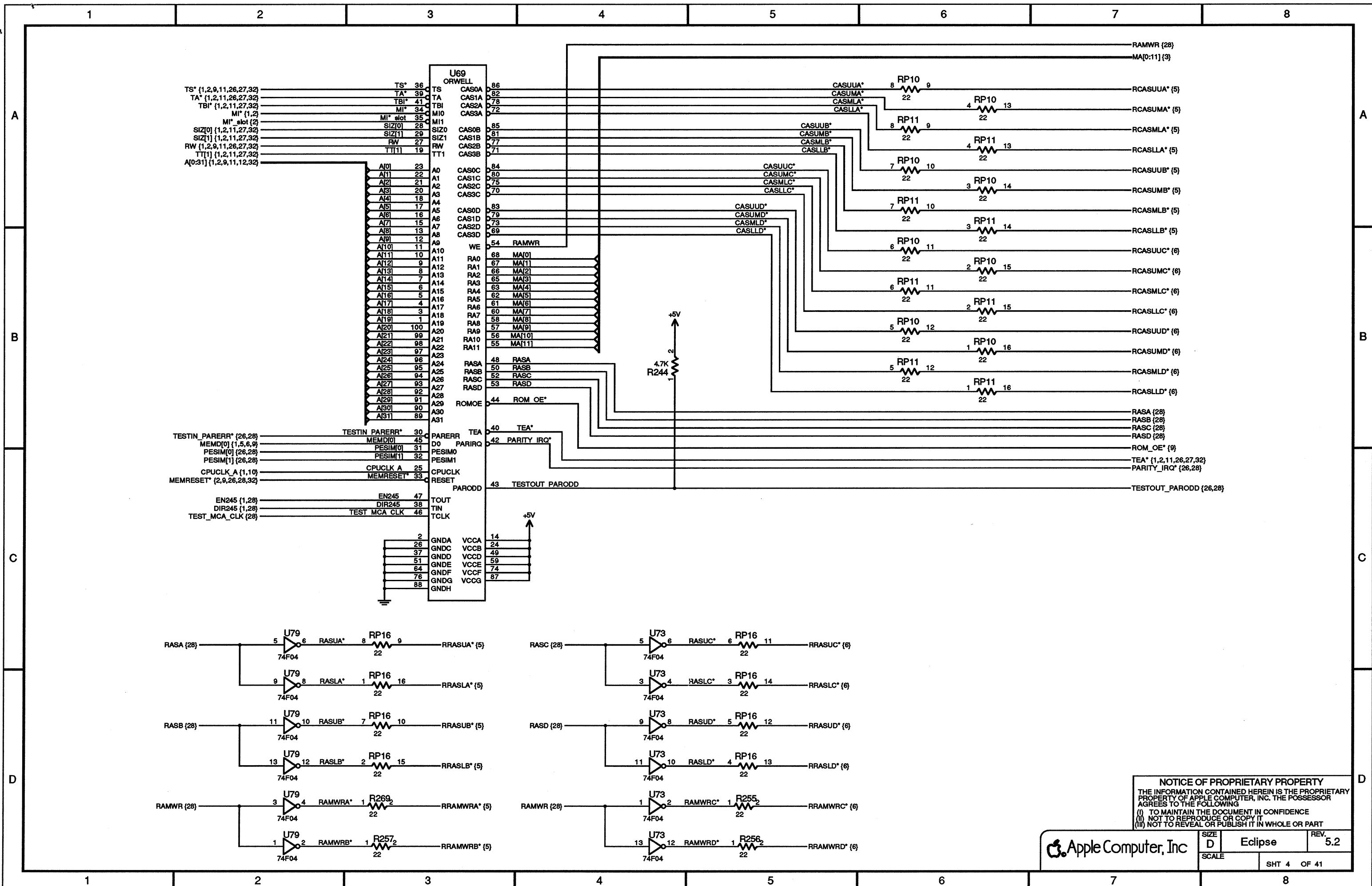
Processor Direct Slot (PDS)

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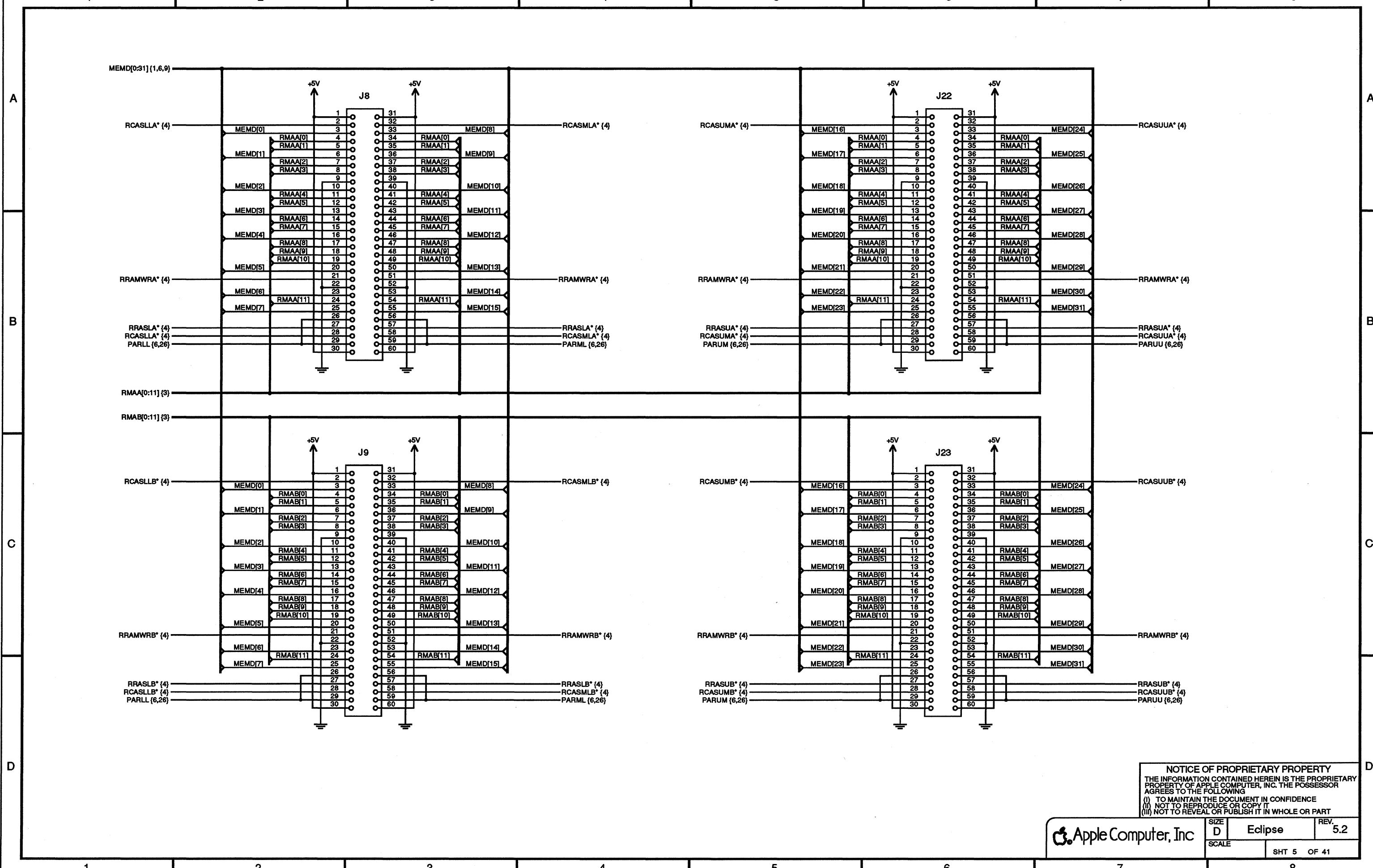
Apple Computer, Inc.		SIZE	REV.
SCALE	SHT 2 OF 41	D	Eclipse
		5.2	

1 2 3 4 5 6 7 8





1 2 3 4 5 6 7 8



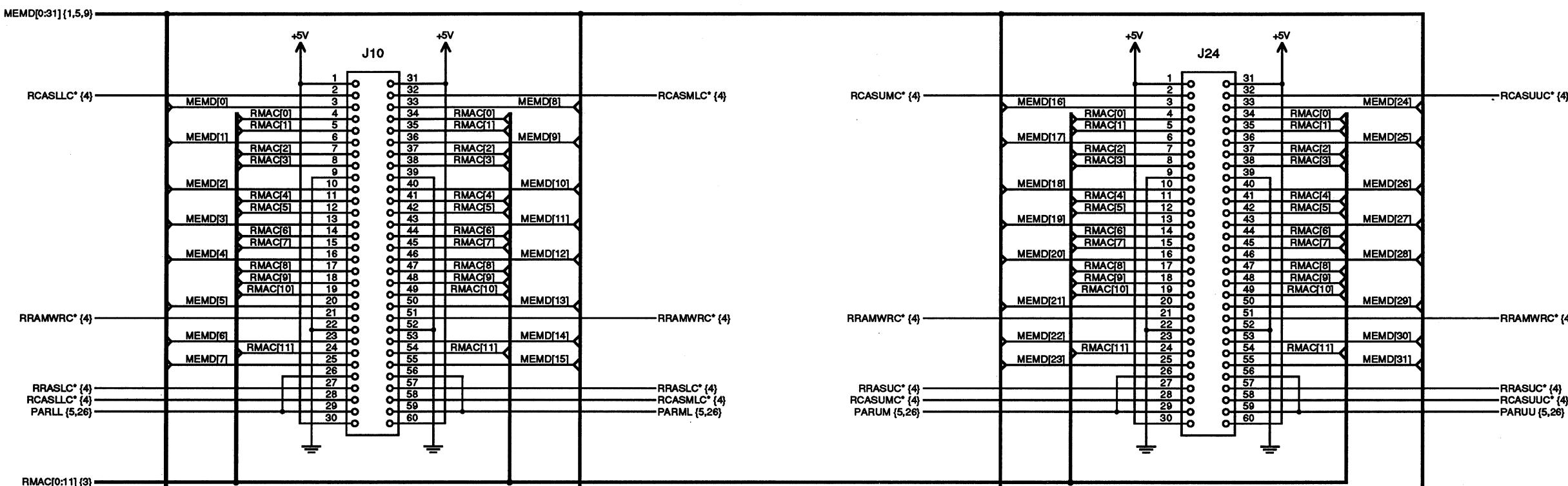
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SIZE	Eclipse	REV.
SCALE	SHT 5	OF 41

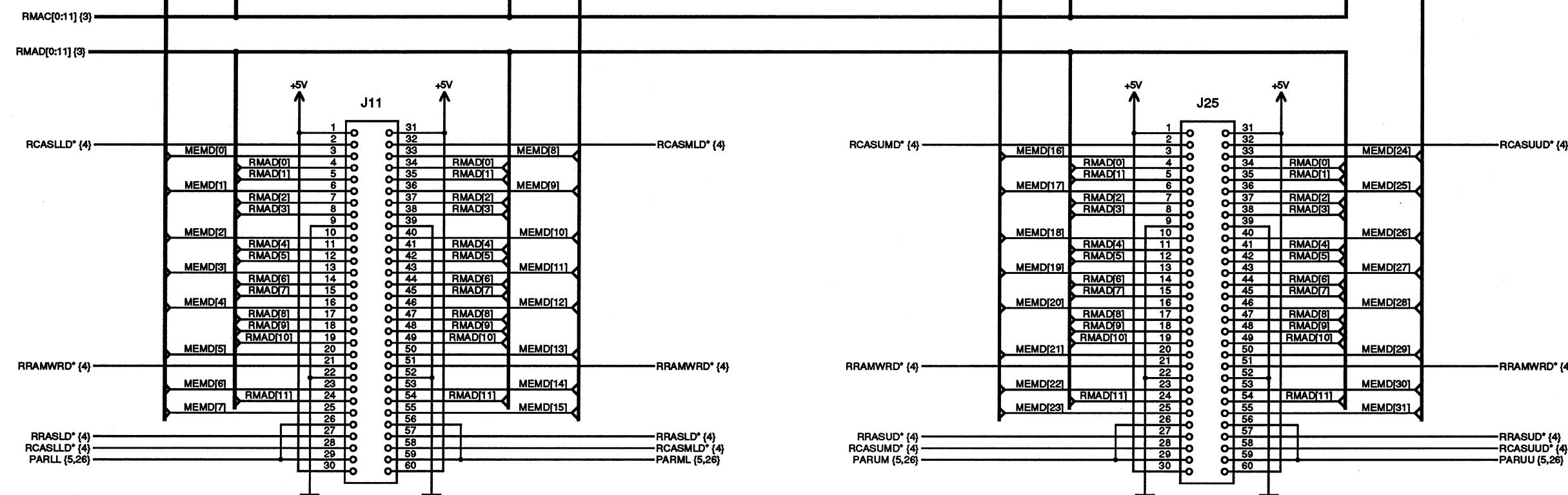
1 2 3 4 5 6 7 8

A



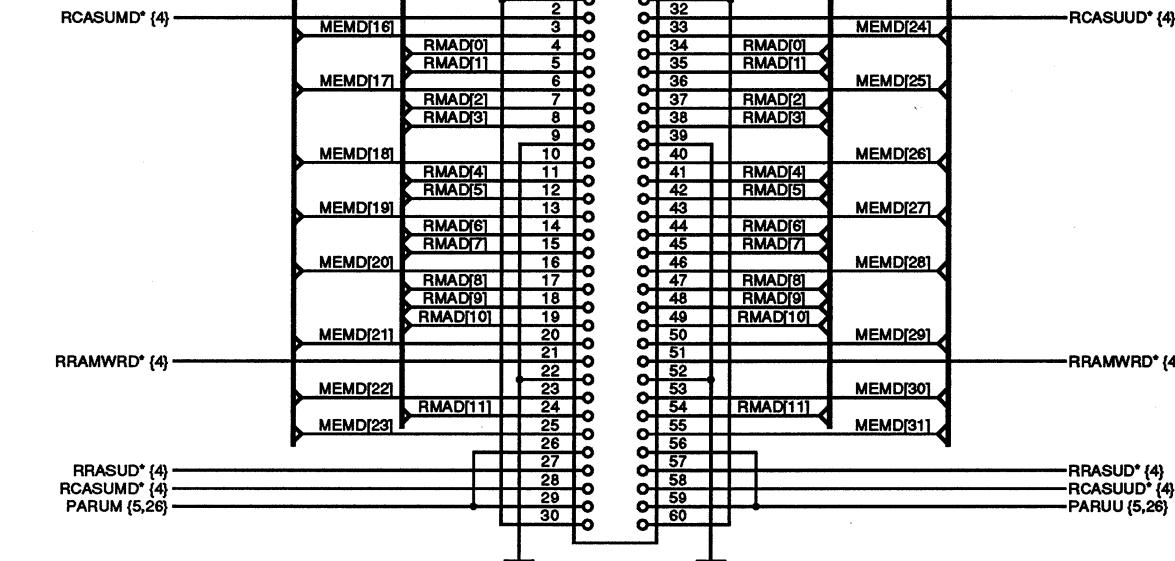
A

B



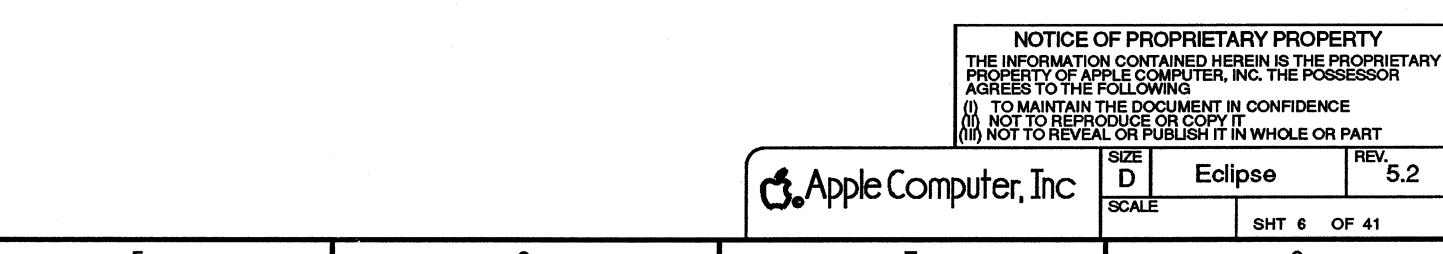
B

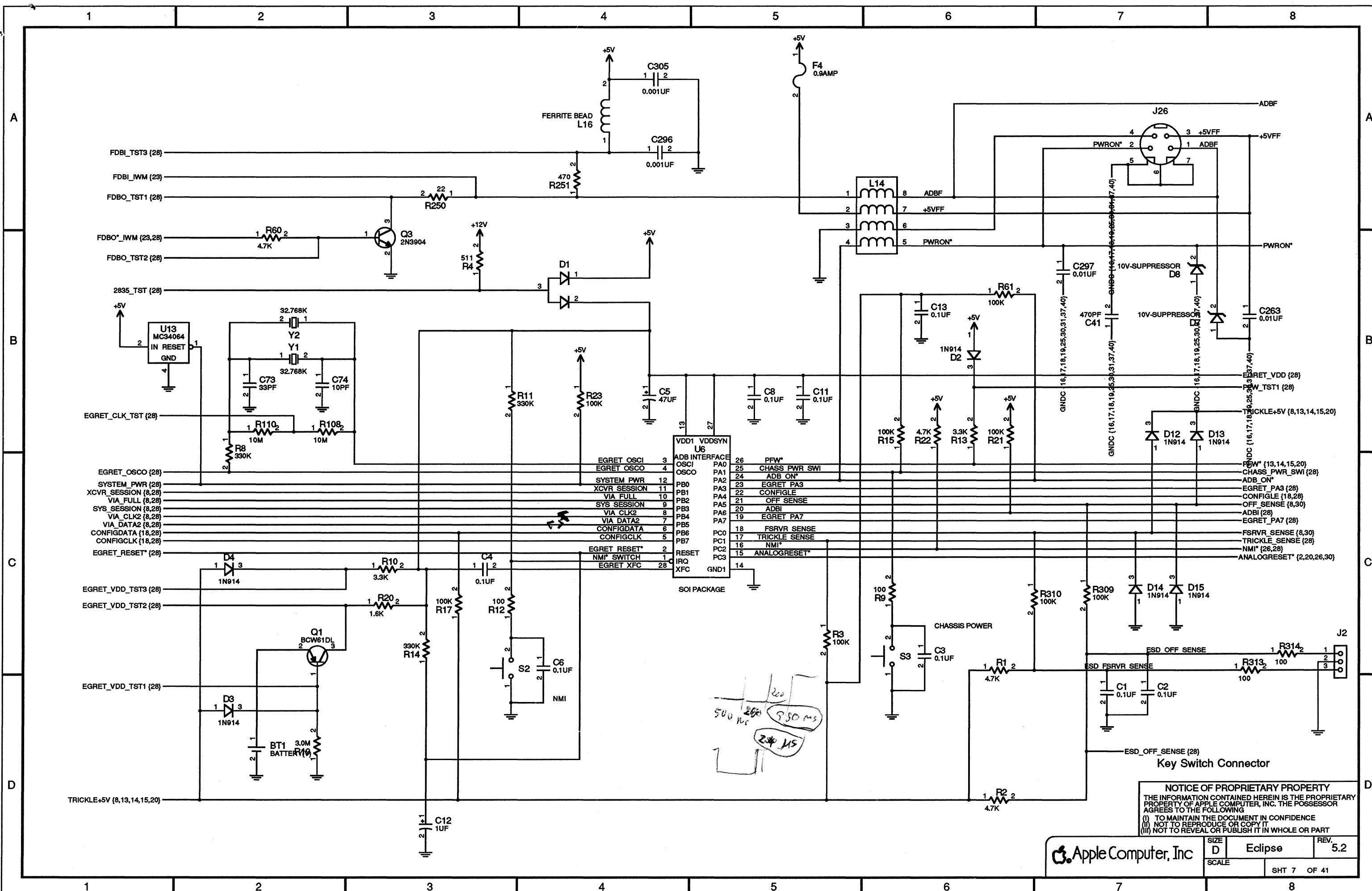
C



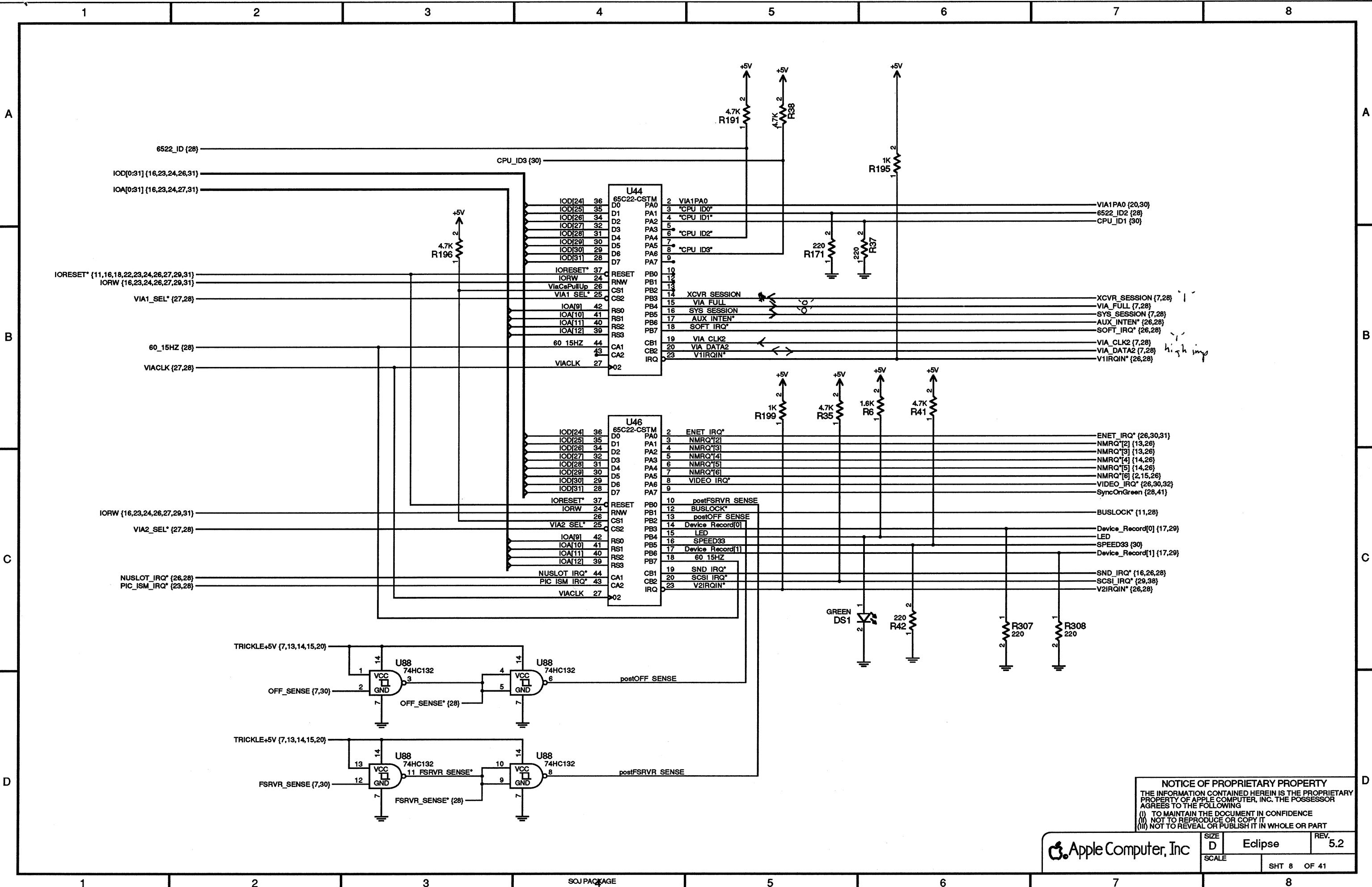
C

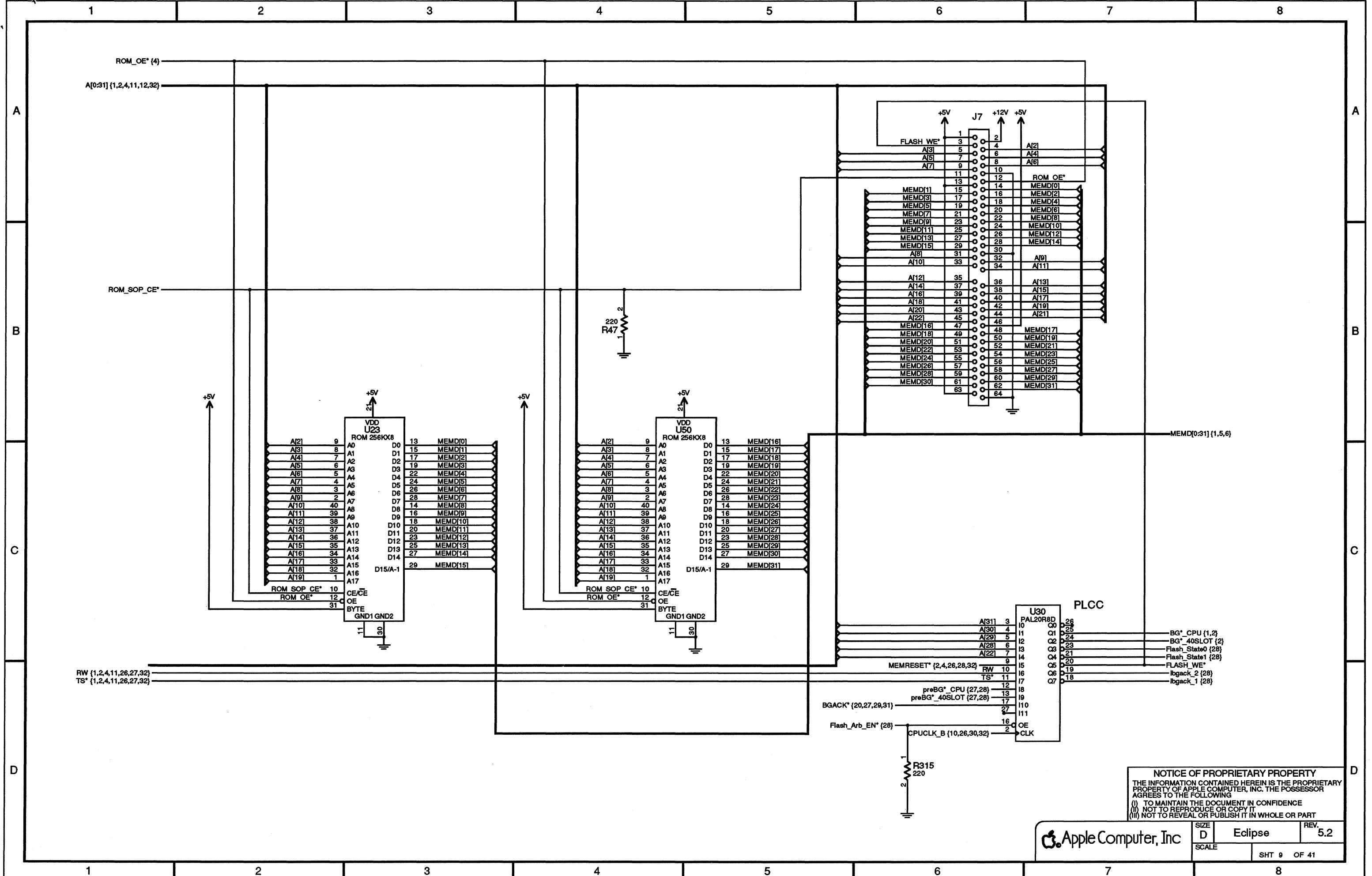
D

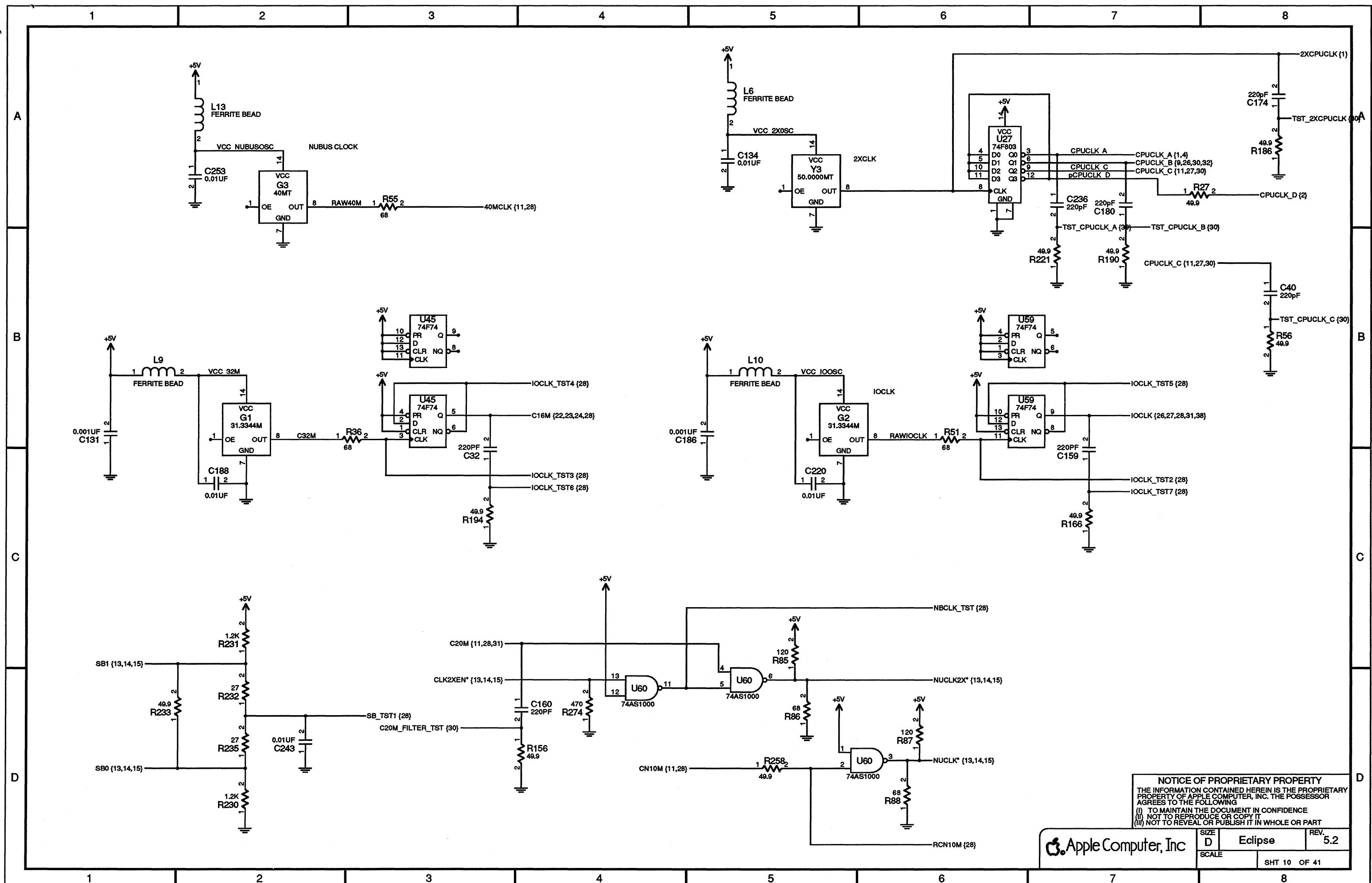


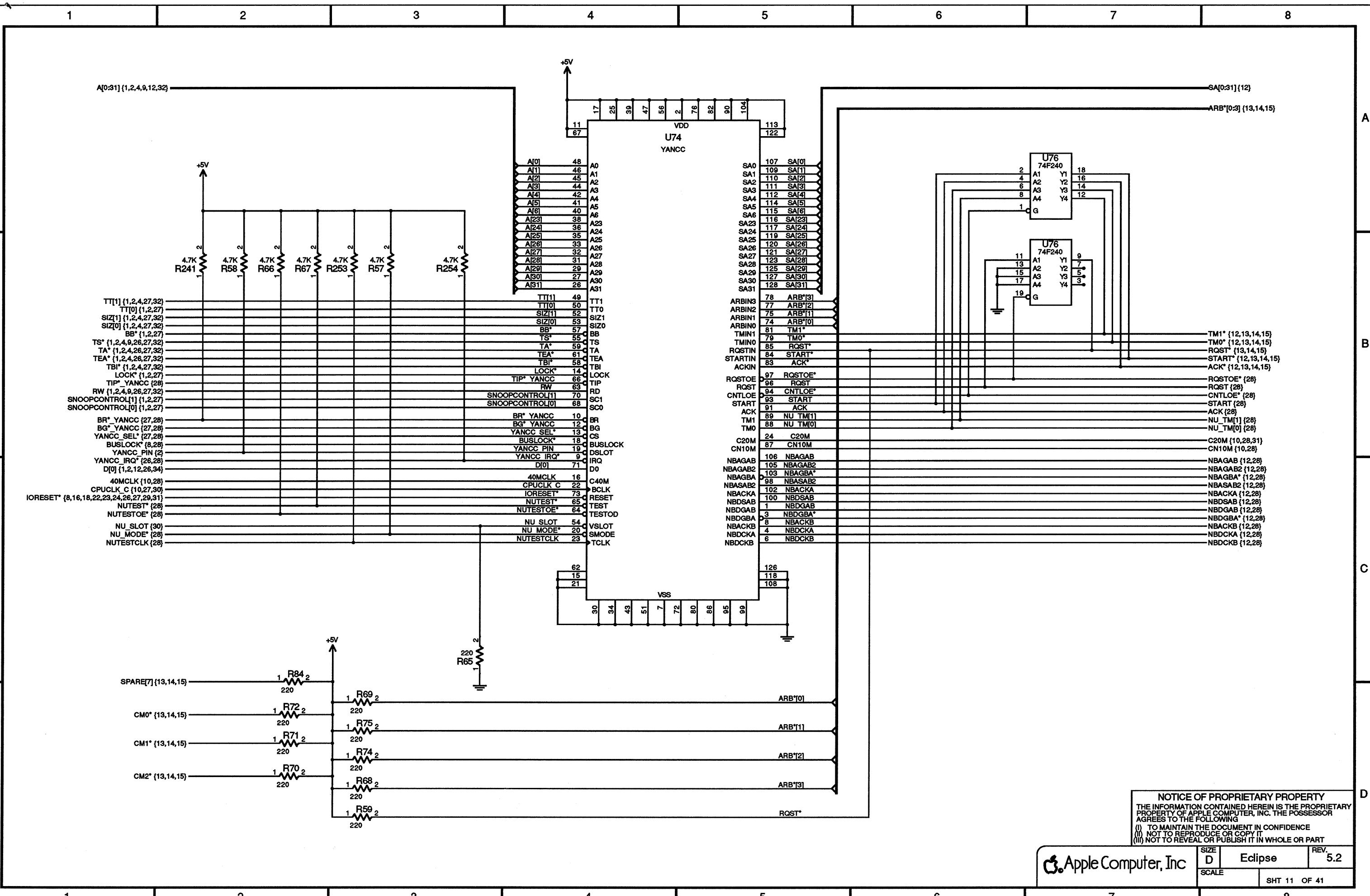


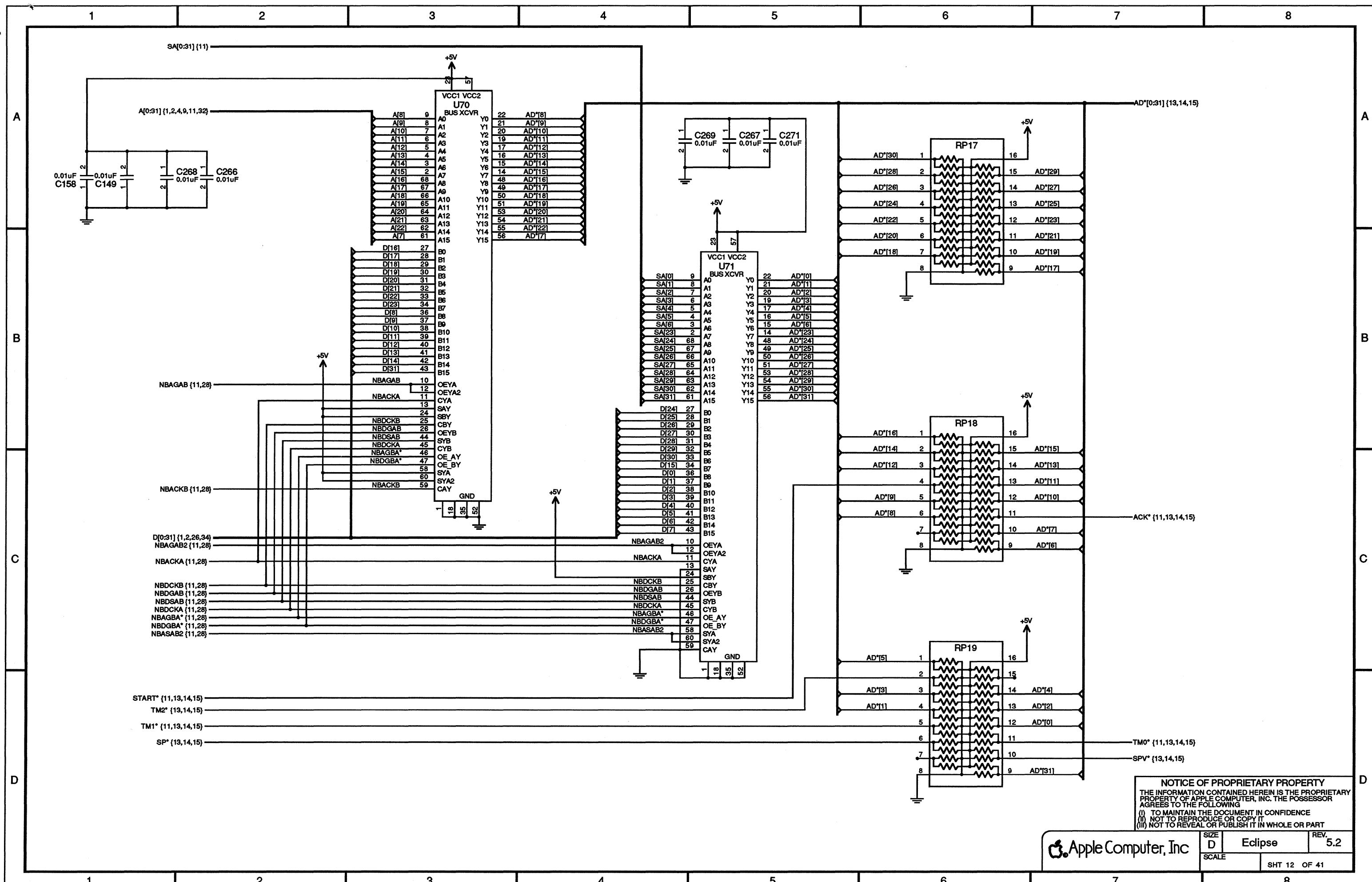
1 2 3 4 5 6 7 8







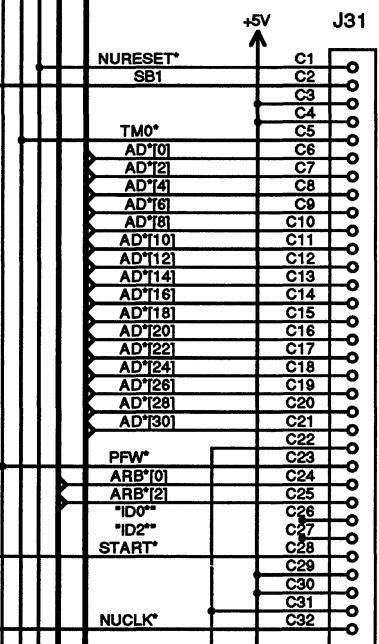
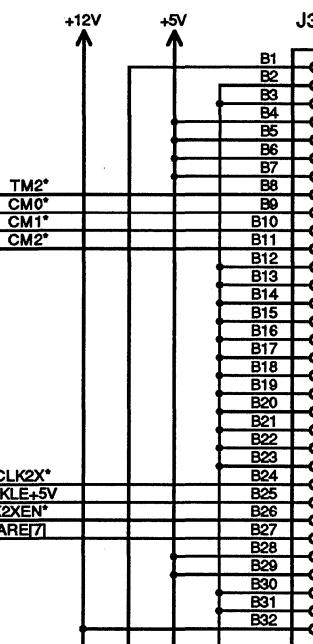
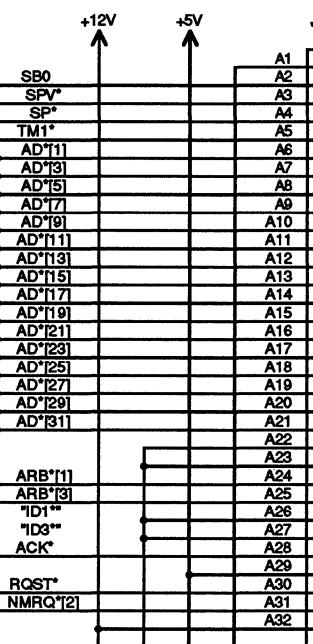




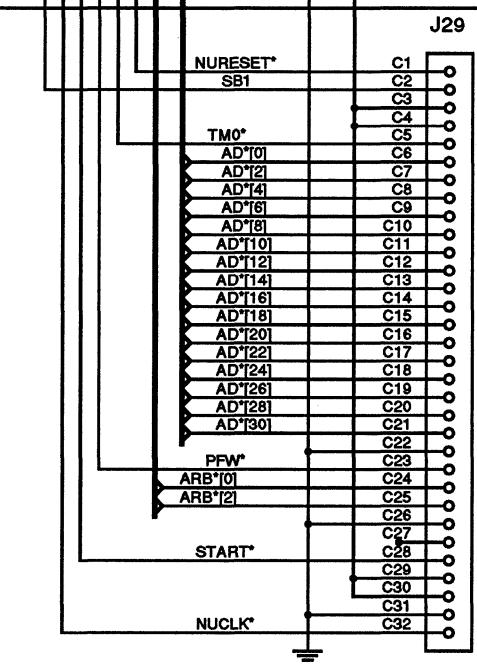
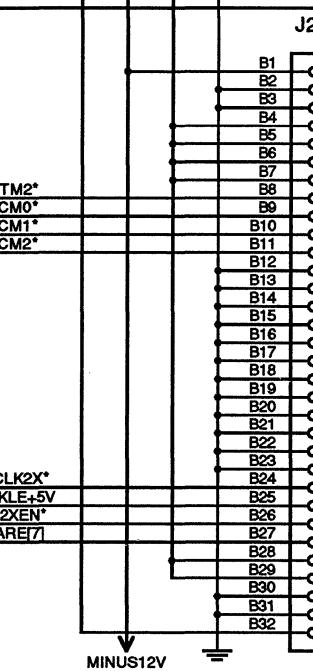
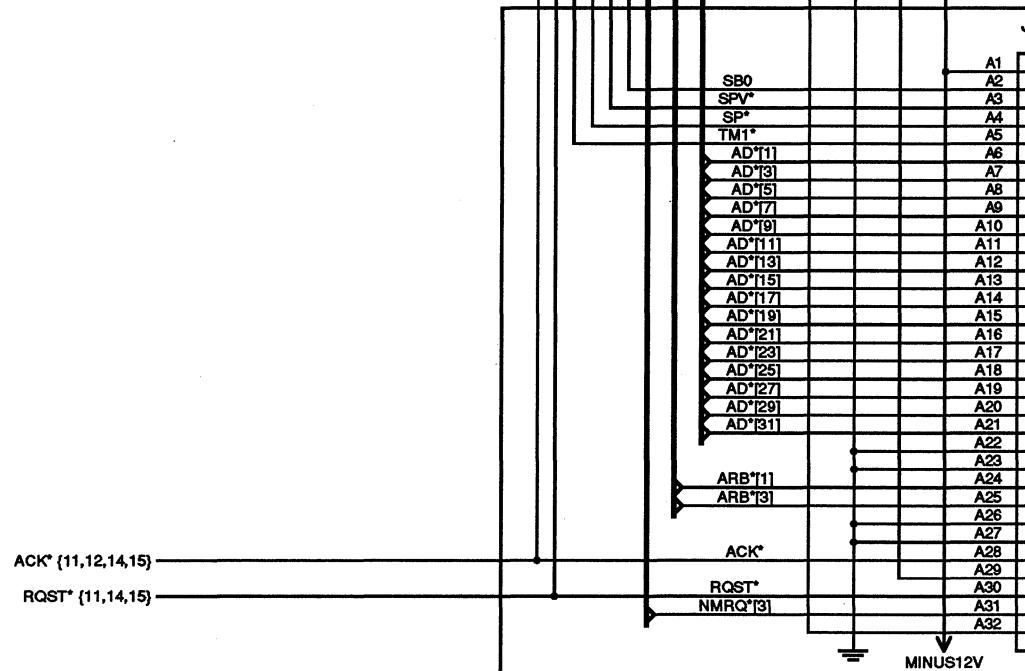
1 2 3 4 5 6 7 8

AD*[0:31] {12,14,15}
 ARB*[0:3] {11,14,15}
 NMREQ*[2:6] {14}
 NURESET* {14,15,26}
 TM0* {11,12,14,15}
 PFW* {7,14,15,20}
 START* {11,12,14,15}
 NUCLK* {10,14,15}
 SB1 {10,14,15}

SB0 {10,14,15}
 SPV* {12,14,15}
 SP* {12,14,15}
 TM1* {11,12,14,15}

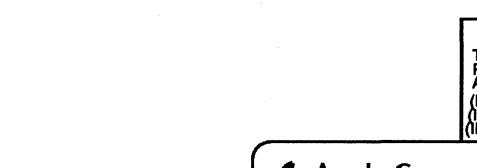
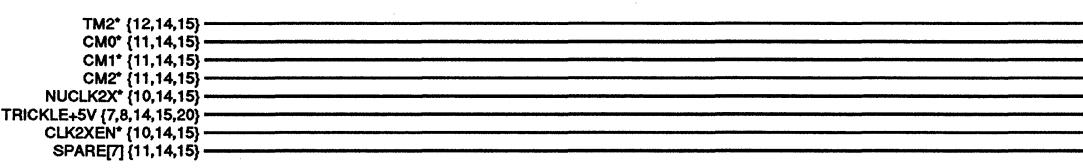


SLOT2
A16



SLOT3
B16

ACK* {11,12,14,15}
 RQST* {11,14,15}



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SIZE	Eclipse	REV.
D		5.2
SCALE		SHT 13 OF 41

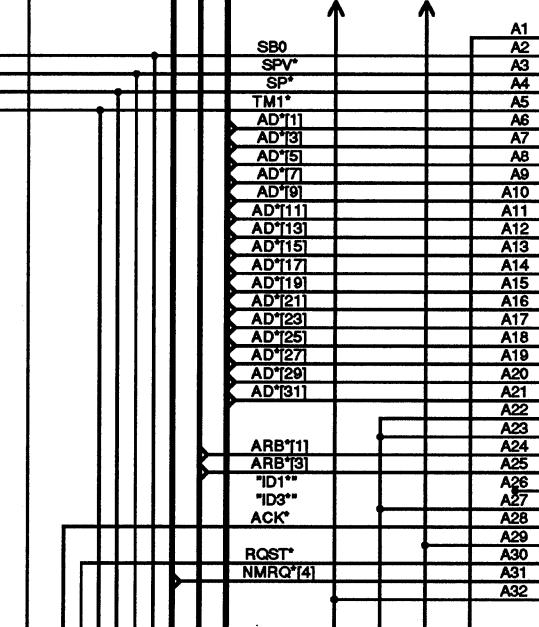
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1 2 3 4 5 6 7 8

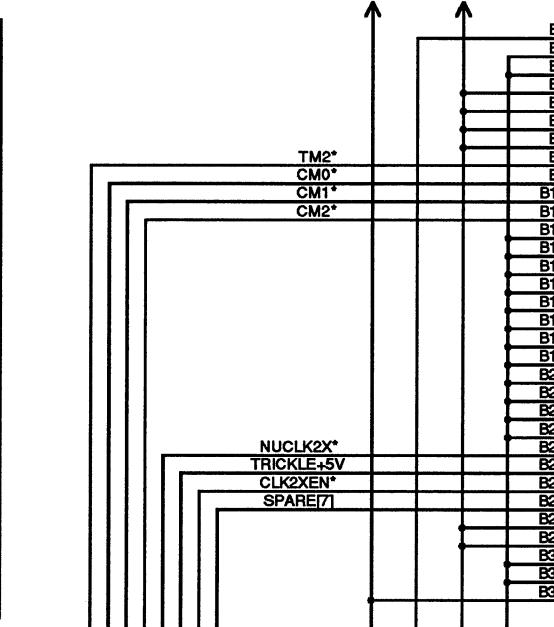
AD[0:31] {12,13,15}
ARB[0:3] {11,13,15}
NMRC[2:6] {13}
NURESET* {13,15,26}
TM0* {11,12,13,15}
PFW* {7,13,15,20}
START* {11,12,13,15}
NUCLK {10,13,15}
SB1 {10,13,15}

SB0 {10,13,15}
SPV* {12,13,15}
SP* {12,13,15}
TM1* {11,12,13,15}

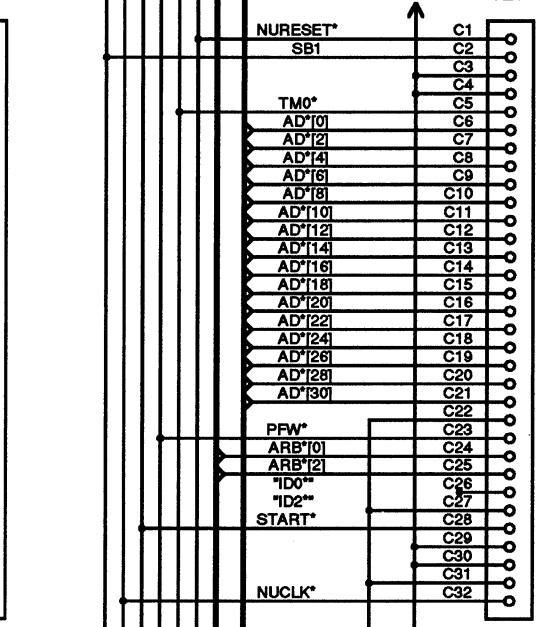
+12V +5V J28



+12V +5V J28

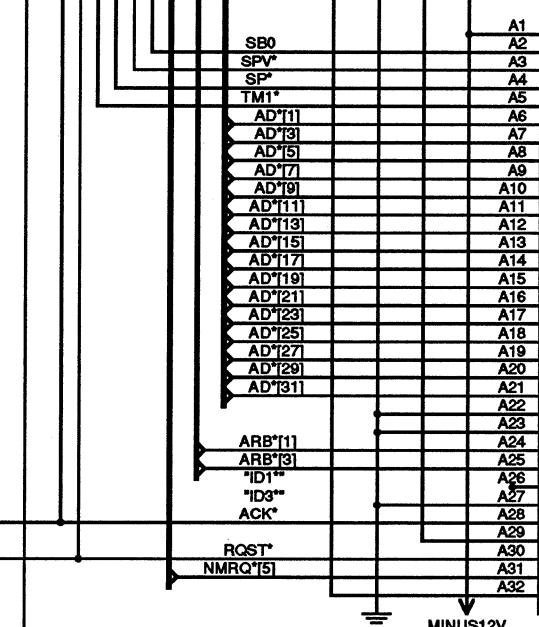


+5V J28

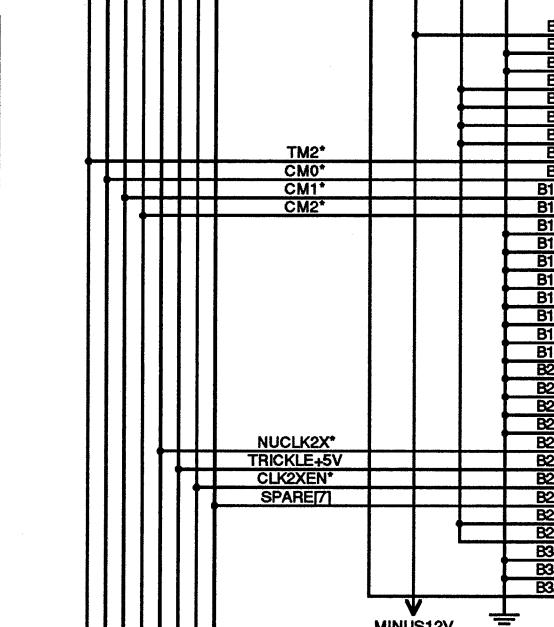


SLOT4
C16

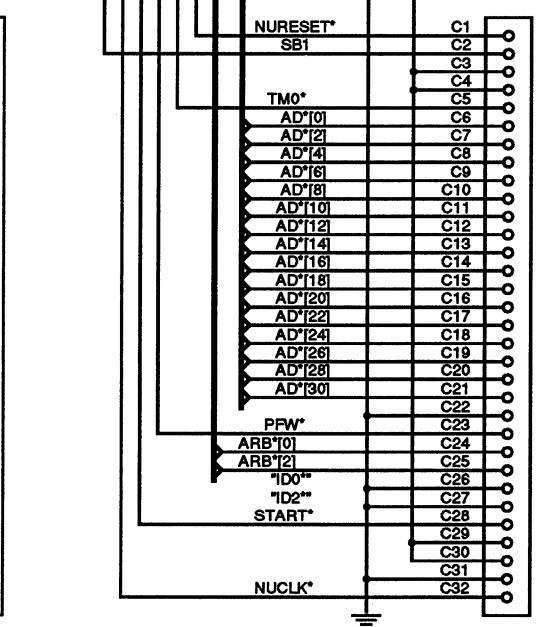
J27



J27



J27



SLOT 5
D16

ACK* {11,12,13,15}
RQST* {11,13,15}

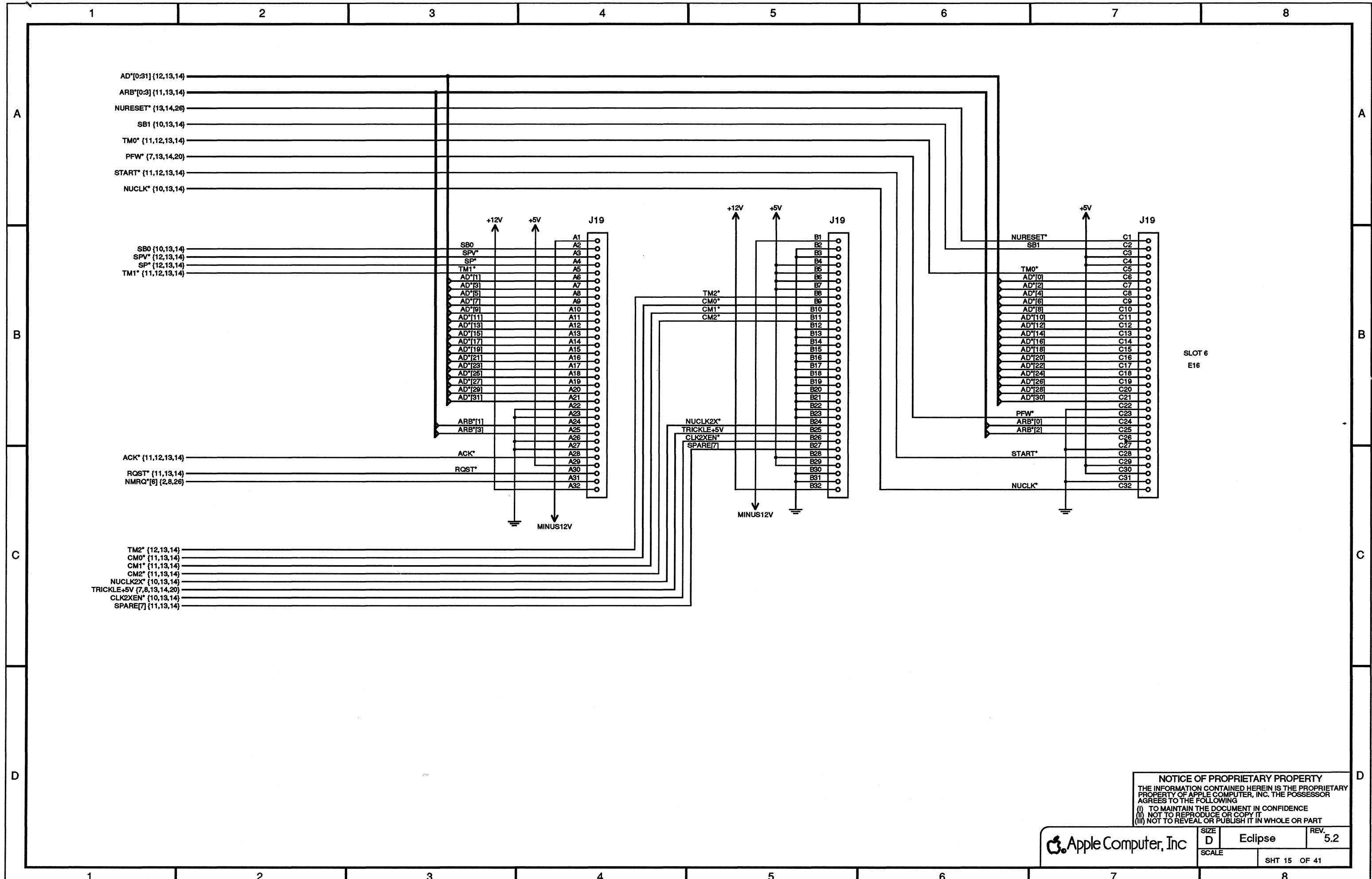
TM2* {12,13,15}
CM0* {11,13,15}
CM1* {11,13,15}
CM2* {11,13,15}
NUCLK2X* {10,13,15}
TRICKLE+5V {7,8,13,15,20}
CLK2XEN* {10,13,15}
SPARE[7] {11,13,15}

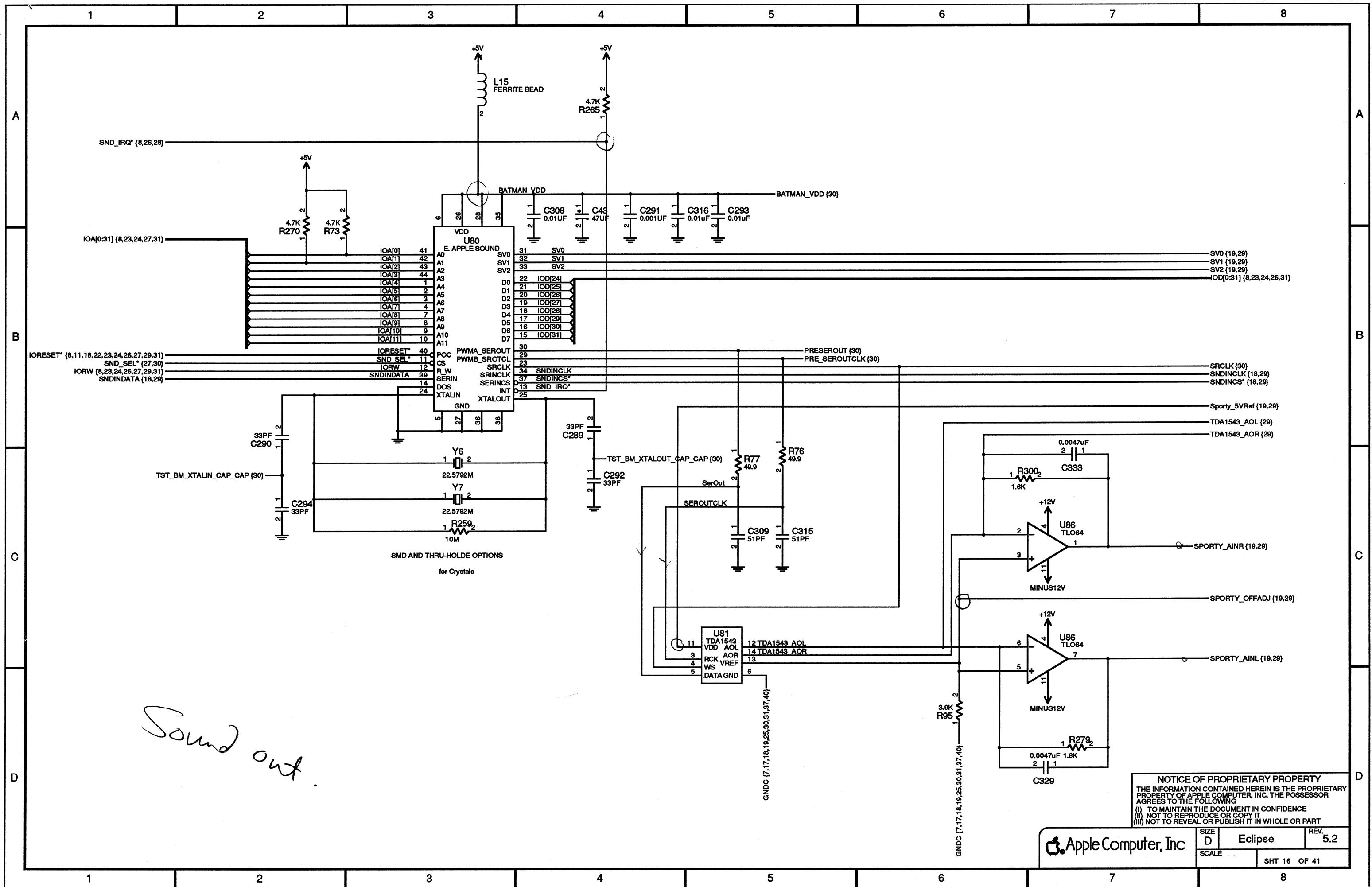
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Apple Computer, Inc.

SIZE	Eclipse	REV.
SCALE	SHT 14 OF 41	5.2

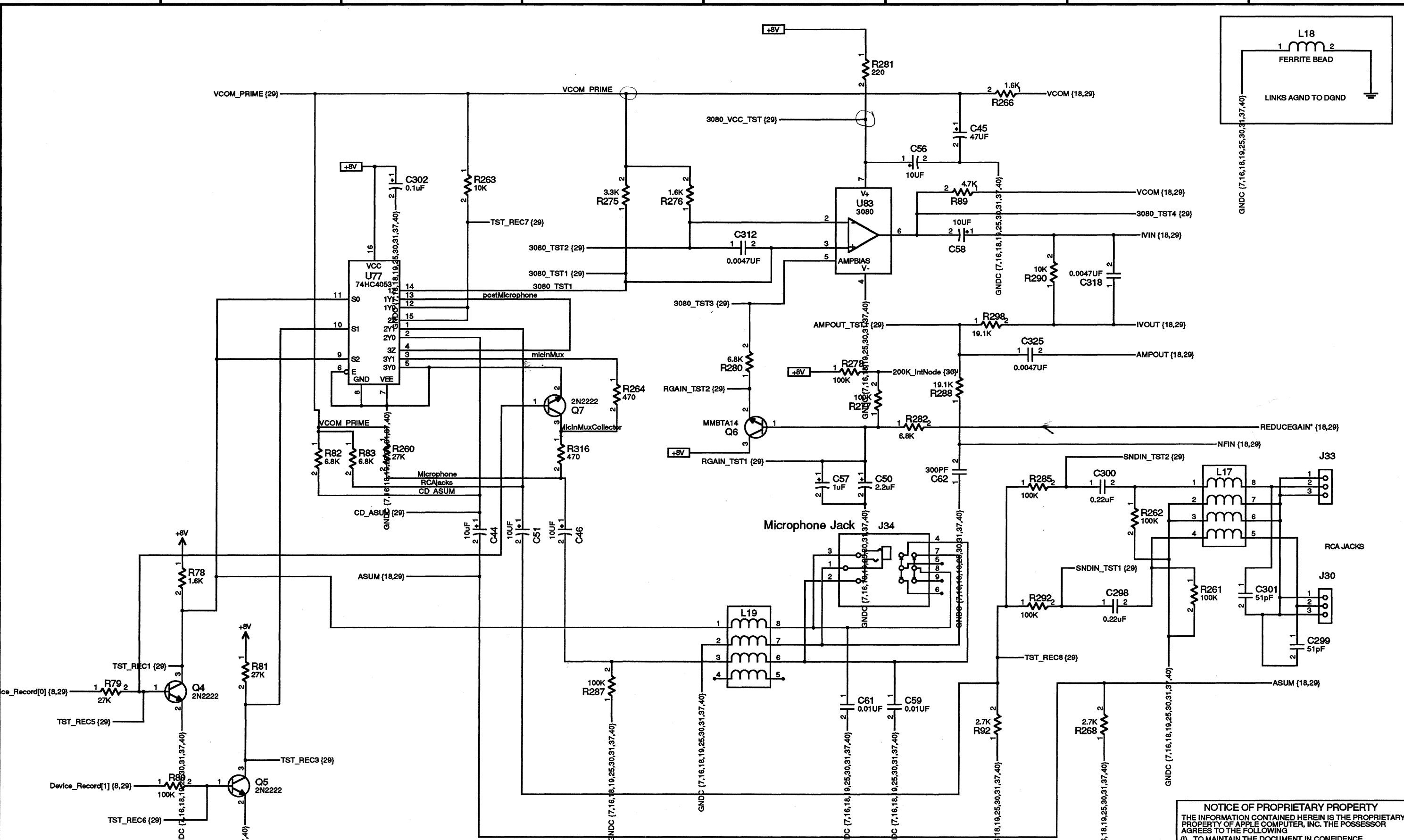
1 2 3 4 5 6 7 8





1 2 3 4 5 6 7 8

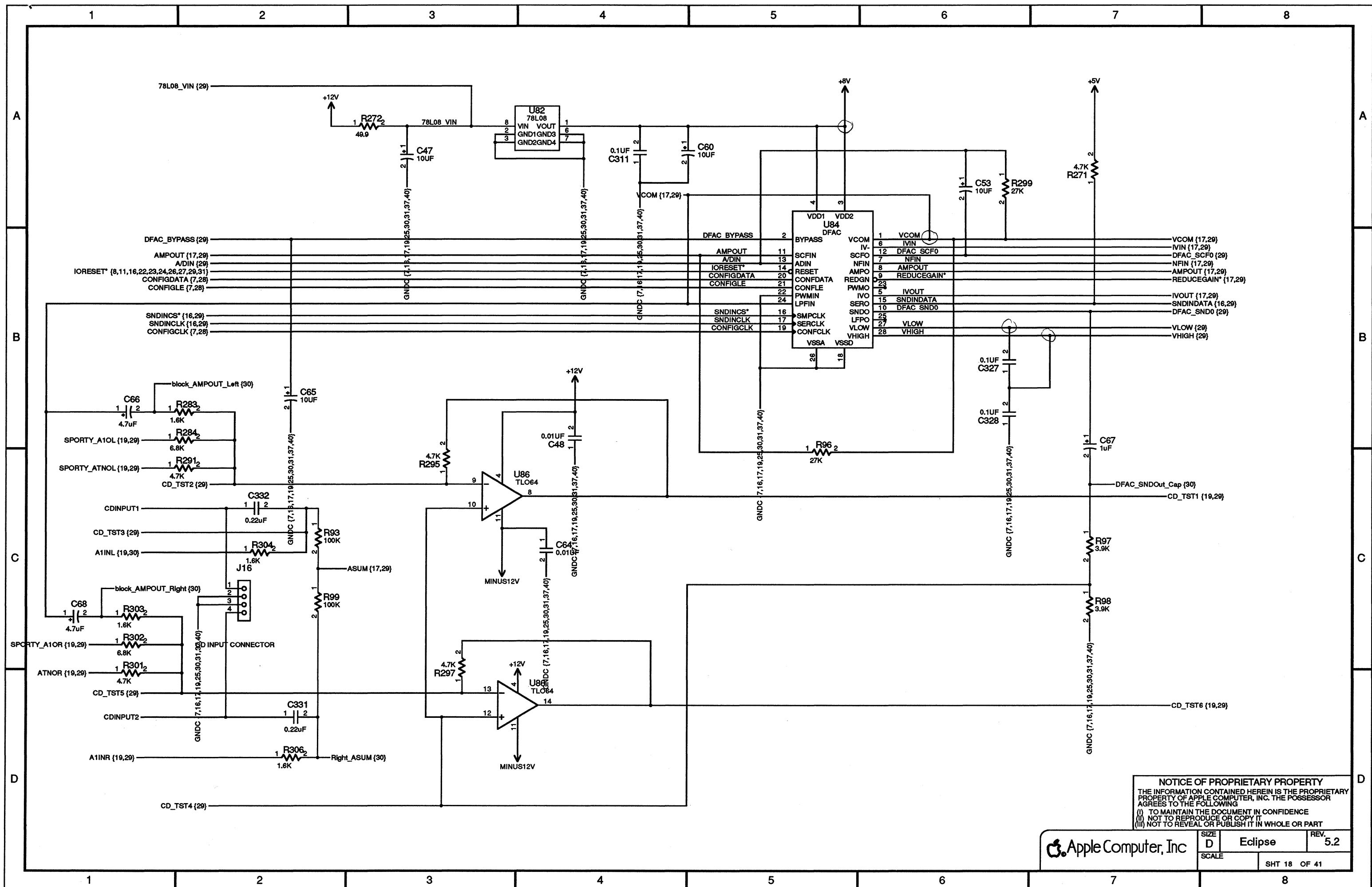
A

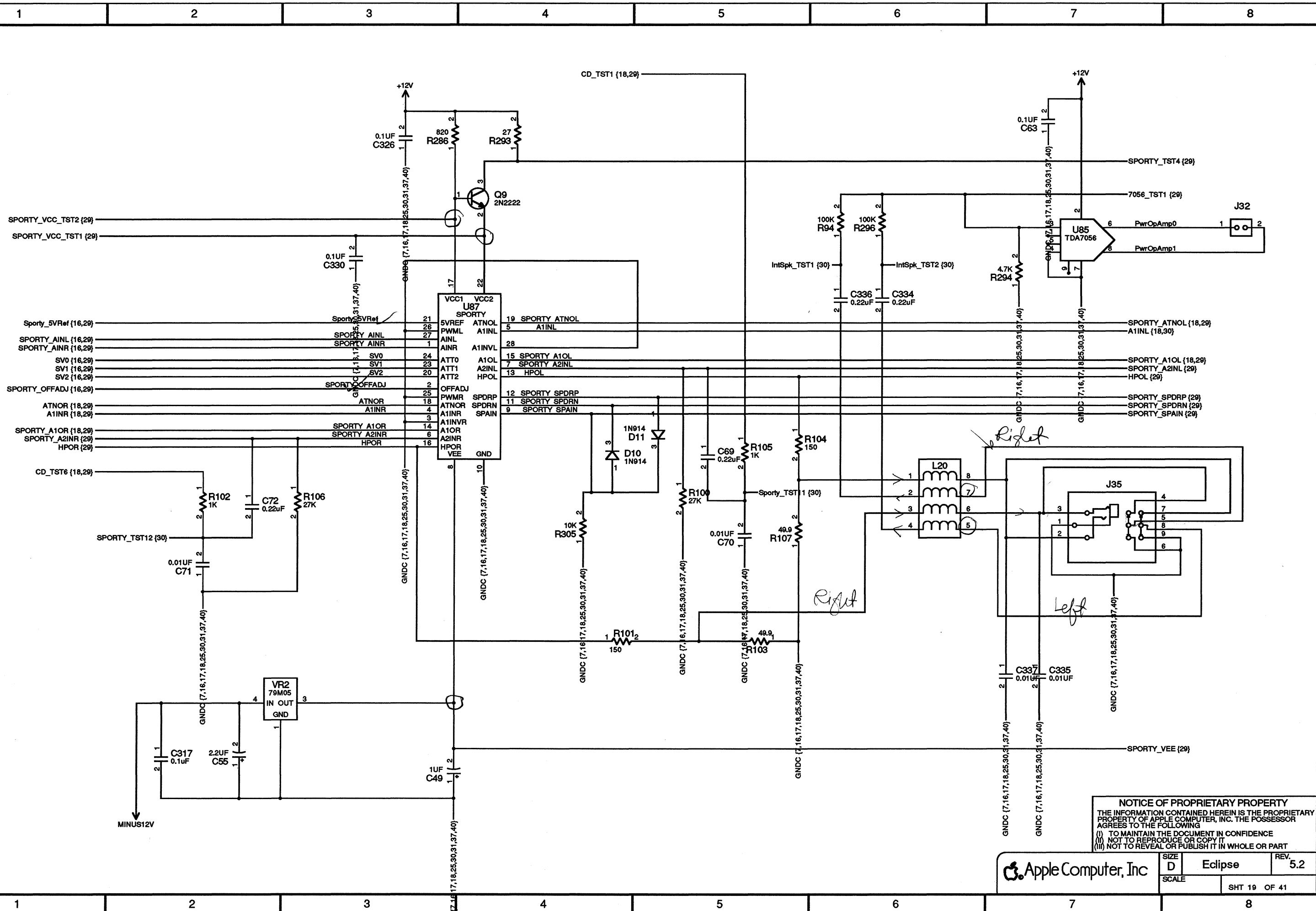


The LH Memorial Page

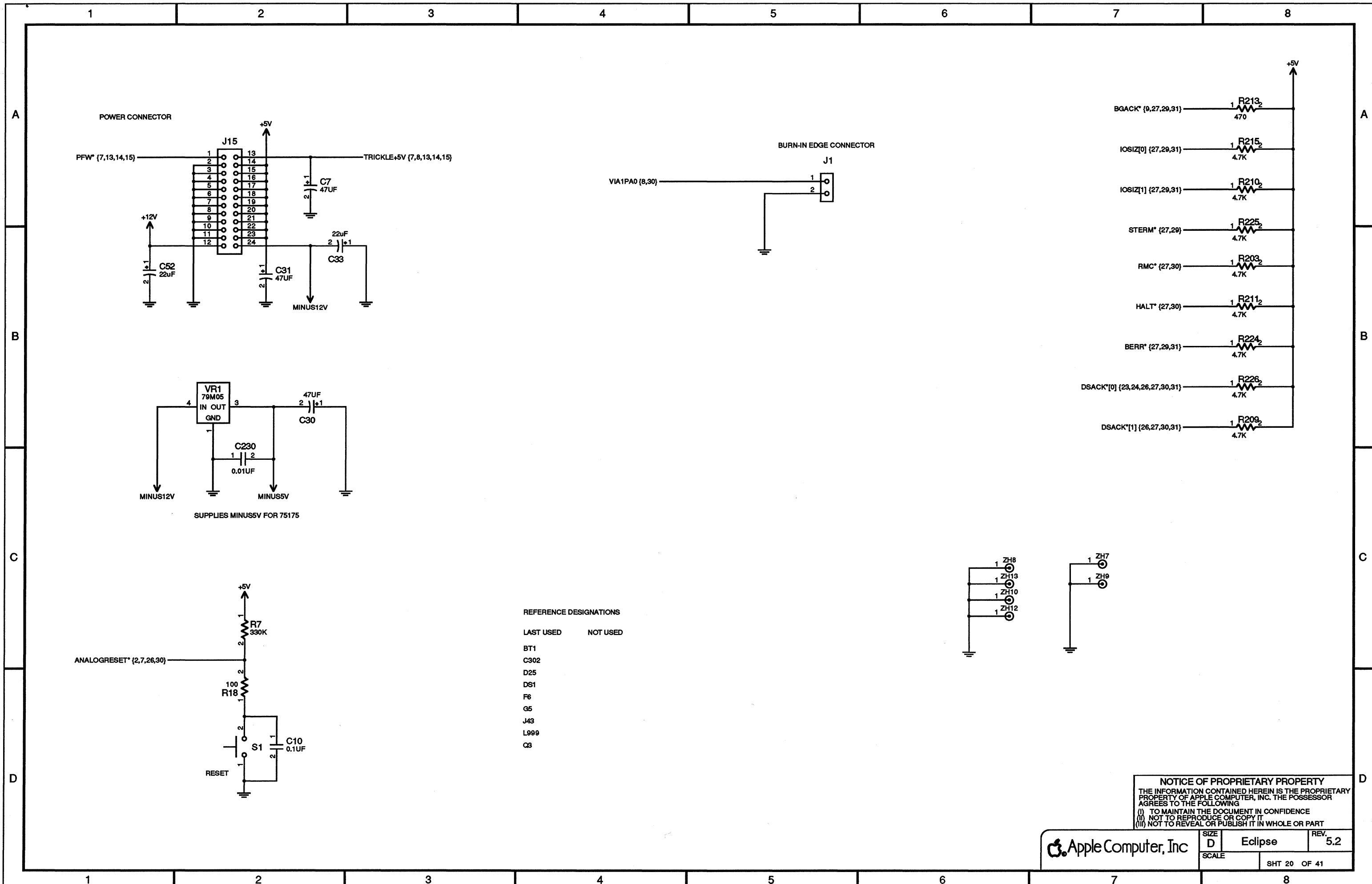
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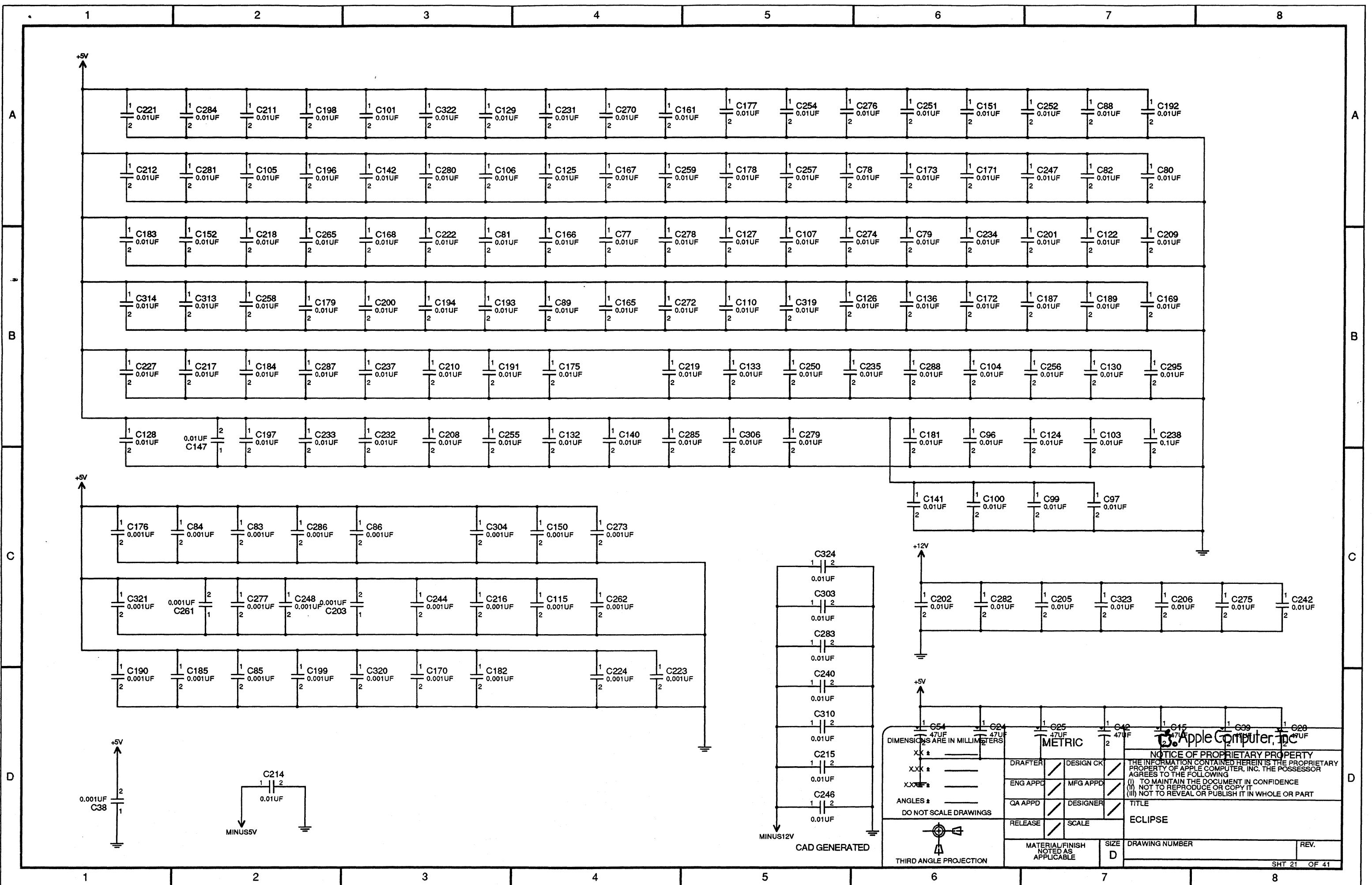
Apple Computer, Inc		SIZE D	Eclipse	REV. 5.2
SCALE			SHT 17	OF 41

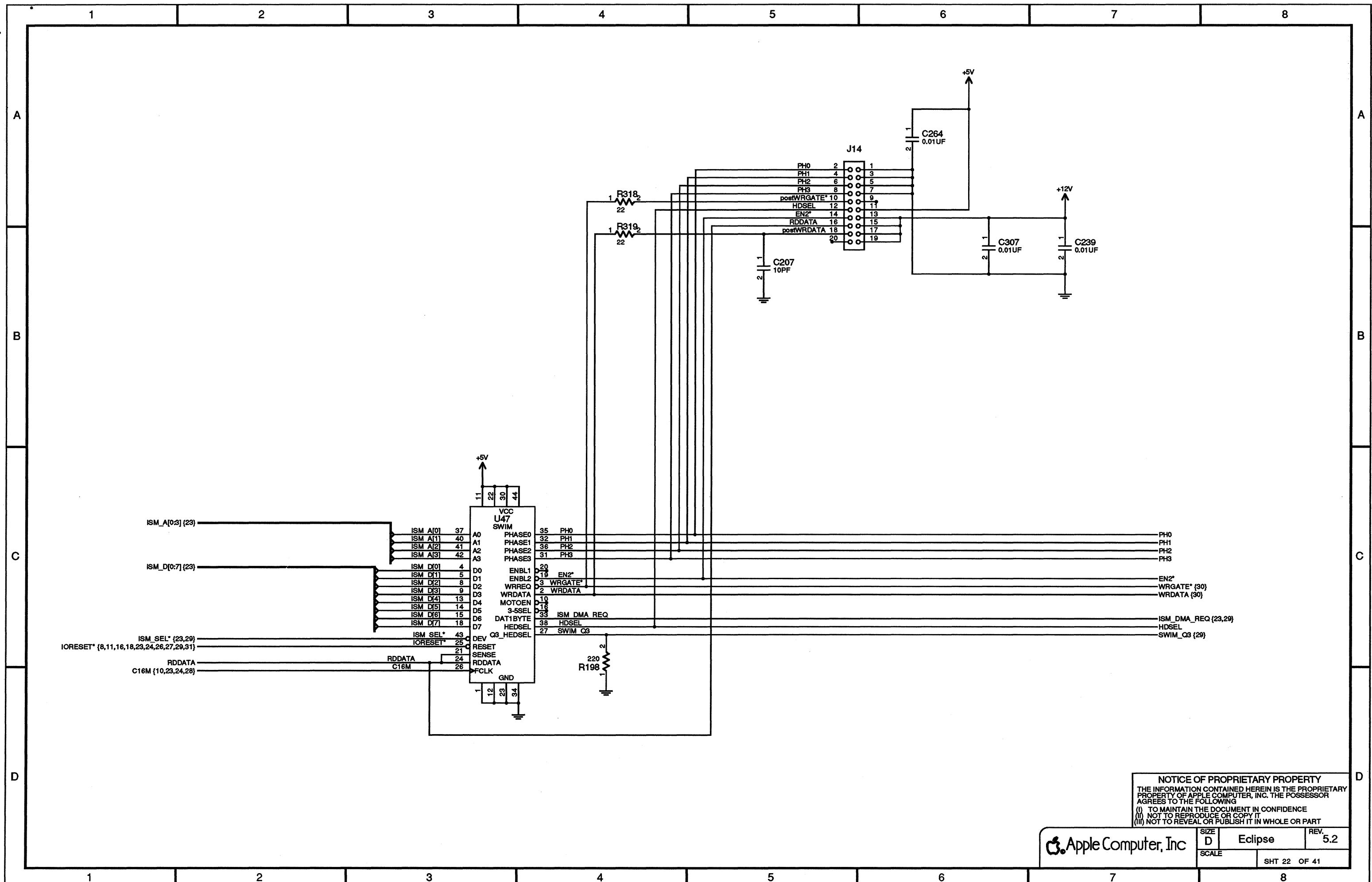




1 2 3 4 5 6 7 8

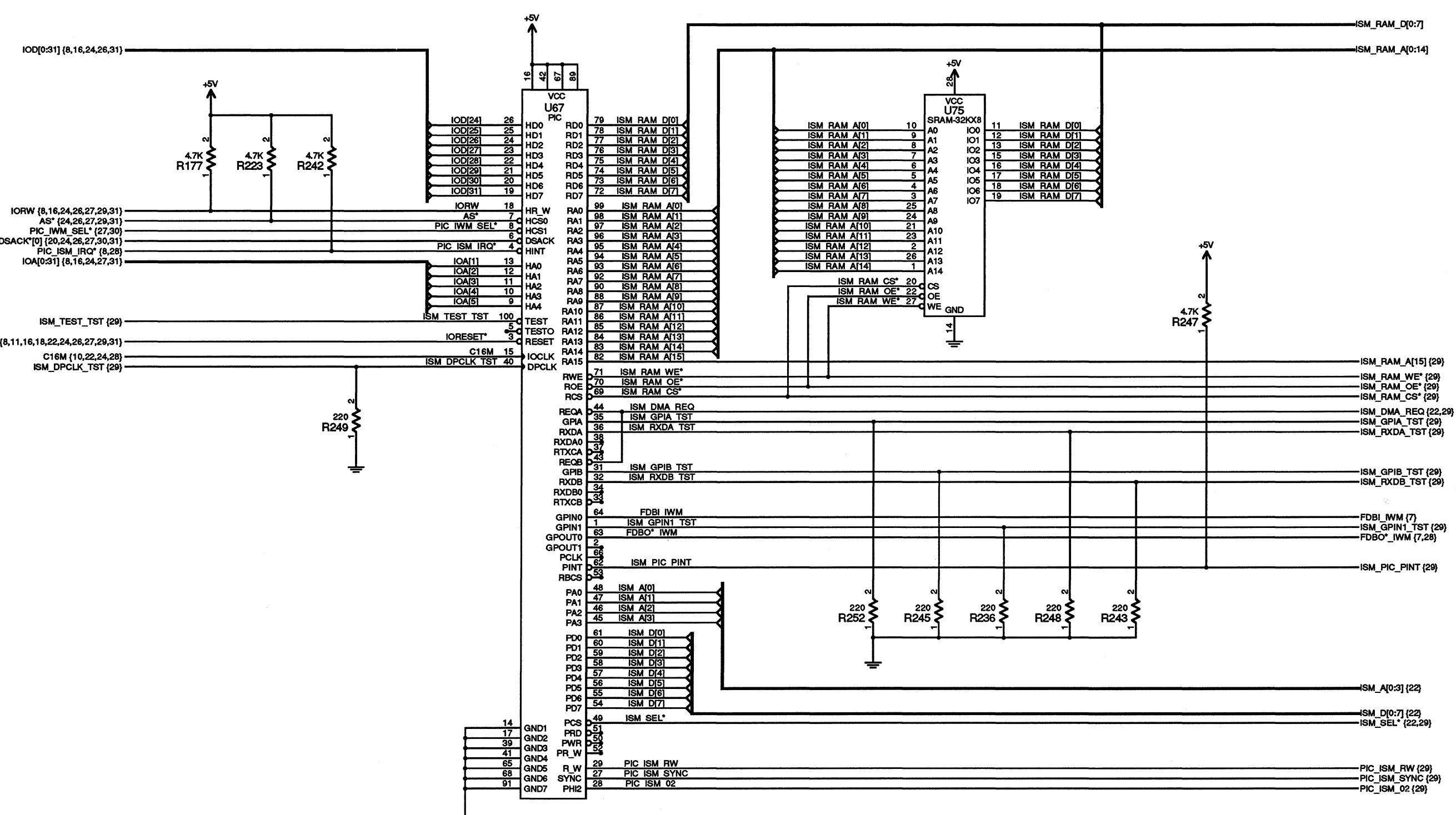






1 2 3 4 5 6 7 8

A

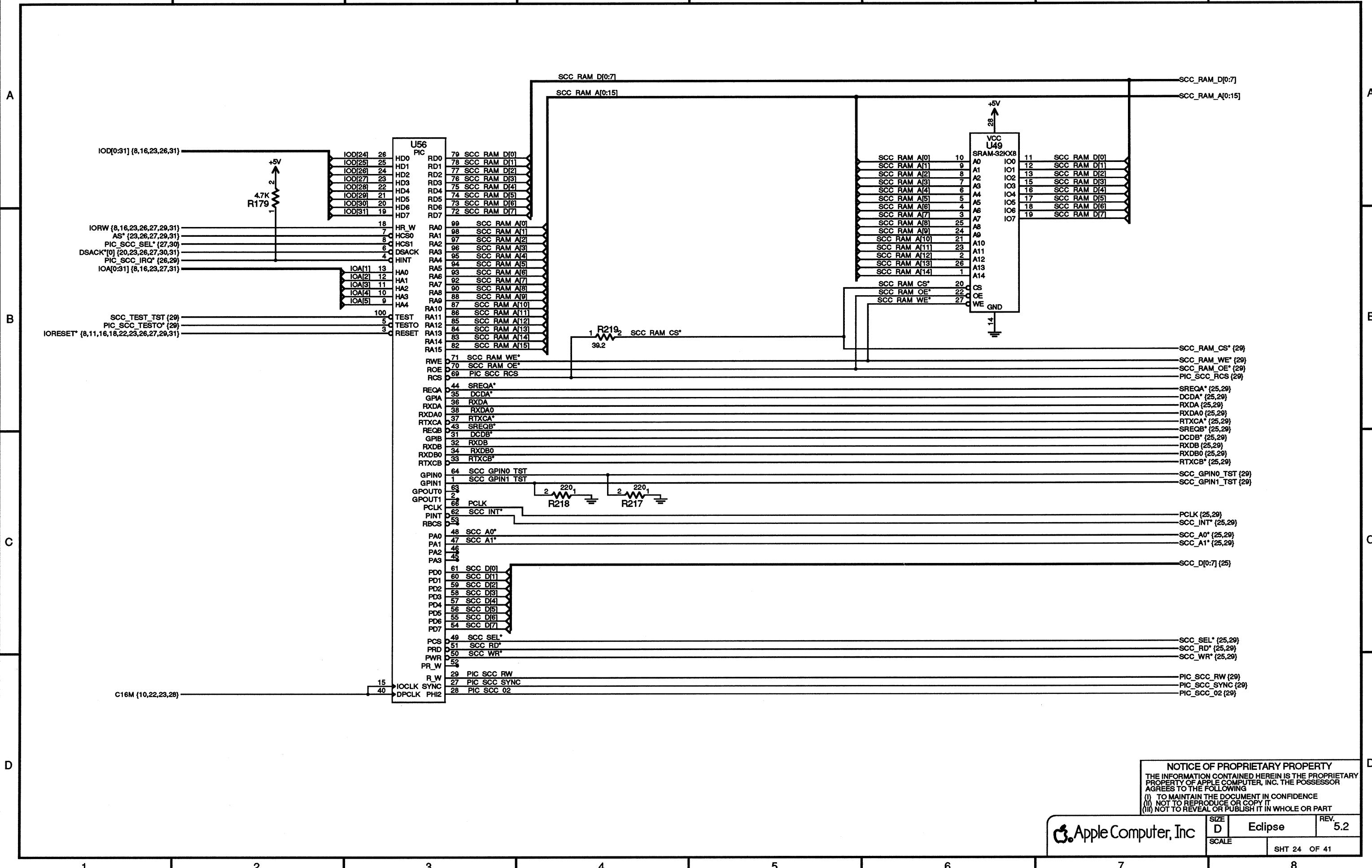


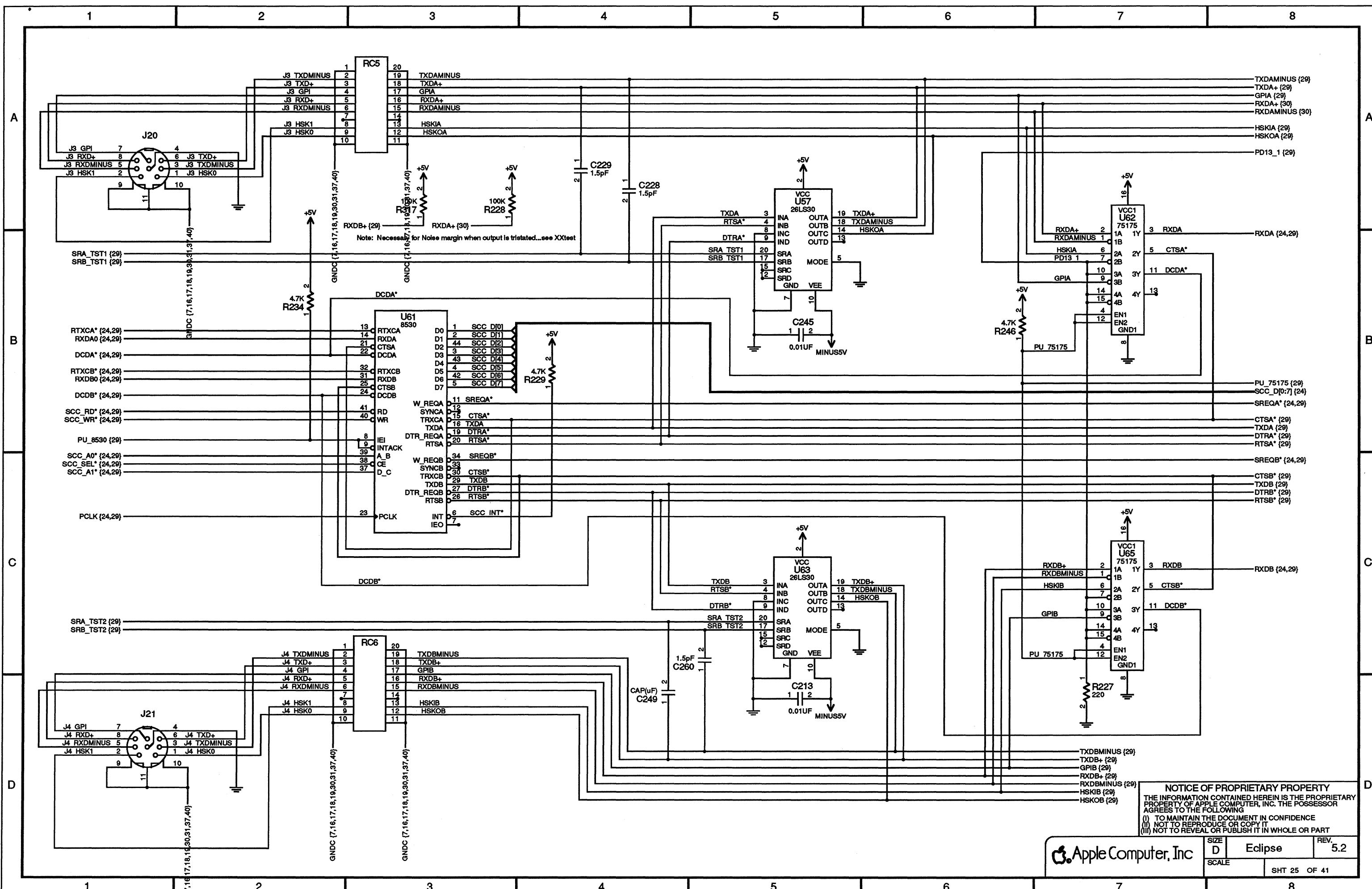
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Apple Computer, Inc.

SIZE	Eclipse	REV.
D		5.2
SCALE	SHT 23	OF 41

1 2 3 4 5 6 7 8





1 2 3 4 5 6 7 8

A

A

B

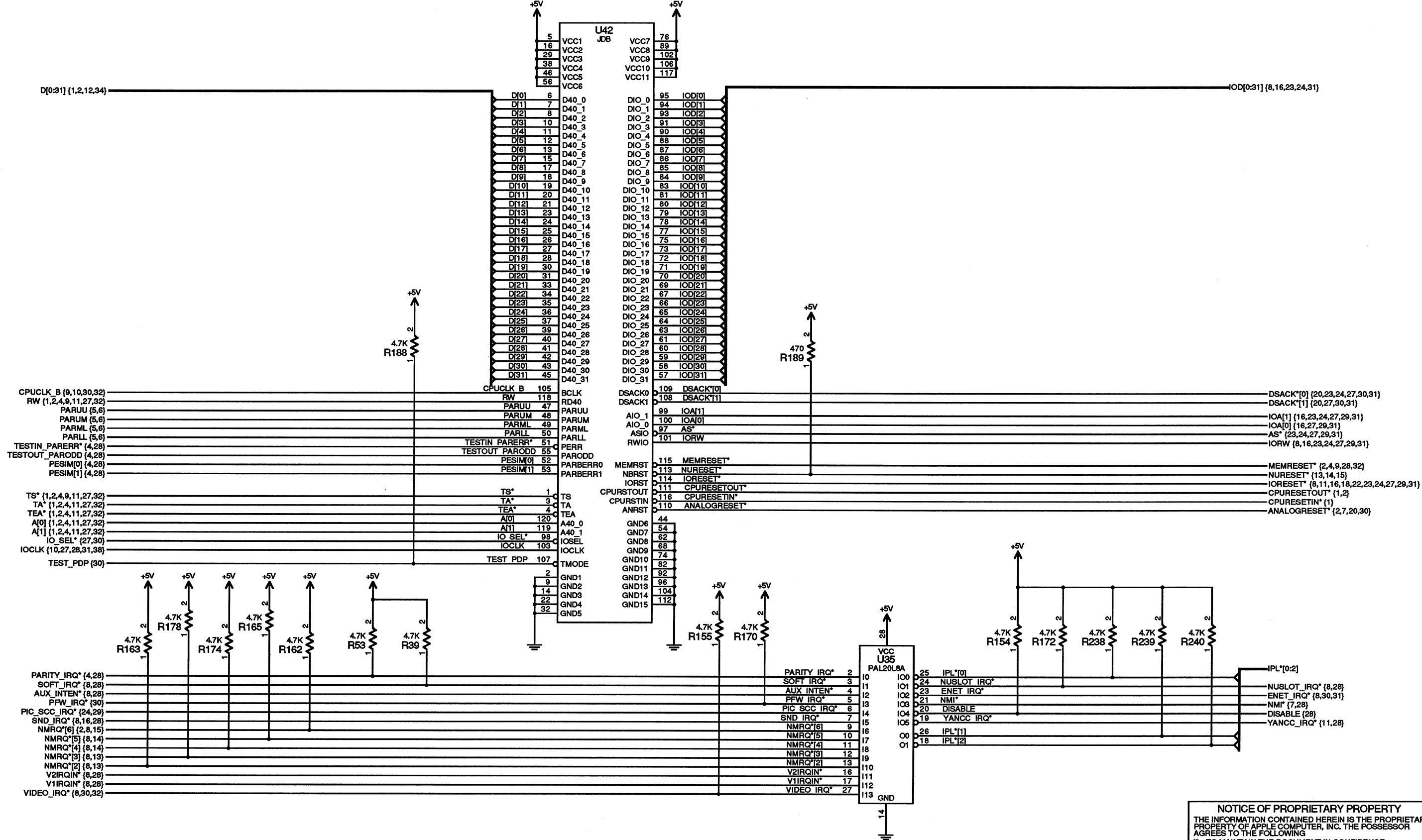
B

C

C

D

D

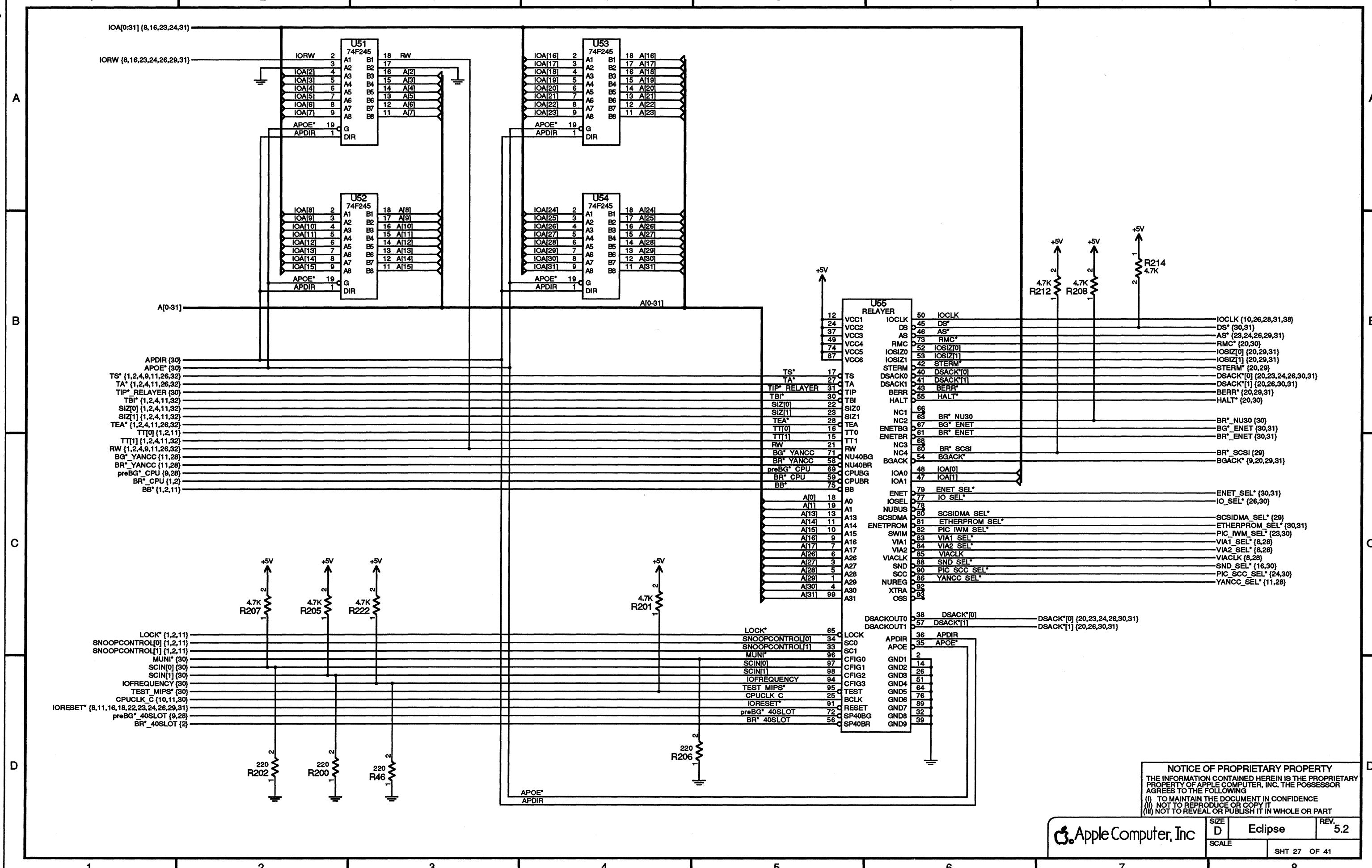


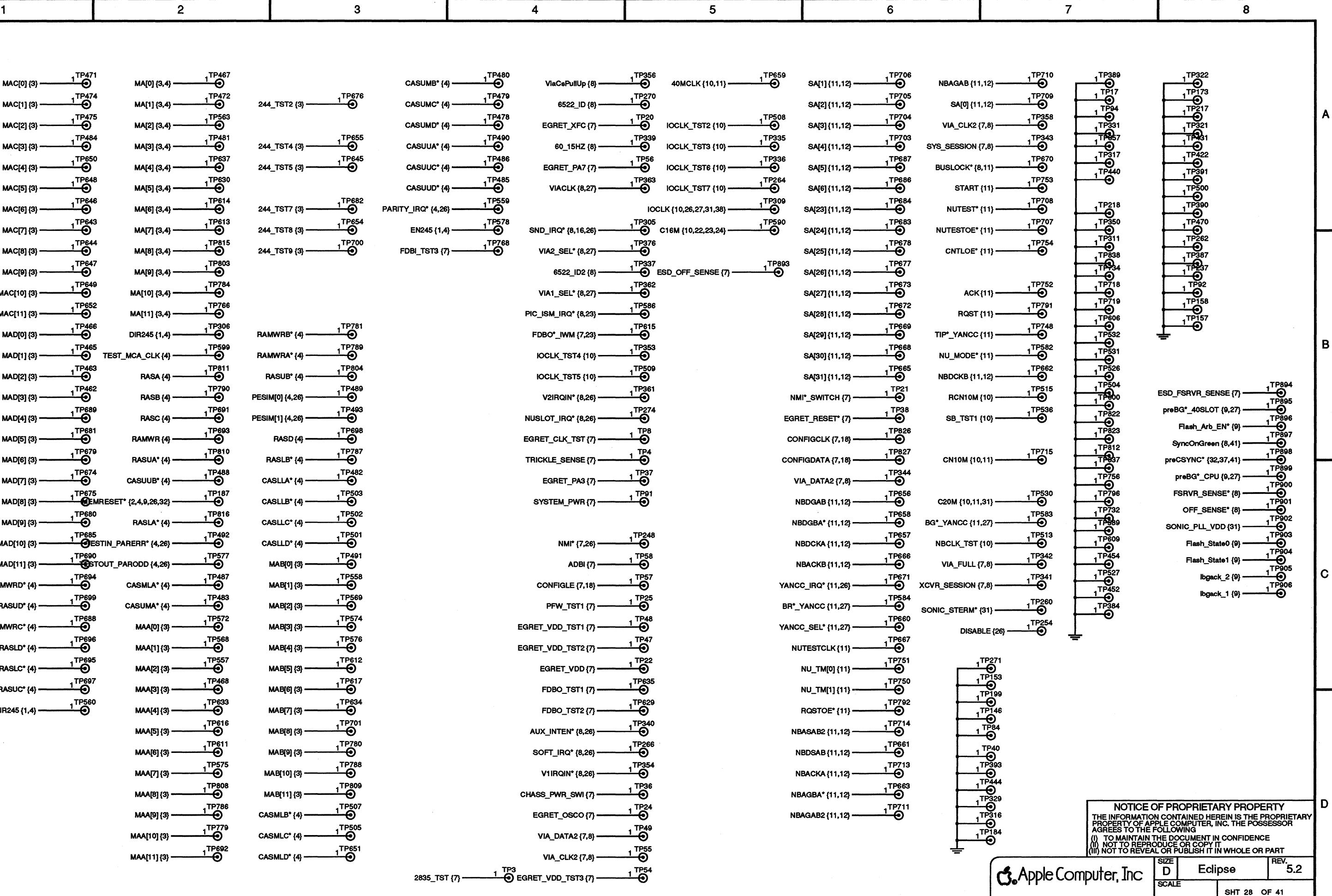
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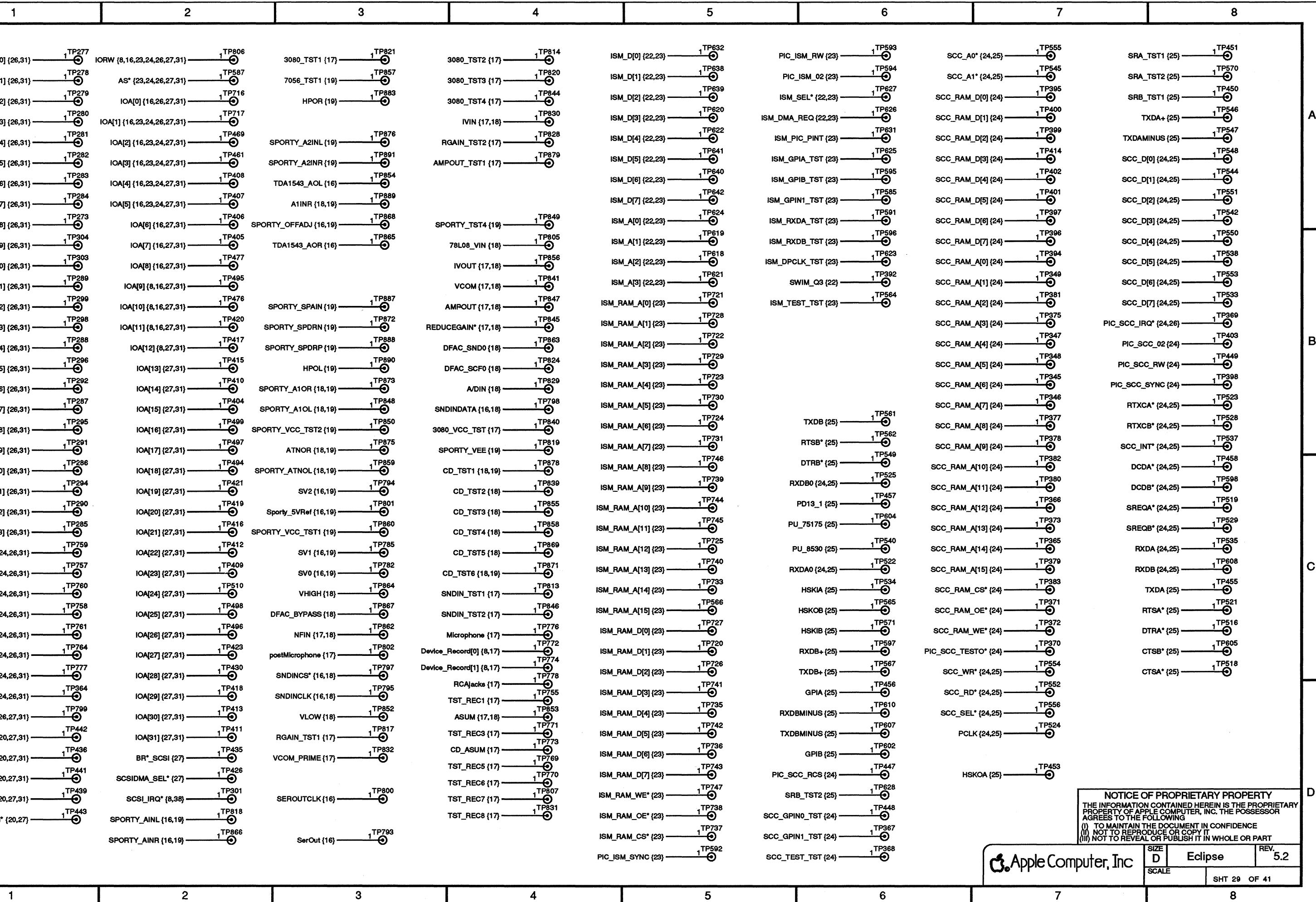
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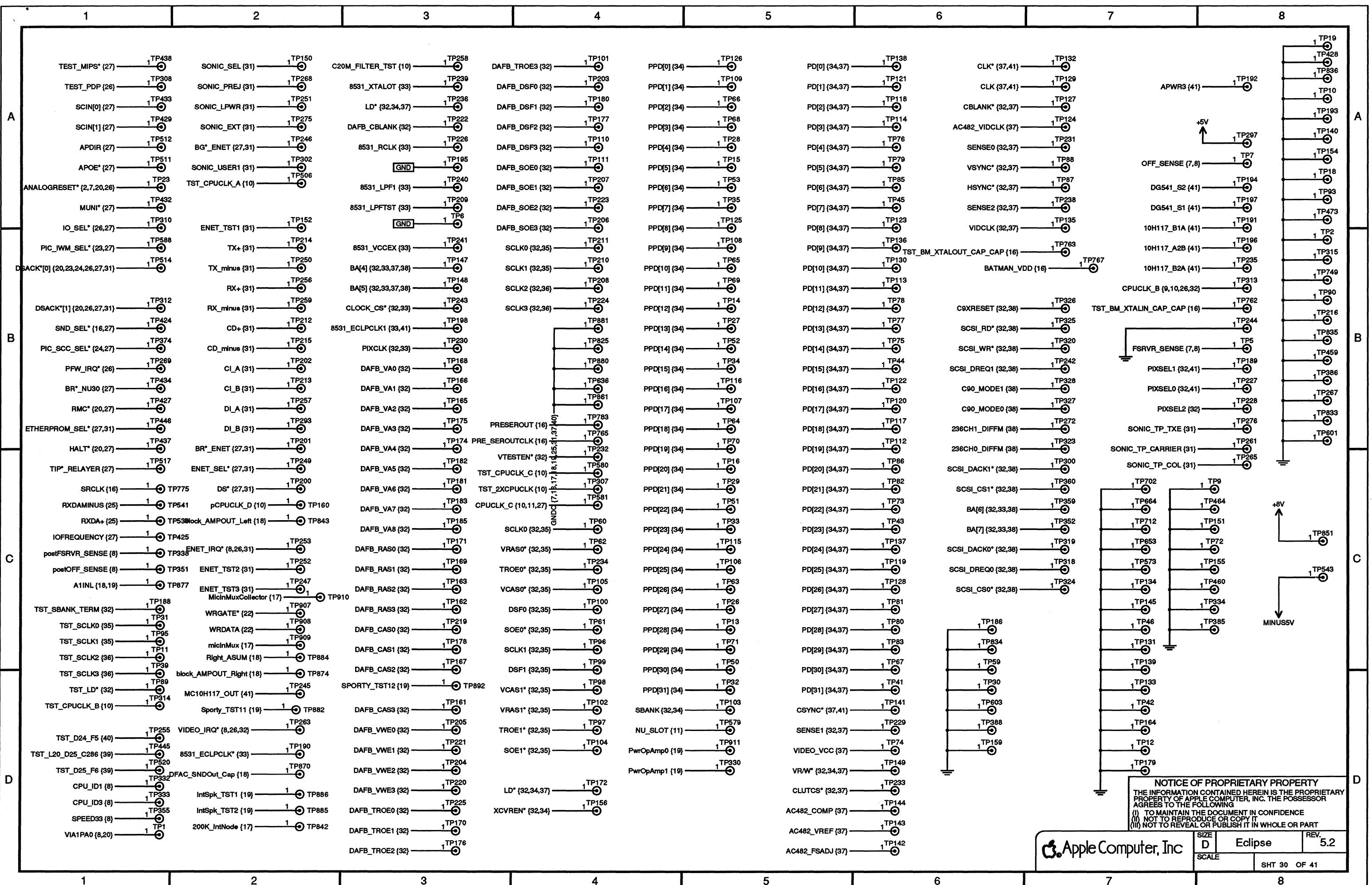
SIZE	Eclipse	REV.
SCALE	SHT 26 OF 41	5.2

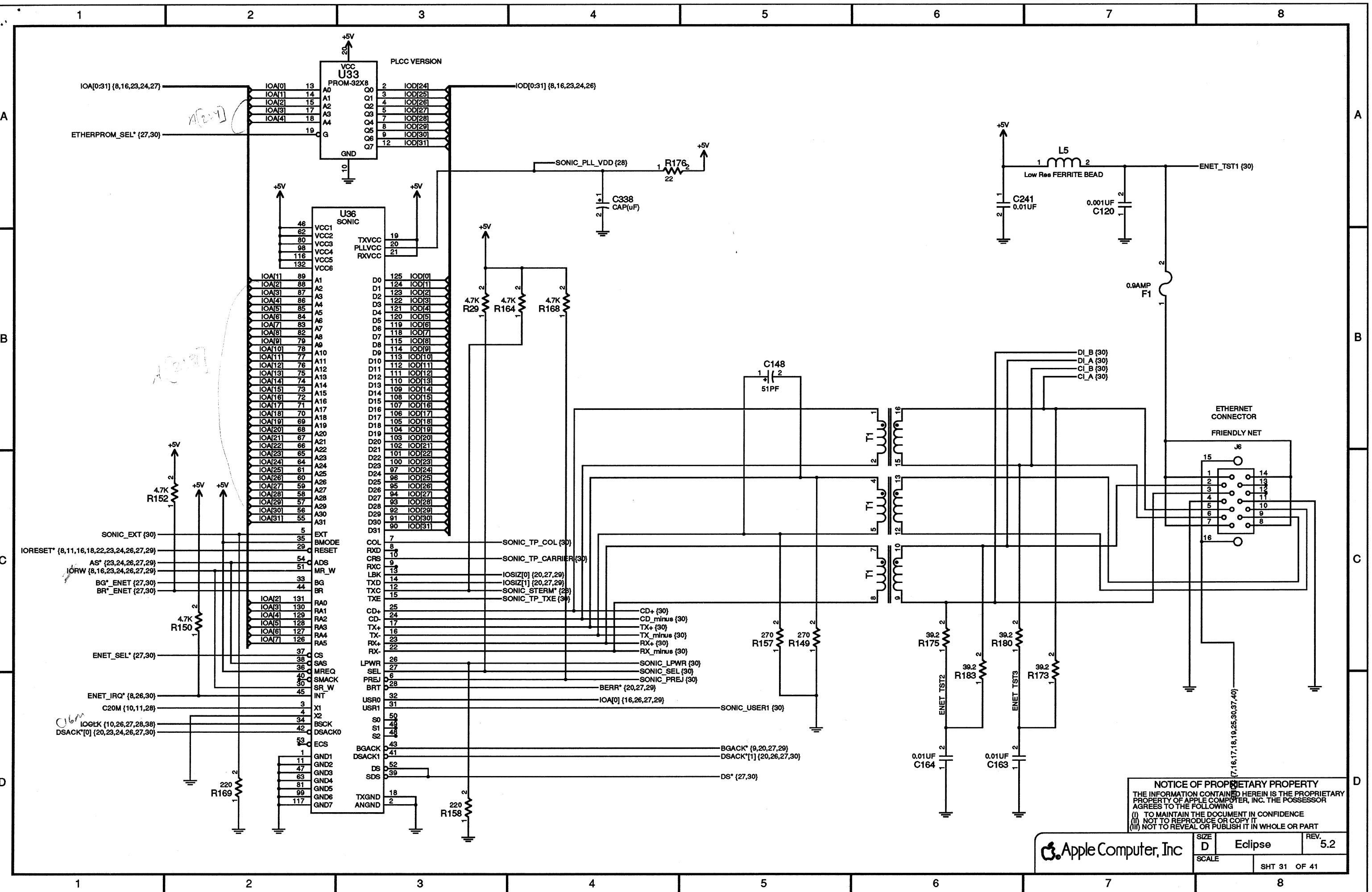
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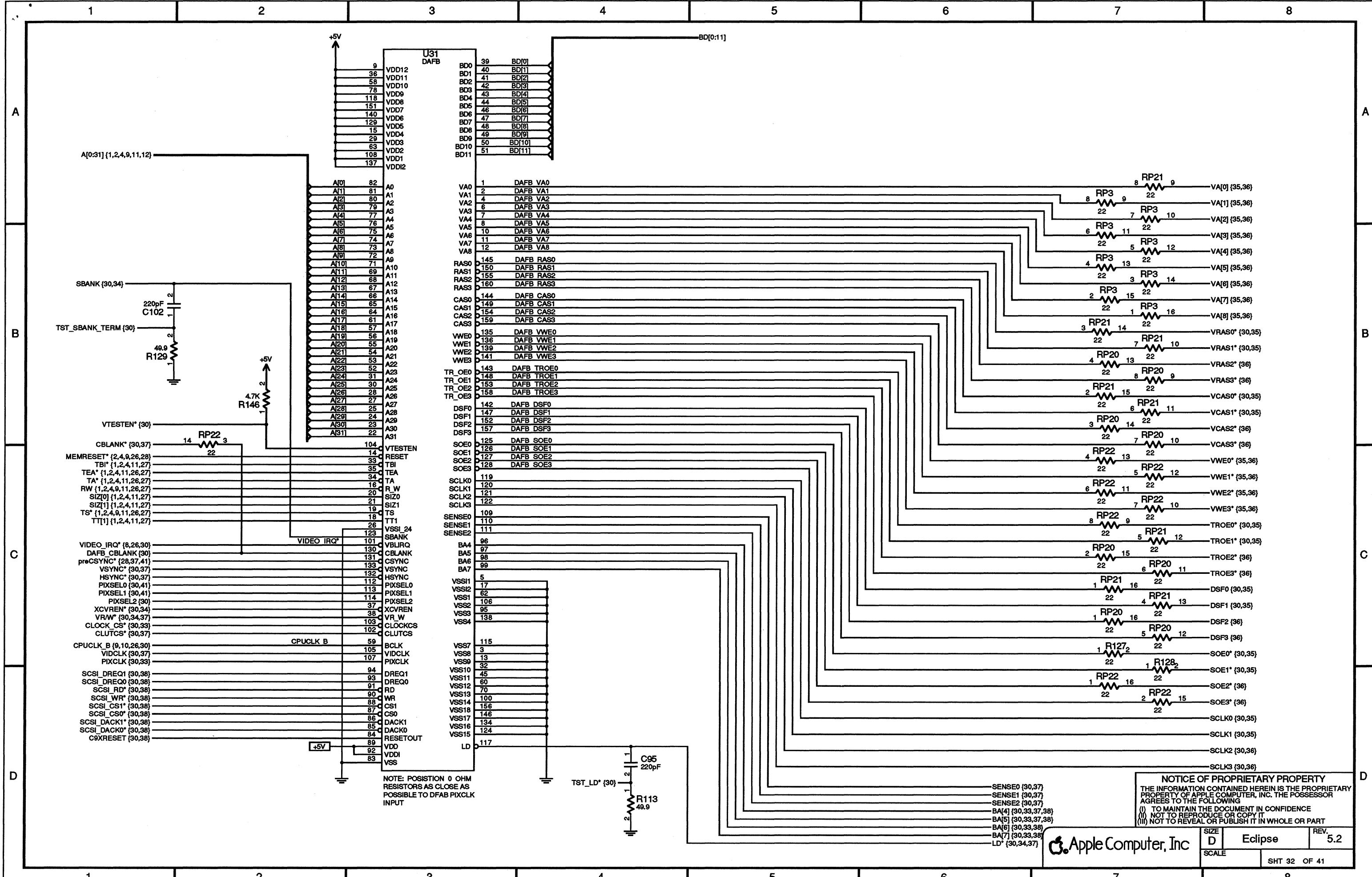


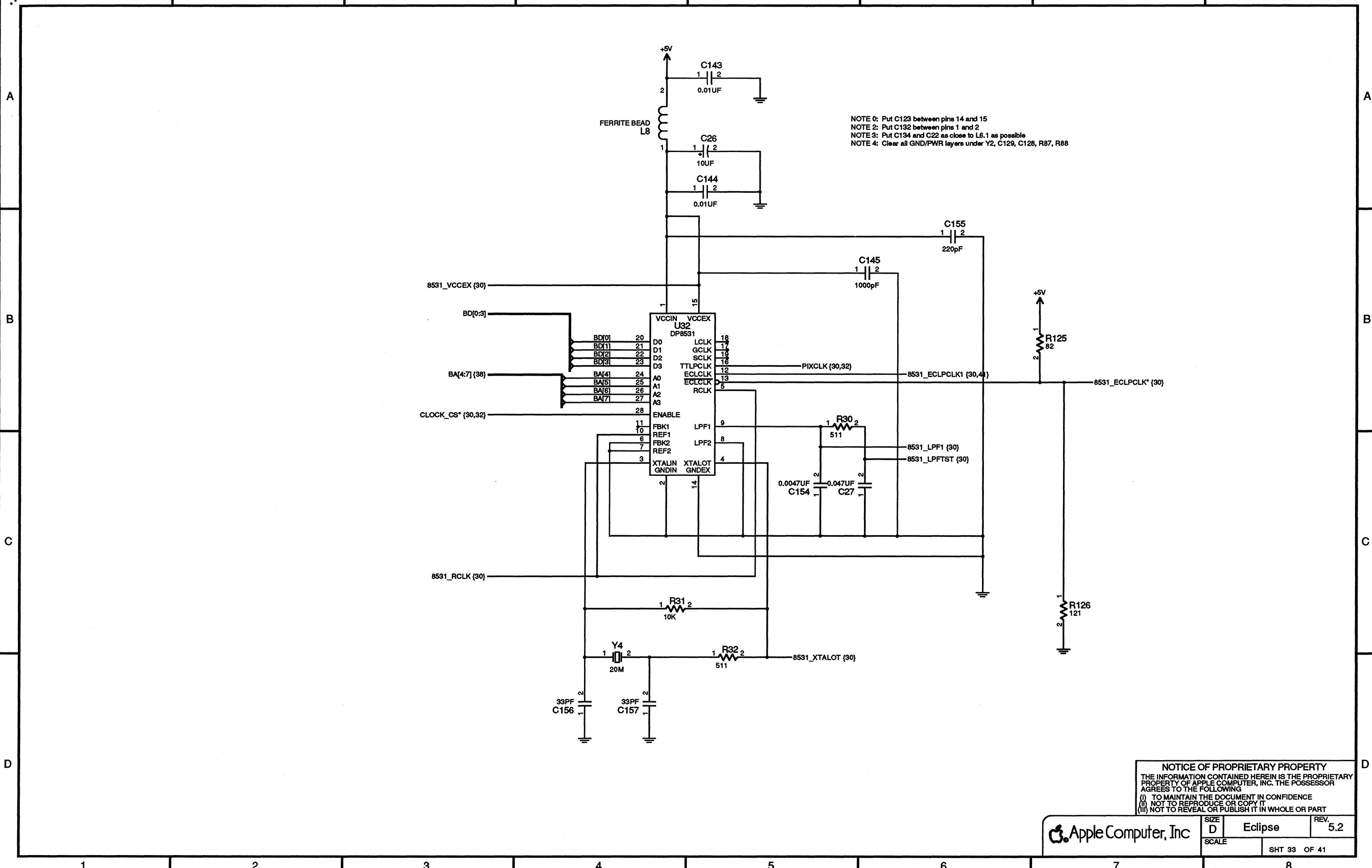




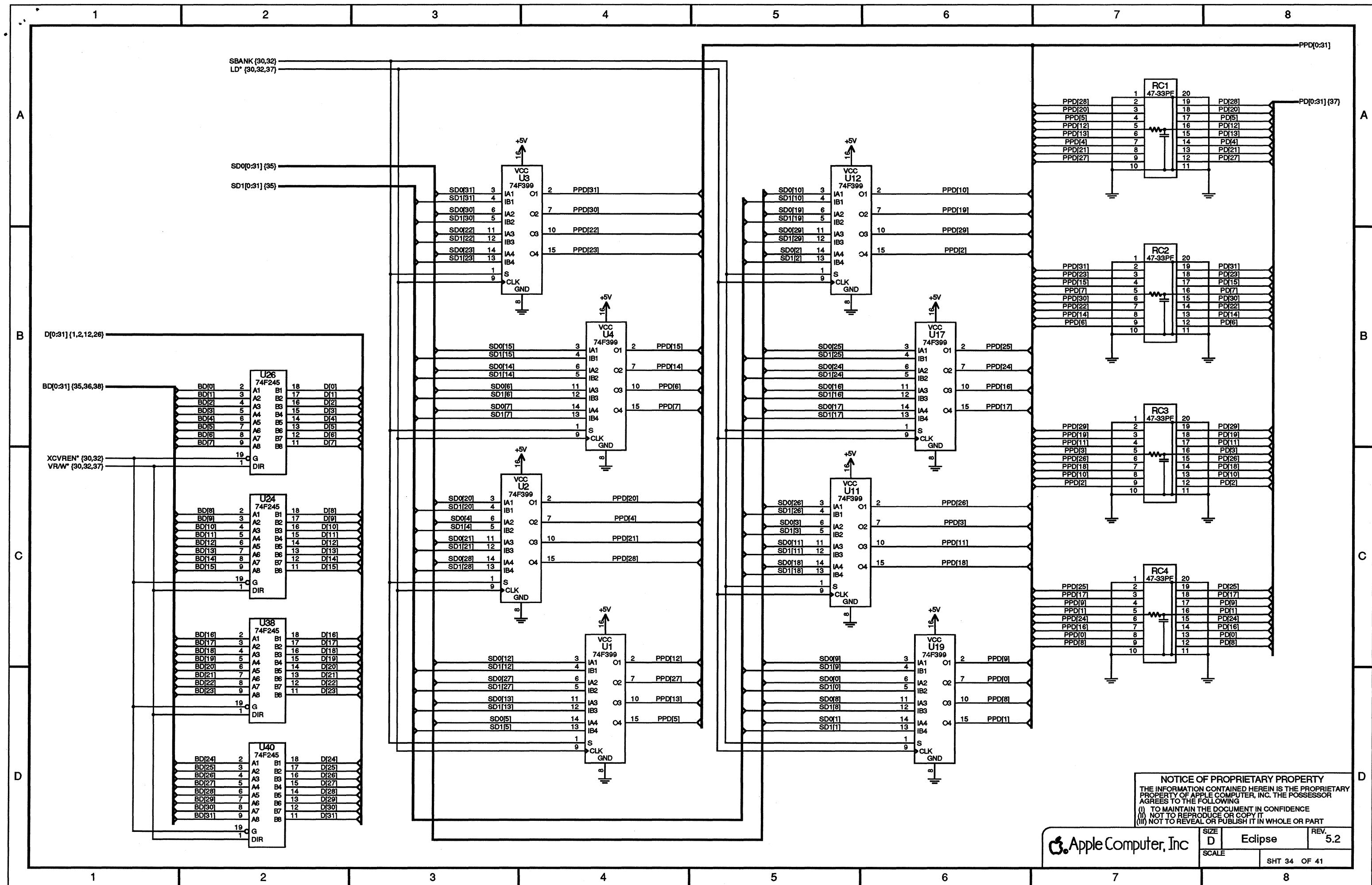


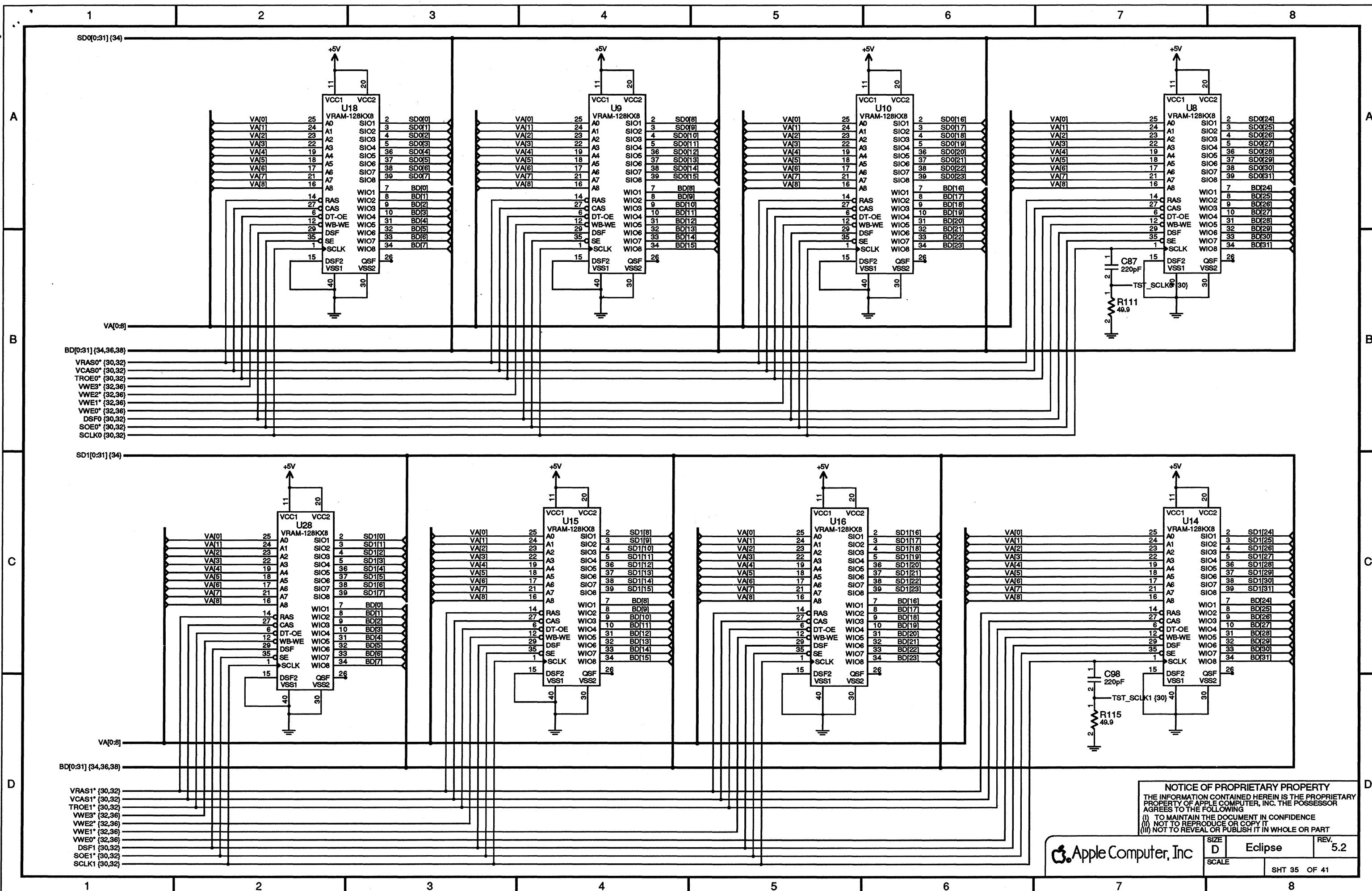
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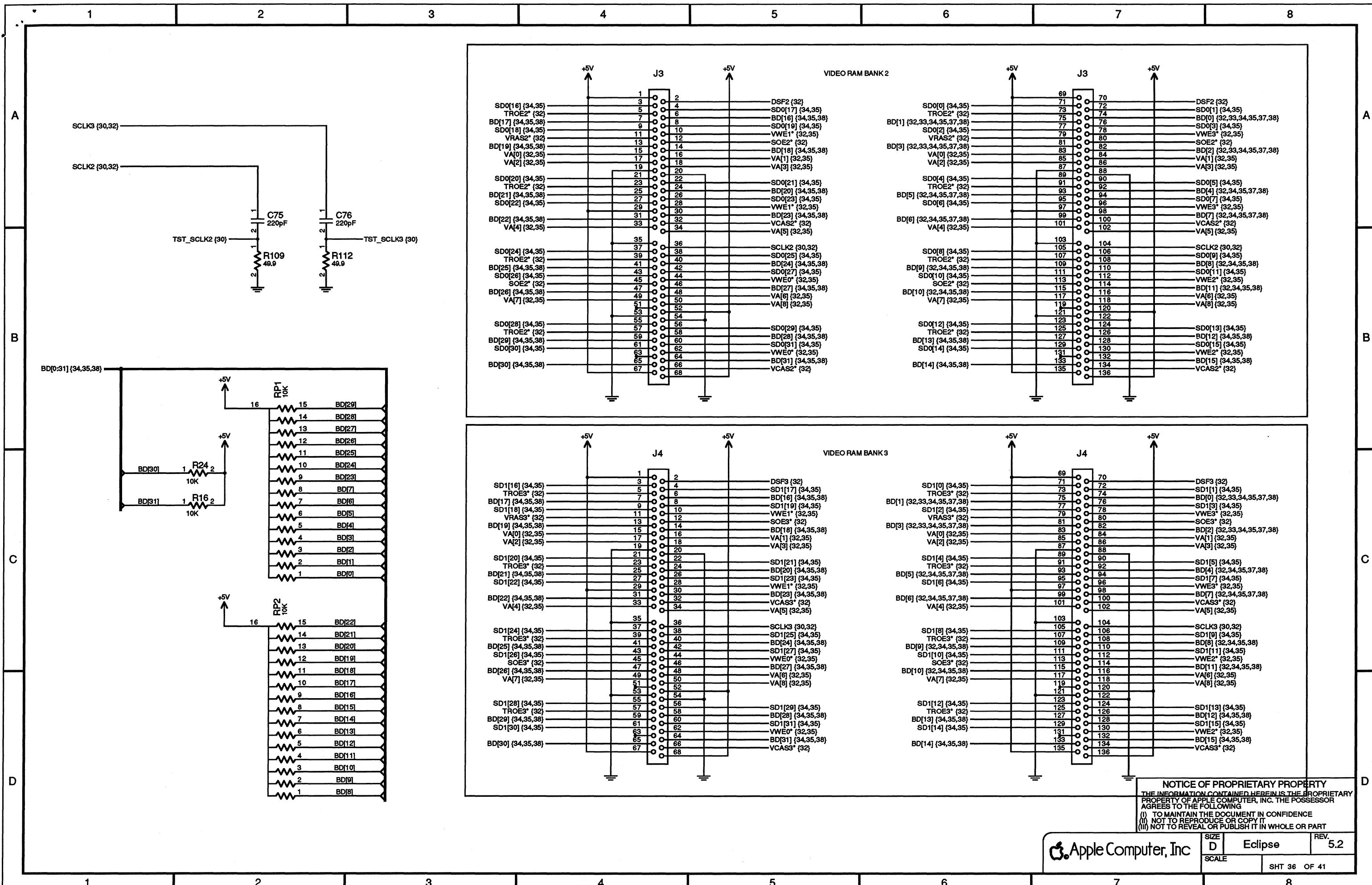




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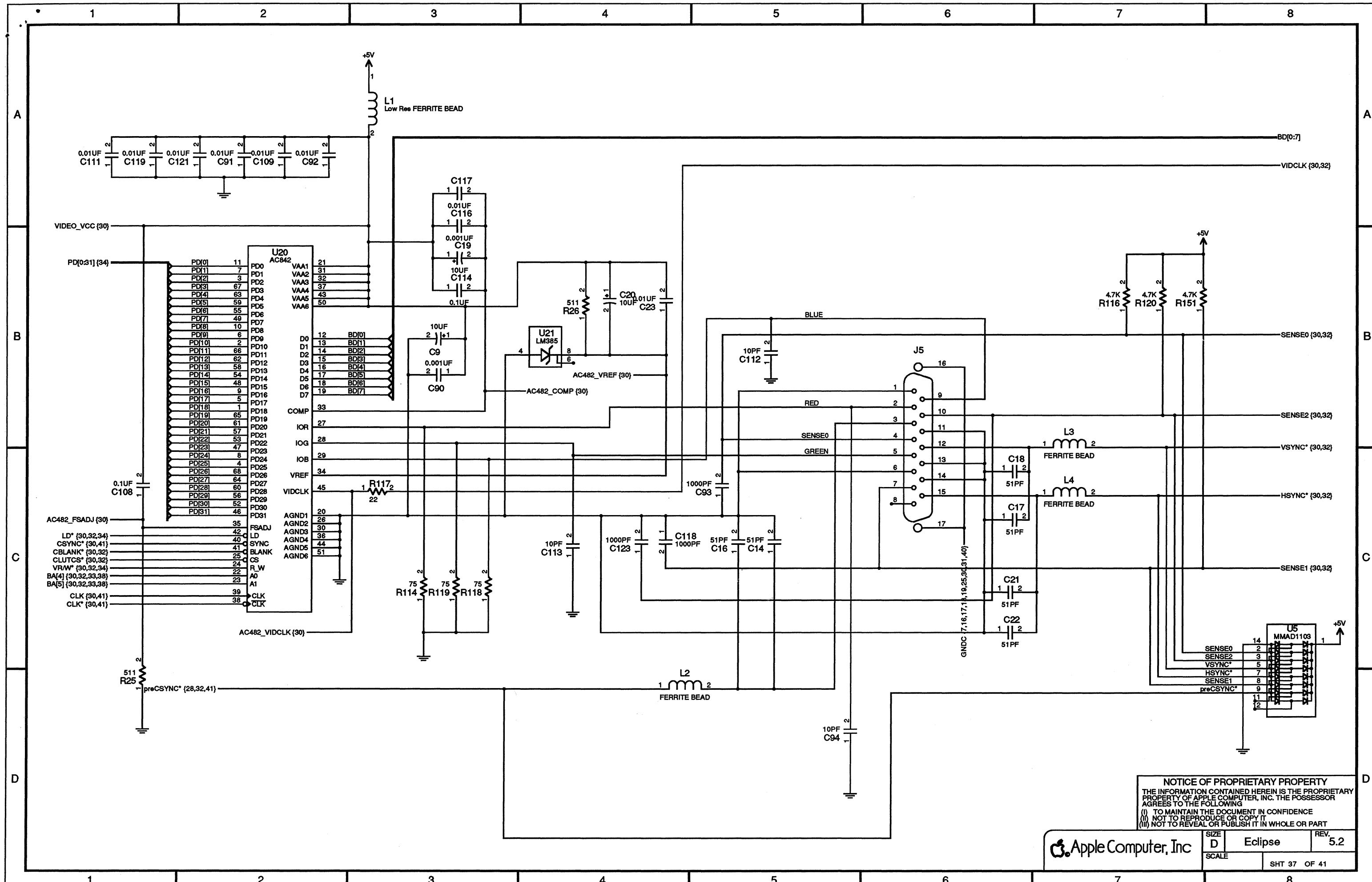
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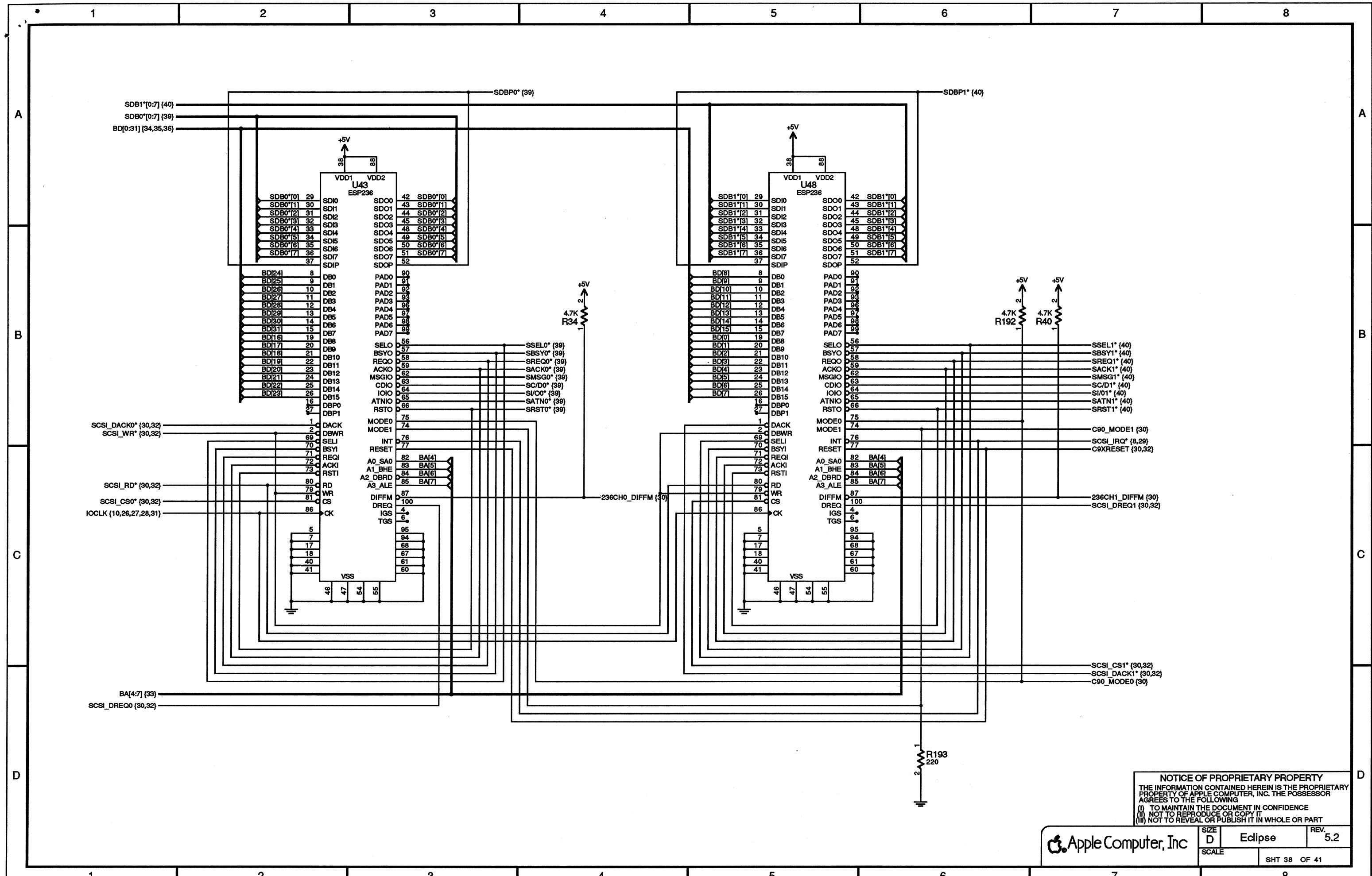
SIZE D Eclipse REV. 5.2

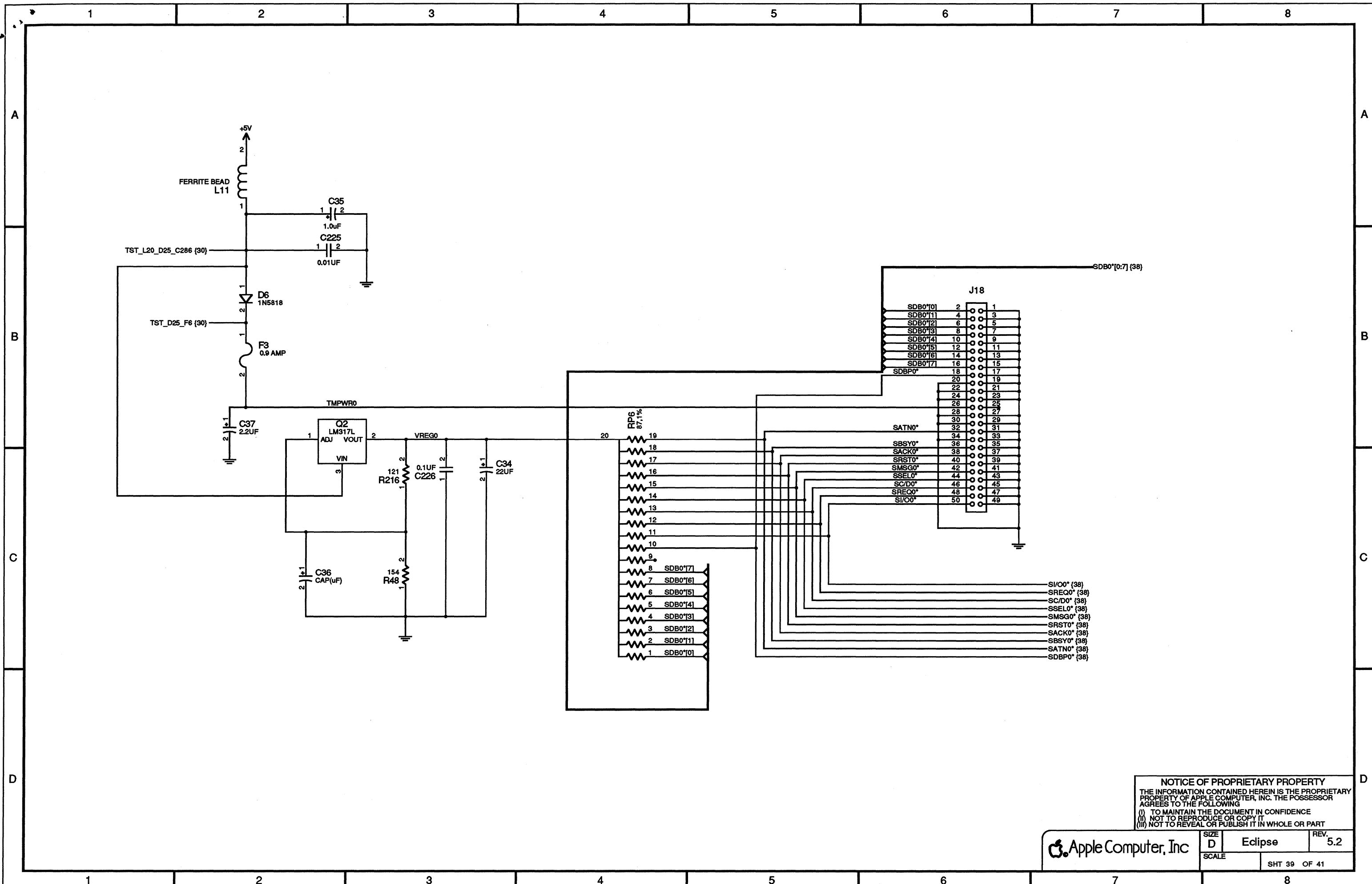


SIZE	Eclipse	REV.
D		5.2

SCALE SHT 36 OF 41







A

A

B

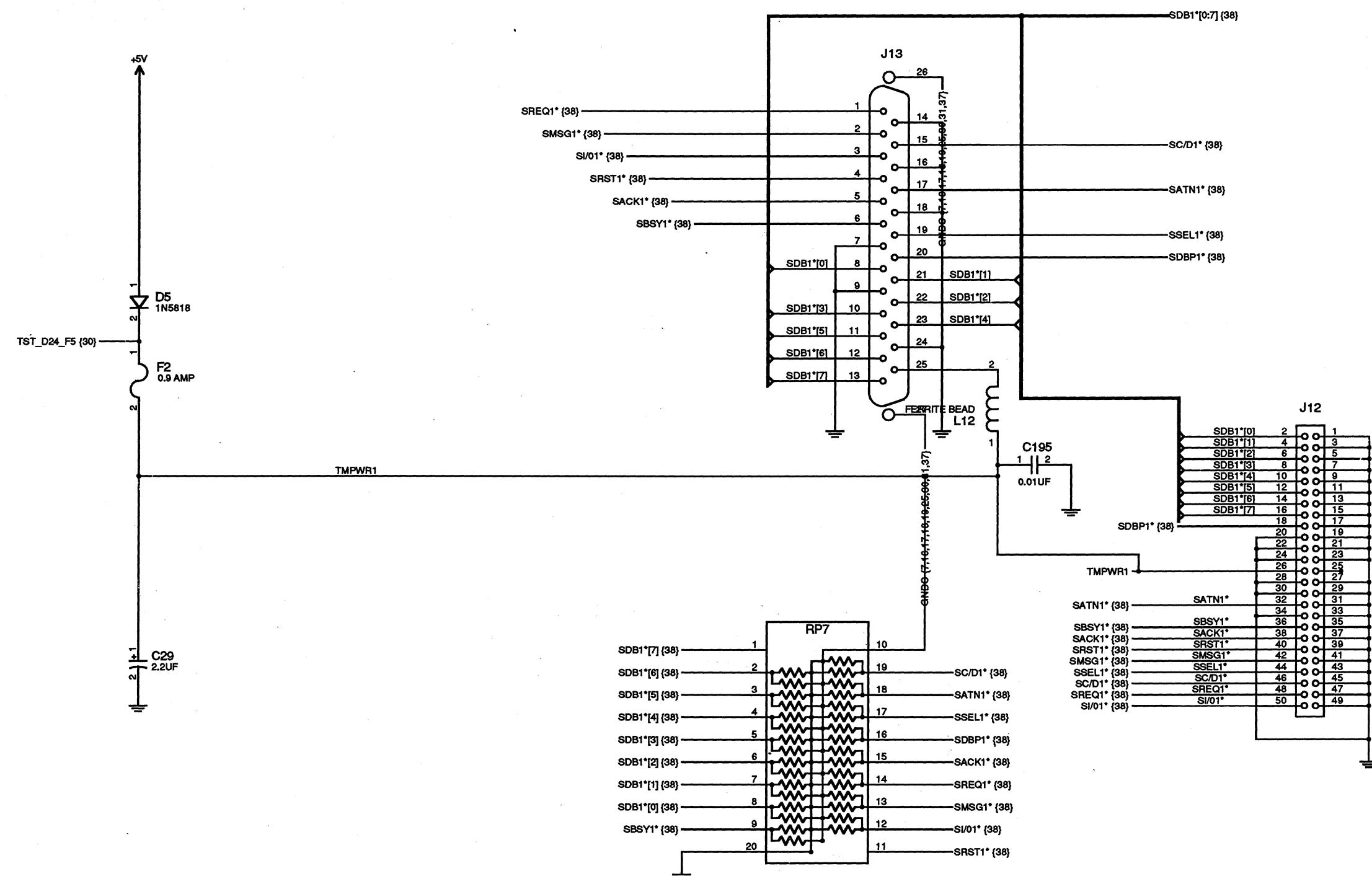
B

C

C

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SIZE D	Eclipse	REV 5.2
SCALE	SHT 40 OF 41	