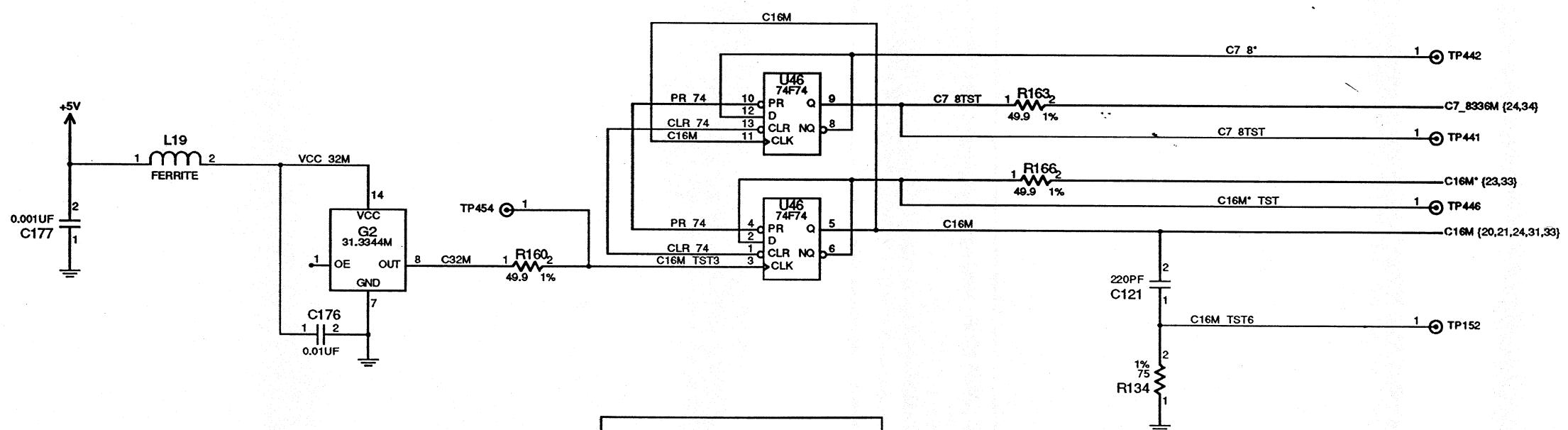
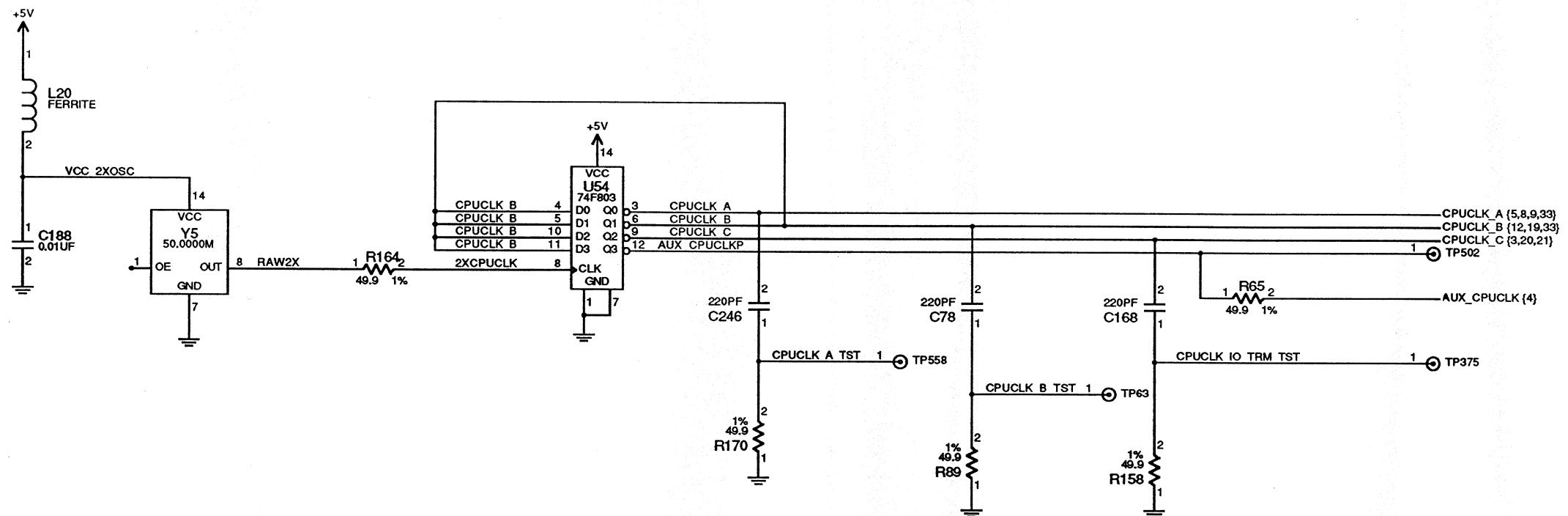


8 7 6 5 4 3 2 1



### CPUCLK and IOCLK Oscillators

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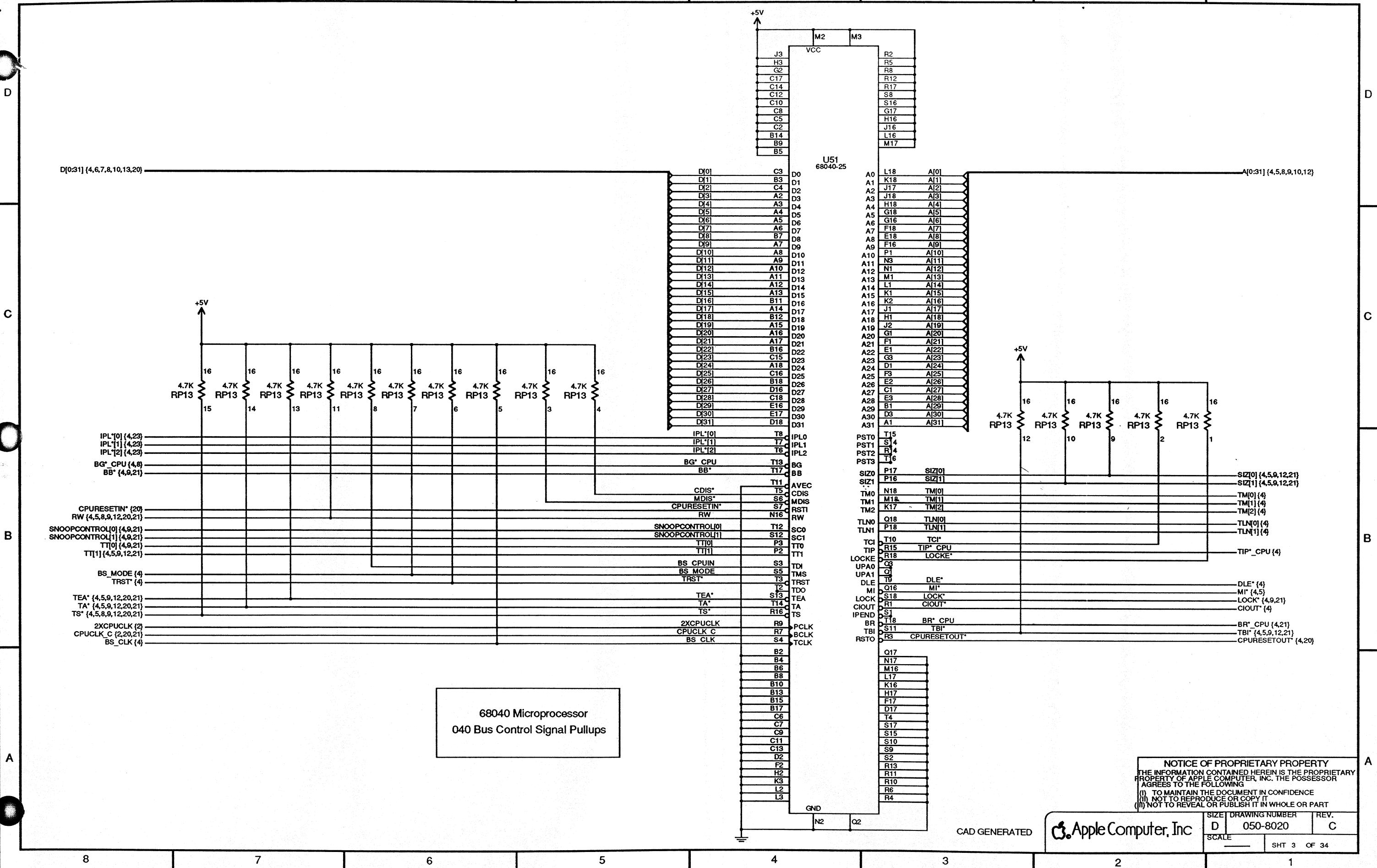
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SCALE		SHT 2 OF 34

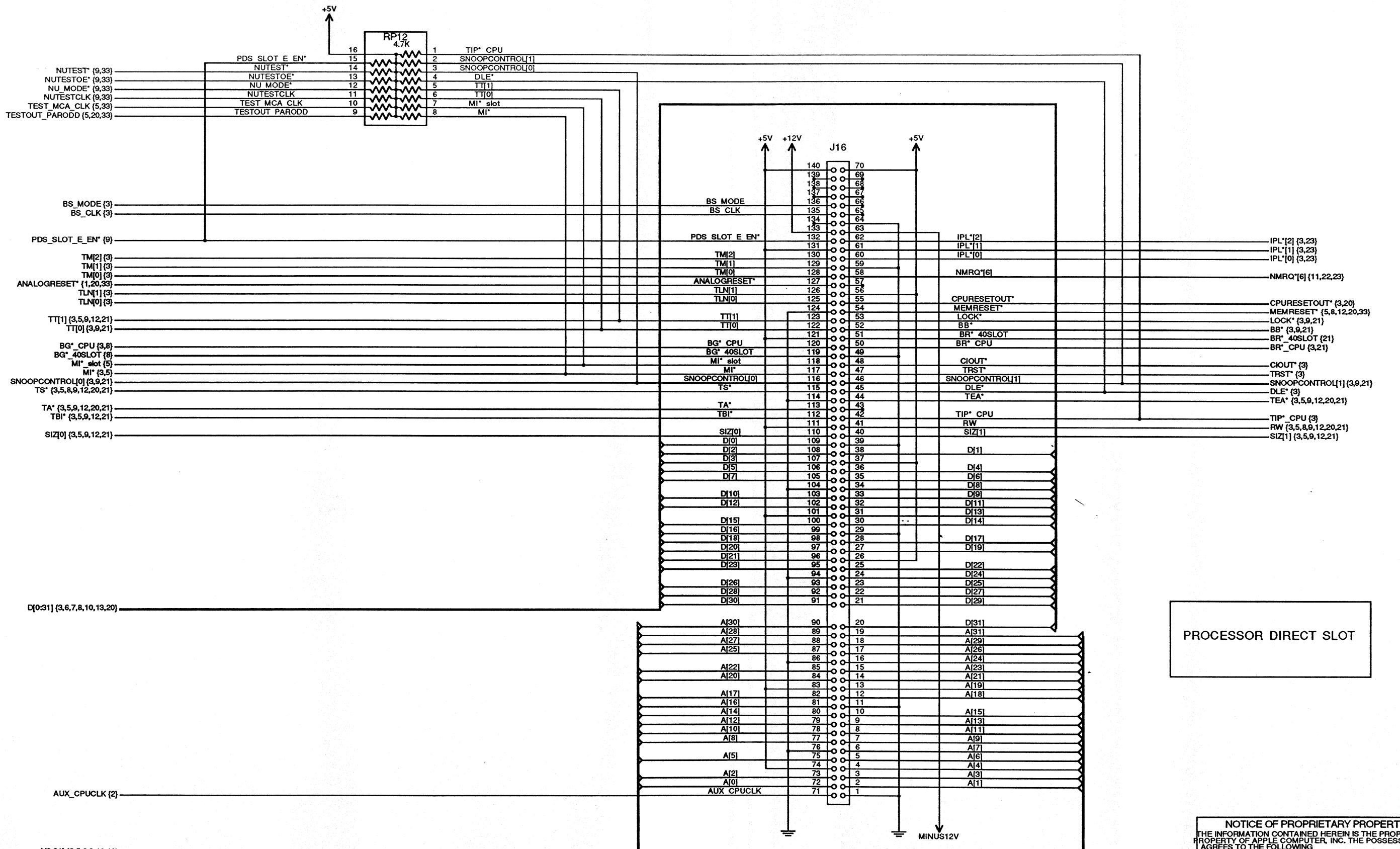
8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1



8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1



PROCESSOR DIRECT SLOT

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SIZE	DRAWING NUMBER	REV.
D	050-8020	C
SCALE		SHT 4 OF 34

8 7 6 5 4 3 2 1

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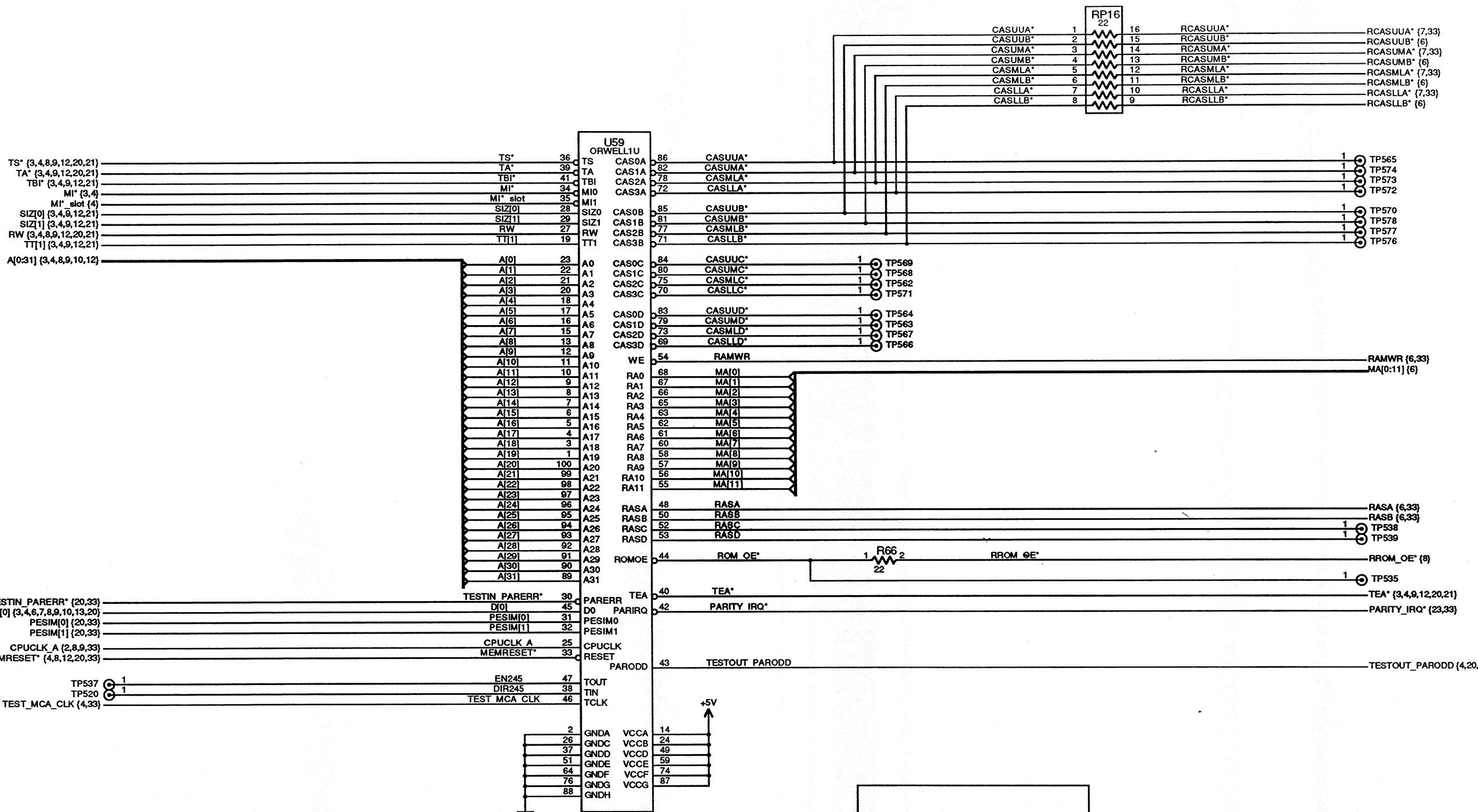
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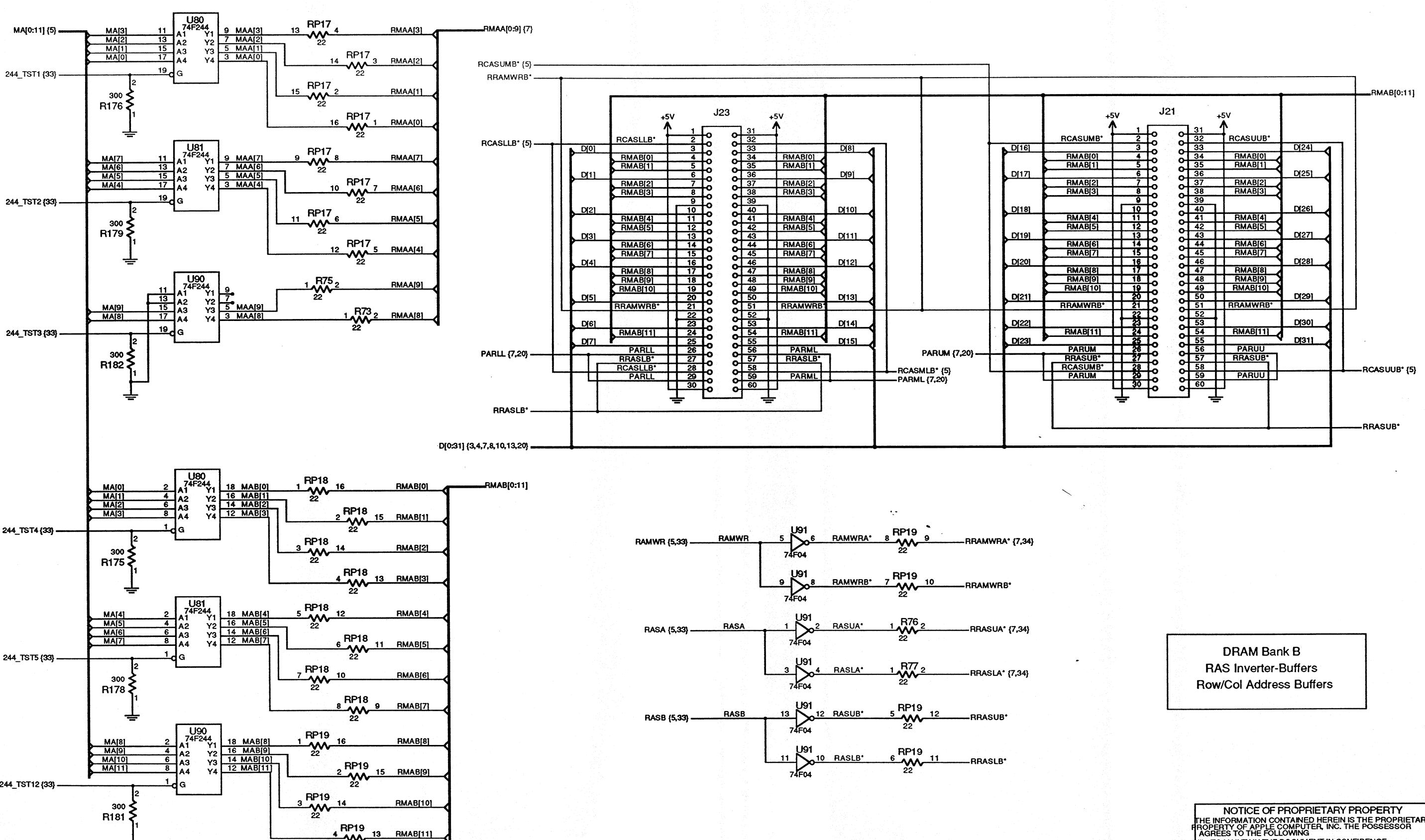
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SIZE	DRAWING NUMBER	REV.
D	050-8020	C
SCALE		SHT 5 OF 34

8 7 6 5 4 3 2 1



DRAM Bank B  
RAS Inverter-Buffers  
Row/Col Address Buffers

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SIZE	DRAWING NUMBER	REV.
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	SHT 6	OF 34

CAD GENERATED

Apple Computer, Inc

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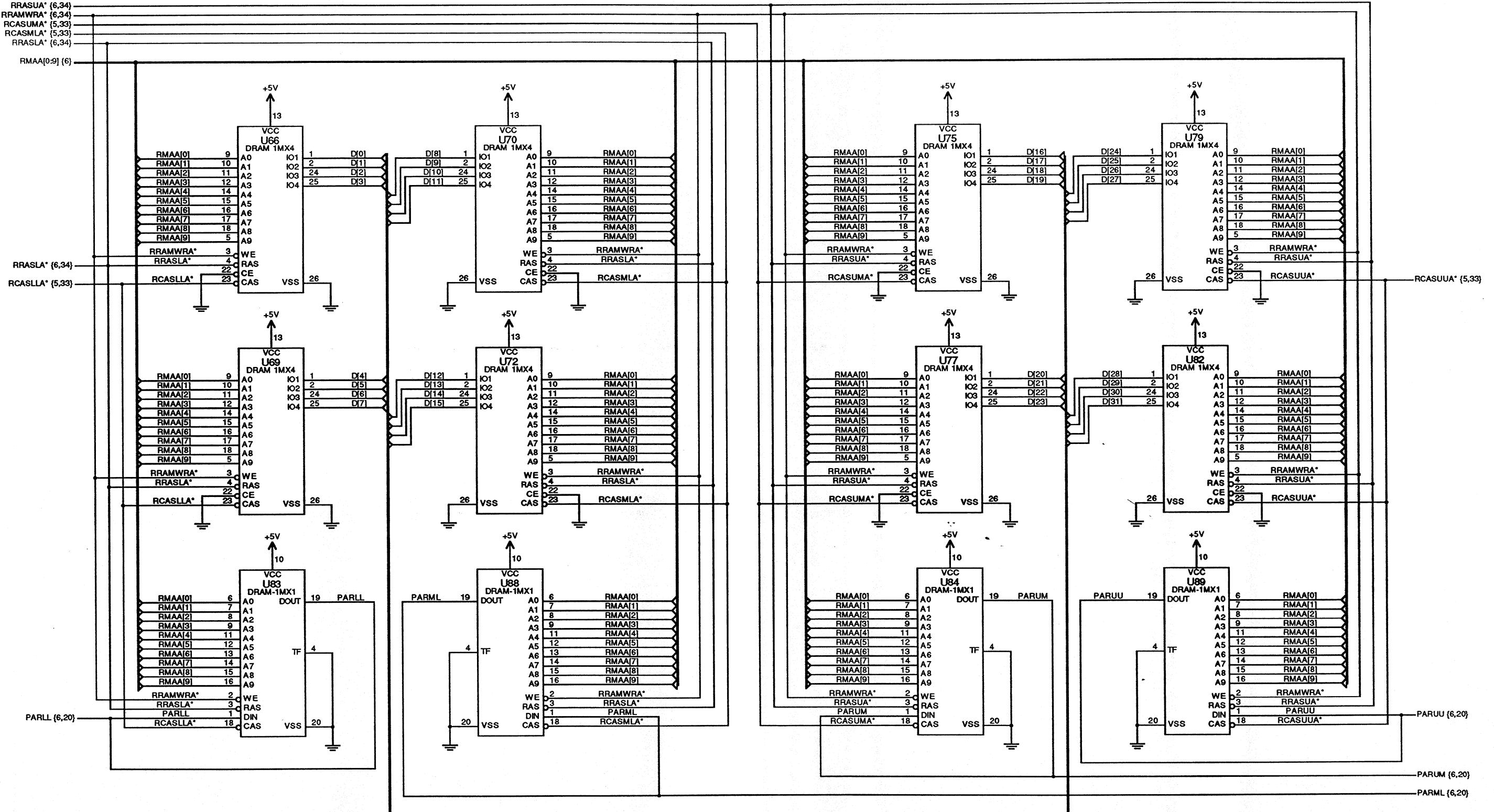
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DRAM Bank A

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SIZE	DRAWING NUMBER	REV.
D	050-8020	C
SCALE	SHT 7	OF 34

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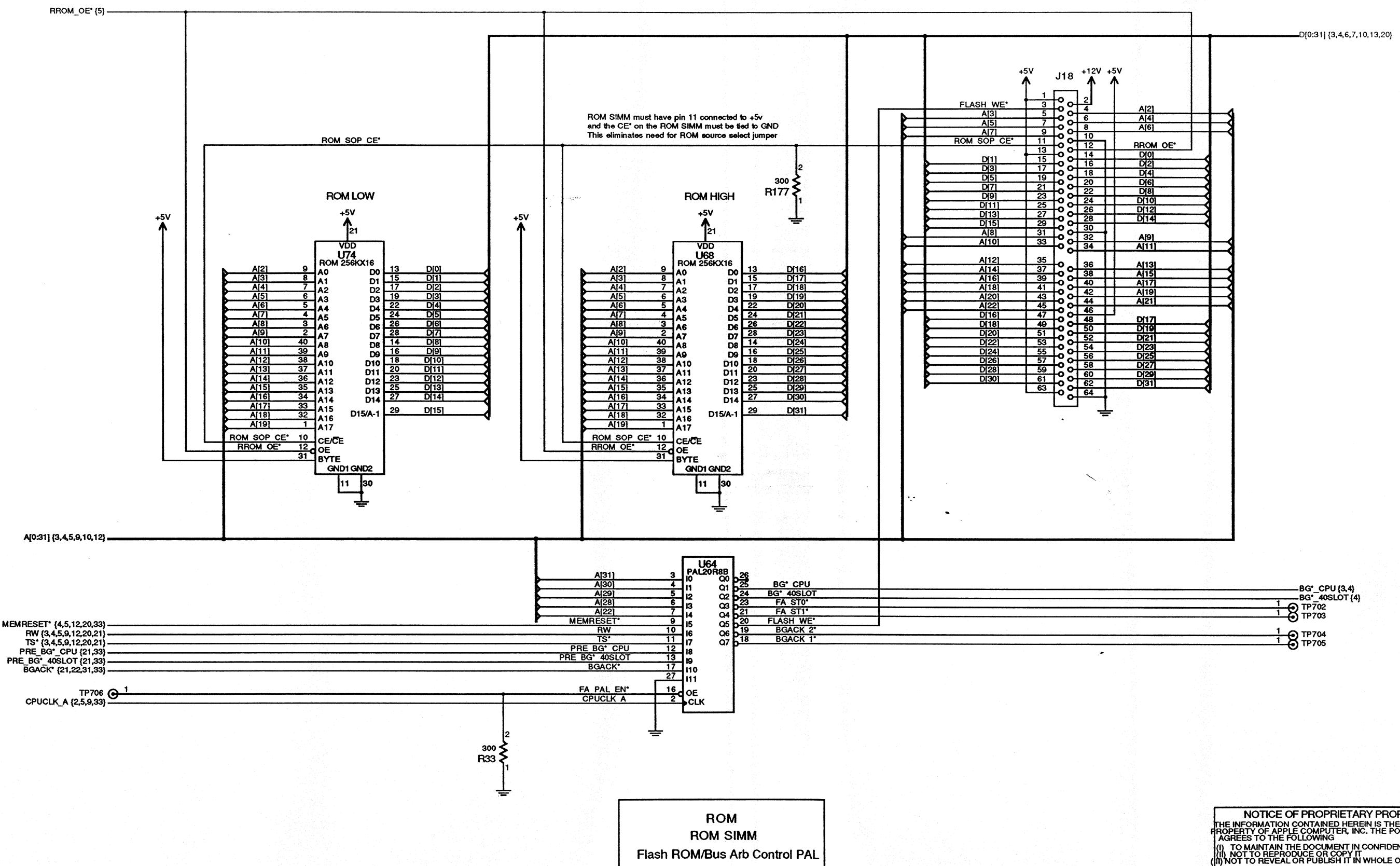
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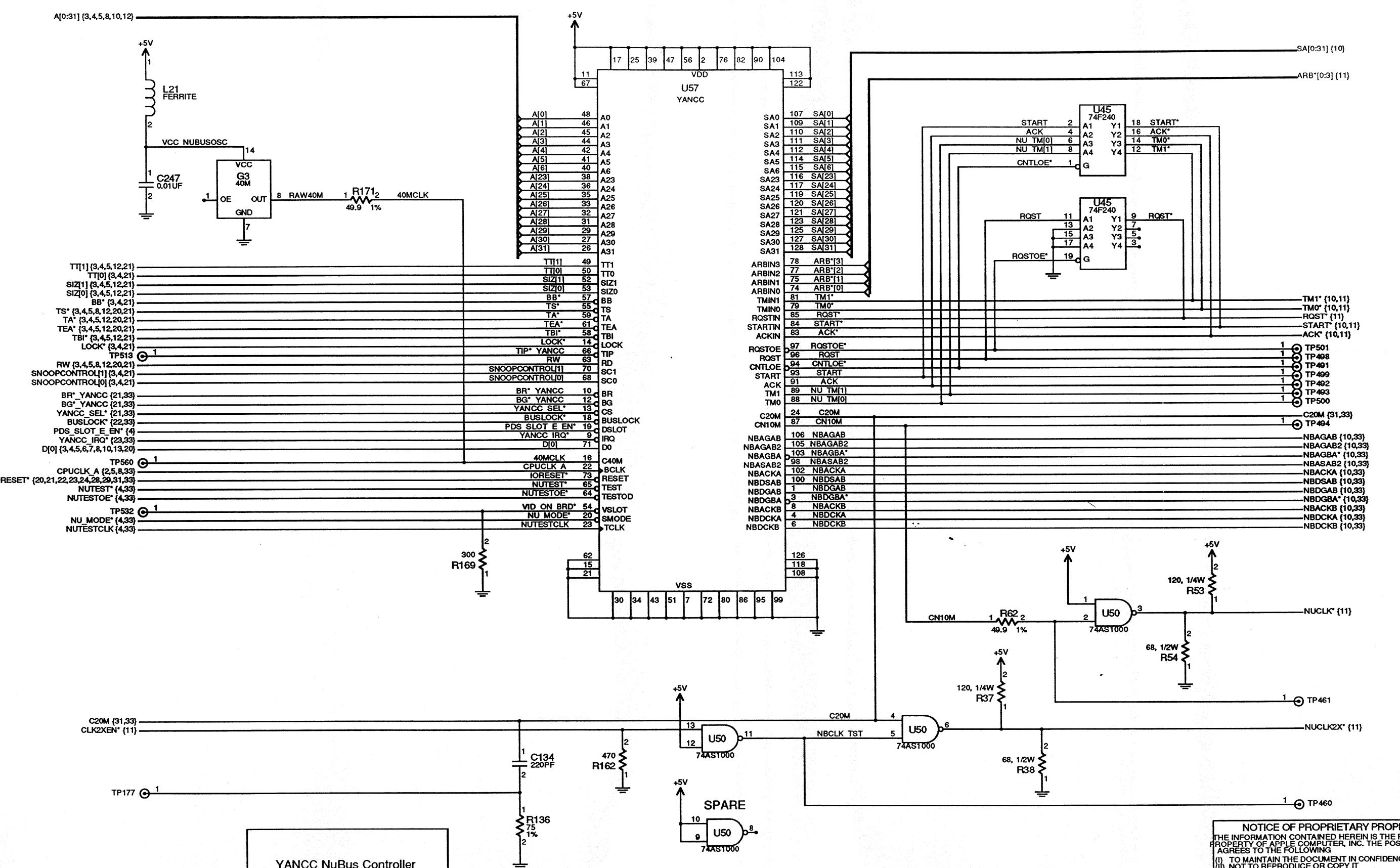
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SCALE		SHT 8	OF 34	1



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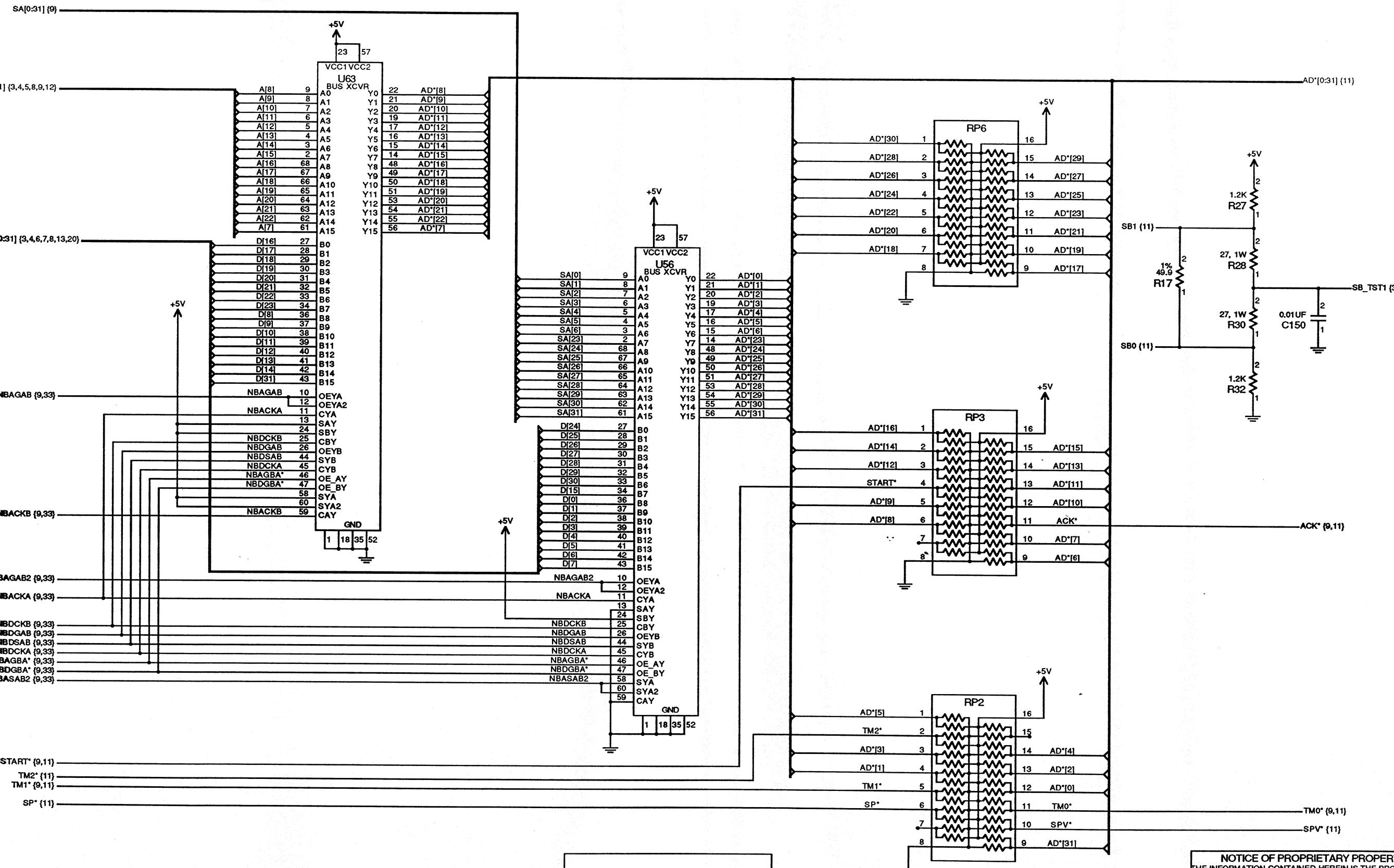
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SIZE	DRAWING NUMBER	REV.
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SCALE	SHT 9 OF 34	

8 7 6 5 4 3 2 1



NuBus Transceivers  
NuBus Termination RPAKs

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SIZE	DRAWING NUMBER	REV.
D	050-8020	C
SCALE		SHT 10 OF 34

8 7 6 5 4 3 2 1

D

NURESET\* (20)

TM0\* (9,10)

PFW\* (1,26)

START\* (9,10)

NUCLK\* (9)

S81 (10)

S80 (10)

SPV\* (10)

SP\* (10)

TM1\* (9,10)

+12V +5V J11

A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 A22 A23 A24 A25 A26 A27 A28 A29 A30 A31 A32

SB0 SPV\* SP\* TM1\* AD'1] AD'3] AD'5] AD'7] AD'9] AD'11] AD'13] AD'15] AD'17] AD'19] AD'21] AD'23] AD'25] AD'27] AD'29] AD'31]

ARB'11] ARB'3] ID1\* ID3\*\* ACK\* ROST\* NMRO[5]

ACK\* (9,10)

ROST\* (9)

+12V +5V J11

B1 B2 B3 B4 B5 B6 B7 B8 B9 B10 B11 B12 B13 B14 B15 B16 B17 B18 B19 B20 B21 B22 B23 B24 B25 B26 B27 B28 B29 B30 B31 B32

SB0 SPV\* SP\* TM1\* AD'1] AD'3] AD'5] AD'7] AD'9] AD'11] AD'13] AD'15] AD'17] AD'19] AD'21] AD'23] AD'25] AD'27] AD'29] AD'31]

TM2\* CM0\* CM1\* CM2\*

NUCLK2X\* NU STDBYPWR CLK2XEN\* NUCBUSY\*

NUCLK\* (9)

PFW\* (1,26)

ARB'10] ARB'2] ID0\* ID2\*\* START\* (9,10)

ACK\* (9,10)

ROST\* (9)

MINUS12V

MINUS12V

SLOT 5  
D16

D

C

B

A

+5V

R49 220, 1/4W

R50 220, 1/4W

R51 220, 1/4W

R52 220, 1/4W

+12V +5V J10

A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 A22 A23 A24 A25 A26 A27 A28 A29 A30 A31 A32

SB0 SPV\* SP\* TM1\* AD'1] AD'3] AD'5] AD'7] AD'9] AD'11] AD'13] AD'15] AD'17] AD'19] AD'21] AD'23] AD'25] AD'27] AD'29] AD'31]

ARB'11] ARB'3] ID1\* ID3\*\* ACK\* ROST\* NMRO[6]

NMRO[2:6]

ACK\* (9,10)

ROST\* (9)

MINUS12V

+12V +5V J10

B1 B2 B3 B4 B5 B6 B7 B8 B9 B10 B11 B12 B13 B14 B15 B16 B17 B18 B19 B20 B21 B22 B23 B24 B25 B26 B27 B28 B29 B30 B31 B32

SB0 SPV\* SP\* TM1\* AD'1] AD'3] AD'5] AD'7] AD'9] AD'11] AD'13] AD'15] AD'17] AD'19] AD'21] AD'23] AD'25] AD'27] AD'29] AD'31]

TM2\* CM0\* CM1\* CM2\*

NUCLK2X\* NU STDBYPWR CLK2XEN\* NUCBUSY\*

NUCLK\* (9)

PFW\* (1,26)

ARB'10] ARB'2] ID0\* ID2\*\* START\* (9,10)

ACK\* (9,10)

ROST\* (9)

MINUS12V

SLOT 6  
E16

B

A

A

TM2\* (10)

NUCLK2X\* (9)

CLK2XEN\* (9)

NuBus Slots D & E

CAD GENERATED

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SIZE	DRAWING NUMBER	REV.
D	050-8020	C
SCALE	SHT 11 OF 34	

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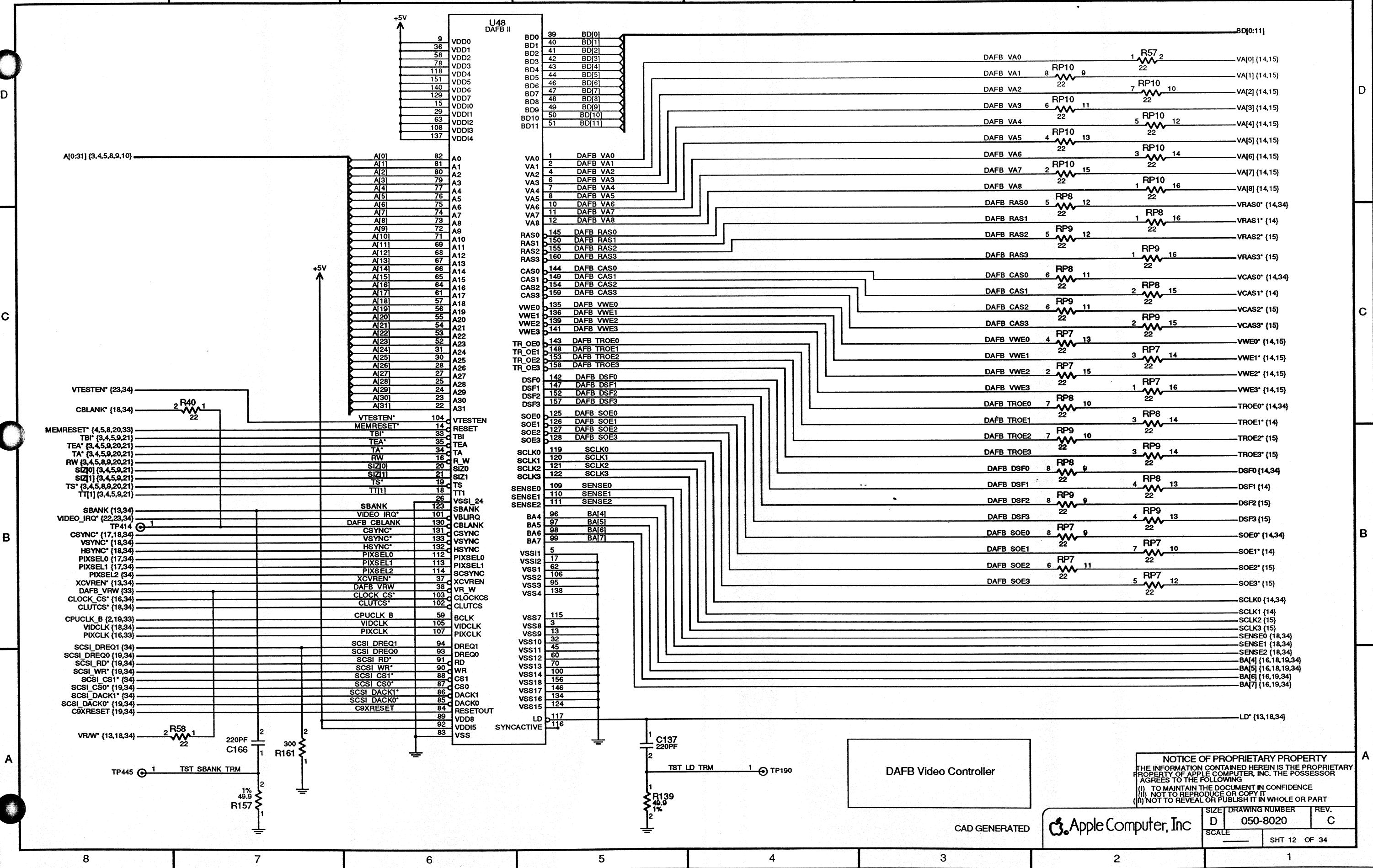
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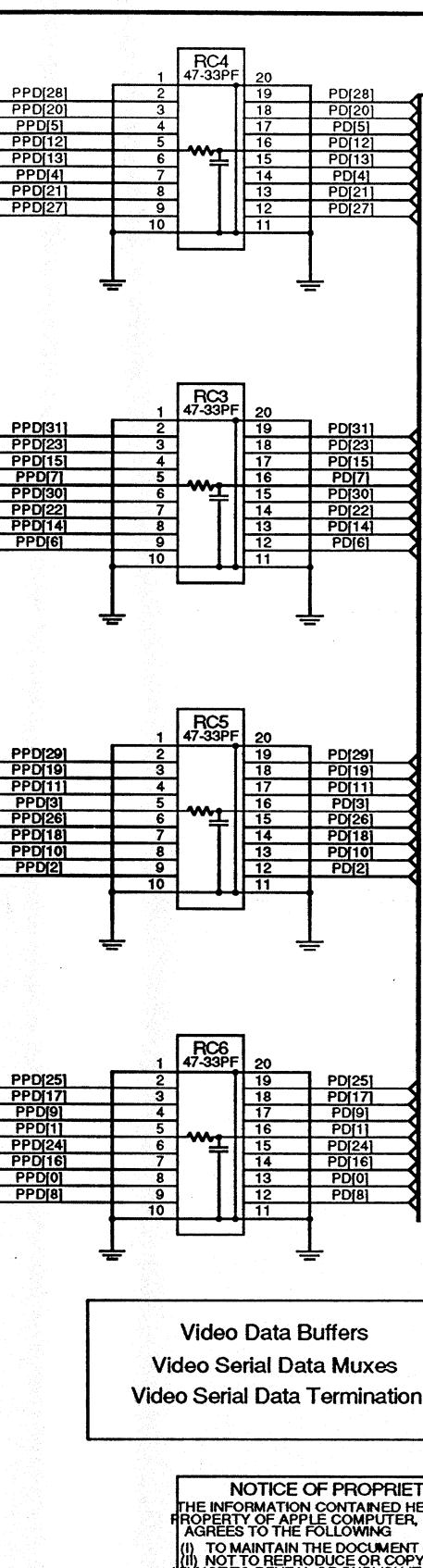
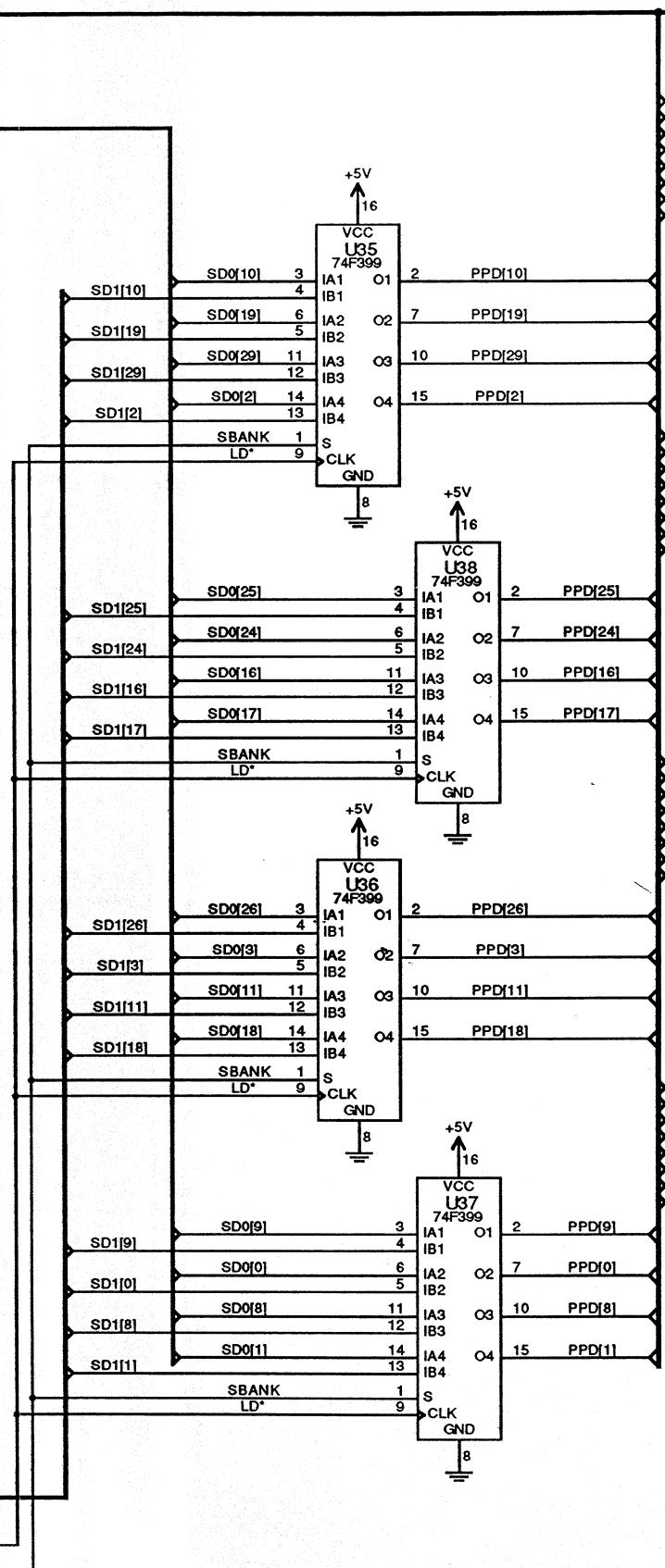
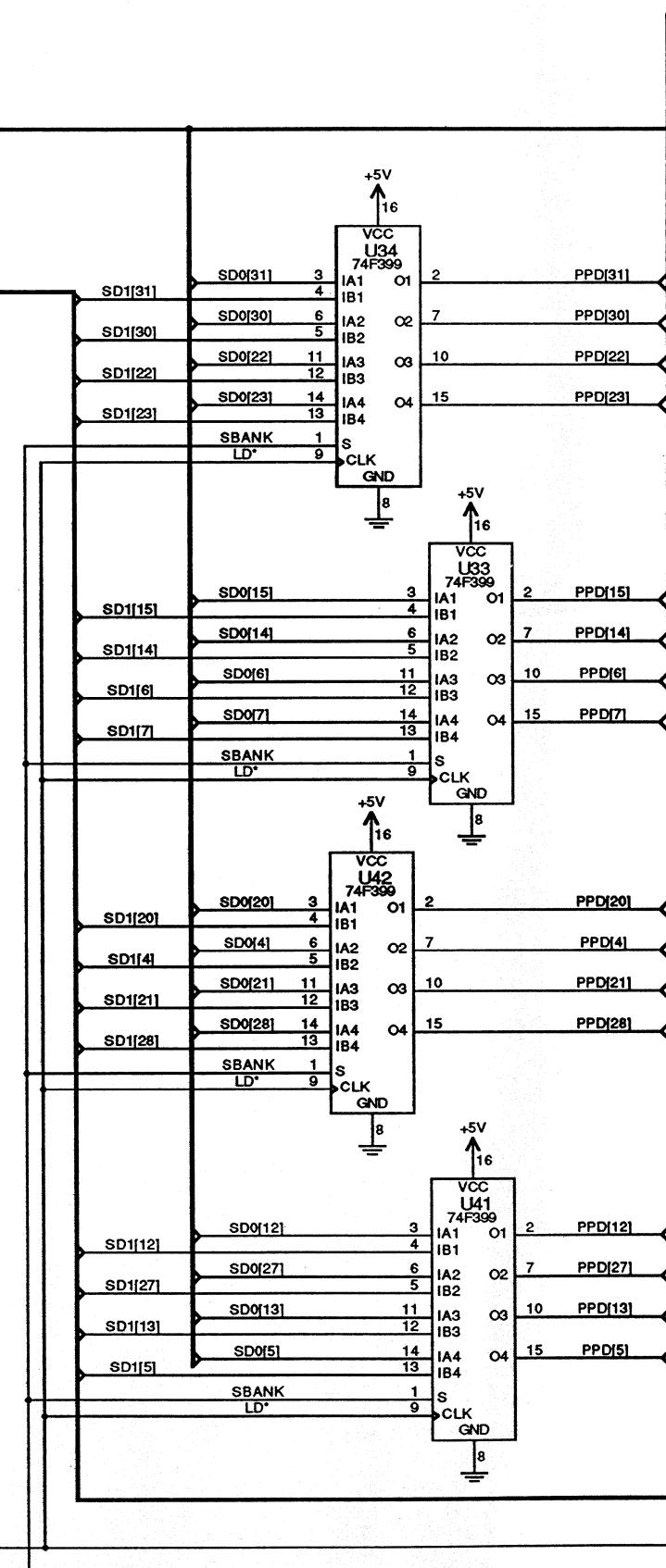
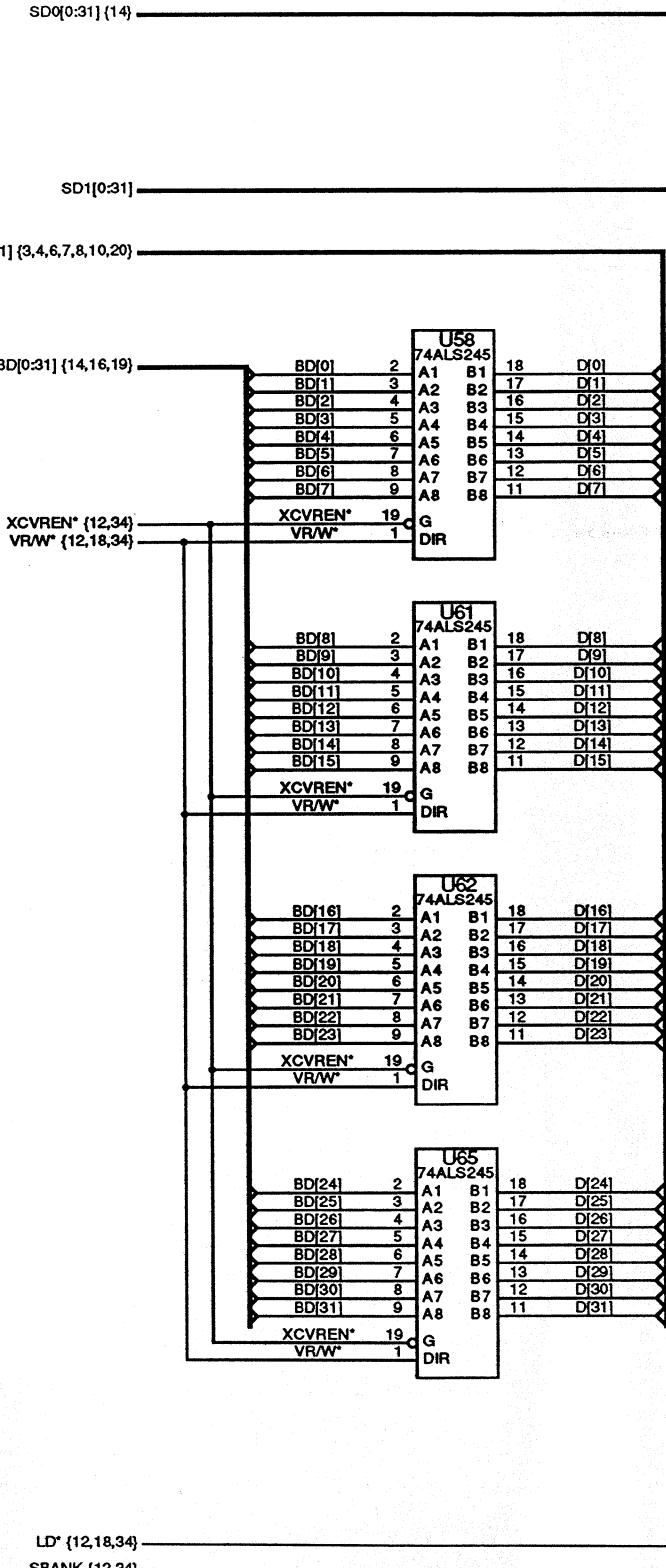
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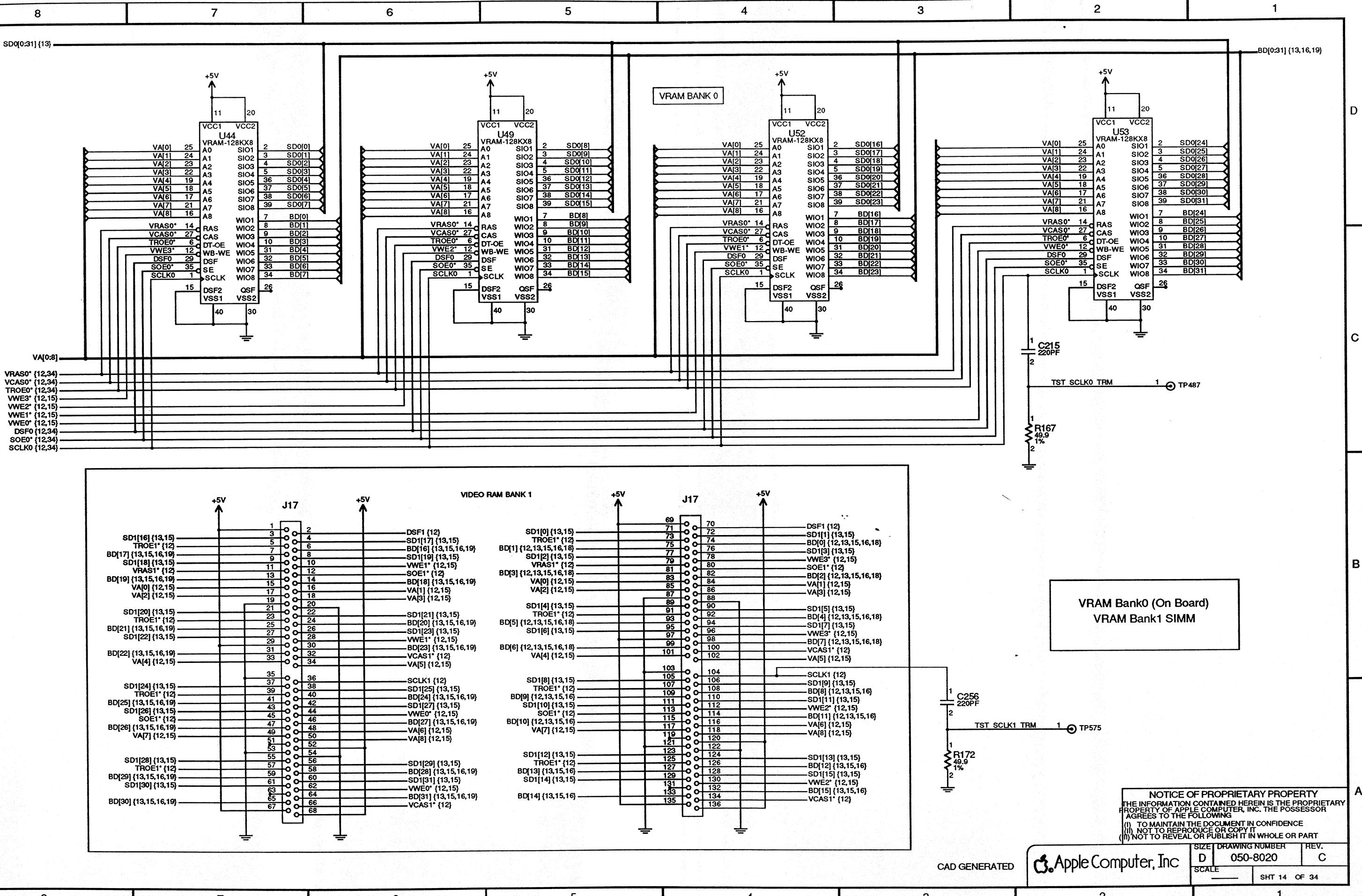
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D	050-8020	C
SCALE		

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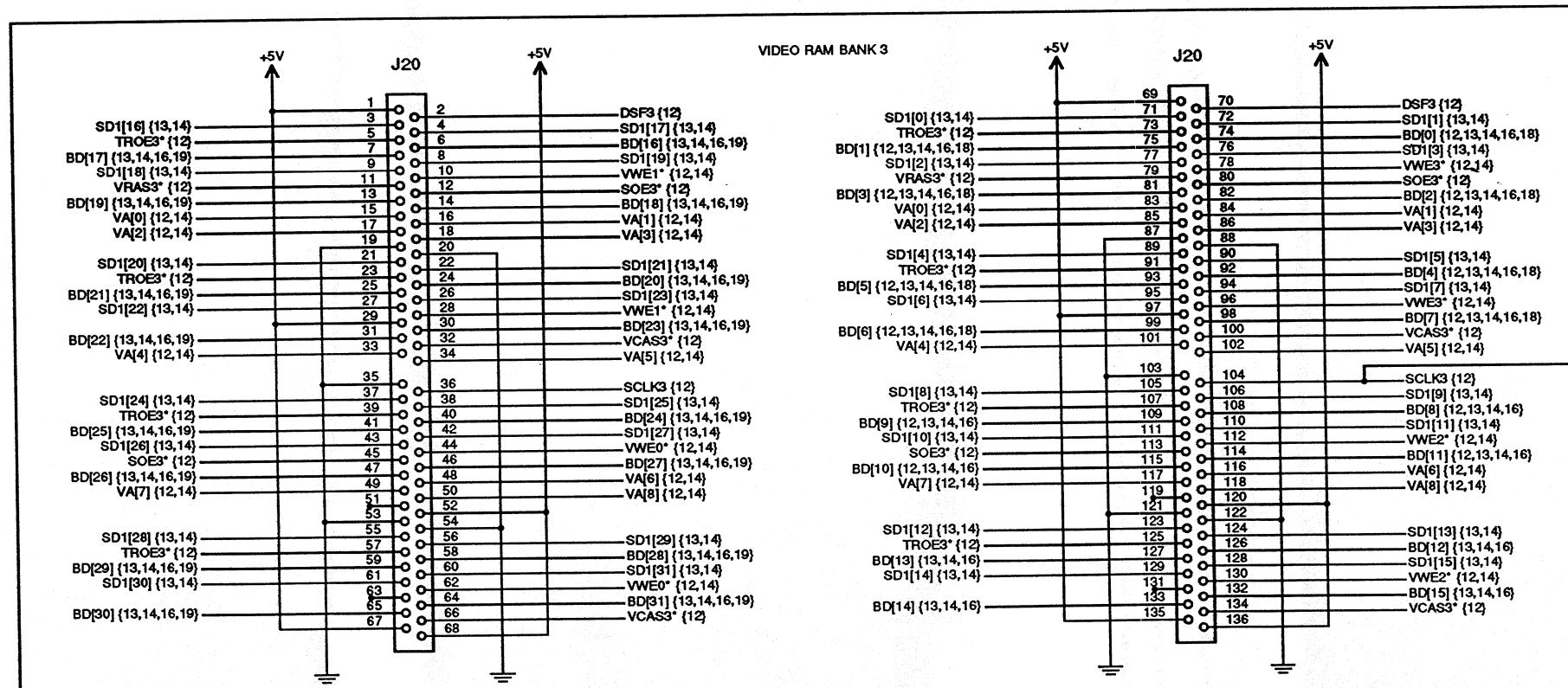
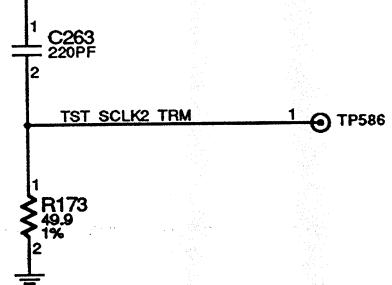
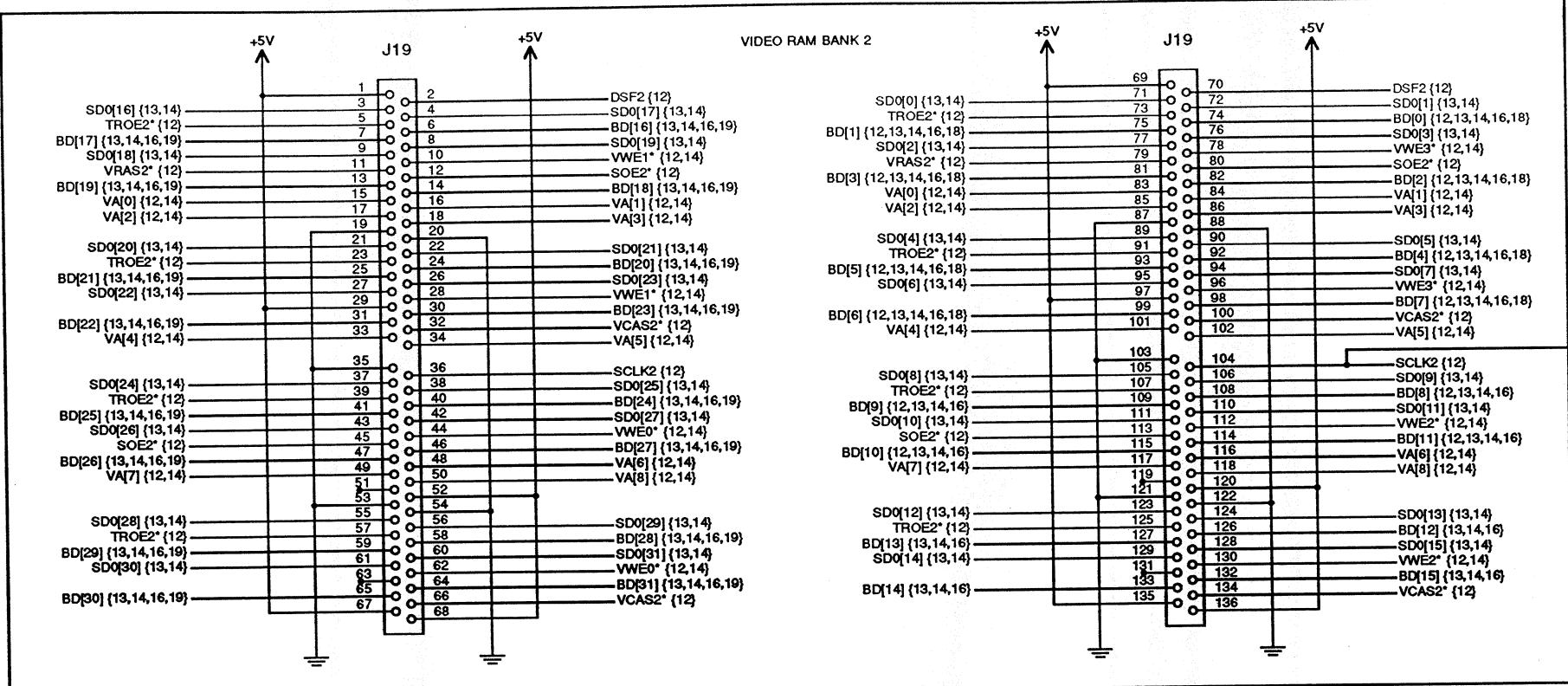
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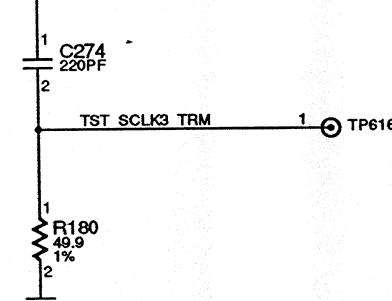
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VRAM Banks 2 and 3 (SIMMS)



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SIZE	DRAWING NUMBER	REV.
D	050-8020	C
SCALE	SHT 15 OF 34	

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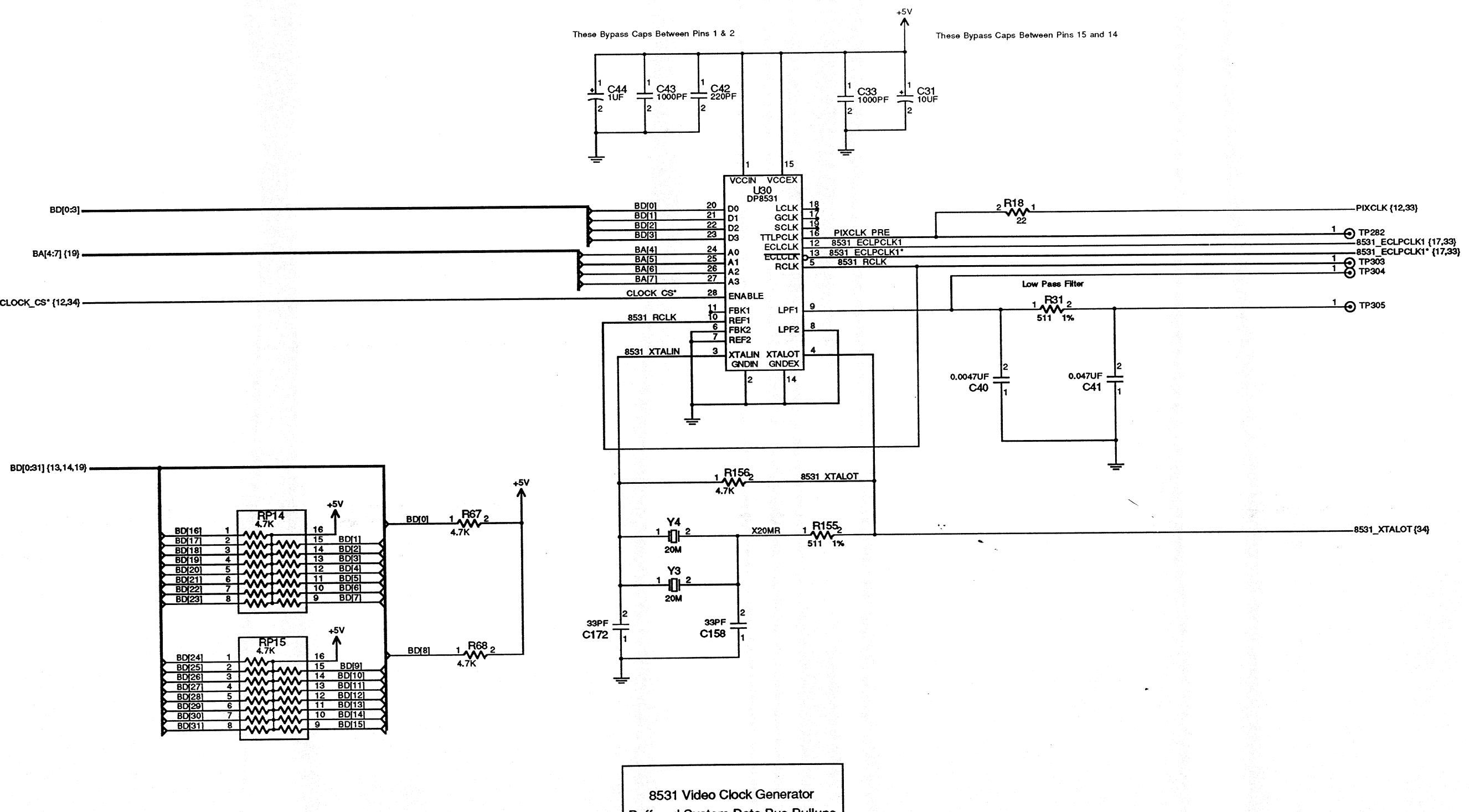
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These Bypass Caps Between Pins 1 &amp; 2

These Bypass Caps Between Pins 15 and 14



8531 Video Clock Generator  
Buffered System Data Bus Pullups

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SCALE	SHT 16	OF 34

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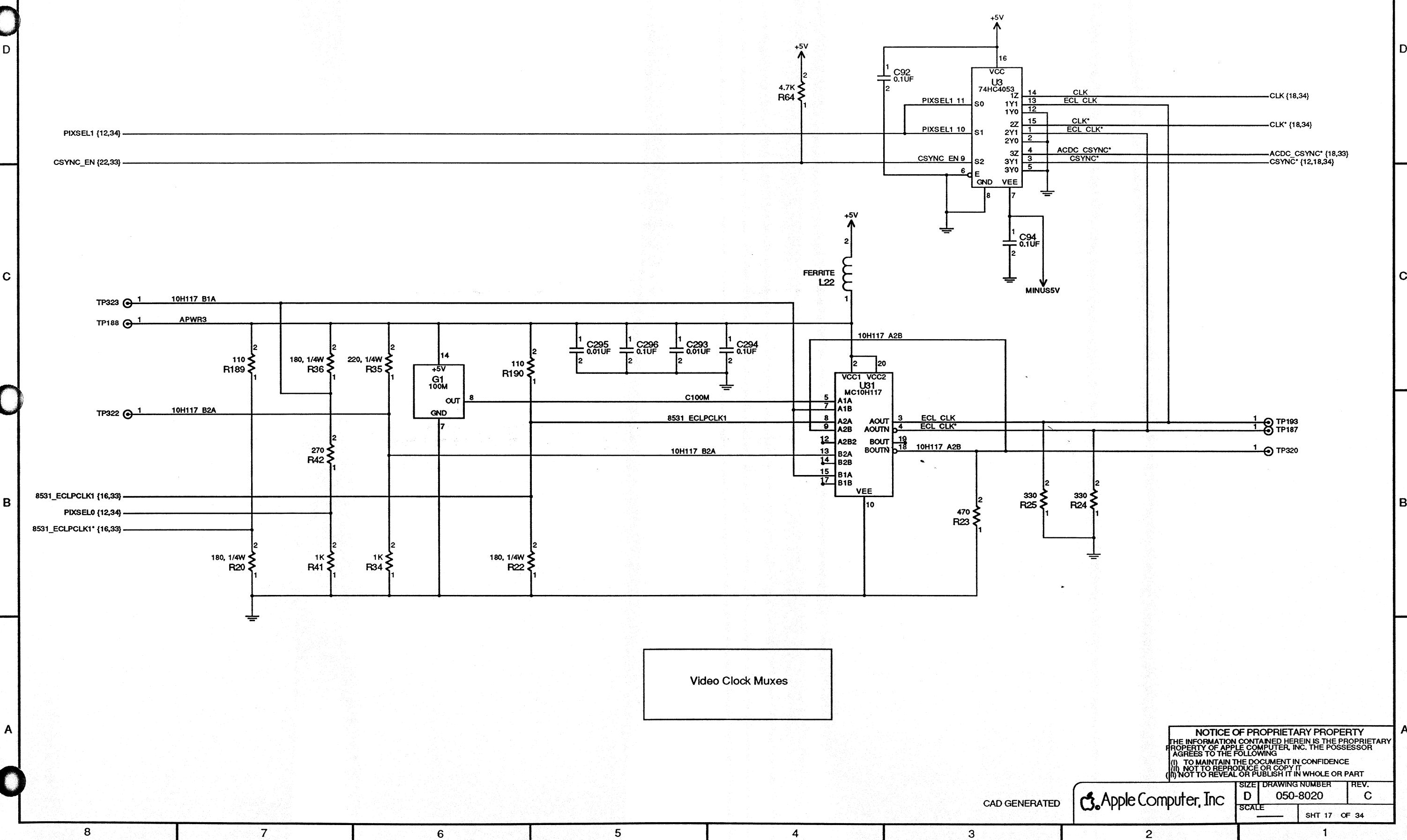
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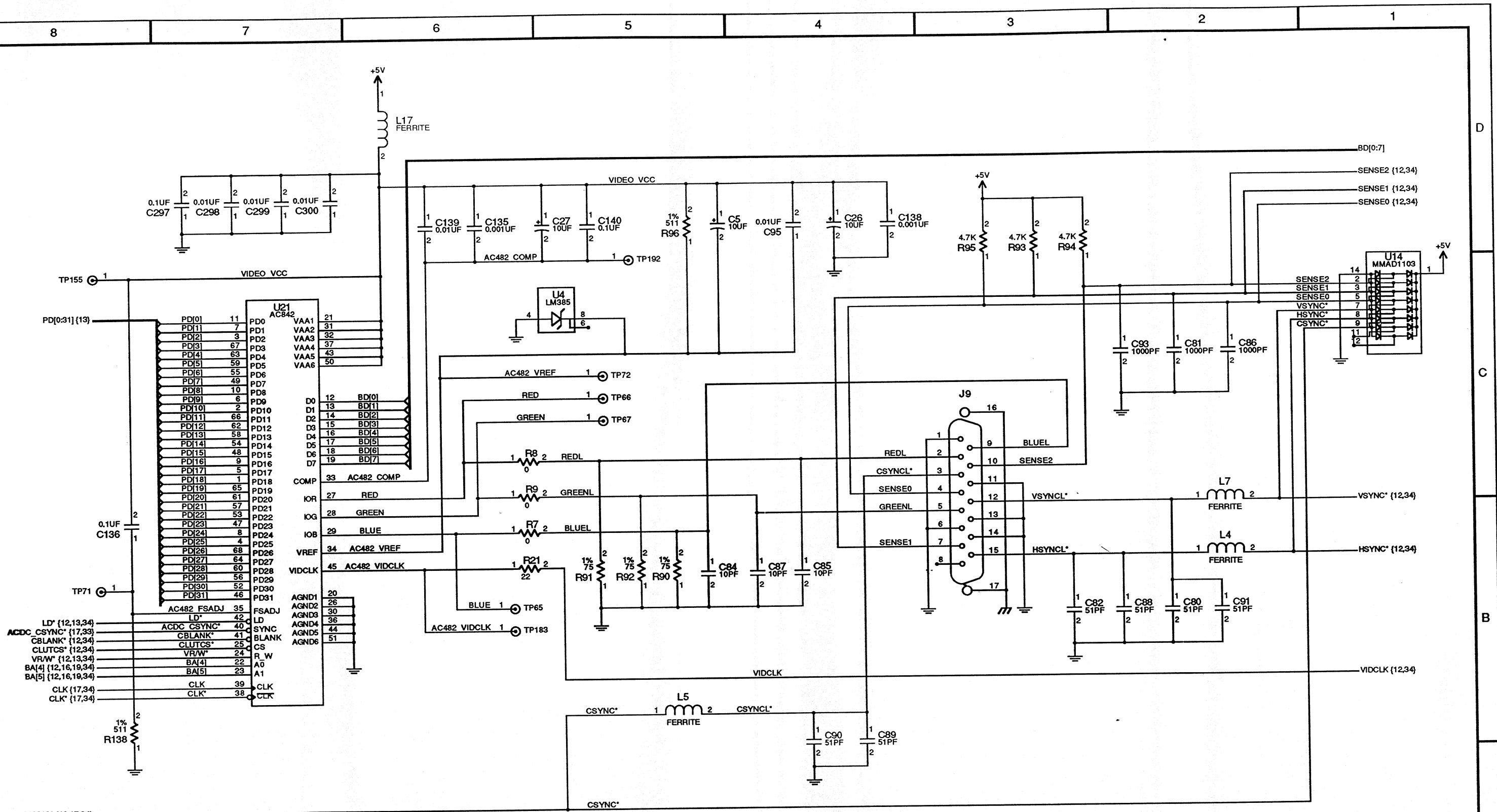
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8 7 6 5 4 3 2 1

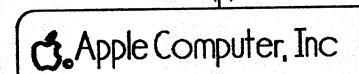


ACDC Video CLUT/DAC

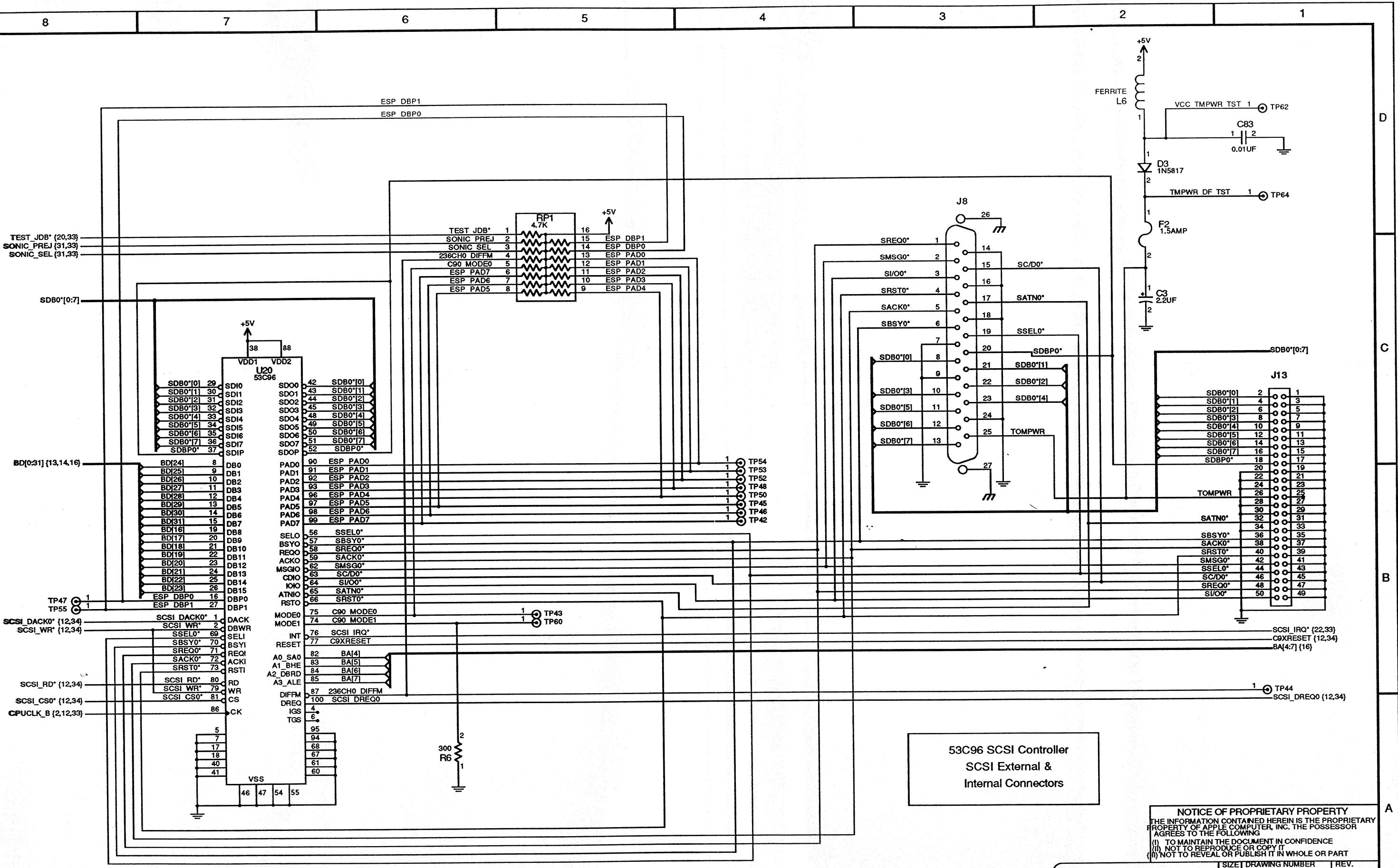
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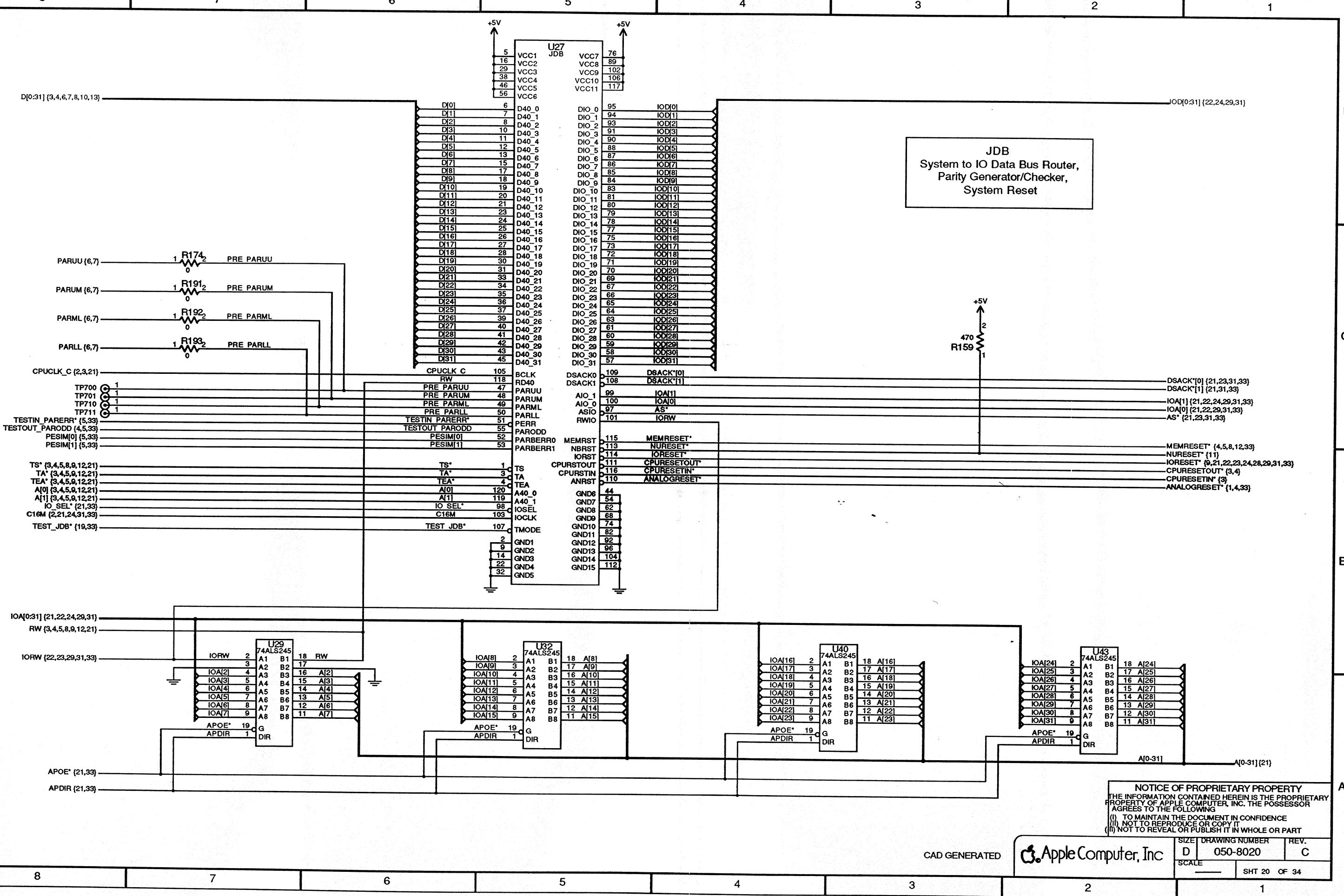
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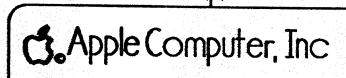
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SCALE	SHEET 1 OF 24	





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SIZE	DRAWING NUMBER	REV.
D	050-8020	C
SCALE		ONE INCH = 10 FEET

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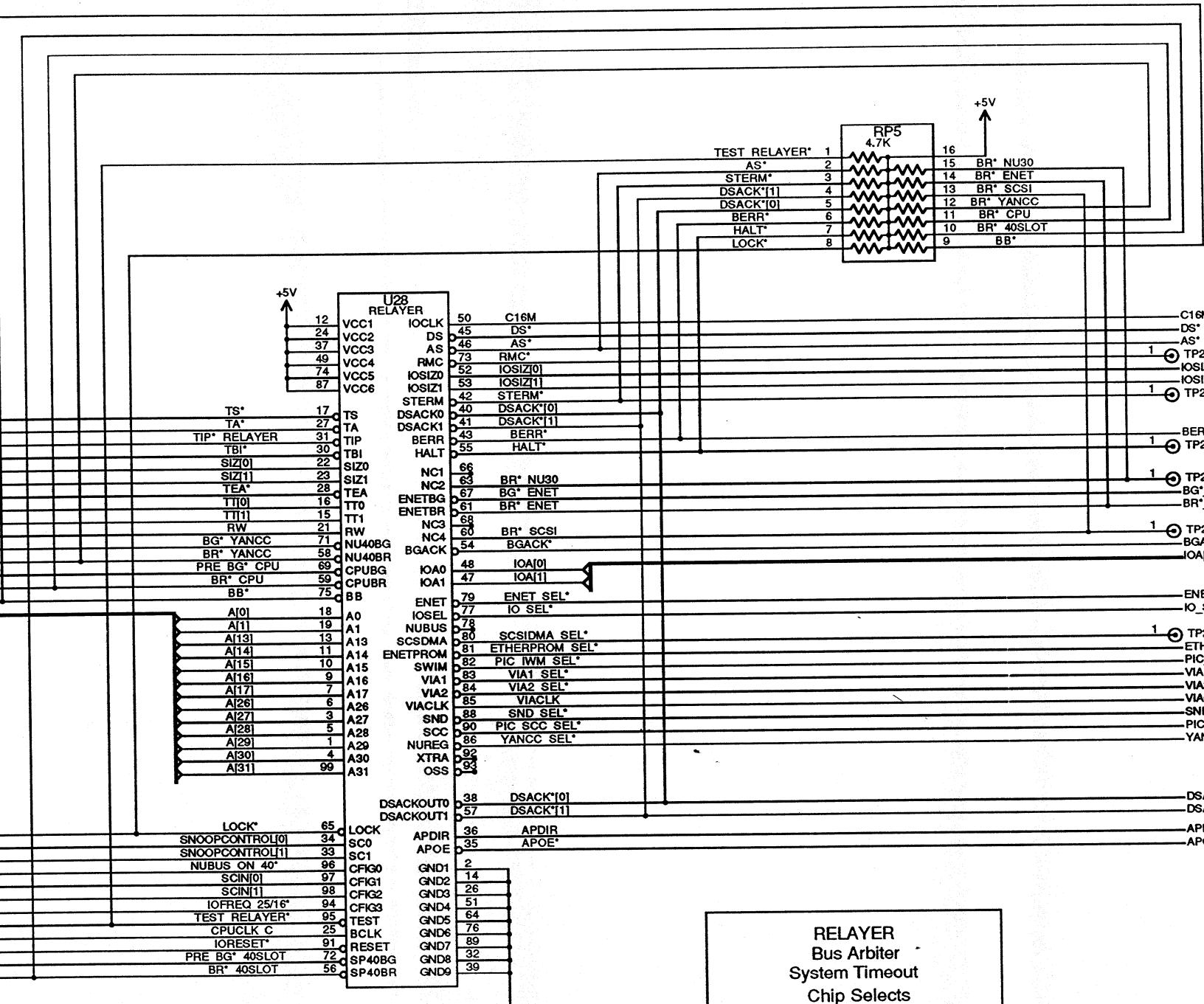
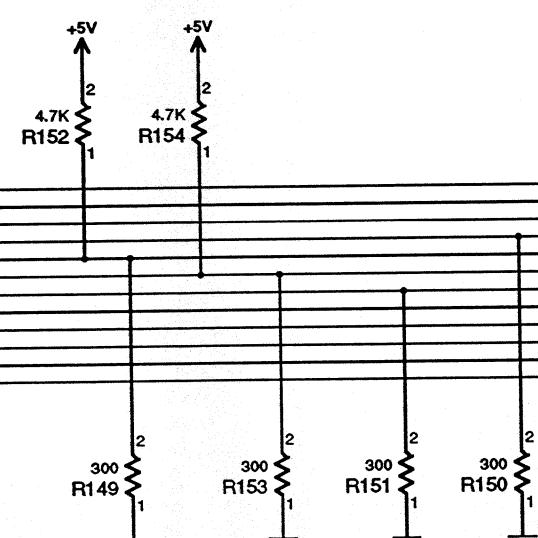
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TS\* {3,4,5,8,9,12,20}  
 TA\* {3,4,5,9,12,20}  
 TBI\* {3,4,5,9,12}  
 SI[0] {3,4,5,9,12}  
 SI[1] {3,4,5,9,12}  
 TEA\* {3,4,5,9,12,20}  
 TI[0] {3,4,9}  
 TI[1] {3,4,5,9,12}  
 RW {3,4,5,8,9,12,20}  
 BG\* YANCC {9,33}  
 BR\* YANCC {9,33}  
 PRE\_BG\* CPU {8,33}  
 BR\* CPU {3,4}  
 BB\* {3,4,9}  
 A[0-31] {20}

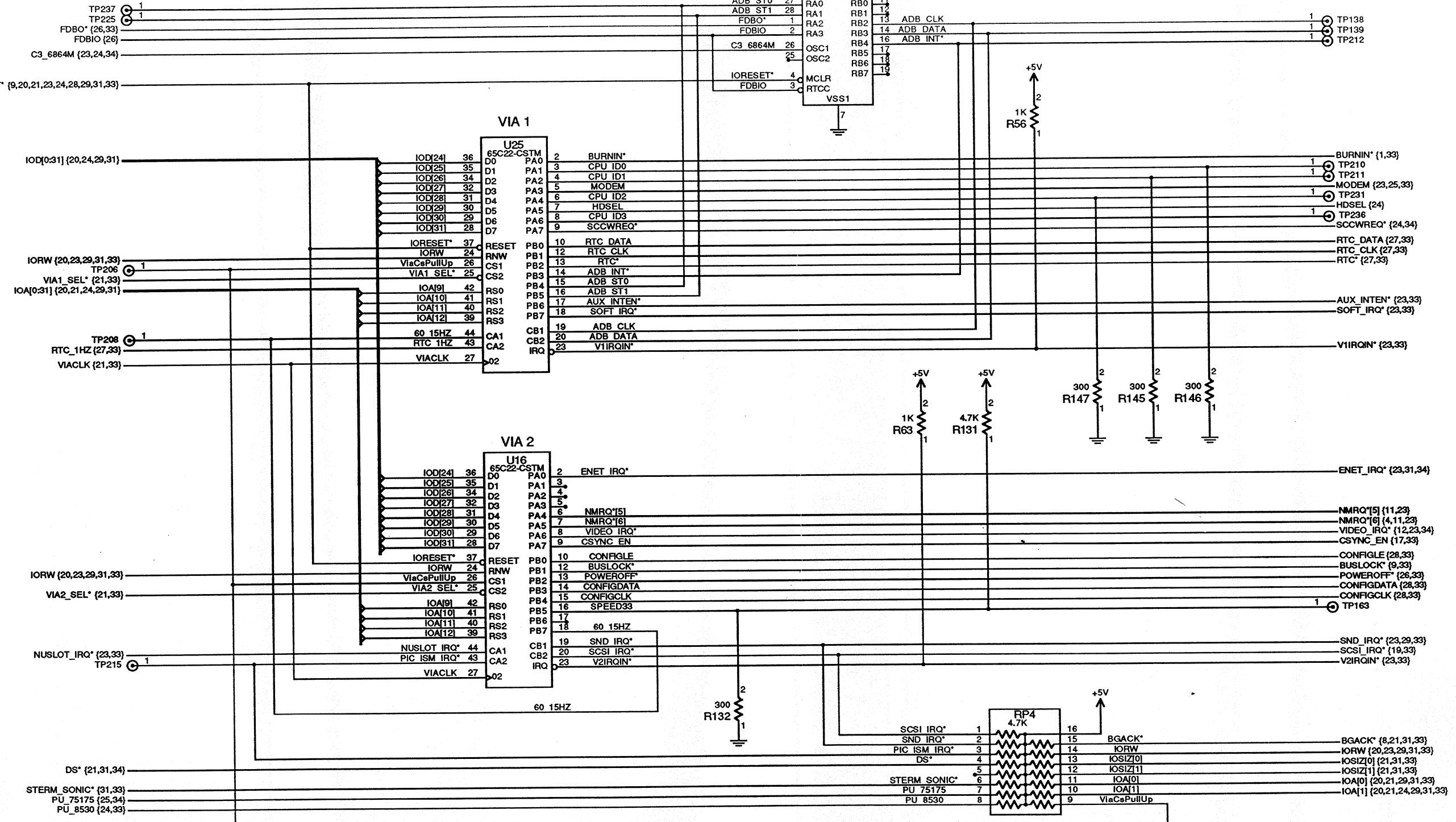


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SCALE	SHT 21 OF 34	

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SIZE	DRAWING NUMBER	REV.
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SCALE \_\_\_\_\_

SHT 22 OF 34

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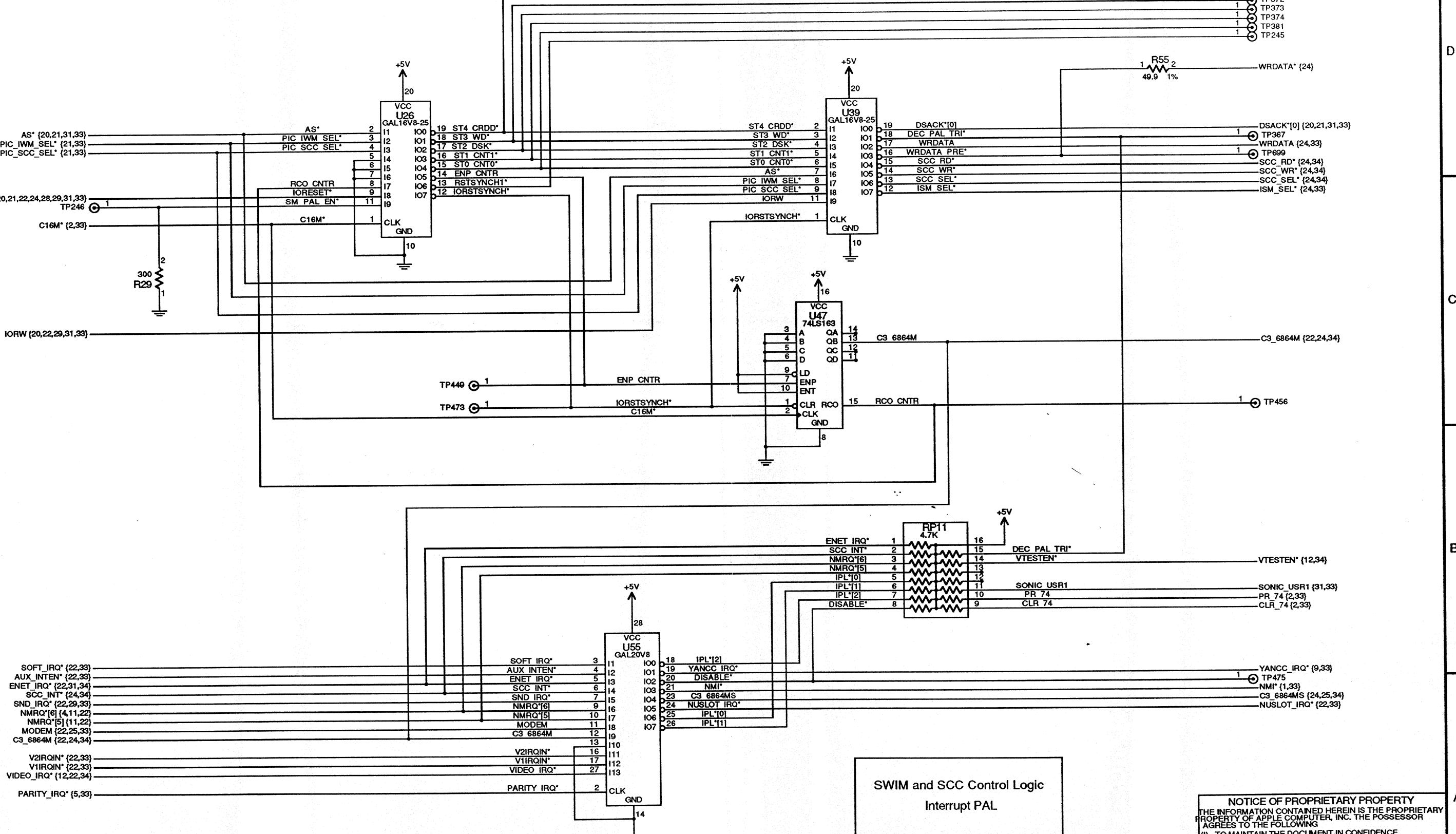
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SWIM and SCC Control Logic  
Interrupt PAL

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SHEET	DRAWING NUMBER	REV.
D	050-8020	C
SCALE	SHT 23	OF 34

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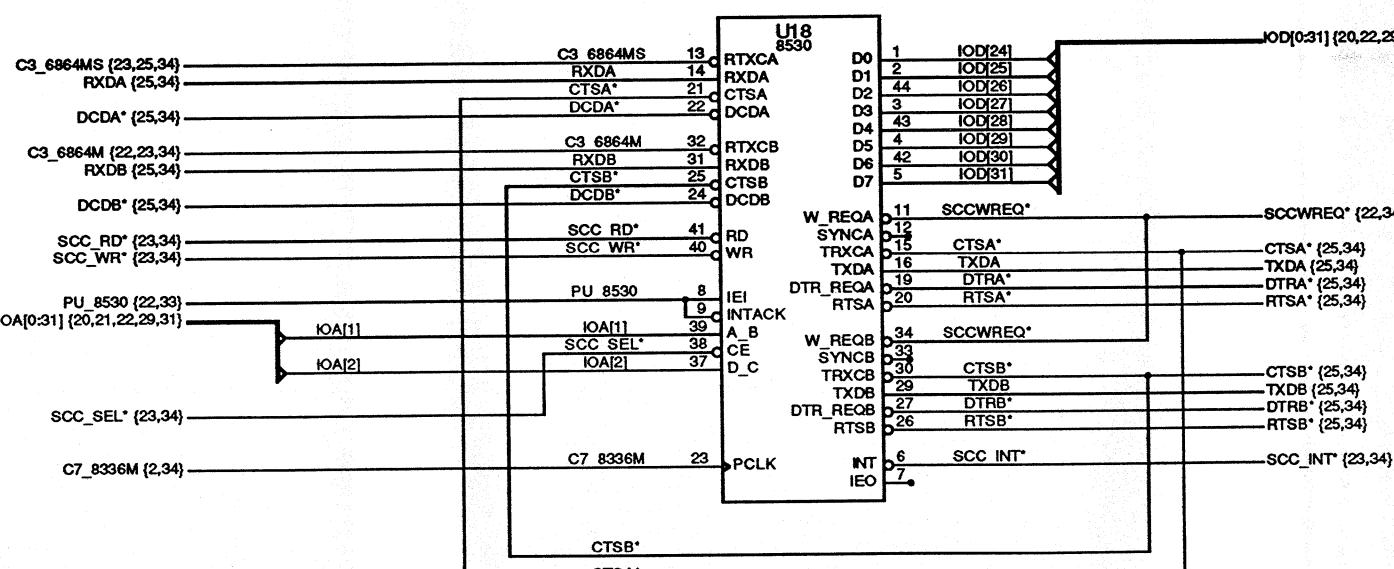
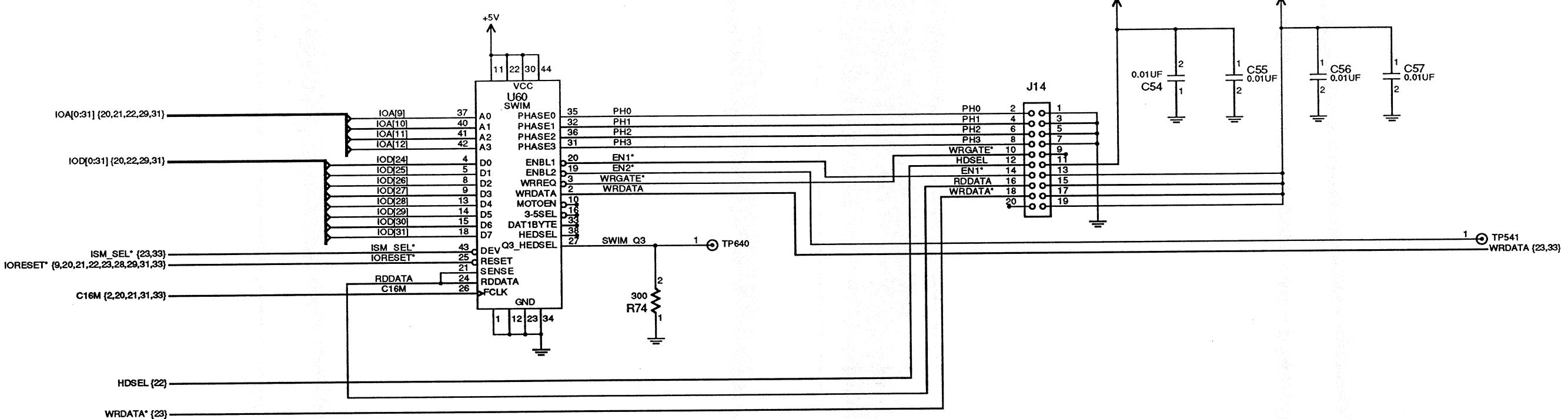
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SIZE	DRAWING NUMBER	REV.
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SCALE		SHT 24 OF 34

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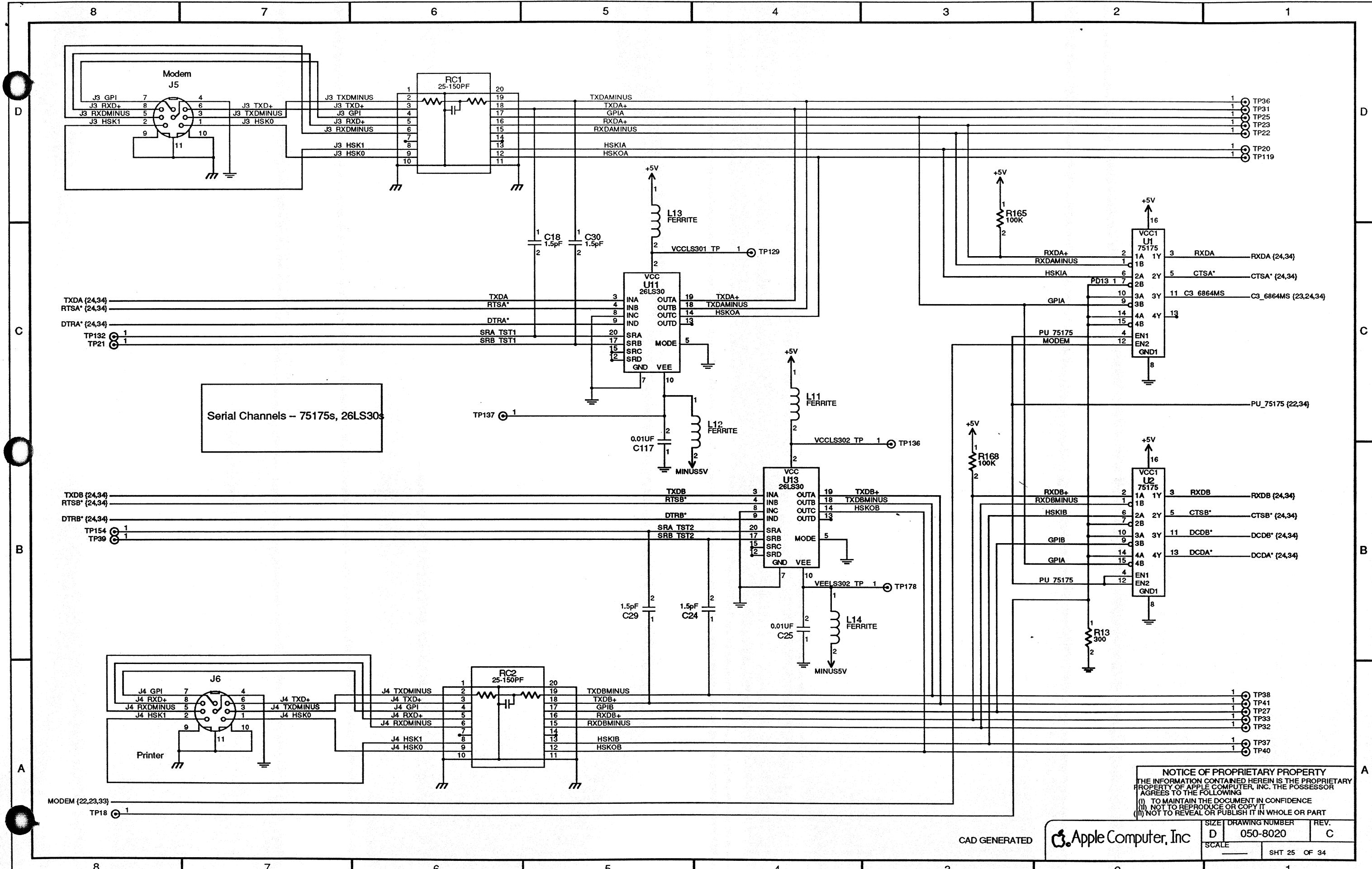
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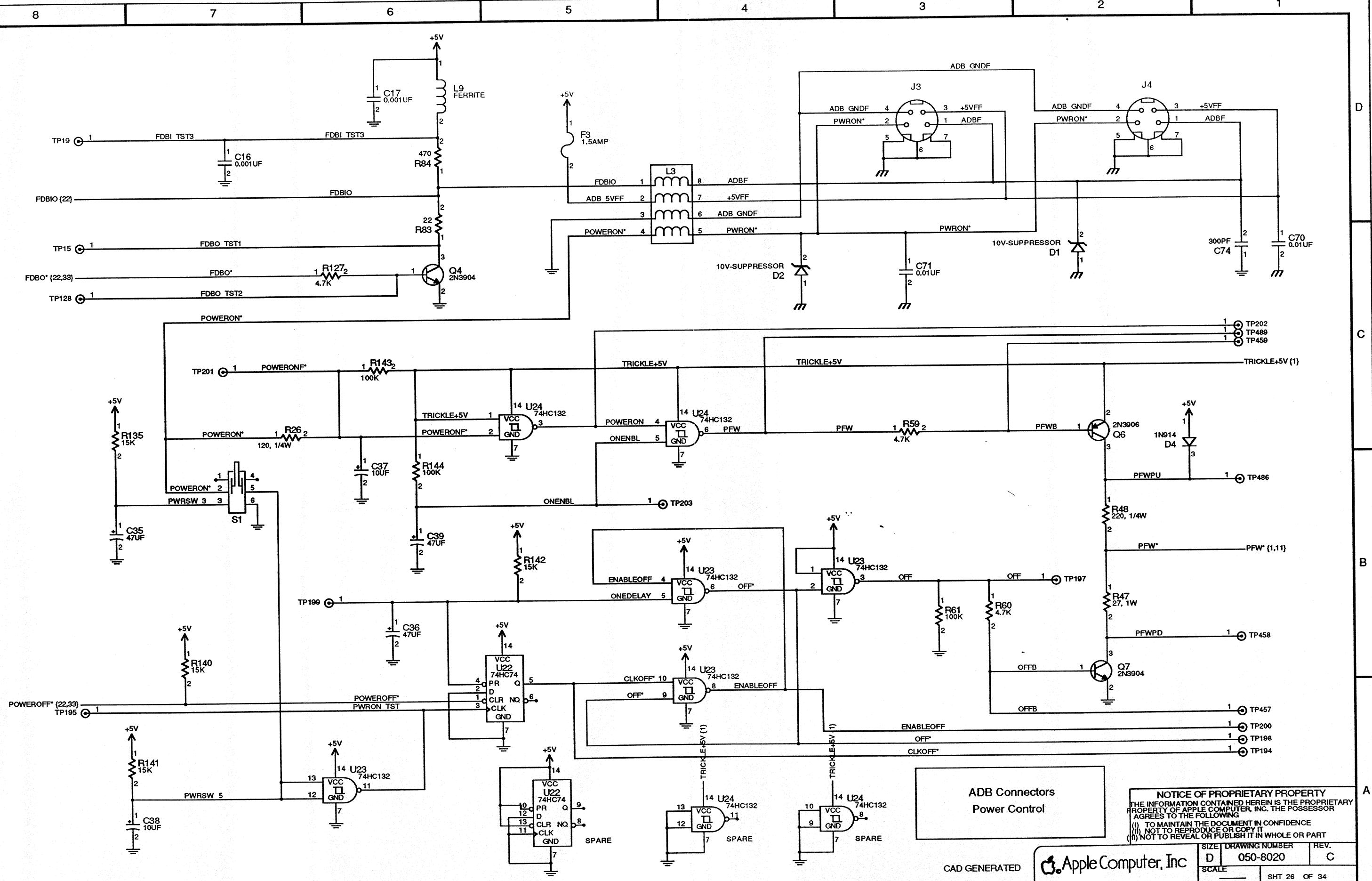
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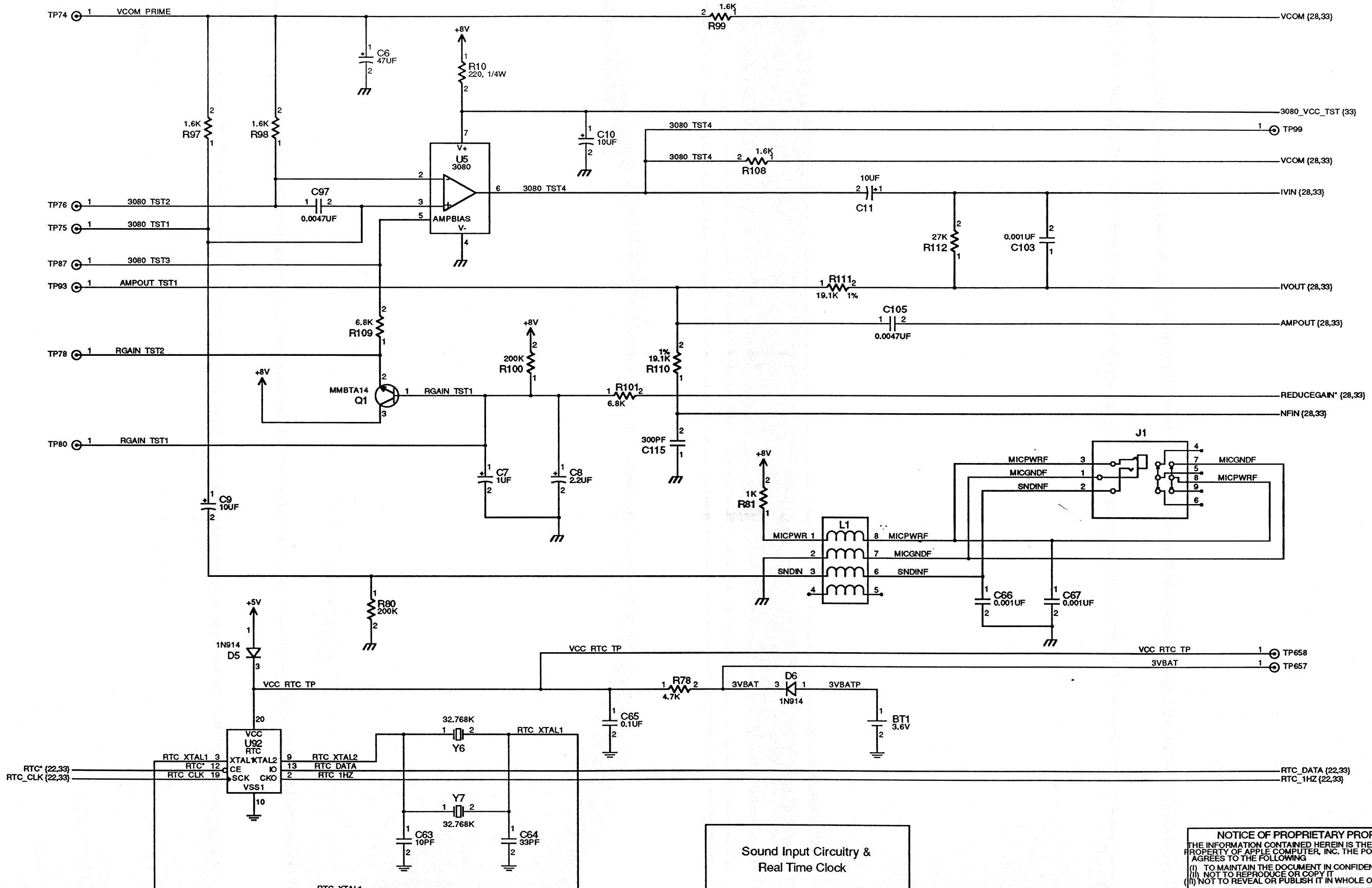
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SIZE	DRAWING NUMBER	REV.
D	050-8020	C
SCALE		

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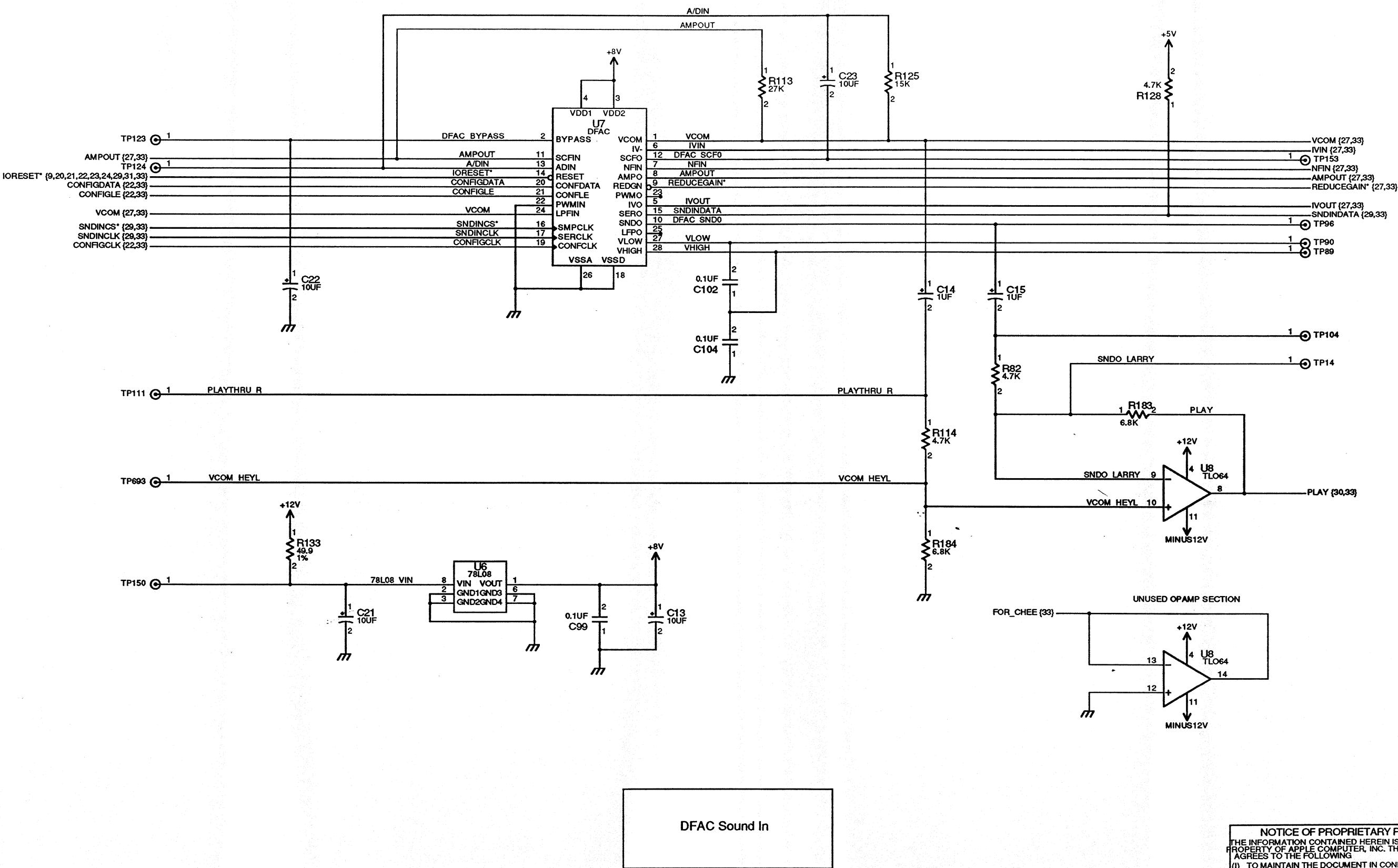
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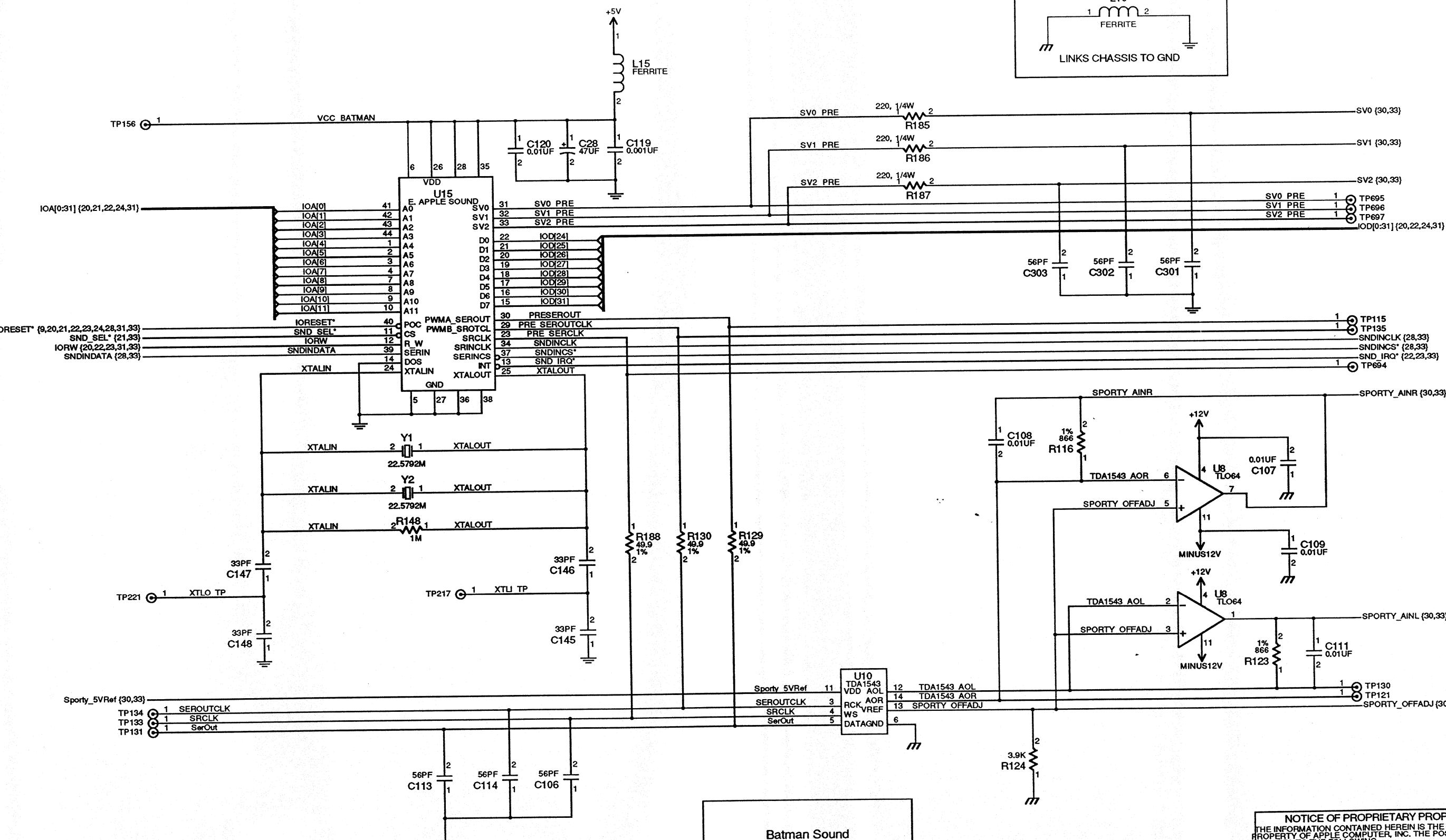
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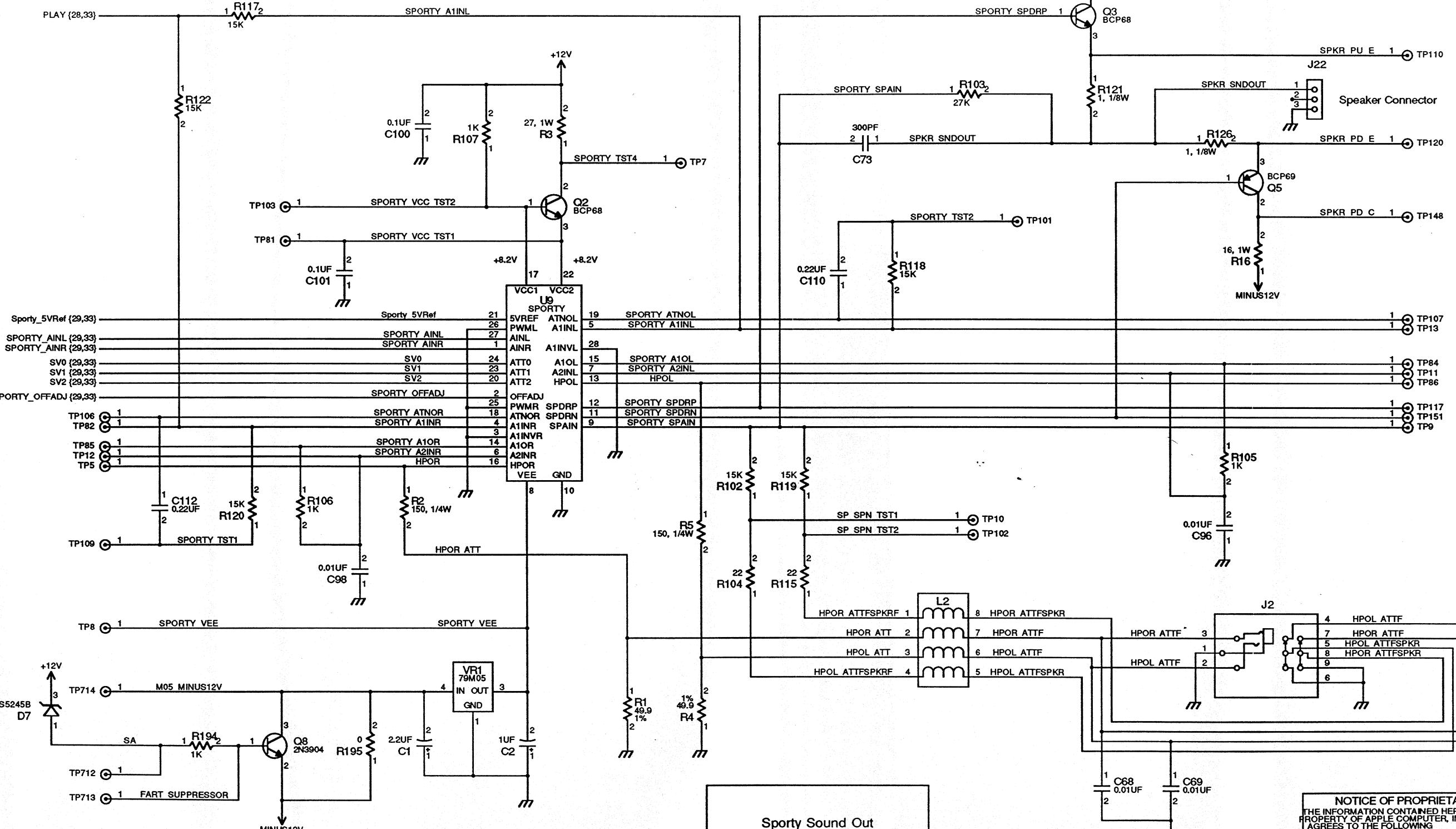
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