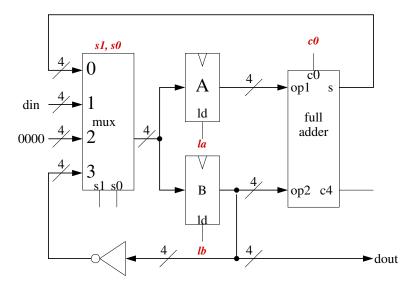
## Sample Sequential Circuit Questions SOLUTIONS

1. Consider the following logic diagram of a sequential system:



- (a) On the diagram, label all the control inputs only.
- (b) Propose a control word format for this system, indicating which control inputs are defined by which bit positions.

ANSWER:

- (c) For each of the following tasks determine if it can be performed by a single  $\mu$ -instruction; that is, by a single control word assignment. If it can, then give the control word that will cause the task to be executed, using your control word format from question (b).
  - i.  $B \leftarrow A plus B$

ANSWER: Yes: 00010

ii.  $\mathbf{A} \leftarrow \mathbf{0}$ 

ANSWER: Yes: 1010X

iii.  $\mathbf{A} \leftarrow \mathbf{B}'$ 

ANSWER: Yes: 1110X

iv.  $\mathbf{B} \leftarrow -\mathbf{B}$ 

ANSWER: No, it will take 2  $\mu$ -instructions

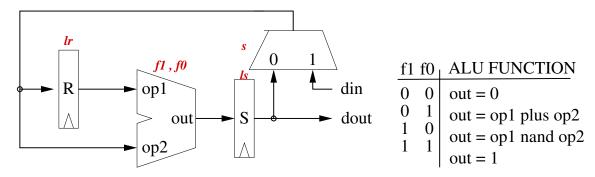
(d) Without using data input port 1 of the MUX, Find a sequence of  $\mu$ -instructions, expressed as register transfer statements, that enable the sequential system to perform the following tasks:

HINT: How is the functional specification of an n-bit Full Adder expressed?

i.  $\mathbf{A} \leftarrow \mathbf{1}$ 

ii.  $\mathbf{A} \leftarrow -1$ 

2. A digital system is implemented by the following logic diagram:



R and S are storage registers.

- (a) On the logic diagram assign and label the control inputs to all devices as required.
- (b) Which of the following register transfer statements are  $\mu$ -instructions? For those that are, indicate what values the control inputs must have to achieve the desired effect. For those that are not, give a sequence of  $\mu$ -instructions that will result in the desired effect.
  - i.  $S \leftarrow 1$

Control word: 0111X

ii. S  $\leftarrow$  R plus 1

ANSWER: Control word: 00111 (with din = 1)

iii.  $S \leftarrow R$  nand S

ANSWER: Control word: 01010

iv.  $S \leftarrow -1$ 

## $\mu$ -instruction

 $S \leftarrow 0$ 

ANSWER:  $\mu$ -program:

 $\mathbf{R} \leftarrow \mathbf{S}$ 

 $S \leftarrow R \text{ nand } S$ 

## OR

## $\mu$ -instruction

 $S \leftarrow 1$ 

 $\mathbf{R} \leftarrow \mathbf{S}$ 

 $S \leftarrow R \text{ nand } S$ 

 $\mathbf{R} \leftarrow \mathbf{S}$ 

 $S \leftarrow 1$ 

 $S \leftarrow R \text{ plus } S$