ALU Operations	Binary Equivalent	Binary Equivalent Breakdown				
Field Size	18-bit	3-bit func	4-bit rs	4-bit rt	4-bit rd	3-bit shamt
Addition	010 … 000	010	rs	rt	rd	000
Subtraction	011 … 000	011	rs	rt	rd	000
Beq (Compare for Equivalence)	…	*	rs	rt	Branch address / immediate	
OR	100 … 000	100	rs	rt	rd	000
AND	101 … 000	101	rs	rt	rd	000
Multiplication	110 … 000	110	rs	rt	rd	000
Division	111 … 000	111	rs	rt	rd	000
Shift Left Logical	001 … 000	001	rs	rt	rd	000
Odd Parity (created)	000 … 000	000	rs	rt	rd	000