

## FeatherWing Implementation Chart

Feather			Backpack			
Header	Pin	Name	Name	Direction (I,O,X)	Type (D, A, P)	Notes
JP1	16	RESET	RESET	O	D	reset switch
	15	3V3	3V3	I	P	logic and selectable 3v pin
	14	AREF				
	13	GND	GND	X	P	
	12	A0_D14				
	11	A1_D15				
	10	A2_D16				
	9	A3_D17				
	8	A4_D18				
	7	A5_D19				
	6	SCK_D25				
	5	MOSI_D24				
	4	MISO_D23				
	3	RX_D0	UART_IN	O	D	jumper-selectable
	2	TX_D1	UART_OUT	I	D	jumper-selectable
	1	D4				
JP3	12	SDA_D22	I2C_SDA	I/O	D	jumper-selectable
	11	SCL_D21	I2C_SCL	I/O	D	jumper-selectable
	10	D5				
	9	D6				
	8	D9				
	7	D10				
	6	D11				
	5	D12				
	4	D13				
	3	VUSB	USB	I	P	selectable ~5v pin
	2	EN				
	1	VBAT	BAT	I	P	selectable ~5v pin