

1101 BOOT SEL: SDCARD -> EMMC GND U5.2 PI15 PH0/UART0 TX/CAN TX/PWM3/TWI1 SCK/PH EINT0 PC0/NAND_WE/SDC2_DS/SPI0_CLK/PC_EINT0 PHI/UARTO_TX/CAN_TX/PWM3/TWI1_SCR/PH_EINTO D15 PHI
PHI/UARTO_TX/CAN_TX/PWM4/TWI1_SDA/PH_EINTO B15 PH2 PI12 PH1/UART0_RX/CAN_KX/PWM4/TWI1_SDAVIT_EINT3 B15 PH2
PH2/UART5_TX/SPDIF_CLK/PWM2/TWI2_SCK/PH_EINT2/EPHY-SPD-LED
PH3/UART5_RX/SPDIF_IN/PWM1/TWI2_SDA/PH_EINT3/EPHY-LINK-LED
PH4/SPDIF_OUT/TWI3_SCK/PH_EINT4
PH4
PH4/SPDIF_OUT/TWI3_SCK/PH_EINT4
B14 PH5 PC1/NAND_ALE/SDC2_RST/PC_EINT1 PC2/NAND_CLE/SPI0_MOSI/PC_EINT2 PI6 R52 12 PC3/NAND_CE1/SPI0_CS0/BOOT_SEL1/PC_EINT3 14 14 PI7 47K PC4/NAND_CE0/SPI0_MISO/BOOT_SEL2/PC_EINT4 PH4/SPDIF_OUT/TWIS_SCRVETT_LEINT5 B14 PH5
PH5/UART2_TX/H_I2S3_MCLK/SPI1_CS0/TWI3_SDA/PH_EINT5 C14 PH6
PH6/UART2_RX/H_I2S3_BCLK/SPI1_CLK/TWI4_SCK/PH_EINT6 B12 PH7
PH7/UART2_RTS/H_I2S3_LRCK/SPI1_MOSI/TWI4_SDA/PH_EINT7 C13 PH8 16 16 15 PC5/NAND_RE/SDC2_CLK/BOOT_SEL3/PC_EINT5 18 18 17 17 19 PI0 PC6/NAND_RB0/SDC2_CMD/BOOT_SEL4/PC_EINT6 PC7/NAND_RB1/SPI0_CS1/PC_EINT7 22 22 PH8/UART2_CTS/H_I2S3_DOUT0/SPI1_MISO/H_I2S3_DIN1/PH_EINT8
PH9/H_I2S3_DIN0/SPI1_CS1/H_I2S3_DOUT1/PH_EINT9
PH10/IR_RX/TCON_TRIG1/PH_EINT10

C13 PH8
A12 PH9
B13 PH10 PC8/NAND_DQ7/SDC2_D3/PC_EINT8 24 24 F1 PH7 PC9/NAND_DQ6/SDC2_D4/PC_EINT9 PH10 26 26 PC10/NAND_DQ5/SDC2_D0/PC_EINT10 28 28 PH9 PC11/NAND_DQ4/SDC2_D5/PC_EINT11 PIO/RGMII_RXD3/RMII_NULL/DMIC_CLK/H_I2S0_MCLK/HDMI_SCL/PI_EINT0 D4 PI1
PI1/RGMII_RXD2/RMII_NULL/DMIC_DATA0/H_I2S0_BCLK/HDMI_SDA/PI_EINT1 G6 PI2 G2 PC12/NAND_DQ\$/PC_EINT12 PI3 PC13/NAND_DQ3/SDC2_D1/PC_EINT13 H1 PC13/NAND_DQ2/SDC2_D6/PC_EINT14 34 34 33 33 PI13 PI2/RGMII_RXD1/RMII_RXD1/DMIC_DATA1/H_I2S0_LRCK/HDMI_CEC/PI_EINT2 -H2 PC14/NAND_DQ2/SDC2_D2/SP10_WP/PC_EINT15 PH4 SDC2_D7 PH6 PH2 PC16/NAND_DQ0/SDC2_D7/SPI0_HOLD/PC_EINT16 PH3 40 40 PI5/RGMII_RXCTL/RMII_CRS_DV/UART2_TX/TS0_CLK/TWI0_SCK/PI_EINT5 PI6/RGMII_RXCTD/RMII_CRS_DV/UART2_TX/TSU_CLR/TWI0_SCR/PI_EINT5

PI6/RGMII_NULL/RMII_RXER/UART2_TX/TS0_ERR/TWI0_SDA/PI_EINT5

G4

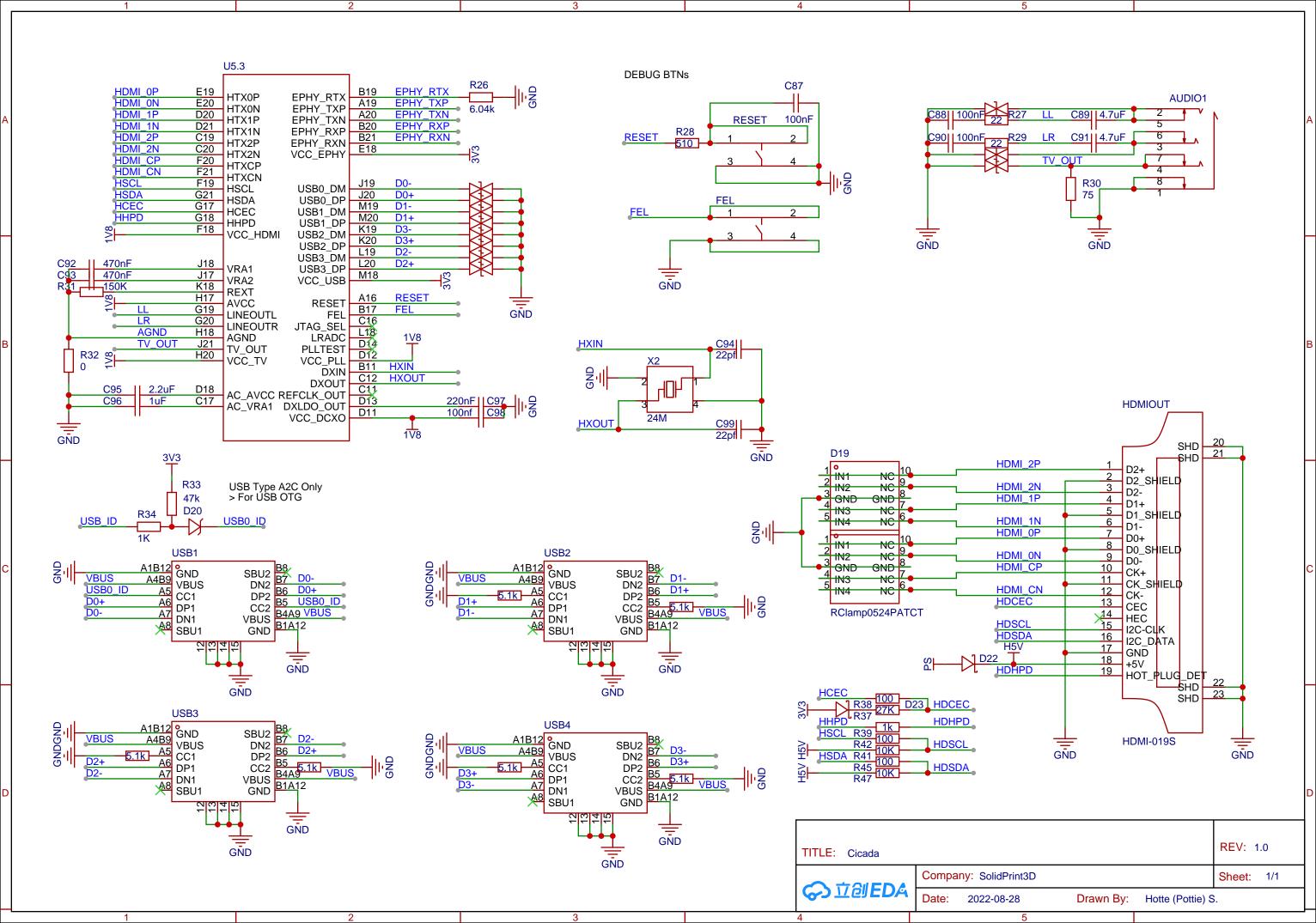
G4 PF0/SDC0_D1/JTAG_MS/PF_EINT0 PF1/SDC0_D0/JTAG_DI/PF_EINT1 PI8/RGMII_TXD2/RMII_NULL/UART2_CTS/TS0_DVLD/TWI1_SDA/PI_EINT8 -PF2/SDC0_CLK/UART0_TX/PF_EINT2 PF3/SDC0_CMD/JTAG_DO/PF_EINT3 33 PI10/RGMII_TXD0/RMII_IXD0/UART3_RX/ISU_DI/IWIZ_SDA/II_LINTIO K4 PI11
PI11/RGMII_TXCK/RMII_TXCK/UART3_RTS/TS0_D2/PWM1/PI_EINT11 K5 PI12
PI12/RGMII_TXCTL/RMII_TXEN/UART3_CTS/TS0_D3/PWM2/PI_EINT12 E5 PI13
PI13/RGMII_CLKIN/RMII_NULL/UART4_TX/TS0_D4/PWM3/PI_EINT13 L6 PI14 GND GND PF3/SDC0_D3/UART0_RX/PF_EINT4 PF5/SDC0_D2/JTAG_CK/PF_EINT5 K2 PF6/PF_EINT6 PI14/MDC/UART4_RX/TS0_D5/PWM4/PI_EINT14 L4 PI15
PI15/MDIO/UART4_RTS/TS0_D6/CLK_FANOUT0/PI_EINT15 K6 PI16
PI16/EPHY_25M/UART4_CTS/TS0_D7/CLK_FANOUT1/PI_EINT16 VCC_PI SDC1_CMD C7 PG0/SDC1_CLK/PG_EINT0 PG1/SDC1_CMD/PG_EINT1 33 C6 PG2/SDC1_D0/PG_EINT2 DAT2 DAT1 PG3/SDC1_D1/PG_EINT3 DAT0 CD/DAT3 C18 PMU_SCK PL0/RSB_SCK/S_TWI0_SCK PG4/SDC1_D2/PG_EINT4 CMD VSS B18 PMU_SDA SDC1_D3 B7
UART1_TX A4
UART1_RX B4
UART1_RTS C4
UART1_RTS C4
UART1_RTS C4 PL1/RSB_SDA/S_TWI0_SDA VDD CLK C4 PG8/UART1_RTS/PLL_LOCK_DBG/JTAG_DO/PG_EINT8 PG9/UART1_CTS/JTAG_DI/AC_ADCY/PG_EINT9 PG9/UART1_CTS/JIAG_DI/AC_ADCT/PG_EINT9
PG10/H_I2S2_MCLK/X32KFOUT/AC_MCLK/PG_EINT10
PG11/H_I2S2_BCLK/BIST_RESULT0/AC_SYNC/PG_EINT11
PG12/H_I2S2_LRCK/BIST_RESULT1/AC_ADCL/PG_EINT12
PG13/H_I2S2_DOUT0/H_I2S2_DIN1/BIST_RESULT2/AC_ADCR/PG_EINT13

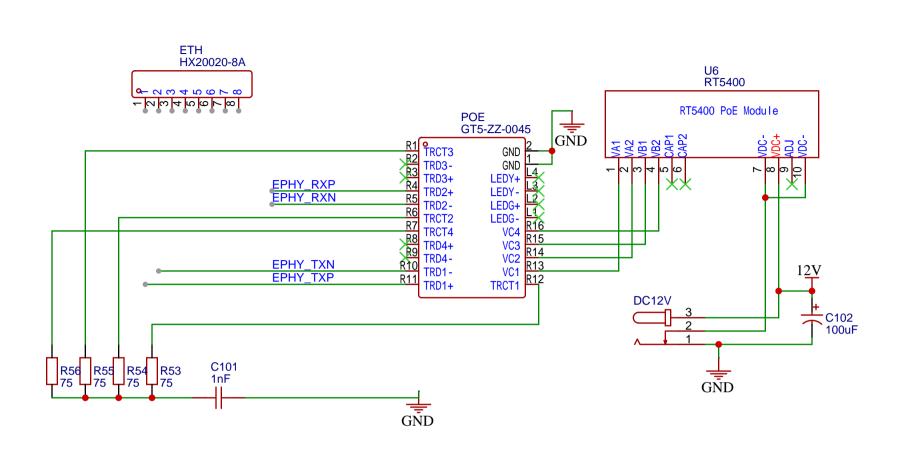
B9
PG9/UART1_CTS/JIAG_DI/AC_ADCT/PG_EINT10
PG10/H_I2S2_MCLK/X32KFOUT/AC_MCLK/PG_EINT11
PG11/H_I2S2_BCLK/BIST_RESULT1/AC_ADCL/PG_EINT13
PG13/H_I2S2_DOUT0/H_I2S2_DIN1/BIST_RESULT2/AC_ADCR/PG_EINT13 R16 47K PG14/H_I2S2_DIN0/H_I2S2_DOUT1/BIST_RESULT3/AC_ADCX/PG_EINT14 PG15/UART2_TX/TWI4_SCK/PG_EINT15 PG16/UART2_RX/TWI4_SDA/PG_EINT16 3V3 PG17/UART2_RTS/MCSI_SCK/TWI3_SCK/PG_EINT17 USB_ID PG18/UART2_CTS/MCSI_SDA/TWI3_SDA/PG_EINT18 = GND PG19/MCSI_MCLK/PWM1/PG_EINT19 GND VCC_PG **GND** GND R17 47K LPCLK _SIG ANT **LPCLK** VDD_IO C66 WHXI C67 U4 XTAL2 NC NC NC XTAL1 B6 SDC2_D7 B5 SDC2_D6 B4 SDC2_D5 VBAT1 R19—— 47K BT_WKUP_HOST VSSQ C68 1uF DAT7 N5 VSSQ N2 VSSQ C4 VSSQ VBAT2 PCM_CLK DAT6 PCM_DIN DAT5
DAT4
DAT4
DAT3
DAT3
DAT2
DAT2 GND GND C71||1uF 를 GND VDD33_PA C70 1uF C73 1uF C72 1uF PCM DOUT J5 A6 K8 H10 G5 E7 VDD12_CLK VSS PCM_SYNC R20 47K VDD12_DIG VSS BT_HOST_WKUP A4 SDC2_D1 VDD_RTC VSS DAT1 BT_RSTN VDD25_EF A3 SDC2_D0 UART1_RX VSS DAT0 UART TX JART1_TX VSS UART_RX LDO_SE VSS JART1 RT C6 P5 **UART CTS** C74 1uF **VCCQ** VDD_SENSE UART_RTS **VCCQ** R21 47K SDC1_CMD VDD_VLX VDDI C2 C78 1uF C79 1uF C84 1uF SDIO CMD SDIO_D0 VDD14 IX RFU DS H5 SDC2 DS K5 SDC2 RST M5 SDC2 CMD M6 SDC2 CLK 47K SDC1_D1 22pf 14 SDIO_D0 17 SDIO_D1 15 SDIO_D2 VDD14_RX 47K SDC1_D2 47K SDC1_D3 VDD14_DIG C82 C83 15 SDIO_D3 2.2uF | 2.2uF TEN SDC1_CLK 16 SDIO_CLK WL_REG_ON AP_REG_ON ≝ WL_RSTN EPAD _ GND 35 WIRQ CLKREQO 18 GND WHXO R51 47K XR829 R50 47K ___C86 ij GND 100nF 를 GND TITLE: REV: 1.0 Cicada Company: SolidPrint3D Sheet: 1/1 ₩立创EDA

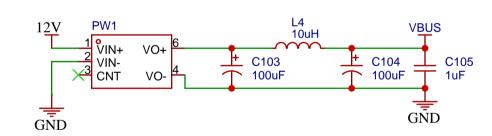
Drawn By:

Hotte (Pottie) S.

2022-08-28







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