```c
 1  /*
 2   * uart1.h
 3   *
 4   * Created: 30.1.2017 13:57:03
 5   *  Author: atom2
 6   */
 7
 8
 9  #ifndef UART1_H_
10  #define UART1_H_
11
12
13  #define UART1_BUFFER
14  #define UART1_DEFAULT_BAUD      9600
15  #define DEFAULT_TIMEOUT      5
16
17
18  #define UART1_UDR        UDR1
19  #define UART1_UCSRC      UCSR1C
20  #define UART1_UCSRB      UCSR1B
21  #define UART1_UCSRA      UCSR1A
22  #define UART1_UBRRL      UBRR1L
23  #define UART1_UBRRH      UBRR1H
24  #define UART1_RXIE       RXCIE1
25  #define UART1_TXIE       TXCIE1
26  #define UART1_RX_vect    USART1_RX_vect
27  #define UART1_TX_vect    USART1_TX_vect
28  #define UART1_RXEN       RXEN1
29  #define UART1_TXEN       TXEN1
30  #define UART1_UDRE       UDRE1
31
32
33  #define UART1_UCSZ10     UCSZ10
34  #define UART1_UCSZ11     UCSZ11
35  #define UART1_UCSZ12     UCSZ12
36
37
38
39
40  #endif /* UART1_H_ */
```