

## GTU CSE 312 / 504 Quiz

Std Code: 128

Thrashing is one of the problems that we can see on our memory systems. Due to it, may cause tons of page faults. And page replacement is a thing for disk to Main Memory; it is a disaster. Working set is a local address of technique. So each process has own space to use at the end. For thrashing process needs for memory space because it uses more different pages frequently and for that control between  $\delta$  and age according to lectures books returns page replacement. For instance thrashing process uses 256 page while it has only 128 on his address space. So first it travels whole space to turn 1's to 0 then replace and it is problem because we travel for once for nothing and resets R bit and it may cause misunderstanding.

Firstly, software assisted LRU is selecting least recently used page with assists of software with techniques of pseudo bits and alarm signals of combination. On the other hand, hardware assisted LRU also tries to find least recently used page with combination of muxes and timing mechanism of hardware dedicated components. Software assisted LRU is easy to implement and it makes it without hardware structure so it might be solvable after build, for hardware assisted LRU it is more complex and for inefficiency or wrong implementation costs whole system.