EEPROM MEMORY FW (PSoC 5 firmware - SoftHand Pro and Generic)

	BYTE 1	BYTE 2	BYTE 3	BYTE 4	BYTE 5	BYTE 6	BYTE 7	BYTE 8	BYTE 9	BYTE 10	BYTE 11	BYTE 12	BYTE 13	BYTE 14	BYTE 15	BYTE 16	STRUCT NAME
1	FLAG	UNUSED BYTES (15)															
2	EMG COUNTER 1				EMG COUNTER 2				POSITION HISTOGRAM[0]				POSITION HISTOGRAM[1]				CNT
3	POSITION HISTOGRAM[2]				POSITION HISTOGRAM[3]				POSITION HISTOGRAM[4]				POSITION HISTOGRAM[5]				
4	POSITION HISTOGRAM[6]				POSITION HISTOGRAM[7]				POSITION HISTOGRAM[8]				POSITION HISTOGRAM[9]				
5	CURRENT HISTOGRAM[0]				CURRENT HISTOGRAM[1]				CURRENT HISTOGRAM[2]				CURRENT HISTOGRAM[3]				
6	REST COUNTER				WIRE DISP				TIME ON				TIME REST				
7	UNUSED BYTES 1 (16)																
8	UNUSED BYTES 1 (16)																
9	UNUSED BYTES 1 (16)																
10	UNUSED BYTES 1 (16)																
11	ID	HW MAINT. DAY	HW MAINT. MONTH	HW MAINT. YEAR	STATS PERIOD BEGIN DAY	STATS PERIOD BEGIN MONTH	STATS PERIOD BEGIN YEAR	RIGHT LEFT	RESET COUNTERS	USE 2ND MOTOR	BAUDRATE	USER ID	DEV TYPE				DEV
12	KP				KI				KD				KP_C				MOTOR[0]
13	KI_C				KD_C				KP_DL				KI_DL				
14	KD_DL				KP_CDL				KI_CDL				KD_CDL				
15	ACTIV	PWM RESCALING	DRIVER MOTOR TYPE	POSITION LIMIT FLAG	POS_LIM_INF				POS_LIM_SUP				MAX STEP NEG				
16	MAX STEP POS				CURRENT LOOKUP_0				CURRENT LOOKUP_1				CURRENT LOOKUP_2				
17	CURRENT LOOKUP_3				CURRENT LOOKUP_4				CURRENT LOOKUP_5				CURR_LIMIT		INPUT	CONTR	
18	ENCODER LINE	PWM RATE LIMITER	NOT REVERS. FLAG														
19	KP				KI				KD				KP_C				MOTOR[1]
20	KI_C				KD_C				KP_DL				KI_DL				
21	KD_DL				KP_CDL				KI_CDL				KD_CDL				
22	ACTIV	PWM RESCALING	DRIVER MOTOR TYPE	POSITION LIMIT FLAG	POS_LIM_INF				POS_LIM_SUP				MAX STEP NEG				
23	MAX STEP POS				CURRENT LOOKUP_0				CURRENT LOOKUP_1				CURRENT LOOKUP_2				
24	CURRENT LOOKUP_3				CURRENT LOOKUP_4				CURRENT LOOKUP_5				CURR_LIMIT		INPUT	CONTR	
25	ENCODER LINE	PWM RATE LIMITER	NOT REVERS. FLAG														
26	ENCODER RAW READ CONF FLAGS (5)				RESOLUTION (3)				OFFSET 0				OFFSET 1				ENC[0]
27	OFFSET 2				MULTIPLIER 0				MULTIPLIER 1				MULTIPLIER 2				
28	DOUBLE ENCODER	HANDLE RATIO	ENCODER IDX USE FOR CONTROL			GEARS PARAM N1	GEARS PARAM N2	GEARS PARAM I1									
29	ENCODER RAW READ CONF FLAGS (5)				RESOLUTION (3)				OFFSET 0				OFFSET 1				ENC[1]
30	OFFSET 2				MULTIPLIER 0				MULTIPLIER 1				MULTIPLIER 2				
31	DOUBLE ENCODER	HANDLE RATIO	ENCODER IDX USE FOR CONTROL			GEARS PARAM N1	GEARS PARAM N2	GEARS PARAM I1									
32	EMG THRESHOLD 1		EMG THRESHOLD 2		EMG MAX VALUE 1				EMG MAX VALUE 2				EMG SPEED	EMG CALIB. STARTUP	SWITCH EMG		EMG
33	READ IMU FLAG	SPI READ DELAY	IMU CONF FLAGS (5) - IMU 0						IMU CONF FLAGS (5) - IMU 1				IMU CONF FLAGS (4) - IMU 2				IMU
34	IMU FLAG - IMU 2 (1)	IMU CONF FLAGS (5) - IMU 3							IMU CONF FLAGS (5) - IMU 4								
35	CHECKED TIME (6)						READ EXP PORT FLAG	READ ADC SENS PORT	ADC CONF FLAGS (12) - CHANNEL [0,7]								EXP
36	ADC CONF FLAGS (12) - CHANNEL [8,11]																
37	USER CODE STRING								USER EMG STRUCT								USER[0]
38	USER EMG STRUCT																
39	USER CODE STRING								USER EMG STRUCT								USER[1]
40	USER EMG STRUCT																
41	USER CODE STRING								USER EMG STRUCT								USER[2]
42	USER EMG STRUCT																
43	REST POSITION				REST POSITION DELAY				REST POSITION VEL				REST POSITION				SH
44																	

Each struct size is multiple of 16 bytes (EEPROM row length)