

# SIMONE CERVINI

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I am a recent graduate in Computer Science Engineering with a versatile background ranging from web application development to hardware design. I've developed software projects using Java, C, and JavaScript (React), and I've gained practical experience designing digital systems on FPGAs using VHDL.

**SKILLS** | VHDL, Java, React, JavaScript, C, Python, SQL, Thymeleaf, HTML, CSS

## WORK EXPERIENCE

**CUSTOMER CARE NEXTERIA S.R.L**  
SEPTEMBER 2022 – JANUARY 2023

Customer support through request analysis and problem-solving activities to resolve anomalies.

**MATHS PROFESSOR IIS FALCONE-RIGHI**  
JANUARY 2022 – MAY 2022

Organization, planning and delivery of teaching for five classes at a high school.

## EDUCATION

**POLITECNICO DI MILANO, CAMPUS LEONARDO**  
BACHELOR'S DEGREE IN COMPUTER ENGINEERING

Among the courses covered: Logic Networks, Databases, Algorithms, Software Engineering, Transmission Devices, Computer Architecture and Operating Systems, Electronics and Electrical Engineering.

**IIS FALCONE-RIGHI, CORSICO**  
HIGH SCHOOL DIPLOMA IN COMPUTER SCIENCE

## PROJECTS

**DIFFERENTIAL FILTER** in VHDL

Design of a complex synthesizable component on FPGA, description in VHDL and creation of test benches for testing.

**VIRTUAL TTRPG** in JavaScript (React)

Implementation of a web solution to support online role-playing, with image sharing and real-time icon movement.

**DOWNLOAD ORGANIZER** in Python

Development of a Windows tool in Python that reorganizes the Download folder in the background according to some customizable rules.

**ERYANTIS** in Java

Team design and development of the digital version of the board game Eryantis.

**WEB GROUP MANAGER** in Java and JavaScript

Development of a web workgroup manager, implementing both front-end and back-end features. The website was developed in two versions to cover different possibilities.

## LANGUAGES

Italian | English (fluent) | Spanish (basic – reading) | French (basic – reading)