Control bits								Interrupt flags						
	P_SEL	DEST_OP	X_OP	Y_OP	START	EXP/M		RESET		READY	MEM_ ERR	FIFO_ FULL	FIFO_ ERR	
Π	31 30	29 28	27 26	25 24	23	22	21	20	19-16	15	14	13	12	11-0