LCD MODULE

MODULE NO.:

GM12864-072C SERIES

Customer:	
Approved by:	

RECORDS OF REVISION

Part Number	Revision	Revision Content	Revised on
GM12864-072C	00	First issue	June. 20th, 2016

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1. FUNCTIONS & FEATURES

• LCD TYPE:

MODULE MODEL	LCD TYPE	REMARK
GMG12864-072C	FSTN Transmissive Negative Mode	-

Driving Scheme : 1/65 Duty, 1/9Bias

Viewing direction : 6 O'clock
 Drive IC : ST7567S
 Power Supply Voltage : 3.0V
 V_{LCD} : 9.00V
 Interface : 4-SPI

RoSH Compliant

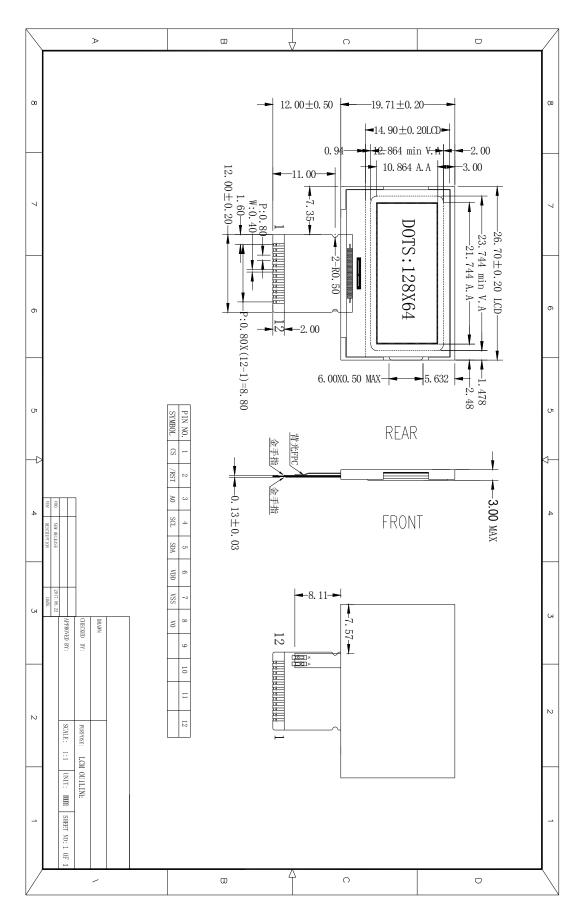
2. MECHANICAL SPECIFICATIONS

• Module Size : 26.70 (L) x19.71 (W) x3.00Max (T) mm

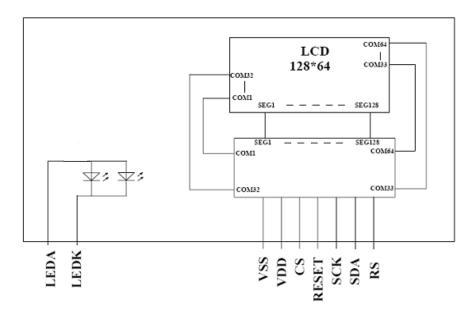
Viewing Area
 23.744(L) x 12.864 (W) mm
 Active Area
 21.744 (L) x 10.864 (W) mm
 Dot Pitch
 0.17 (W) x 0.17 (H) mm
 Dot Size
 0.154(W) x 0.154 (H) mm

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3. EXTERNAL DIMENSIONS (♥☐ unit: mm)



4. BLOCK DIAGRAM



5. PIN ASSIGNMENT

PIN	SYMBOL	Descriptions
1	CS	Chip Select. Chip is selected when CS0 = "L".
2	RST	When RST="L", all control registers are re-initialized by their default states. Since UC1701x has built-in Power-On Reset and Software Reset command, RST pin is not required for proper chip operation. An RC Filter has been included on-chip. There is no need for external RC noise filter. When RST is not used, connect the pin to VDD.
3	CD	Select Control data or Display data for read/write operation. "L": Control data "H": Display data
4	SCK	Serial clock input.
5	SDA	Serial data input.
6	VDD	This is a voltage supply pin. It must be connected to external source.
7	GND	Ground of Logic Circuit
8	V0	V0 is the LCD driving voltage for common circuits at negative
9	XV0	frame. XV0 is the LCD driving voltage for common circuits at positive frame. XV0in is the V0 input of common circuits.
10	VG	VG is the LCD driving voltage for segment circuits.
11	LEDA	Backlight Anode
12	LEDK	Backlight Cathode

6. ABSOLUTE MAXIMUM RATINGS

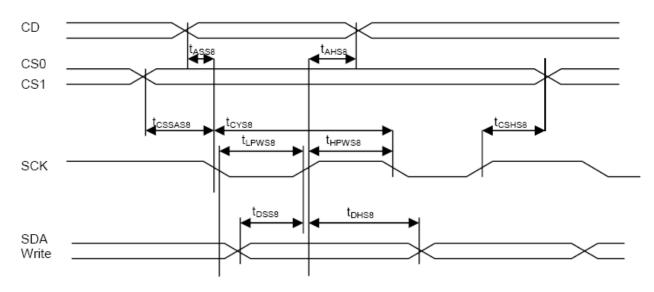
PARAMETER	SYMBOL	RATING	TEST CONDITIONS	UNIT
Supply voltage for logic	$V_{ m DD}$	-0.3~+7.0		V
LCD drive voltage	V_{OP}	13(MAX)	V _{OP} =V _{LCD} -VSS	V
Input voltage	V_{IN}	-0.3 to V _{DD} +0.3		V
Operating temperature range	T_{OPR}	-20 to 70	Ta=25℃	$^{\circ}$
Storage temperature range	T_{STR}	-30 to 80	Ta=25℃	$^{\circ}$

7. ELECTRICAL CHARACTERISTICS

7.1. DC CHARACTERISICS

ITEM	gymbal	STANI	DARD VA	ALUE	TEST CONDITION	TINITE
I I ENI	symbol	MIN	TYP	MAX	TEST CONDITION	UNIT
Supply Voltage For Logic	V_{DD}	2.7	3.0	3.3		V
LCD Bias Voltage	V _{OP}	8.80	9.00	9.20	$V_{OP}=V_{LCD}-V_{SS}$] '
Current Consumption	I_{DD}			0.75	V _{DD} =3.0V±5%	mA

7.2. AC CHARACTERISTICS



Symbol	Signal	Descri	ption	Condition	Min.	Max.	Units
t _{ASS8}	CD	Address	setup time		0		nS
t _{AHS8}	CD	Address	hold time		0		110
t _{CSSAS8}	CS1/CS0	Chip select	setup time		5	_	nS
t _{CSHS8}	C31/C30	Criip select	hold time		5	_	113
t _{CYS8}		Cycle time	read		100	_	nS
ICYS8		Cycle time	write		30	_	113
t	sck	Low pulse	read		50	_	nS
t _{LPWS8}	SCK	width	write		15		113
t		High pulse	read		50		nS
t _{HPWS8}		width	write		15	_	113
t _{DSS8}	SDA	Data	setup time		12		nS
t _{DHS8}	SDA	Dala	hold time		0	_	113

8. COMMANDS

		R/W	COMMAND BYTE									
INSTRUCTION	A0	(RWR)	D7	D6	D5	D4	D3	D2	D1	D0	DESCRIPTION	
(1) Display ON/OFF	0	0	1	0	1	0	1	1	1	D	D=1, display ON D=0, display OFF	
(2) Set Start Line	0	0	0	1	S5	S4	S3	S2	S1	S0	Set display start line	
(3) Set Page Address	0	0	1	0	1	1	Y3	Y2	Y1	Y0	Set page address	
(4)Set Column Address	0	0	0	0	0	1	X7	Хв	X5	X4	Set column address (MSB)	
(4)Oct Column Address	0	0	0	0	0	0	ХЗ	X2	X1	X0	Set column address (LSB)	
(5) Read Status	0	1	0	MX	D	RST	0	0	0	0	Read IC Status	
(6) Write Data	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write display data to RAM	
(7) Read Data	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read display data from RAM	
(8) SEG Direction	0	0	1	0	1	0	0	0	0	MX	Set scan direction of SEG MX=1, reverse direction MX=0, normal direction	
(9) Inverse Display	0	0	1	0	1	0	0	1	1	INV	INV =1, inverse display INV =0, normal display	
(10) All Pixel ON	0	0	1	0	1	0	0	1	0	AP	AP=1, set all pixel ON AP=0, normal display	
(11) Bias Select	0	0	1	0	1	0	0	0	1	BS	Select bias setting 0=1/9; 1=1/7 (at 1/65 duty)	
(12) Read-modify-Write	0	0	1	1	1	0	0	0	0	0	Column address increment: Read:+0 , Write:+1	
(13) END	0	0	1	1	1	0	1	1	1	0	Exit Read-modify-Write mode	
(14) RESET	0	0	1	1	1	0	0	0	1	0	Software reset	
(15) COM Direction	0	0	1	1	0	0	MY	-	-	-	Set output direction of COM MY=1, reverse direction MY=0, normal direction	
(16) Power Control	0	0	0	0	1	0	1	VB	VR	VF	Control built-in power circuit ON/OFF	
(17) Regulation Ratio	0	0	0	0	1	0	0	RR2	RR1	RR0	Select regulation resistor ratio	
(18) Set EV	0	0	1	0	0	0	0	0	0	1	Double command!! Set	
(10) Set EV	0	0	0	0	EV5	EV4	EV3	EV2	EV1	EV0	electronic volume (EV) level	
	0	0	1	1	1	1	1	0	0	0	Double command!!	
(19) Set Booster	0	0	0	0	0	0	0	0	0	BL	Set booster level: BL=0: 4X BL=1: 5X	
(20) Power Save	0	0			Cor	mpound	Comm	and			Display OFF + All Pixel ON	
(21) NOP	0	0	1	1	1	0	0	0	1	1	No operation	
(22)SPI Read Status	0	×	1	1	1	1	1	1	0	0	SPI read status command	
(22)SET Read Status	0	*	0	MX	D	RST	ID3	ID2	ID1	ID0	SPI read status	
(23)SPI Read DDRAM	0	×	1	1	1	1	1	1	0	1	SPI read DDRAM command	
(23)3FT Read DURAM	1	×	D7	D6	D5	D4	D3	D2	D1	D0	SPI read DDRAM	
(24) VI CD E-1	0	0	1	1	1	1	1	1	1	1		
(24) VLCD Enhance	0	0	0	1	1	0	1	0	1	1	VLCD Enhance ON	
	0	0	1	1	1	1	1	1	1	0		

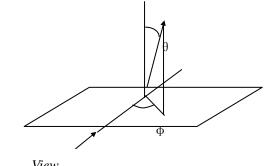
Note: Symbol "-" means this bit can be "H" or "L".

9. LCD PANEL OPTICAL CHARACTERISTICS

9.1 OPTICAL CHARACTERISTICS

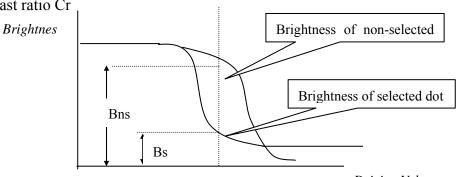
ITEM	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS		
		ф=0°С		10	15	20			
Viewing anala	θ	ф=180°С	Ta=25 ℃	15	35	45	Dag		
Viewing angle	U	φ=90℃	Cr≥2.0	20	25	30	Deg		
		φ=270°C		φ=270°C		20	25	30	
Contrast Ratio	Cr(MAX)	$\theta = 0$ and Ta	3	4	4.4				
Degrange time	T_R	Ta=25°C		200	217.649	250			
Response time	T_{F}	Ta=25°C	2	250	276.527	300	ms		

9.2 θ and φ



- The contrast of the display is optimal when viewed in the "View Direction" ($\Phi = 0$ °)
- $0^{\circ} \leqslant \theta \leqslant 90^{\circ}$, $0^{\circ} \leqslant \phi \leqslant 360^{\circ}$

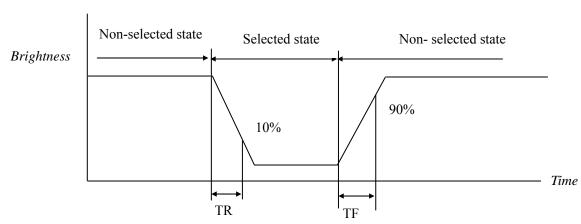
9.3 Contrast ratio Cr



Driving Voltage

Contrast Ratio: Cr= Bns/Bs

9.4 Response times T_R and T_F



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10. MODULE ACCEPT QUALITY LEVEL (AQL)

10.1 AQL Standard Value: Critical Defect =0.1, Major Defect=0.65; Minor Defect =2.5.

10.2 Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II

11. RELIABILITY TEST.

Operating life time: 50,000 hours

(at room temperature without direct irradiation of sunlight)

Reliability characteristics shall meet following requirements.

Test Item	Test Condition
High temperature storage	+80°C x 96HR
Low temperature storage	-30°C x 96HR
High temperature operation	+70°C x 96HR
Low temperature operation	-20°C x 96HR
High temperature, High humidity	+60℃ x 90%RH x 96HR
Thermal shock	$-20^{\circ}C \times 30\min \rightarrow 25^{\circ}C \times 10s \rightarrow +70^{\circ}C \times 30\min$ x 5 Cycles
Vibration test	Frequency x Swing x Time
violation test	40Hz x 4mm x 4hrs
Drop test	Height x no. of drop
Drop test	1.0m x 6 drops

12. LCD MODULES HANDLING PRECAUTIONS

- Please remove the protection foil of polarizer before using.
- The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - -Be sure to ground the body when handling the LCD module.
 - -Tools required for assembly, such as soldering irons, must be properly grounded.
 - -To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - -The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

■ Storage precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0 $^{\circ}$ C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

13. OTHERS

- Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
 - Exposed area of the printed circuit board
 - Terminal electrode sections