

RESEARCH ARTICLE

Optimal Multi-Bit Toffoli Gate Synthesis

YOUNG-MIN JUN¹, (Member, IEEE), AND IN-CHAN CHOI¹, (Member, IEEE)

Quantum Machine Learning Laboratory, School of Industrial Management Engineering, Korea University, Seoul 02841, South Korea

Corresponding author: In-Chan Choi (ichoi@korea.ac.kr)

This research was supported by the Basic Research Program through the National Research Foundation of Korea (NRF) funded by the Ministry of Science and ICT (MSIT). (No. 2020R1A4A307986411) This research was also supported by the NRF grant funded by the Korea Government (MSIT). (No. 2017R1E1A1A0307098814)

ABSTRACT Multi-bit Toffoli gates form an essential quantum gate class for quantum algorithms. They should be efficiently decomposed into elementary single- or multi-qubit quantum gates, such as CNOT, T, and Hadamard, for a scalable implementation of a quantum algorithm. We propose an engineering method for the practical synthesis of a multi-bit Toffoli gate. Two optimization models and their closed-form solutions are presented for optimal decomposition of the multi-bit Toffoli gate. These models are based on linearized multi-objective integer programming with parameters such as the number of target ancillae, ancillae states, and basis gates. The proposed method supports the systematic handling of quantum circuit constraints, including the total number of available qubits and maximum circuit depth, which depend on various quantum hardware specifications. Our approach exhibits promise in the noisy intermediate-scale quantum environment by providing a rapid and optimal method for synthesizing multi-bit Toffoli gates in diverse and unpredictable quantum hardware specifications.

INDEX TERMS Combinatorial optimization, NISQ, quantum algorithm, Toffoli gate synthesis.

I. INTRODUCTION

Quantum computing is an emerging research area that has achieved promising results in various fields. Successful quantum algorithms, including Grover's [1] and Shor's [2] algorithms, theoretically provide quadratic or exponential acceleration in the computation time compared to classical algorithms. Quantum algorithms require a quantum circuit implementation [3]. The circuit of a quantum algorithm is composed of quantum gates (single-/two-qubit or qudit/qutrit gates) and is evaluated by certain criteria, including the number of two-qubit gates, circuit depth, or number of ancilla qubits.

The multi-bit Toffoli gate is one of the famous multi-qubit gates [4], [5], [6] that are essential for complex quantum algorithm implementation [1], [2], [7], [8], [9] and quantum error/fault tolerance studies [10], [11], [12], [13]. In the implementation of the Toffoli gate (controlled-controlled-NOT), it has been reported that six CNOT gates are required for optimal cost implementation [14] and that five two-qubit gates, including non-CNOT gates, are required as a minimum resource [15], [16]. The extended N -bit Toffoli gate

uses $N - 1$ control qubits to invert the quantum state of a target qubit when the conditions of the control qubits are met [14], [17]. In the implementation of the N -bit Toffoli gate, the number of two-qubit gates, which is a commonly used cost measure, generally increases, and subsequently, the quantum algorithm performance deteriorates owing to the low computing fidelity and limited coherence time that are inherent in quantum computing systems [18]. Several decomposition methods for the N -bit Toffoli gate have been proposed [19], [20], [21], [22], [23] to mitigate the performance deterioration.

The implementation of the multi-bit Toffoli gate is not unique; it varies in single- and multi-qubit gates in terms of the gate library that it uses and the number and states of ancillary qubits. For an N -qubit universal gate without ancillae, the theoretical lower bound in the number of two-qubit gates has been demonstrated to be $(4^N - 3N - 1)/4$ [24]. Several studies have been conducted on efficient circuit implementation [18] to approach the theoretical result. The methods proposed in [25], [26], [27], [28], and [29] decompose the multi-bit Toffoli gate into single-/two-qubit gates using ancilla qubits, whereas those presented in [18], [30], [31], [32], [33], [34], [35], and [36] use the quantum gates of higher-dimensional Hilbert space, such as qudit/qutrit gates.

The associate editor coordinating the review of this manuscript and approving it for publication was Christos Anagnostopoulos¹.

Our study focuses on the multi-bit Toffoli gate decomposition method with ancillary qubits, which is applicable to currently accessible quantum hardware. In a previous study [20], a framework that uses a single ancilla or $N - 3$ ancillae was introduced to decompose the N -bit Toffoli gate into single-qubit gates and CNOT gates. Moreover, Maslov [26] employed the relative-phase Toffoli gates to reduce the decomposition circuit depth and number of ancillae to $\lceil (N - 3)/2 \rceil$, where $\lceil \xi \rceil$ denotes the smallest integer that is greater than or equal to ξ . The best-known N -bit Toffoli decomposition method that was presented in [27] requires $N - 1$ ancillae, $O(\log N)$ depth, and $N - 3$ measurement operations. All previous studies considered only a fixed number of ancilla qubits and had a single objective to minimize, e.g., the T-depth [21], [29], [37], [38], [39]. Therefore, it is difficult to deal with the transitions in quantum hardware specifications, viz. varying quantum costs [40].

The objective of this study is to develop an efficient scheme to synthesize the multi-bit Toffoli gate with the minimum total quantum resource usage, while accommodating the varying input requirements. Studies that address such synthesis involving flexible user-specified input requirements are rare. An N -bit Toffoli gate implementation method with an arbitrary number of ancilla qubits has previously been presented [28]. Our study proposes an optimal N -bit Toffoli gate implementation that minimizes the weighted quantum cost with parameters including the targeting ancillae number, ancillae states, gate library, and building block gates. Our method decomposes the N -bit Toffoli gate into scalable multi-bit building block gates using M determined/arbitrary state ancillae. Moreover, we consider scalable relative-phase Toffoli gates as an instance of a building block that employs the Clifford+T gate library, and derive the optimal face and closed-form solutions of the corresponding model.

The contribution of this study is threefold. First, we present a new optimal N -bit Toffoli gate implementation method that can handle user-specified parameters, such as the state and number of ancillae, gate library class, gate quantum cost, and weight of each gate. Second, we propose a scalable multi-bit relative-phase Toffoli gate implementation that does not require additional ancilla qubit resources. Third, our study provides a rigorous analysis on the optimal face and closed-form solutions to the proposed mathematical models. Our approach exhibits potential usefulness in the Noisy Intermediate Scale Quantum (NISQ) era, in which the rapid changes in quantum hardware and software make parameters such as the quantum cost of the gate library unpredictable. The optimal models based on linearized multi-objective integer programming accommodate parameters such as the circuit size, gate library, quantum cost, and building blocks. As quantum hardware and software are altered, our models can substitute the new gate library, quantum cost, and building blocks for the Clifford+T library, proposed quantum cost, and relative-phase Toffoli building blocks, respectively. Moreover, our approach guarantees optimality and provides the closed-form solutions that can be used in determining

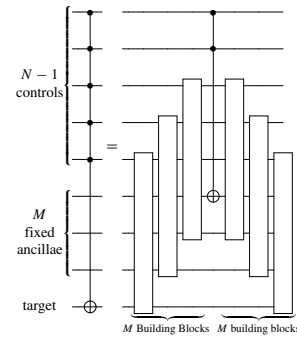
the optimal number of ancillae for the N -bit Toffoli gate implementation with respect to the quantum cost structure.

The remainder of this paper is organized as follows. In Section II, the optimal Toffoli gate implementation model for the target number and states of ancillae is presented. Section III describes the multi-bit relative-phase Toffoli gate that employs the Clifford+T library. Section IV presents the optimal face and closed-form solutions to the optimization models. In Section V, we outline our computational experiments and their results. Finally, Section VI presents the conclusions and directions for future research.

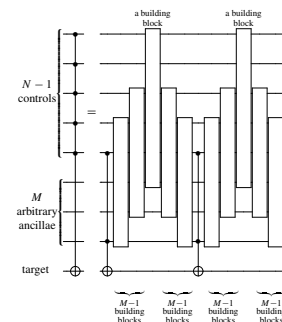
II. OPTIMAL N -BIT TOFFOLI GATE SYNTHESIS FRAMEWORK

The Toffoli gate synthesis problem (TGSP) involves determining the number of elementary building blocks to produce an N -bit Toffoli gate synthesis circuit with the minimum weighted quantum cost sum for the given parameters. We assume that the multi-bit building block gates are implementable with no ancillae. The states of the ancillae label the mathematical models of the TGSP as $\text{TGSP}_{|0\rangle}$ and $\text{TGSP}_{|X\rangle}$, which correspond to fixed and arbitrary, respectively.

First, the synthesis framework suggests the circuit structure of the $\text{TGSP}_{|0\rangle}(N, M)$ using M pairs of multi-bit building blocks and a Toffoli gate. The circuit structure is described as follows:



Thereafter, the circuit structure of the $\text{TGSP}_{|X\rangle}(N, M)$ is represented by $4M - 2$ building blocks.



The $\text{TGSP}_{|0\rangle}(N, M, \mathbb{J}, \mathbb{R}, \mathbb{P})$ and $\text{TGSP}_{|X\rangle}(N, M, \mathbb{J}, \mathbb{R}, \mathbb{P})$ are modeled as follows, in which the problems minimize the weighted sum of the quantum costs $r_{nj} \in \mathbb{R}$ and weights

$p_j \in \mathbb{P}$ of the gates $j \in \mathbb{J}$, thereby addressing the target N -bit Toffoli gate and M ancillae:

Notation	Description
N	Number of qubits for the target multi-bit Toffoli gate
M	Number of ancilla qubits
\mathbb{J}	Basis gate set (i.e., the gate library)
$j \in \mathbb{J}$	Basis gate
n	Number of qubits for the building blocks
$r_{nj} \in \mathbb{R}$	Quantum cost of basis gate j for n -bit building block
t_j	Quantum cost of basis gate j for a Toffoli gate
$p_j \in \mathbb{P}$	Proportional weight of basis gate j
x_n	Number of n -bit building block pairs/quads
$y_n \in \mathbb{B}$	Indicator variable identifying whether the n -bit building block has a pair containing the top of the control qubits

TGSP_[0]($N, M, \mathbb{J}, \mathbb{R}, \mathbb{P}$):

$$\text{Minimize } 2 \sum_{n \in \mathbb{N}} \sum_{j \in \mathbb{J}} p_j r_{nj} x_n + \sum_{j \in \mathbb{J}} p_j t_j \quad (1)$$

$$\text{subject to } \sum_{n \in \mathbb{N}} x_n = M \quad (2)$$

$$\sum_{n \in \mathbb{N}} n \cdot x_n = N + 2M - 3 \quad (3)$$

$$x_n \in \mathbb{Z}^{0+} \text{ for } n \in \mathbb{N} \quad (4)$$

TGSP_[X]($N, M, \mathbb{J}, \mathbb{R}, \mathbb{P}$):

$$\text{Minimize } \sum_{n \in \mathbb{N}} \sum_{j \in \mathbb{J}} p_j r_{nj} (4x_n - 2y_n) + 2 \sum_{j \in \mathbb{J}} p_j t_j \quad (5)$$

$$\text{subject to } \sum_{n \in \mathbb{N}} x_n = M \quad (6)$$

$$\sum_{n \in \mathbb{N}} n \cdot x_n = N + 2M - 3 \quad (7)$$

$$\sum_{n \in \mathbb{N}} y_n = 1 \quad (8)$$

$$x_n - y_n \geq 0 \text{ for } n \in \mathbb{N} \quad (9)$$

$$x_n \in \mathbb{Z}^{0+} \text{ for } n \in \mathbb{N} \quad (10)$$

$$y_n \in \mathbb{B} \text{ for } n \in \mathbb{N} \quad (11)$$

The first integer optimization model **TGSP_[0]** pertains to the determined ancillae, and the second, **TGSP_[X]**, relates to the arbitrary ancillae. In Equations (1) and (5), the parameters indicate the weights and costs of the gates in the building blocks and a Toffoli gate. The constraints in Equations (2) and (6) denote the total number of building block pairs/quads, which is the number of ancilla qubits M . Equations (3) and (7) represent the total number of qubits pairs/quads, which is the sum of N target qubits, M overlapping control qubits, M qubits that are used for ancillae, and a three-qubit for a Toffoli gate. In the **TGSP_[X]**, Equation (8) indicates that the n -bit building blocks are used twice, whereas the remaining gates are used four times.

III. BUILDING BLOCK: MUTLI-BIT RELATIVE PHASE TOFFOLI GATE SYNTHESIS

As an illustrative example of the practical implementation of the **TGSP**'s solutions, we provide an instance of the

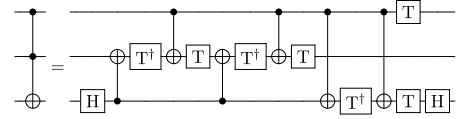


FIGURE 1. Toffoli gate realization with the Clifford+T library.

gate library and building blocks. First, we refer to previous research [26], providing a three-bit Toffoli gate implementation using the Clifford+T gate library. Next, based on the three-/four-bit Toffoli gate implementation in the same study, we present a scalable n -bit relative-phase Toffoli gate implementation without ancillae.

A. Clifford+T LIBRARY

The Clifford+T gate library is an instance of the parameter \mathbb{J} , the gate library of **TGSP**. The Clifford+T library identifies a universal logic gate with Clifford gates; NOT, CNOT, H, Z, S, and S^\dagger gates; and T and T^\dagger gates [41]. Definitions of each gate are given below.

$$\mathbb{J} = \{NOT, CNOT, H, Z, S/S^\dagger, T/T^\dagger\}$$

Gate	Circuit	Matrix
NOT or X	\oplus	$\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$
$CNOT$	$\bullet \oplus$	$\text{diag}(I, I, I, \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix})$
H	\boxed{H}	$\frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$
S	\boxed{S}	$\begin{bmatrix} 1 & 0 \\ 0 & i \end{bmatrix}$
S^\dagger	$\boxed{S^\dagger}$	$\begin{bmatrix} 1 & 0 \\ 0 & -i \end{bmatrix}$
T	\boxed{T}	$\begin{bmatrix} 1 & 0 \\ 0 & e^{\frac{i\pi}{4}} \end{bmatrix}$
T^\dagger	$\boxed{T^\dagger}$	$\begin{bmatrix} 1 & 0 \\ 0 & e^{-\frac{i\pi}{4}} \end{bmatrix}$

B. THREE-BIT TOFFOLI GATE SYNTHESIS

The matrix representation of the three-bit Toffoli gate, a building block in our analysis, is as follows:

$$TOF(3)(q_1, q_2; q_3) = \text{diag}(1, 1, 1, 1, 1, 1, \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix})$$

Among others, the three-bit Toffoli gate can be decomposed with the Clifford+T gate library [26], as shown in Figure 1. Hereafter, we refer to the number of gates as the quantum cost as an example, i.e.,

$$t_{T/T^\dagger} = 7, t_{CNOT} = 6, t_H = 2$$

C. MULTI-BIT RELATIVE PHASE TOFFOLI GATE SYNTHESIS

This section presents the n -bit relative-phase Toffoli RTOF(n) gate implementation and its quantum cost. The RTOF(n) gate

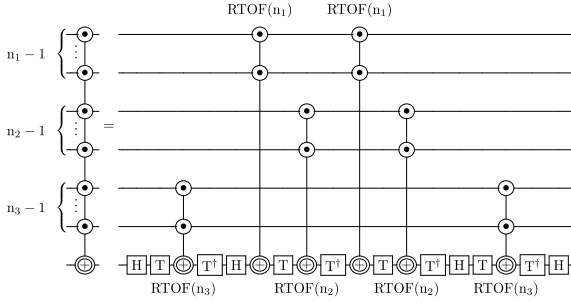
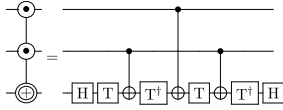
FIGURE 2. $n_1 + n_2 + n_3 - 2$ -bit relative phase Toffoli gate.

FIGURE 3. Three-bit relative-phase Toffoli gate [26].

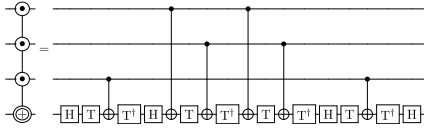


FIGURE 4. Four-bit relative-phase Toffoli gate [26].

is a relative-phase-shifted version of the n -bit Toffoli gate $\text{TOF}(n)$. The advantage of this implementation is that no auxiliary qubits are required. This advantage implements the N -bit Toffoli gate synthesis method that corresponds to an arbitrary number of auxiliary qubits.

Let I', X' be a relative phase version of I, X . Then, $\text{RTOF}(n)$ is described as follows [42]:

$$\begin{aligned} \text{RTOF}(n)(\mathbb{Q}; q_n) &= \text{diag}(c_0, \dots, c_{2^n-3}, \begin{bmatrix} 0 & c_{2^n-2} \\ c_{2^n-1} & 0 \end{bmatrix}), \\ &= \text{diag}(I'_1, \dots, I'_{n-1}, X'), \end{aligned}$$

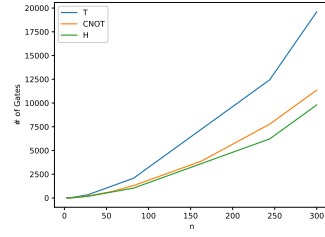
where $c_i \in \mathbb{C}^1$, $\mathbb{Q} = \{q_0, \dots, q_{n-1}\}$ and $|c_0| = \dots = |c_{2^n-1}| = 1$.

Based on this definition, we propose an $\text{RTOF}(n)$ ($n \geq 5$, $n = n_1 + n_2 + n_3 - 2$) implementation, given $\text{RTOF}(n_m)$ ($m = 1, 2, 3$) gates, to utilize $\text{RTOF}(n)$ as the building block.

The correctness of the above circuit follows from the following equations for arbitrary relative-phase gates I'_m, X'_m , $m = 1, 2, 3$ and I', X' :

$$\begin{aligned} I' &= HTI'_1T^\dagger HI'_2T^\dagger I'_3T^\dagger I'_4T^\dagger HTI'_1T^\dagger H \\ X' &= HTX'_1T^\dagger HX'_2TX'_3T^\dagger X'_4TX'_3T^\dagger HTX'_1T^\dagger H \end{aligned}$$

To implement $\text{RTOF}(n)$ ($n < 5$), we replace $\text{RTOF}(2)$ with CNOT and construct a circuit of $\text{RTOF}(3)$ and $\text{RTOF}(4)$, as shown in Figures 3 and 4.

FIGURE 5. $\text{RTOF}(n)$'s number of basis gates.

The quantum cost $\mathbf{r}_n = (r_{n,T}, r_{n,\text{CNOT}}, r_{n,H})$ of the $\text{RTOF}(n)$ circuit is defined by the recurrence relation. As n increases, two $\text{RTOF}(3 + \lfloor \frac{n-5}{3} \rfloor)$ gates replace two $\text{RTOF}(2 + \lfloor \frac{n-5}{3} \rfloor)$ gates, and the quantum cost increases exponentially (Figure 5).

$$\mathbf{r}_n = \mathbf{r}_{n-1} + 2(\mathbf{r}_{3+\lfloor \frac{n-5}{3} \rfloor} - \mathbf{r}_{2+\lfloor \frac{n-5}{3} \rfloor}),$$

where \mathbf{r}_n is $\text{RTOF}(n)$'s number of basis gates, and

$$\begin{aligned} \mathbf{r}_3 &= (4, 3, 2), \mathbf{r}_4 = (8, 6, 4), \mathbf{r}_5 = (16, 10, 8), \\ \mathbf{r}_6 &= (24, 14, 12), \mathbf{r}_7 = (32, 18, 16). \end{aligned}$$

This section presents the relative phase Toffoli gate implementation as a practical instance of the n ($< N$)-bit building block that constructs the N -bit Toffoli gate. With the proposed $\text{RTOF}(n)$, the optimal solution to the **TGSP** yields a physically implementable quantum circuit.

IV. CLOSED-FORM SOLUTIONS

We obtain the optimal face of **TGSP** and provide a closed-form solution to this problem. The following theorems and lemmas provide the optimal faces of **TGSPs**, and the corollaries of each theorem and lemma provide an optimal solution for the **TGSPs**.

Theorem (Jensen's Inequality [43]): For any convex function f , numbers m_1, m_2, \dots, m_K in its domain, and positive weights u_k ,

$$f\left(\frac{\sum u_k m_k}{\sum u_k}\right) \leq \frac{\sum u_k f(m_k)}{\sum u_k}$$

Equality holds if and only if $m_1 = m_2 = \dots = m_K$ or if f is linear in a domain containing m_1, m_2, \dots, m_K .

Lemma 1: Let $\mathcal{M}, \mathcal{N}, \mathcal{K}$ be positive integers such that $\mathcal{M} \leq \mathcal{N}$. Then, let $\mathbb{K} = \{1, \dots, \mathcal{K}\}$, $h = \lfloor \mathcal{N}/\mathcal{M} \rfloor$, and $\xi = \mathcal{N}/\mathcal{M} - \lfloor \mathcal{N}/\mathcal{M} \rfloor$.

$$\begin{aligned} (P1) \quad & \text{Minimize } \sum_{k \in \mathbb{K}} \alpha_k u_k. \\ & \text{subject to } \sum_{k \in \mathbb{K}} u_k = \mathcal{M} \\ & \sum_{k \in \mathbb{K}} k u_k = \mathcal{N} \\ & u_k \in \mathbb{Z}^{0+}. \end{aligned}$$

We assume that $(\alpha_{k+1} - \alpha_k) \geq (\alpha_k - \alpha_{k-1})$, $\alpha_k \geq 0$ for $k \in \mathbb{K} \setminus \{\mathcal{K}\}$. The optimal solutions $\mathbf{u}^* = (u_1^*, \dots, u_K^*)$ to

(P1) lie on the optimal face:

$$\left\{ \mathbf{u}^* : \sum_{k \in \mathbb{K}} u_k^* = \mathcal{M}, \right. \\ \sum_{k \in \mathbb{K}} k u_k^* = \mathcal{N}, \\ \sum_{k \in \mathbb{K}} \alpha_k u_k^* = \mathcal{M} \xi (\alpha_{h+1} - \alpha_h) + \mathcal{M} \alpha_h, \\ \left. u_k^* \in \mathbb{Z}^{0+}, k \in \mathbb{K} \right\}.$$

Proof: We show that the optimal solutions \mathbf{u}^* are feasible solutions with the lower bound value of the objective function. First, the first and second terms of the optimal face are the constraints of (P1), so \mathbf{u}^* are feasible solutions.

To obtain the lower bound value, define a function f with the same function value as the objective function at discrete positive integers. Let $f : \mathbb{R} \rightarrow \mathbb{R}$ be a piecewise linear function defined on intervals of integer numbers, where

$$f(m) = (\alpha_{k+1} - \alpha_k)(m - k) + \alpha_k \\ \text{for } m \in [k, k + 1], k \in \mathbb{K} \setminus \{\mathcal{K}\}.$$

Note that $f(m)$ is a convex function because of the assumption that $(\alpha_{k+1} - \alpha_k) \geq (\alpha_k - \alpha_{k-1})$. Because $f(k) = \alpha_k$, the objective function can be written as

$$\sum_{k \in \mathbb{K}} \alpha_k u_k = \sum_{k \in \mathbb{K}} u_k f(k).$$

We use Jensen's inequality [43] to find the lower bound of the objective function. Because $f(m)$ is a convex function, Jensen's inequality [43] states that

$$\frac{\sum_{k \in \mathbb{K}} u_k f(k)}{\sum_{k \in \mathbb{K}} u_k} \geq f\left(\frac{\sum_{k \in \mathbb{K}} u_k k}{\sum_{k \in \mathbb{K}} u_k}\right).$$

Thus, the lower bound on the objective value for all feasible solutions in (P1) can be obtained as

$$\sum_{k \in \mathbb{K}} \alpha_k u_k^* = \sum_{k \in \mathbb{K}} u_k f(k) \geq \mathcal{M} f\left(\frac{\mathcal{N}}{\mathcal{M}}\right).$$

The feasible solutions are the optimal solutions if solution \mathbf{u}^* yields the lower bound of the objective value of (P1).

$$\sum_{k \in \mathbb{K}} \alpha_k u_k^* = \mathcal{M} f\left(\frac{\mathcal{N}}{\mathcal{M}}\right)$$

Therefore, the optimal solutions to (P1) are the non-negative integer solutions to the following linear system:

$$\left\{ \begin{array}{l} \sum_{k \in \mathbb{K}} u_k = \mathcal{M} \\ \sum_{k \in \mathbb{K}} k u_k = \mathcal{N} \\ \sum_{k \in \mathbb{K}} \alpha_k u_k = \mathcal{M} f\left(\frac{\mathcal{N}}{\mathcal{M}}\right) = \mathcal{M}(\alpha_{h+1} - \alpha_h) \xi + \mathcal{M} \alpha_h \end{array} \right.$$

□

Corollary 1: A closed-form solution \mathbf{u}^* for the optimal face of (P1) is

$$\begin{aligned} u_h^* &= \mathcal{M} - \mathcal{M} \xi \\ u_{h+1}^* &= \mathcal{M} \xi \\ u_k^* &= 0 \text{ for } k \in \mathbb{K} \setminus \{h, h + 1\}. \end{aligned}$$

Proof: The optimal solutions to (P1) are the non-negative integer solutions to the following linear system:

$$\left\{ \begin{array}{l} \sum_{k \in \mathbb{K}} u_k = \mathcal{M} \\ \sum_{k \in \mathbb{K}} k u_k = \mathcal{N} \\ \sum_{k \in \mathbb{K}} \alpha_k u_k = \mathcal{M} f\left(\frac{\mathcal{N}}{\mathcal{M}}\right) \end{array} \right.$$

Initially, the solution \mathbf{u}^* is a non-negative integer solution because \mathcal{N}/\mathcal{M} is greater than or equal to h and $\mathcal{M} \xi$ is $\mathcal{N} - \mathcal{M}h$, which is an integer value.

We show that the optimal solution satisfies the first and second equations by

$$\begin{aligned} u_h^* + u_{h+1}^* &= \mathcal{M} \\ h u_h^* + (h + 1) u_{h+1}^* &= \mathcal{N}. \end{aligned}$$

The right-hand side of the third equation is

$$\begin{aligned} \mathcal{M} f\left(\frac{\mathcal{N}}{\mathcal{M}}\right) &= \alpha_h (\mathcal{M} - \mathcal{M} \xi) + \alpha_{h+1} \mathcal{M} \xi \\ &= \alpha_h u_h^* + \alpha_{h+1} u_{h+1}^*. \end{aligned}$$

The non-negative integer solution \mathbf{u}^* is the optimal solution to (P1). □

Theorem 1: The optimal solutions $\mathbf{x}^* = (x_3^*, \dots, x_{N-1}^*)$ to the $\mathbf{TGSP}_{(0)}$ lie on the optimal face.

$$\left\{ \mathbf{x}^* : \begin{array}{l} \sum_{n \in \mathbb{N}} x_n = \mathcal{M}, \\ \sum_{n \in \mathbb{N}} n x_n^* = \mathcal{N} + 2\mathcal{M}, \\ \sum_{j \in \mathbb{J}} p_j r_{nj} x_n^* = 2\mathcal{M} \xi \left(\sum_{j \in \mathbb{J}} p_j r_{h+3,j} - \sum_{j \in \mathbb{J}} p_j r_{h+2,j} \right) \\ \quad + 2\mathcal{M} \sum_{j \in \mathbb{J}} p_j r_{h+2,j}, \\ x_n^* \in \mathbb{Z}^{0+}, n \in \mathbb{N} \end{array} \right\}$$

Proof: We show that the objective function of $\mathbf{TGSP}_{(0)}$ is transformed into the objective function of Lemma 1. Let $f : \mathbb{R} \rightarrow \mathbb{R}$ be a piecewise linear function defined on intervals of integer numbers, where

$$\begin{aligned} f(m) &= 2 \left(\sum_{j \in \mathbb{J}} p_j r_{n+1,j} - \sum_{j \in \mathbb{J}} p_j r_{nj} \right) (m - n) + 2 \sum_{j \in \mathbb{J}} p_j r_{nj} \\ &\text{for } m \in [n, n + 1], n \in \mathbb{N} \setminus \{N - 1\}. \end{aligned}$$

Because the term $\sum_{j \in \mathbb{J}} t_j p_j$ in the objective of $\mathbf{TGSP}_{(0)}$ is constant, and letting $\alpha_{n-2} = 2 \sum_{j \in \mathbb{J}} p_j r_{nj}$, $k = n - 2$, $u_k := x_{k+2}$, $\mathcal{K} := N - 1$, $\mathcal{M} := \mathcal{M}$, and $\mathcal{N} := \mathcal{N} - 3$, the model can be reformulated as

$$\begin{aligned} &\text{Minimize } \sum_{k \in \mathbb{K}} \alpha_k u_k \\ &\text{subject to } \sum_{k \in \mathbb{K}} u_k = \mathcal{M} \\ &\quad \sum_{k \in \mathbb{K}} k u_k = \mathcal{N} \end{aligned}$$

Moreover,

$$(\alpha_{k+1} - \alpha_k) \geq (\alpha_k - \alpha_{k-1}),$$

because

$$(\mathbf{r}_{n+1} - \mathbf{r}_n) \geq (\mathbf{r}_n - \mathbf{r}_{n-1}) \text{ and } \sum_{j \in \mathbb{J}} p_j = 1, p_j \geq 0.$$

Furthermore, because M and $N - 3$ are positive integers with $M \leq N - 3$, Lemma 1 induces the optimal face. \square

Corollary 2: A closed-form solution on the optimal face of the **TGSP**_{|0⟩} is

$$\begin{aligned} x_{h+2}^* &= M - M\xi \\ x_{h+3}^* &= M\xi \\ x_n^* &= 0 \text{ for } n \in \mathbb{N} \setminus \{h+2, h+3\}. \end{aligned}$$

Proof: Using Corollary 1, the non-negative integer solution \mathbf{x}^* is an optimal solution to **TGSP**_{|0⟩}. \square

Lemma 2: Let the integer programming problem (P2) be formulated as

$$\begin{aligned} (P2) \quad & \text{Minimize } \sum_{k \in \mathbb{K}} \alpha_k (2u_k - v_k). \\ & \text{subject to } \sum_{k \in \mathbb{K}} u_k = \mathcal{M} \\ & \sum_{k \in \mathbb{K}} k \cdot u_k = \mathcal{N} \\ & \sum_{k \in \mathbb{K}} v_k = 1 \\ & u_k - v_k \geq 0 \quad \text{for } k \in \mathbb{K} \\ & u_k \in \mathbb{Z}^{0+} \quad \text{for } k \in \mathbb{K} \\ & v_k \in \mathbb{B} \quad \text{for } k \in \mathbb{K} \end{aligned}$$

Let k^* be an integer such that $v_{k^*} = 1$. Additionally, let l and ϕ be $l = \lfloor (\mathcal{N} - k^*)/(\mathcal{M} - 1) \rfloor$ and $\phi = (\mathcal{N} - k^*)/(\mathcal{M} - 1) - l$, respectively. Then, the optimal solutions $\mathbf{u}^*, \mathbf{v}^* = (v_1^*, \dots, v_{\mathcal{K}}^*)$ to (P2) lie on the optimal face.

$$\begin{aligned} \{ \mathbf{u}^*, \mathbf{v}^* : \sum_{k \in \mathbb{K}} u_k^* &= \mathcal{M}, \\ \sum_{k \in \mathbb{K}} k u_k^* &= \mathcal{N}, \\ \sum_{k \in \mathbb{K}} v_k^* &= 1, \\ \sum_{k \in \mathbb{K}} \alpha_k (2u_k^* - v_k^*) &= 2(\mathcal{M} - 1)(\alpha_{l+1} - \alpha_l)\phi + 2(\mathcal{M} - 1)\alpha_l + \alpha_{k^*}, \\ u_k^* \in \mathbb{Z}^{0+}, v_k^* \in \mathbb{B}, k \in \mathbb{K} \}. \end{aligned}$$

Proof: We show that the optimal solutions \mathbf{u}^* are feasible solutions with the lower bound value of the objective function. First, as the first to third terms of the optimal face are the constraints of (P2), $\mathbf{u}^*, \mathbf{v}^*$ are feasible solutions.

To obtain the lower bound value, let $f : \mathbb{R} \rightarrow \mathbb{R}$ be a piecewise linear function defined on intervals of integer numbers, where

$$\begin{aligned} f(m) &= (\alpha_{k+1} - \alpha_k)(m - k) + \alpha_k \\ &\text{for } m \in [k, k+1], k \in \mathbb{K} \setminus \{k^*\} \end{aligned}$$

Because $\alpha_k = f(k)$, the objective function can be written as

$$\sum_{k \in \mathbb{K}} \alpha_k (2u_k - v_k) = \sum_{k \in \mathbb{K} \setminus \{k^*\}} 2u_k f(k) + f(k^*)$$

As per Jensen's inequality [43], the objective function follows

$$\sum_{k \in \mathbb{K}} (2u_k - v_k) f(k) \geq 2(\mathcal{M} - 1) f\left(\frac{\mathcal{N} - k^*}{\mathcal{M} - 1}\right) + f(k^*)$$

Because the right-hand side is the lower bound of the objective function, $\mathbf{u}^*, \mathbf{v}^*$ are the optimal solutions of (P2). \square

Corollary 3: A closed-form solution $\mathbf{u}^*, \mathbf{v}^*$ on the optimal face of (P2) is

$$\begin{aligned} u_l^* &= (\mathcal{M} - 1) - (\mathcal{M} - 1)\phi, \\ u_{l+1}^* &= (\mathcal{M} - 1)\phi, \\ u_{k^*}^* &= 1, \\ v_{k^*}^* &= 1, \\ u_k^* &= 0 \text{ for } k \in \mathbb{K} \setminus \{l, l+1, k^*\}, \\ v_k^* &= 0 \text{ for } k \in \mathbb{K} \setminus \{k^*\}, \end{aligned}$$

where k^* is the solution to

$$\alpha_{k^*+1} - \alpha_{k^*} = \begin{cases} 2(\alpha_l - \alpha_{l-1}), & \text{for } k^* = \mathcal{N} - \mathcal{M}l \\ 2(\alpha_{l+1} - \alpha_l), & \text{for } k^* \neq \mathcal{N} - \mathcal{M}l \end{cases} \quad (12)$$

Proof: First, \mathbf{u}^* satisfies the first to third equations of the optimal face, which means that \mathbf{u}^* is a feasible solution.

Next, we show that the fourth equation is a function of k^* and is minimal when Eq. 12 is satisfied. Given \mathcal{M} and \mathcal{N} , the right-hand side of the fourth equation is a function of k^* .

$$2(\mathcal{M} - 1)(\alpha_{l+1} - \alpha_l)\phi + 2(\mathcal{M} - 1)\alpha_l + \alpha_{k^*} \quad (13)$$

When k^* increases to $k^* + 1$, the function 13 changes as follows:

$$\alpha_{k^*+1} - \alpha_{k^*} = \begin{cases} 2(\alpha_l - \alpha_{l-1}), & \text{for } k^* = \mathcal{N} - \mathcal{M}l \\ 2(\alpha_{l+1} - \alpha_l), & \text{otherwise} \end{cases}$$

Because $(\alpha_{k+1} - \alpha_k) \geq (\alpha_k - \alpha_{k-1})$ for $k \in \mathbb{K}$, the difference ratio of the function 13 increases monotonically. Therefore, the function 13 is minimized when k^* satisfies Eq. 12. \square

Theorem 2: Let n^* be an integer such that $y_{n^*} = 1$. The optimal solutions $\mathbf{x}^*, \mathbf{y}^* = (y_3^*, \dots, y_{N-1}^*)$ to the **TGSP**_{|X⟩}

lie on the optimal face.

$$\begin{aligned}
& \left\{ \mathbf{x}^*, \mathbf{y}^* : \sum_{n \in \mathbb{N}} x_n^* = \mathcal{M}, \right. \\
& \quad \sum_{n \in \mathbb{N}} n x_n^* = \mathcal{N} + 2\mathcal{M}, \\
& \quad \sum_{n \in \mathbb{N}} y_n^* = 1, \\
& \quad \sum_{n \in \mathbb{N}} \sum_{j \in \mathbb{J}} p_j r_{nj} (4x_n^* - 2y_n^*) \\
& \quad = 4(\mathcal{M} - 1) \left(\sum_{j \in \mathbb{J}} p_j r_{l+3,j} - \sum_{j \in \mathbb{J}} p_j r_{l+2,j} \right) \phi \\
& \quad + 4(\mathcal{M} - 1) \sum_{j \in \mathbb{J}} p_j r_{l+2,j} + 2 \sum_{j \in \mathbb{J}} p_j r_{n^*,j} \\
& \quad \left. x_n^* \in \mathbb{Z}^{0+}, y_n \in \mathbb{B}, n \in \mathbb{N} \right\}, \\
& \quad \text{where } \mathcal{N} = N - 3, \mathcal{M} = M, k^* = n^* - 2.
\end{aligned}$$

Proof: Let $f : \mathbb{R} \rightarrow \mathbb{R}$ be a piecewise linear function defined on intervals of integer numbers, where

$$\begin{aligned}
f(m) &= \left(\sum_{j \in \mathbb{J}} p_j r_{n+1,j} - \sum_{j \in \mathbb{J}} p_j r_{nj} \right) (m - n) + \sum_{j \in \mathbb{J}} p_j r_{nj} \\
& \quad \text{for } m \in [n, n+1], n \in \mathbb{N}.
\end{aligned}$$

Substituting $k := n - 2$, $u_k := x_{k+2}$, $v_k := y_{k+2}$, $\alpha_k := 2 \sum_{j \in \mathbb{J}} 2p_j r_{nj}$, $\mathcal{K} := N - 3$, $\mathcal{M} := M$, and $\mathcal{N} := N - 3$, Lemma 2 induces the optimal face of $\mathbf{TGSP}_{|\chi\rangle}$. \square

Corollary 4: A closed-form solution $\mathbf{x}^*, \mathbf{y}^*$ on the optimal face of the $\mathbf{TGSP}_{|\chi\rangle}$ is

$$\begin{aligned}
x_{l+2}^* &= (M - 1) - (M - 1)\phi, \\
x_{l+3}^* &= (M - 1)\phi, \\
x_{n^*}^* &= 1 \\
y_{n^*}^* &= 1, \\
x_n^* &= 0 \text{ for } n \in \mathbb{N} \setminus \{l+2, l+3, n^*\}, \\
y_n^* &= 0 \text{ for } n \in \mathbb{N} \setminus \{n^*\},
\end{aligned}$$

where n^* is the solution to

$$\begin{aligned}
& \sum_{j \in \mathbb{J}} p_j (r_{n^*+1,j} - r_{n^*,j}) \\
& = \begin{cases} 2 \sum_{j \in \mathbb{J}} p_j (r_{l+2,j} - r_{l+1,j}) & \text{for } n^* = N - Ml - 3 \\ 2 \sum_{j \in \mathbb{J}} p_j (r_{l+3,j} - r_{l+2,j}) & \text{for } n^* \neq N - Ml - 3 \end{cases}
\end{aligned}$$

Proof: Using Corollary 3, the non-negative integer solution lies on the optimal face of the $\mathbf{TGSP}_{|\chi\rangle}$. \square

Example 1: We provide the optimal solutions to the instances of the \mathbf{TGSP} with $N = 11$, $M = 2$, $\mathbb{P} = \{1, 0, 0\}$ and determined/arbitrary ancillae. The optimal faces in Theorems 1 and 2 represent the optimal solutions to $\mathbf{TGSP}_{|0\rangle}$ and $\mathbf{TGSP}_{|\chi\rangle}$, and Corollaries 2 and 4 provide closed-form solutions.

The optimal solutions to the example with the determined ancillae ($\mathbf{TGSP}_{|0\rangle}$) are

$$\begin{cases} x_6^* = 2, \text{ and the others are zero} \\ x_4^* = x_8^* = 1, \text{ and the others are zero,} \\ x_5^* = x_7^* = 1, \text{ and the others are zero.} \end{cases}$$

The first solution is obtained by Corollary 2. The optimal solutions yield a lower bound for the objective value of 103.

The optimal solutions to the $\mathbf{TGSP}_{|\chi\rangle}$ ($(11, 2, \mathbb{R}, \{1, 0, 0\})$) are

$$\begin{cases} x_3^* = x_6^* = 2, y_6^* = z_6^* = 2, \text{ and the others are zero,} \\ x_3^* = 2, x_5^* = x_7^* = y_5^* = z_7^* = 1, \text{ and the others are zero.} \\ x_3^* = 2, x_5^* = x_7^* = y_7^* = z_5^* = 1, \text{ and the others are zero.} \\ x_3^* = 2, x_4^* = x_8^* = y_4^* = z_8^* = 1, \text{ and the others are zero.} \\ x_3^* = 2, x_4^* = x_8^* = y_8^* = z_4^* = 1, \text{ and the others are zero.} \end{cases}$$

The first solution was obtained from Corollary 4. The solutions yielded a lower bound of the objective value of 112.

V. EXPERIMENTAL ANALYSIS

This section describes the experiments conducted to evaluate the \mathbf{TGSP} model and the closed-form solution to the model on a real quantum computer and a quantum simulator. We observe the objective value of the syntheses circuit in the quantum simulator (IBMQ), which has an all-to-all coupling network and infinite precision to evaluate performance without interference by a quantum hardware error. The Toffoli gate implementation circuit, obtained from the closed-form solution of the model, was executed on the simulator, and the objective value was calculated as the weighted sum of the counted gates of the circuit. The comparison experiment between a recent toolkit (Qiskit) and ours was performed in the NISQ system (Rigetti) to evaluate the accuracy of the implemented Toffoli gate. The accuracy was measured by the number of shots observed in the desired CBS, the resulting CBS obtained from the simulator, among total shots.

To evaluate the performances of the models between previous studies and ours, we compared the objective values of each synthesis for $N = 5, 11$, $M = 1 \sim N - 3$, and fixed/arbitrary ancillae. Figures 6 and 7 show the results of the five methods. Each column in the figures represents the weighted sum of quantum cost, which is the number of gates in this experiment. If the synthesis with given parameters N , M , and P is not available, we leave that column blank. Particularly, Barenco's study [20] utilized the NCV library instead of the Clifford+T library; thus, we record only the number of CNOT gates and the objective value when the weight $P = (0, 1, 0)$. Additionally, He et al.'s study [27] included measurement operations within the Toffoli gate synthesis circuit. Although this method does not guarantee optimality, we adopted the best objective value of three trials with Baker et al.'s model [28] for each M, N because it can deal with an arbitrary number of ancillae and yield a single solution.

Based on the results, our method determines the optimal number of ancillae for a given ancilla resource by the closed-form optimal solution for each number of ancillae.

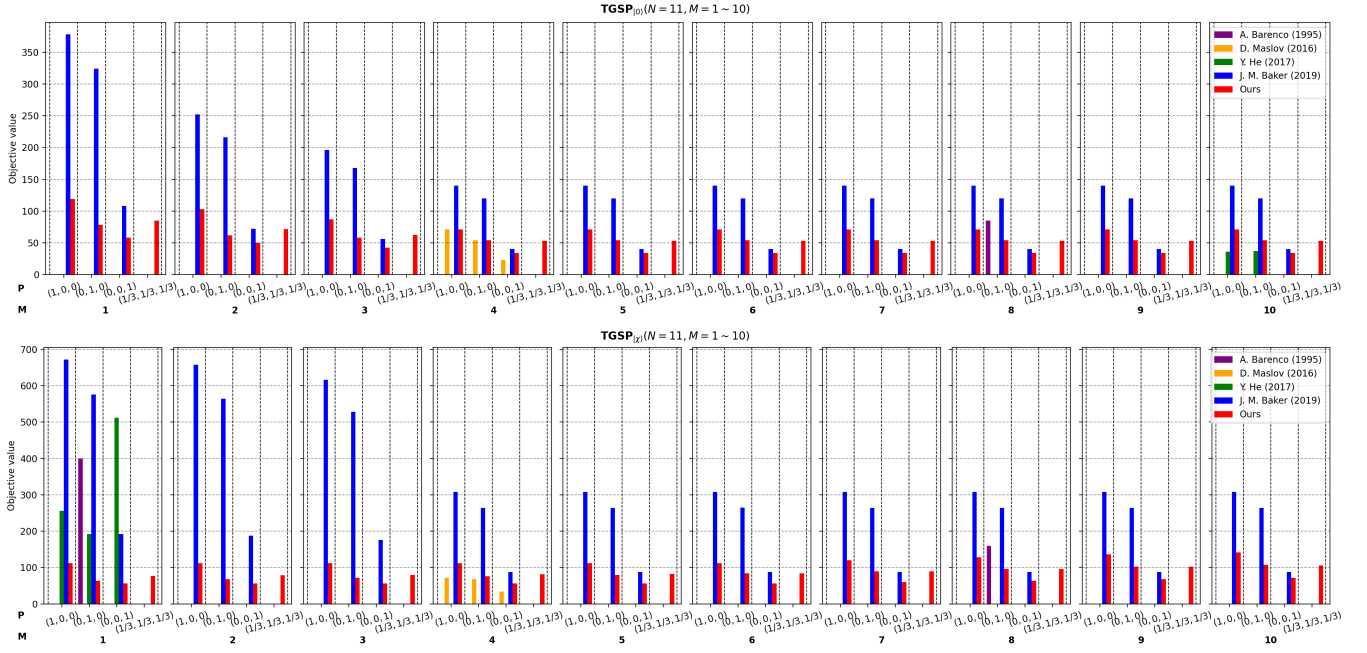


FIGURE 6. Objective values of $TGSP_0$ (top) and $TGSP_{\chi}$ (bottom) with $N = 11, M = 1 \sim 10$. *An empty column indicates that the synthesis is not available. **He et al's result requires $N - 3$ measurement operations.

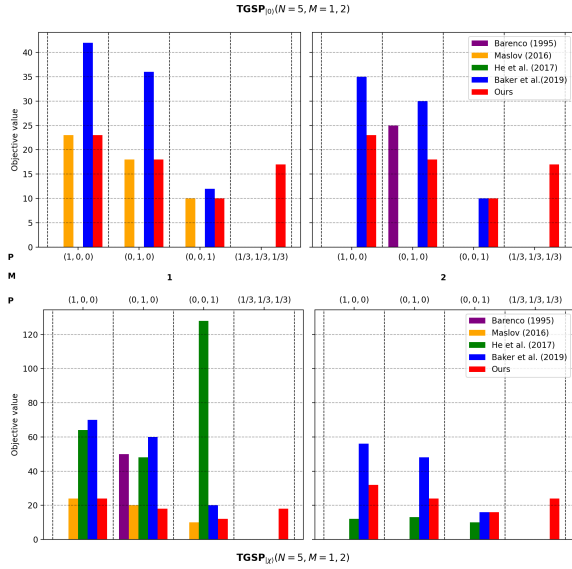


FIGURE 7. Objective values of $TGSP_0$ (top) and $TGSP_{\chi}$ (bottom) with the parameters $N = 5, M = 1, 2$. *An empty column indicates that the synthesis is not available. **He et al's result requires $N - 3$ measurement operations.

With the increasing use of ancilla resources, the objective value decreases to a minimum at a specific point. Thus, there is no need to use the surplus resources after the minimum. As an example, in the following table, we present the optimal number of ancillae for each method for a given ancilla resource in the 11-bit Toffoli gate synthesis to minimize the number of multi-qubit gates, where $P = (0, 1, 0)$. Additionally, the synthesis for arbitrary state ancillae can be implemented for the determined state. Each cell in the table

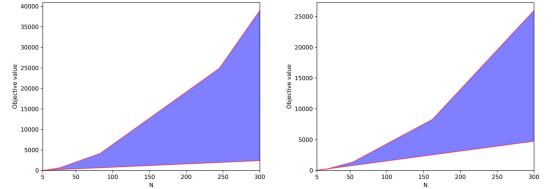


FIGURE 8. Objective values of $TGSP_0$ (Left) and $TGSP_{\chi}$ (Right).

below represents the optimal number of ancilla bits, and the values in parentheses indicate the corresponding numbers of gates. Our method provides the minimum cost of 11-bit Toffoli gate synthesis, except when ten ancilla qubits are available.

Resource	1	2	3	4	5	6	7	8	9	10
Barenco	1(400)	1	1	1	1	1	1	8	8	8
Maslov	N/A	N/A	N/A	4*(54)	4*	4*	4*	4*	4*	4*
He et al.	1(192)	1	1	1	1	1	1	1	1	10*(37)
Baker et al.	1(324)	2(216)	3(168)	4(120)	4	4	4	4	4	4
Ours	1*(78)	2*(62)	3*(58)	4*(54)	4*	4*	4*	4*	4*	4

* Method with the minimum number of gates among all methods

Moreover, we executed $N(5 \sim 300)$ -bit Toffoli gate synthesis with $M(1 \sim N - 2)$ ancillae and $P = (1/3, 1/3, 1/3)$ weights. The quantum cost of an N -bit Toffoli gate synthesis decreases in the blue area when the number of ancillae increases. Utilizing an ancilla qubit, $M = 1$, the quantum cost increases exponentially, but it increases linearly if the synthesis uses $\lceil \frac{N-3}{2} \rceil$ and $\lceil \frac{N-16}{2} \rceil$ ancillae in $TGSP_0$ and $TGSP_{\chi}$, respectively (Figure 8).

To check the potential of quantum error reduction by the proposed models, we acquired the result of the N -bit Toffoli gate synthesis circuit execution in the NISQ device. Compared with the best result of the Qiskit multi-control gate implementation provided by IBMQ with four modes, it took

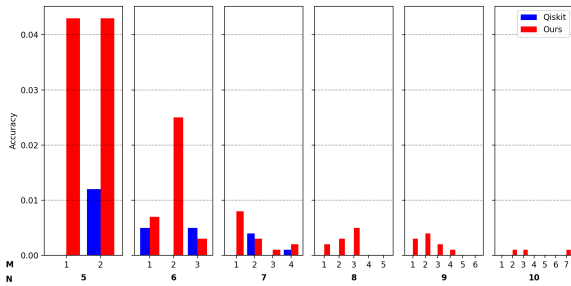


FIGURE 9. Accuracies of TOF(N) implementations with M ancillae.

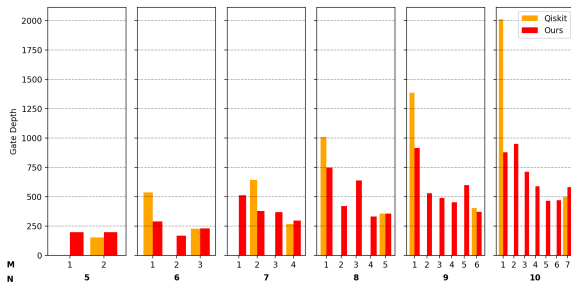


FIGURE 10. Gate depths of TOF(N) implementations with M ancillae.

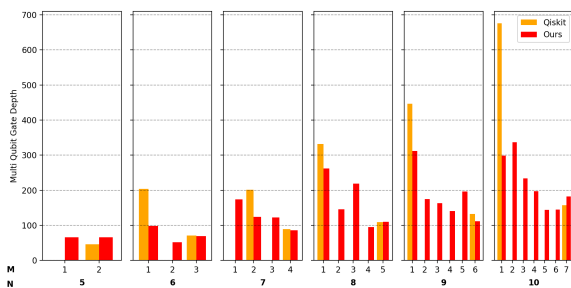


FIGURE 11. Multi-qubit gate depths of TOF(N) implementations with M ancillae.

an average accuracy of five experiments with one thousand shots. Each experiment was executed on a Rigetti quantum system with $N(5 \sim 10)$ -bit Toffoli gate $M(1 \sim N - 3)$ determined/arbitrary ancillae synthesis. Most of the results showed a higher accuracy and lower gate depth than with Qiskit's implementation. (Figures 9, 10, 11)

VI. CONCLUSION

We presented a pragmatic approach to synthesizing a multi-bit Toffoli gate for various user-specified input parameters, including the number of available ancillae, initial states of ancillae, gate libraries to represent building blocks, relative costs of quantum resources, and quantum costs of basis quantum gates. The core of our approach lies in the two combinatorial optimization models and their closed-form solutions. The proposed approach provides users with the flexibility to accommodate various quantitative design factors in creating a context-dependent synthesis circuit for multi-bit Toffoli gates.

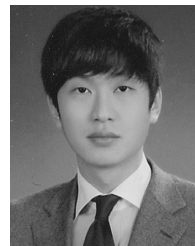
The proposed optimization models can yield the optimal cost circuit for a multi-bit Toffoli gate with alternative RTOF(n) and TOF(3) implementations (e.g., an improved future version). Moreover, the quantum basis gates for Toffoli gate implementation are replaceable. As a result of these properties, the proposed framework provides flexible Toffoli gate synthesis in the NISQ environment in which the precise setting of the quantum circuit parameters is difficult to predict.

In future studies, we plan to use the proposed synthesis framework for the implementation of a quantum circuit with a large number of multi-bit Toffoli gates. The restricted size and length of the quantum circuit will result in different implementations of each generalized Toffoli gate (e.g., different numbers of ancillae). Furthermore, the choice of qubits, used as ancillae for each Toffoli gate, affects the depth and error of the quantum algorithm circuit. Therefore, we seek the optimal basis gate network to minimize the overall cost of a circuit, which requires a large number of multi-bit Toffoli gates.

REFERENCES

- [1] L. K. Grover, "Quantum mechanics helps in searching for a needle in a haystack," *Phys. Rev. Lett.*, vol. 79, no. 2, p. 325, Jul. 1997.
- [2] P. W. Shor, "Algorithms for quantum computation: Discrete logarithms and factoring," in *Proc. 35th Annu. Symp. Found. Comput. Sci.*, 1994, pp. 124–134.
- [3] M. A. Nielsen and I. Chuang, "Quantum computation and quantum information," *Amer. J. Phys.*, vol. 70, p. 558, Apr. 2002.
- [4] Y. Shi, "Both Toffoli and controlled-NOT need little help to do universal quantum computation," 2002, *arXiv:quant-ph/0205115*.
- [5] L. Mattioli and A. Sawicki, "On the universality and membership problems for quantum gates," 2021, *arXiv:2110.04210*.
- [6] A. Sawicki, L. Mattioli, and Z. Zimborás, "How to check universality of quantum gates?" 2021, *arXiv:2111.03862*.
- [7] G. L. Long, "Grover algorithm with zero theoretical failure rate," *Phys. Rev. A, Gen. Phys.*, vol. 64, no. 2, Jul. 2001.
- [8] C. Figgatt, D. Maslov, K. A. Landsman, N. M. Linke, S. Debnath, and C. Monroe, "Complete 3-qubit Grover search on a programmable quantum computer," *Nature Commun.*, vol. 8, no. 1, pp. 1–9, Dec. 2017.
- [9] Y. Nam, Y. Su, and D. Maslov, "Approximate quantum Fourier transform with $O(n \log(n))T$ gates," *Npj Quantum Inf.*, vol. 6, no. 1, pp. 1–6, Mar. 2020.
- [10] D. G. Cory, M. Price, W. Maas, E. Knill, R. Laflamme, W. H. Zurek, T. F. Havel, and S. S. Somaroo, "Experimental quantum error correction," *Phys. Rev. Lett.*, vol. 81, no. 10, p. 2152, 1998.
- [11] M. D. Reed, L. Di Carlo, S. E. Nigg, L. Sun, L. Frunzio, S. M. Girvin, and R. J. Schoelkopf, "Realization of three-qubit quantum error correction with superconducting circuits," *Nature*, vol. 482, no. 7385, pp. 382–385, Feb. 2012.
- [12] A. Paetznick and B. W. Reichardt, "Universal fault-tolerant quantum computation with only transversal gates and error correction," *Phys. Rev. Lett.*, vol. 111, no. 9, Aug. 2013, Art. no. 090505.
- [13] J. Guillaud and M. Mirrahimi, "Repetition cat qubits for fault-tolerant quantum computation," *Phys. Rev. X*, vol. 9, no. 4, Dec. 2019, Art. no. 041053.
- [14] V. V. Shende and I. L. Markov, "On the CNOT-cost of Toffoli gates," *Quantum Inf. Comput.*, vol. 9, no. 5, pp. 461–486, 2009.
- [15] N. Yu, R. Duan, and M. Ying, "Five two-qubit gates are necessary for implementing the Toffoli gate," *Phys. Rev. A, Gen. Phys.*, vol. 88, no. 1, Jul. 2013, Art. no. 010304.
- [16] N. Yu and M. Ying, "Optimal simulation of Deutsch gates and the Fredkin gate," *Phys. Rev. A, Gen. Phys.*, vol. 91, no. 3, Mar. 2015, Art. no. 032302.
- [17] T. Toffoli, "Reversible computing," in *Proc. Int. Colloq. Automata, Languages, Program.* Cham, Switzerland: Springer, 1980, pp. 632–644.
- [18] W. Liu, H. Wei, and L. Kwek, "Universal quantum multi-qubit entangling gates with auxiliary spaces," *Adv. Quantum Technol.*, vol. 5, no. 5, May 2022, Art. no. 2100136.

- [19] J. Preskill, "Quantum computing in the NISQ era and beyond," *Quantum*, vol. 2, p. 79, Aug. 2018.
- [20] A. Barenco, C. H. Bennett, R. Cleve, D. P. DiVincenzo, N. Margolus, P. Shor, T. Sleator, J. A. Smolin, and H. Weinfurter, "Elementary gates for quantum computation," *Phys. Rev. A, Gen. Phys.*, vol. 52, pp. 3457–3467, Nov. 1995.
- [21] M. Saeedi and M. Pedram, "Linear-depth quantum circuits for n -qubit Toffoli gates with no ancilla," *Phys. Rev. A, Gen. Phys.*, vol. 87, no. 6, Jun. 2013, Art. no. 062318.
- [22] T. G. D. Brugiére, M. Baboulin, B. Valiron, S. Martiel, and C. Allouche, "Reducing the depth of linear reversible quantum circuits," *IEEE Trans. Quantum Eng.*, vol. 2, pp. 1–22, 2021.
- [23] M. Arabzadeh, M. S. Zamani, M. Sedighi, and M. Saeedi, "Depth-optimized reversible circuit synthesis," *Quantum Inf. Process.*, vol. 12, no. 4, pp. 1677–1699, Apr. 2013.
- [24] V. V. Shende, I. L. Markov, and S. S. Bullock, "Minimal universal two-qubit controlled-NOT-based circuits," *Phys. Rev. A, Gen. Phys.*, vol. 69, no. 6, Jun. 2004, Art. no. 062321.
- [25] D. Maslov and G. W. Dueck, "Improved quantum cost for n -bit Toffoli gates," *Electron. Lett.*, vol. 39, no. 25, pp. 1790–1791, Dec. 2003.
- [26] D. Maslov, "Advantages of using relative-phase Toffoli gates with an application to multiple control Toffoli optimization," *Phys. Rev. A, Gen. Phys.*, vol. 93, no. 2, Feb. 2016, Art. no. 022311.
- [27] Y. He, M.-X. Luo, E. Zhang, H.-K. Wang, and X.-F. Wang, "Decompositions of n -qubit Toffoli gates with linear circuit complexity," *Int. J. Theor. Phys.*, vol. 56, no. 7, pp. 2350–2361, Jul. 2017.
- [28] J. M. Baker, C. Duckering, A. Hoover, and F. T. Chong, "Decomposing quantum generalized Toffoli with an arbitrary number of ancilla," 2019, *arXiv:1904.01671*.
- [29] A. J. Da Silva and D. K. Park, "Linear-depth quantum circuits for multi-qubit controlled gates," 2022, *arXiv:2203.11882*.
- [30] E. O. Kiktenko, A. S. Nikolaeva, P. Xu, G. V. Shlyapnikov, and A. K. Fedorov, "Scalable quantum computing with qudits on a graph," *Phys. Rev. A, Gen. Phys.*, vol. 101, no. 2, Feb. 2020, Art. no. 022304.
- [31] P. J. Low, B. M. White, A. A. Cox, M. L. Day, and C. Senko, "Practical trapped-ion protocols for universal qudit-based quantum computing," *Phys. Rev. Res.*, vol. 2, no. 3, Jul. 2020, Art. no. 033128.
- [32] J. D. A. Espinoza, K. Groenland, M. Mazzanti, K. Schoutens, and R. Gerritsma, "High-fidelity method for a single-step n -bit Toffoli gate in trapped ions," *Phys. Rev. A, Gen. Phys.*, vol. 103, no. 5, May 2021, Art. no. 052437.
- [33] D. Maslov and Y. Nam, "Use of global interactions in efficient quantum circuit constructions," *New J. Phys.*, vol. 20, no. 3, Mar. 2018, Art. no. 033018.
- [34] K. Groenland, F. Witteveen, K. Schoutens, and R. Gerritsma, "Signal processing techniques for efficient compilation of controlled rotations in trapped ions," *New J. Phys.*, vol. 22, no. 6, Jun. 2020, Art. no. 063006.
- [35] S. E. Rasmussen, K. Groenland, R. Gerritsma, K. Schoutens, and N. T. Zinner, "Single-step implementation of high-fidelity n -bit Toffoli gates," *Phys. Rev. A, Gen. Phys.*, vol. 101, no. 2, Feb. 2020, Art. no. 022308.
- [36] B. P. Lanyon, M. Barbieri, M. P. Almeida, T. Jennewein, T. C. Ralph, K. J. Resch, G. J. Pryde, J. L. O'Brien, A. Gilchrist, and A. G. White, "Simplifying quantum logic using higher-dimensional Hilbert spaces," *Nature Phys.*, vol. 5, no. 2, pp. 134–140, Dec. 2008.
- [37] P. Selinger, "Quantum circuits of T -depth one," *Phys. Rev. A, Gen. Phys.*, vol. 87, no. 4, 2013, Art. no. 042302.
- [38] M. Amy, D. Maslov, M. Mosca, and M. Roetteler, "A meet-in-the-middle algorithm for fast synthesis of depth-optimal quantum circuits," *IEEE Trans. Comput.-Aided Design Integr. Circuits Syst.*, vol. 32, no. 6, pp. 818–830, Jun. 2013.
- [39] M. Soeken, M. Roetteler, N. Wiebe, and G. D. Micheli, "LUT-based hierarchical reversible logic synthesis," *IEEE Trans. Comput.-Aided Design Integr. Circuits Syst.*, vol. 38, no. 9, pp. 1675–1688, Sep. 2019.
- [40] P. Aliferis, D. Gottesman, and J. Preskill, "Quantum accuracy threshold for concatenated distance-3 codes," 2005, *arXiv:quant-ph/0504218*.
- [41] C. M. Dawson and M. A. Nielsen, "The Solovay–Kitaev algorithm," 2005, *arXiv:quant-ph/0505030*.
- [42] M. Amy, D. Maslov, and M. Mosca, "Polynomial-time T -depth optimization of Clifford+ T circuits via matroid partitioning," *IEEE Trans. Comput.-Aided Design Integr. Circuits Syst.*, vol. 33, no. 10, pp. 1476–1489, Oct. 2014.
- [43] J. L. W. V. Jensen, "Sur les fonctions convexes et les inégalités entre les valeurs moyennes," *Acta Mathematica*, vol. 30, pp. 175–193, Jan. 1906.



YOUNG-MIN JUN (Member, IEEE) was born in Seoul, South Korea, in 1987. He received the B.S. degree in industrial management engineering from Korea University, Seoul, in 2012, where he is currently pursuing the Ph.D. degree with the Quantum Machine Learning Laboratory, School of Industrial Management Engineering.

His research interests include computational optimization, algorithms and optimization problems in quantum computing, routing problems in additive manufacturing, and algorithms in topic modeling.



IN-CHAN CHOI (Member, IEEE) received the B.S. degree from Korea University, the B.S. degree from Iowa State University, and the M.S. and Ph.D. degrees in operations research from Columbia University, New York.

He is currently a Professor with Korea University and the Director of the Quantum Machine Learning Laboratory. He has numerous research publications in professional journals, including *Mathematical Programming*, *Journal of Optimization Theory and Applications* (JOTA), *IEEE TRANSACTIONS ON VEHICULAR TECHNOLOGY*, *Computer and Operations Research*, and *Omega*. His primary research interests include computational optimization and its applications, including decision science. His current research interests include advancing quantum optimization and quantum machine learning algorithms.

...