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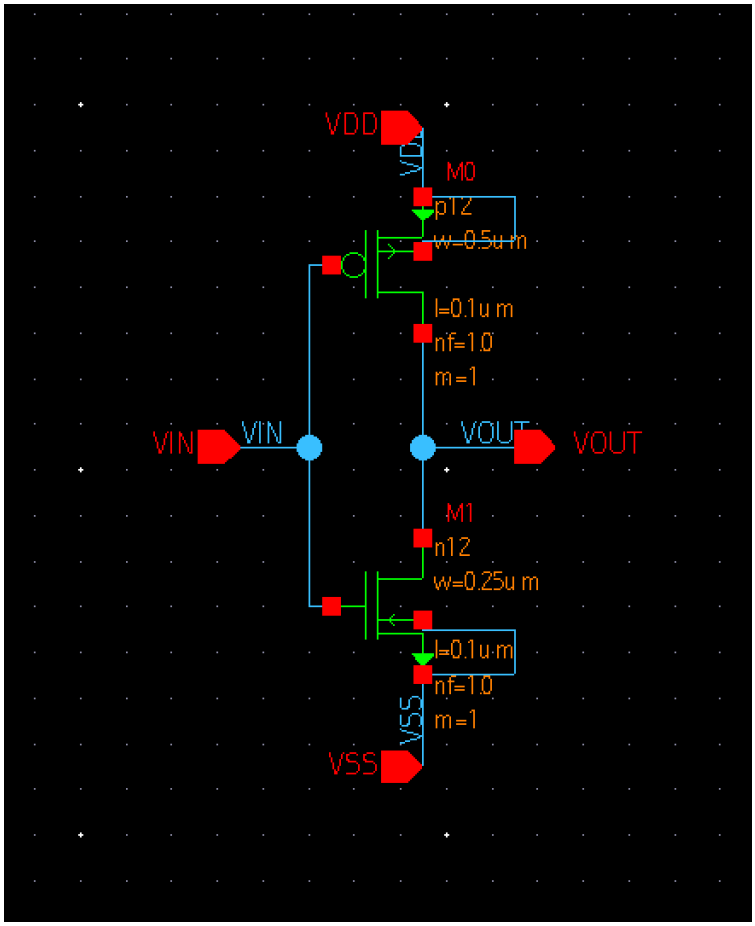
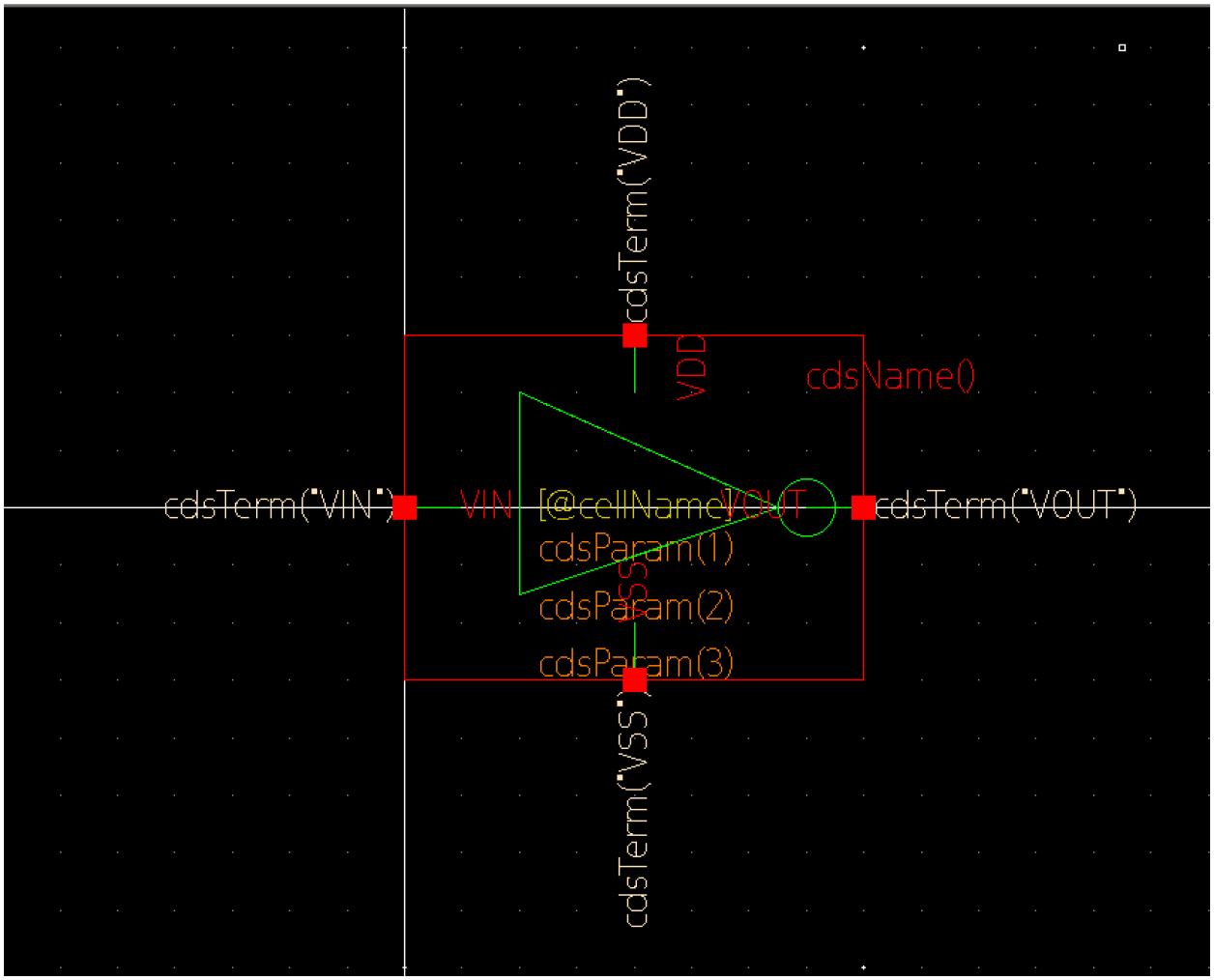
Session 021
Lab 1

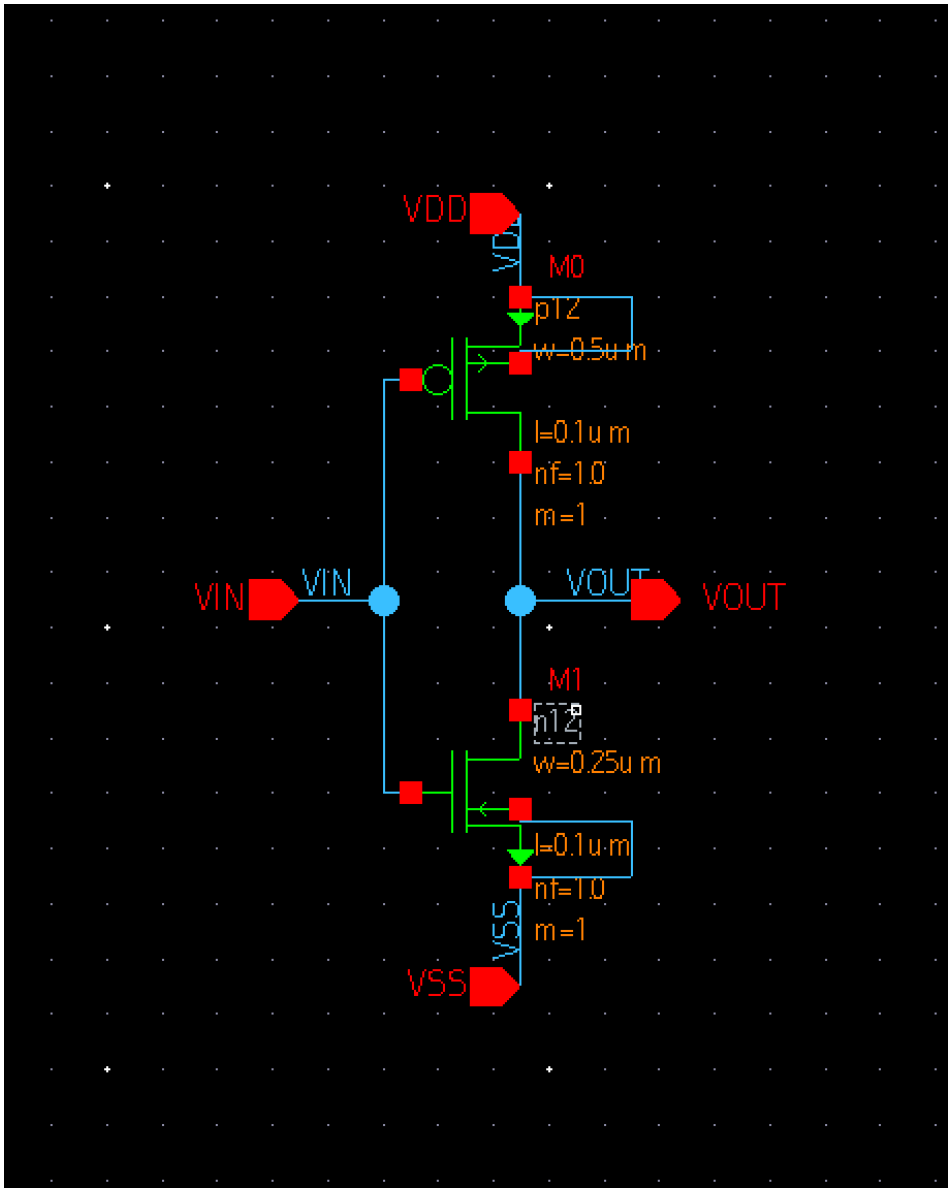
- Summary of what you learned thru this lab (One paragraph)
    - In this lab we learned how to use the cdesign program and create circuits. We also learned how to make new symbols and how to use them in different circuits. In this lab we also learned how to simulate and graph circuits. We also learned how to get specific values from the graphs.
- An inverter schematic view as seen in Fig 13.



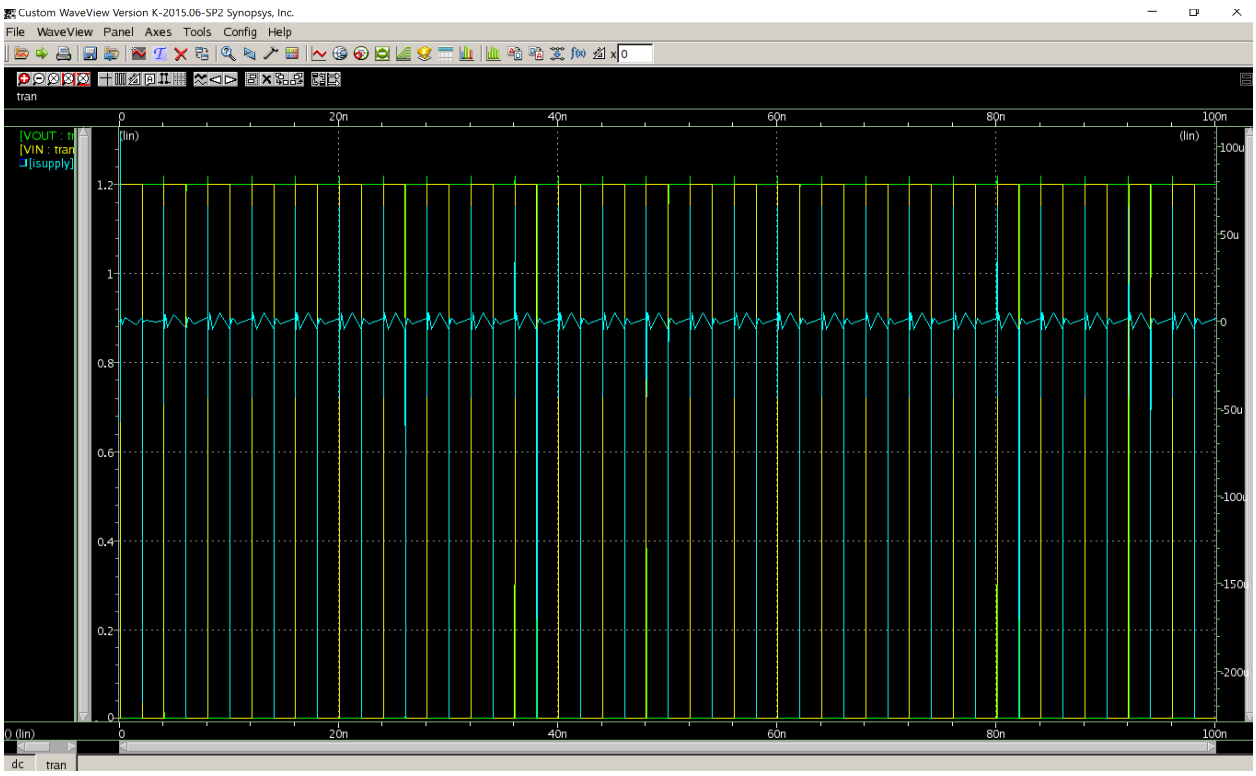
- An inverter symbol view as seen in Fig 15.



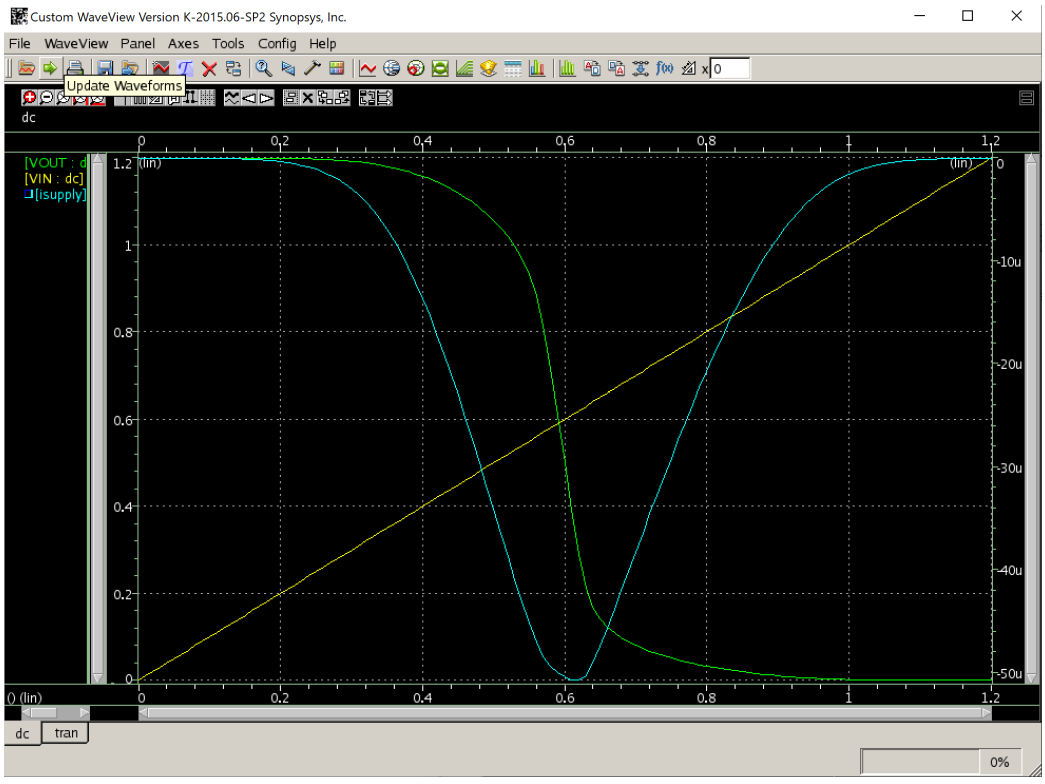
- A test-bench for your inverter design as seen in Fig 18.



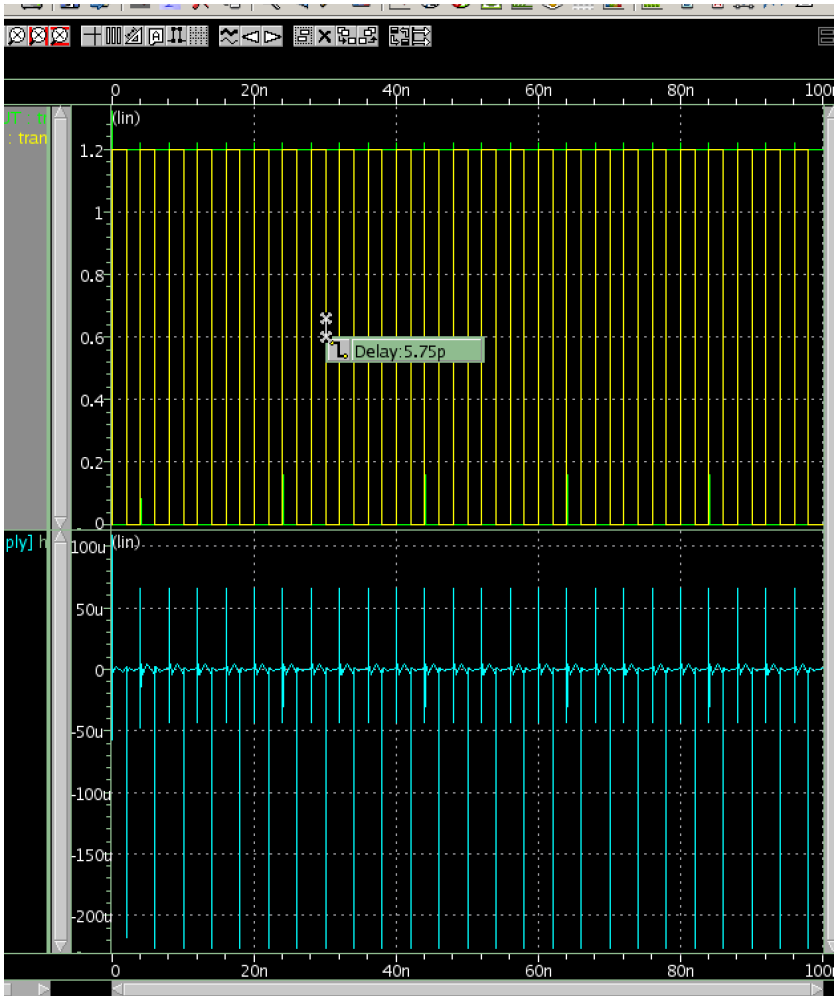
- A transient analysis waveform as seen in Fig. 28.



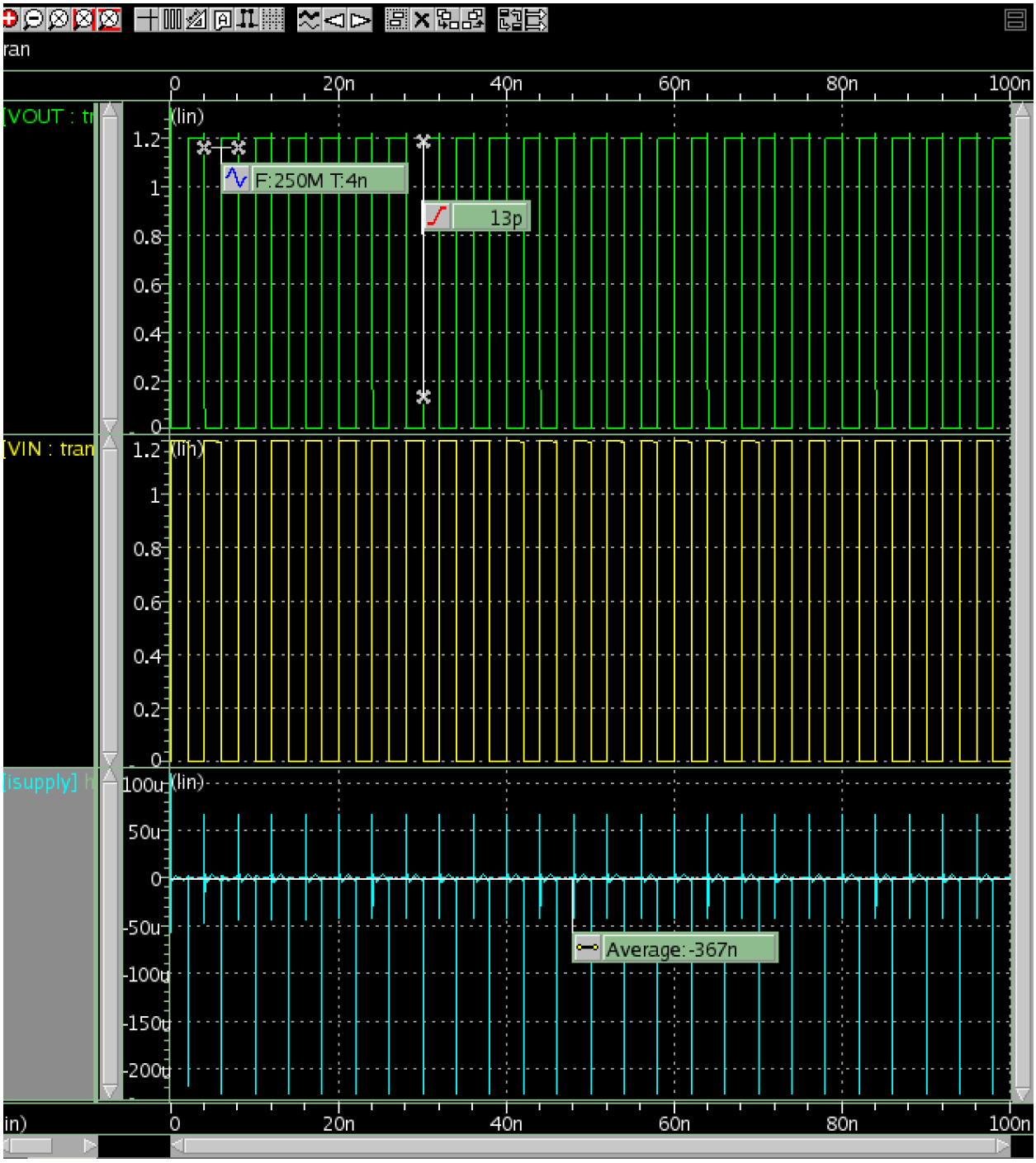
- A DC Sweep analysis waveform as seen in Fig 29



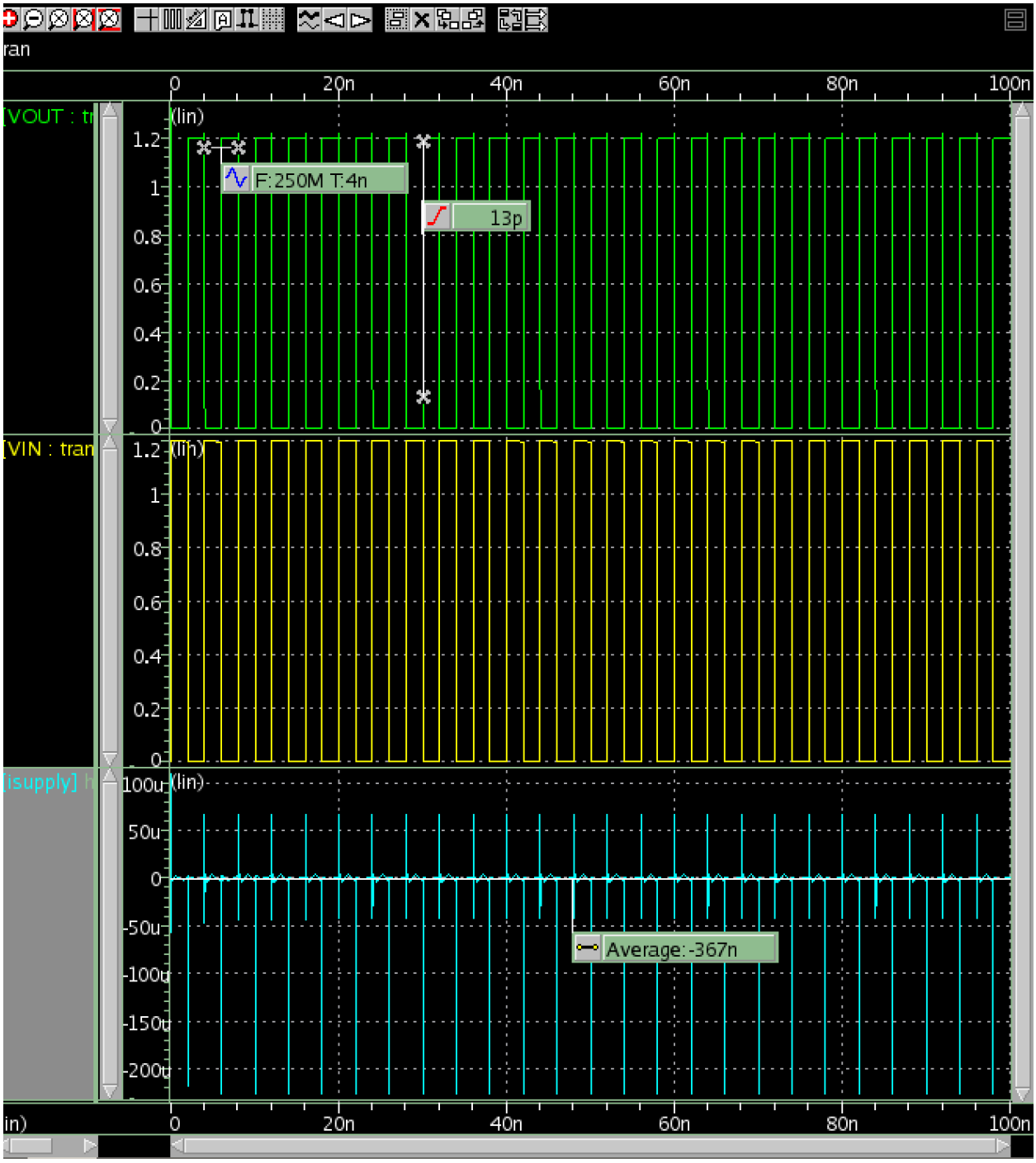
- A delay measurement of VOUT and VIN at 50% to 50% annotated waveform as seen in Fig 33.



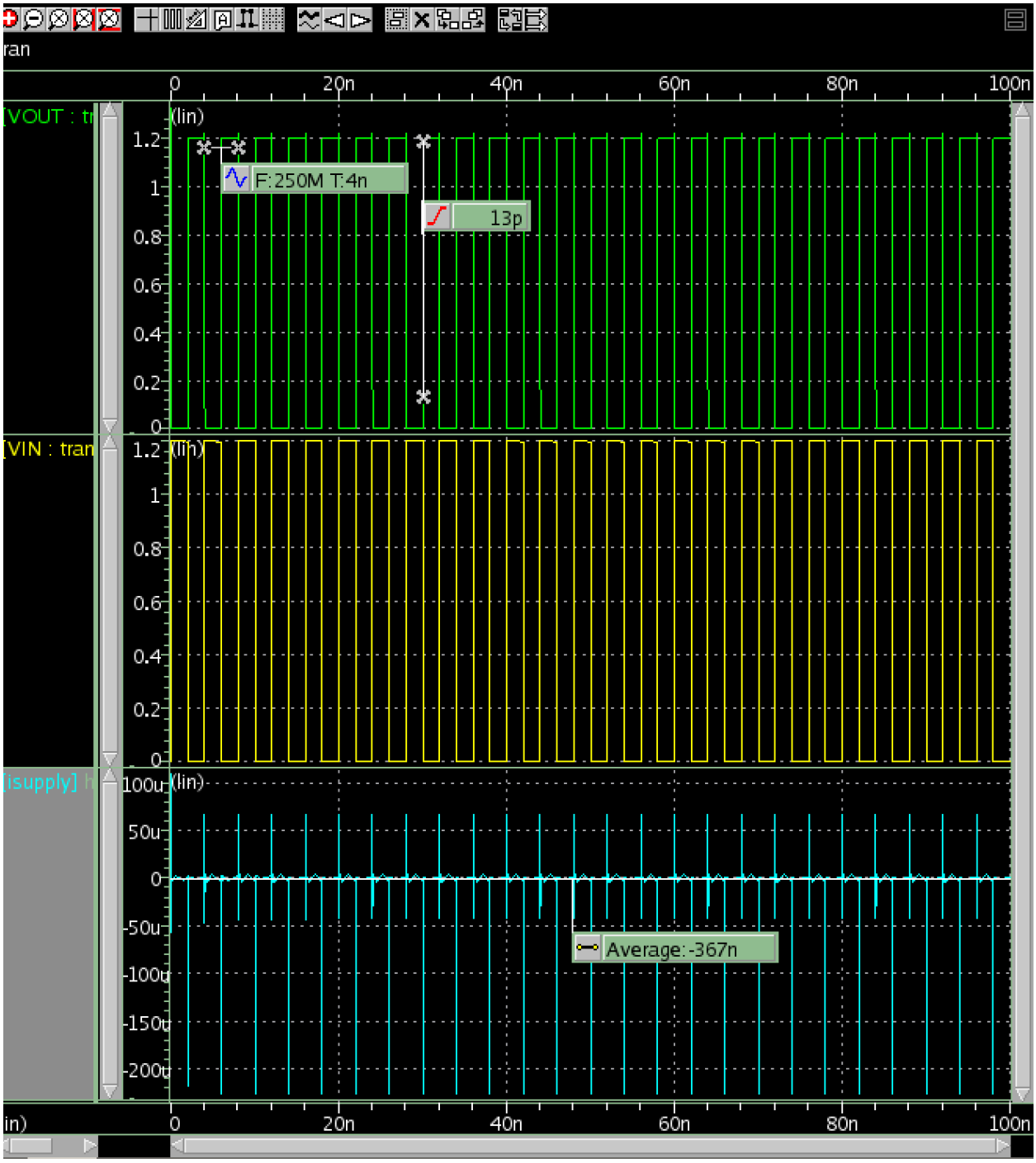
- A rise AND fall measurement at 90% and 10% for VOUT annotated waveform as seen in Fig 35.



- An average current measurement annotated waveform as seen in Fig. 37.



- A frequency measurement for VOUT annotated waveform as seen in Fig. 39.



- Some of the issues if you have
    - One big issue I had with this lab was that I made a small error and the debugger wasn't much help solving it. When i was making the circuit I forgot the decimal for 1.2V and it completely broke the program. The debugger only said that there was

some error in the circuit. After looking at all of the values in the design I was able to find the problem.