UNIST
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CSE467: Computer Security

7-2. Assembly

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The slide is based on Prof. Sang Kil Cha's lecture slide

Our Environment

- Linux (Debian/Ubuntu) on x86
- GNU Binutils (objdump, readelf, strip, etc.)
- GNU Debugger (GDB)
- No IDA Pro
- Vagrant VM for exercise and homework
  - Install latest version of VirtualBox: https://www.virtualbox.org/
  - Install latest version of Vagrant: https://www.vagrantup.com/downloads.html
  - `mkdir YOUR_PATH; cd YOUR_PATH`
  - Download box from https://www.dropbox.com/scl/fi/3ssmv98wky2uvpju5q66s/cse467.box?rlkey=ssu5llgq9n548ugp4fgyfmm6x&dl=1

Our Environment

- Vagrant VM for exercise and homework
  - `vagrant box add cse467 cse467.box`
  - `vagrant init cse467`
  - `vagrant up`
  - `vagrant ssh`

  Just use these two after installation is complete

- (Just for your reference) A Vagrant box is created with
  - Virtual box 7.0.10 r158379
  - Vagrant 1.9.8
  - Debian 9.1.0
  - `ID: vagrant`
  - `PW: vagrant`

Our Goal in Software Security

- Find out whether a program is secure or not.

- To do so, we need to see how the ***binary code*** (= executable code) executes on a machine!

Compilation

- Converting a high-level language into a machine language that the computer can understand

```
int test (int a){
    return 32;
}
```

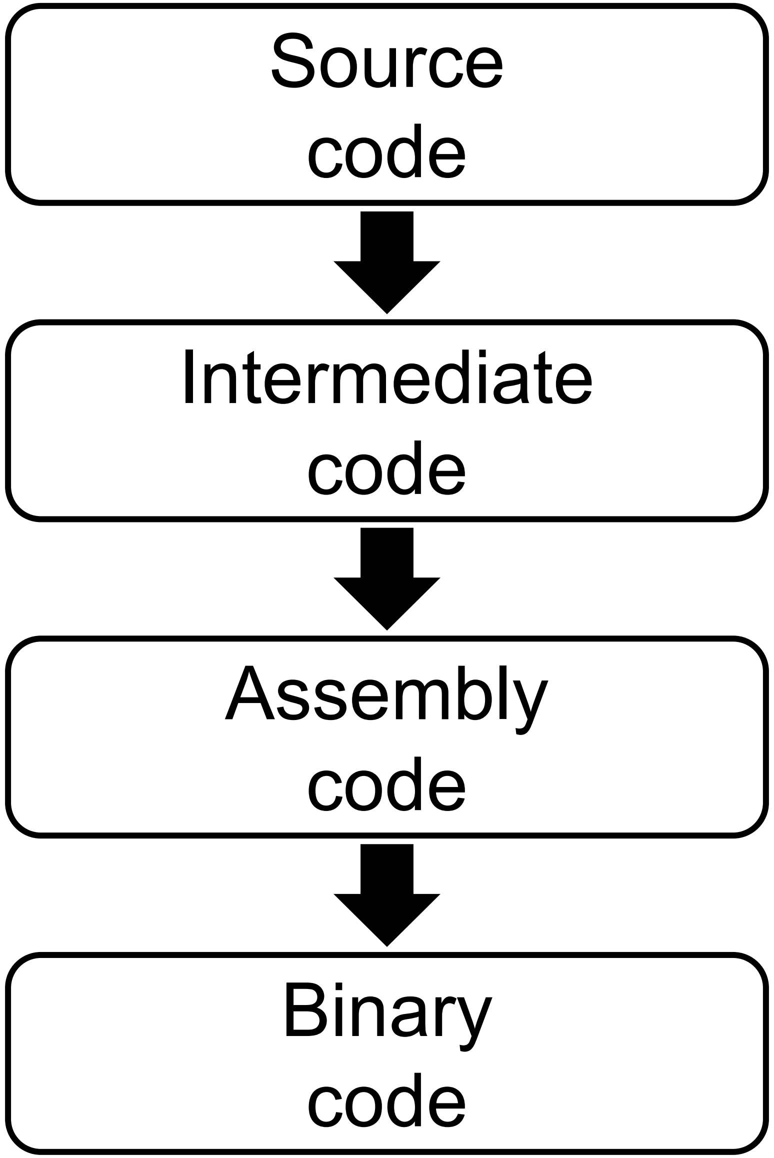
High-level language

Compile

```
01000101010010010101
01001000100000101010
11100011010101010100
10101000010101001010
11100101010010101110
```

Machine language

Compilation Process



Compilation Process

Source code

↓

Intermediate code

↓

Assembly code

↓

Binary code

test.c

```c
int test (int a){
    return 32;
}
```

$clang -emit-llvm -S test.c

test.ll

```
define dso_local i32 @test(i32)
#0 {
  %2 = alloca i32, align 4
  store i32 %0, i32* %2, align 4
  ret i32 32
}
```

Compilation Process

Source code

↓

Intermediate code

↓

Assembly code

↓

Binary code

test.ll

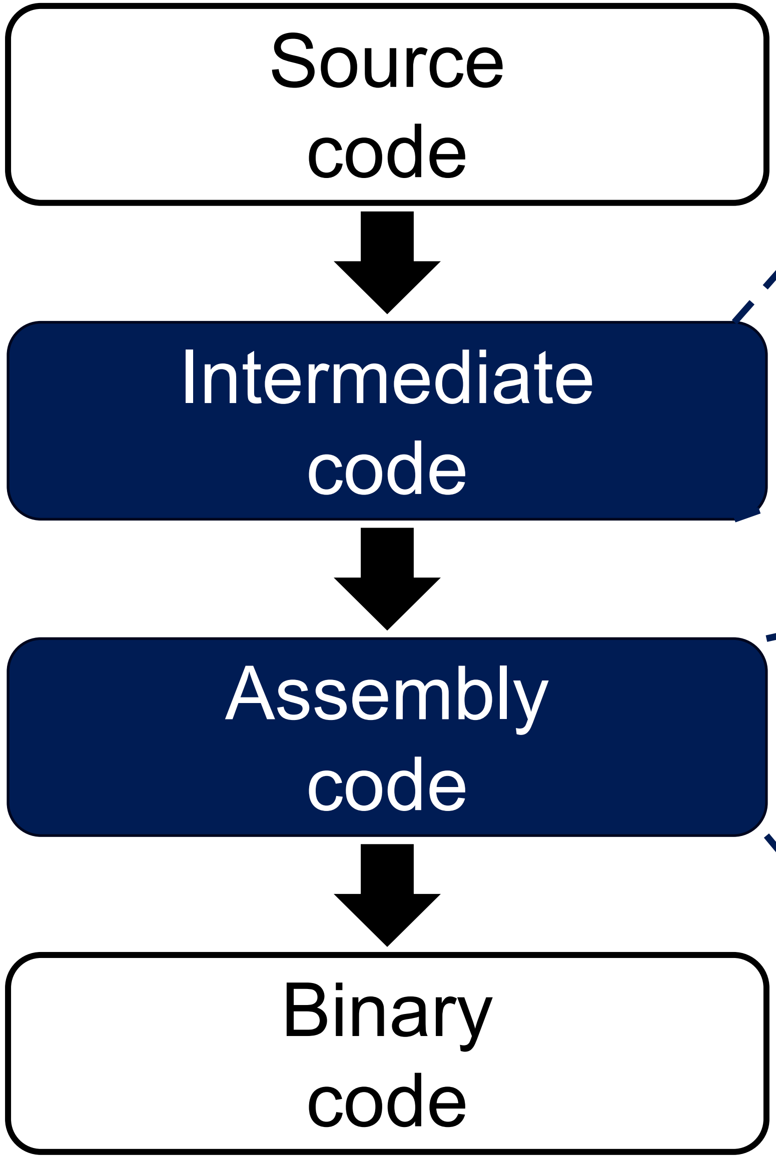
```
define dso_local i32 @test(i32) #0
{
  %2 = alloca i32, align 4
  store i32 %0, i32* %2, align 4
  ret i32 32
}
```

```
$llc --x86-asm-syntax=intel test.ll
```

test.s

```
test:                    # @test
# %bb.0:
push ebp
mov ebp, esp
mov eax, dword ptr [ebp + 8]
mov eax, 32
pop ebp
ret
```

Compilation Process



test.ll

```
define dso_local i32 @test(i32) #0
{
  %2 = alloca i32, align 4
  store i32 %0, i32* %2, align 4
  ret i32 32
}
```

Intel syntax

```
$llc --x86-asm-syntax=intel test.ll
```

test.s

```
test:                    # @test
# %bb.0:
push ebp
mov ebp, esp
mov eax, dword ptr [ebp + 8]
mov eax, 32
pop ebp
ret
```

The last human-readable format

Compilation Process

Source code

Intermediate code

Assembly code

Binary code

test.s

```
test:                  # @test
# %bb.0:
push ebp
mov ebp, esp
mov eax, dword ptr [ebp + 8]
mov eax, 32
pop ebp
ret
```

```
$as -o test.o test.s
```

test.o

```
010001001010010100101010010100
101010010111101001000100101010
101110001010100010111001010011
```

GNU AS (Assembler)


```
$as -o test.o test.s
$ls test.o
```

```
.intel_syntax noprefix

mov eax, ebx
…
```

Compilation Process

Source code

↓

Intermediate code

↓

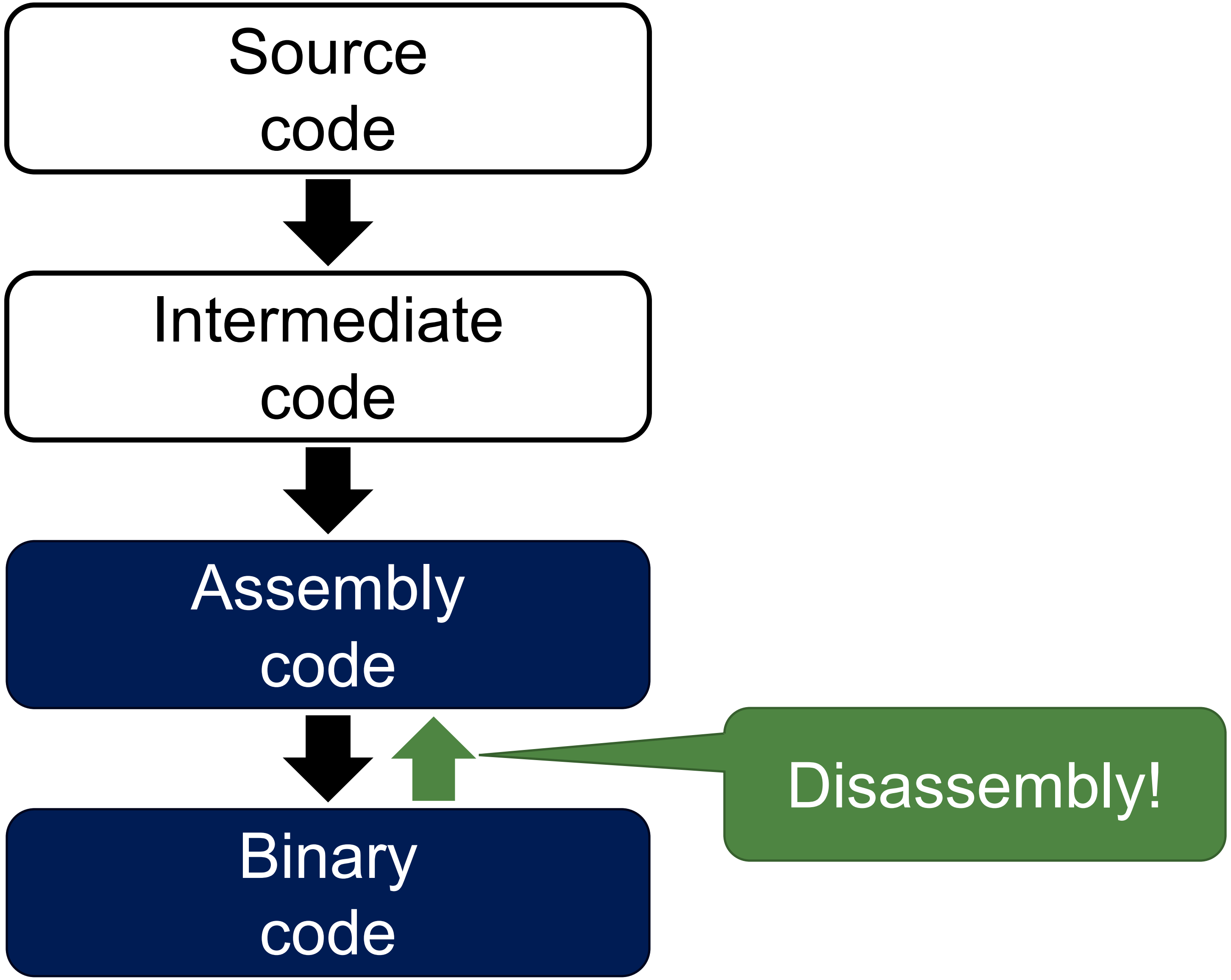
Assembly code

↓

Binary code

Our goal:
Understanding binary

Disassembling Binary Code

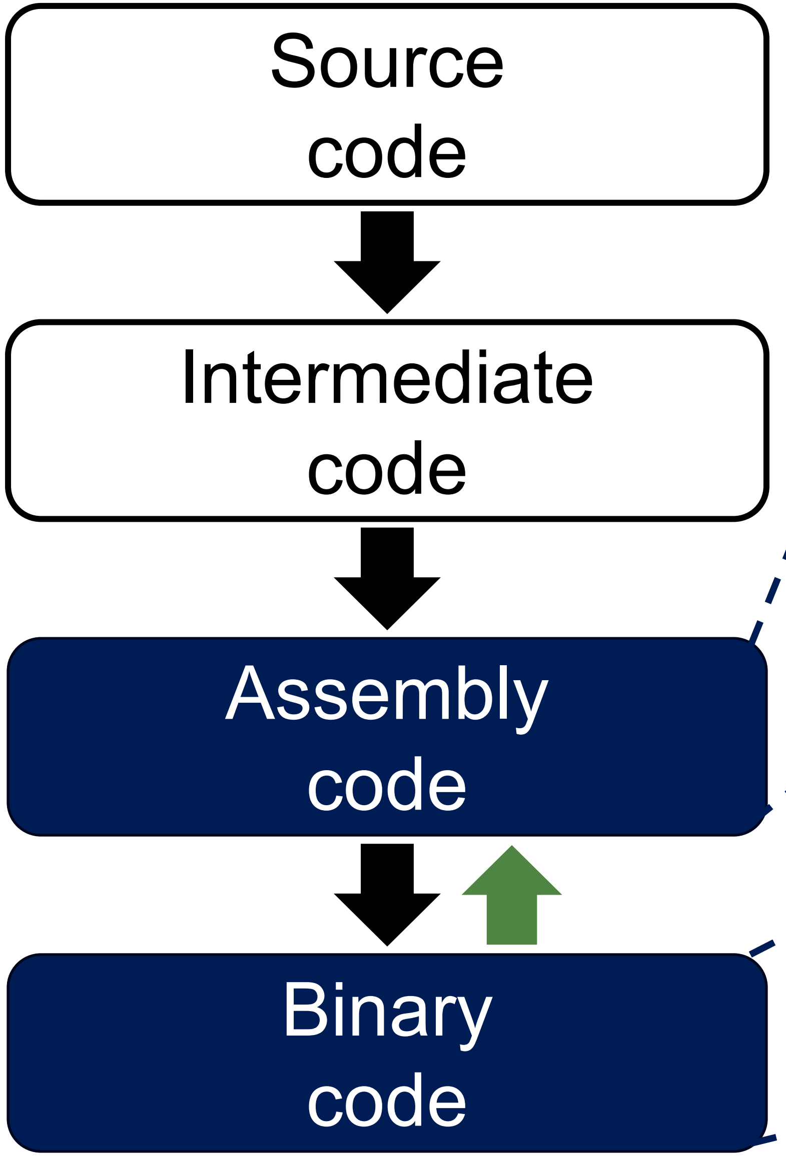


Disassembler:
- Objdump
- IDA
- B2R2
- …

GNU `objdump`

- One of the GNU Binutils
- Perform disassembly on the file

GNU objdump



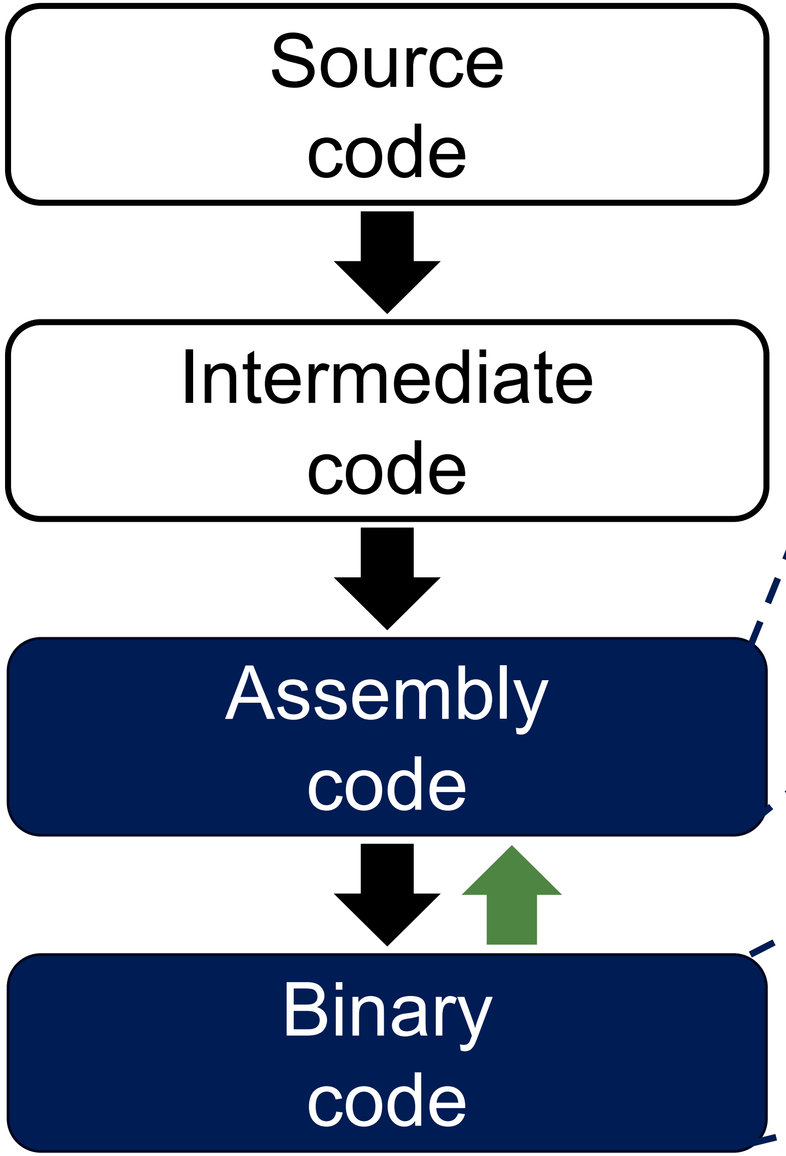

```
00000000 <test>:
 0: 55                push ebp
 1: 89 e5             mov  ebp,esp
 3: 8b 45 08          mov  eax,DWORD PTR [ebp+0x8]
 6: b8 20 00 00 00    mov  eax,0x20
 b: 5d                pop  ebp
 c: c3                ret
```

```
$ objdump -M intel -d test.o
```

test.o

```
010001001010010100101010010100
101010010111101001000100101010
101110001010100010111001010011
```

GNU objdump



Address | Binary code | Disassembled assembly code

```
00000000 <test>:
   0:	55                   	push   ebp
   1:	89 e5                	mov    ebp,esp
   3:	8b 45 08             	mov    eax,DWORD PTR [ebp+0x8]
   6:	b8 20 00 00 00       	mov    eax,0x20
   b:	5d                   	pop    ebp
   c:	c3                   	ret
```

```
$ objdump -M intel -d test.o
```

test.o

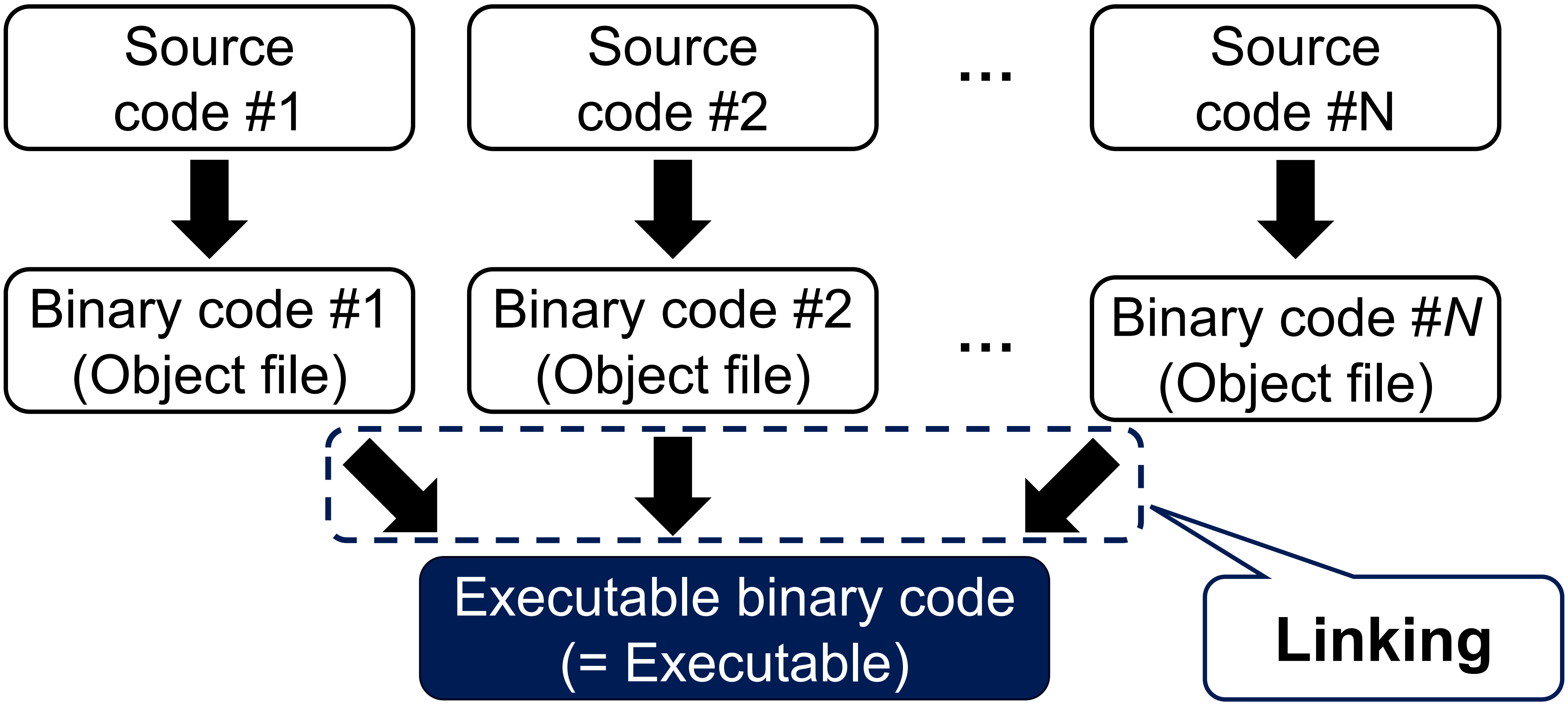
```
01000100101001010010101001010 0
```

```
01000100101001010010101001010100
10101001011110100100010010101 0
```

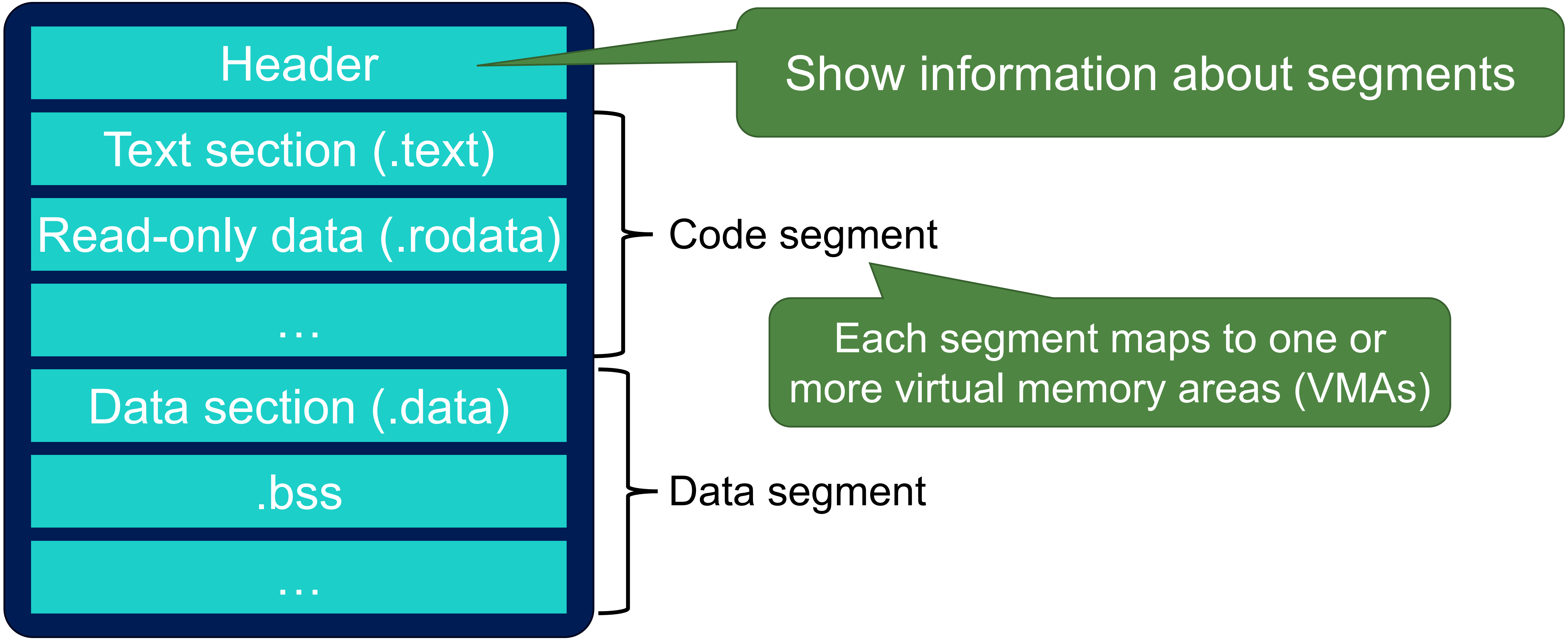
(FYI) Disassembly is Difficult

- The very first step of binary analysis, but it is extremely challenging because of
  - Indirect branches
    - `jmp eax`
    - `call eax`
  - Mixture of code and data

Linking

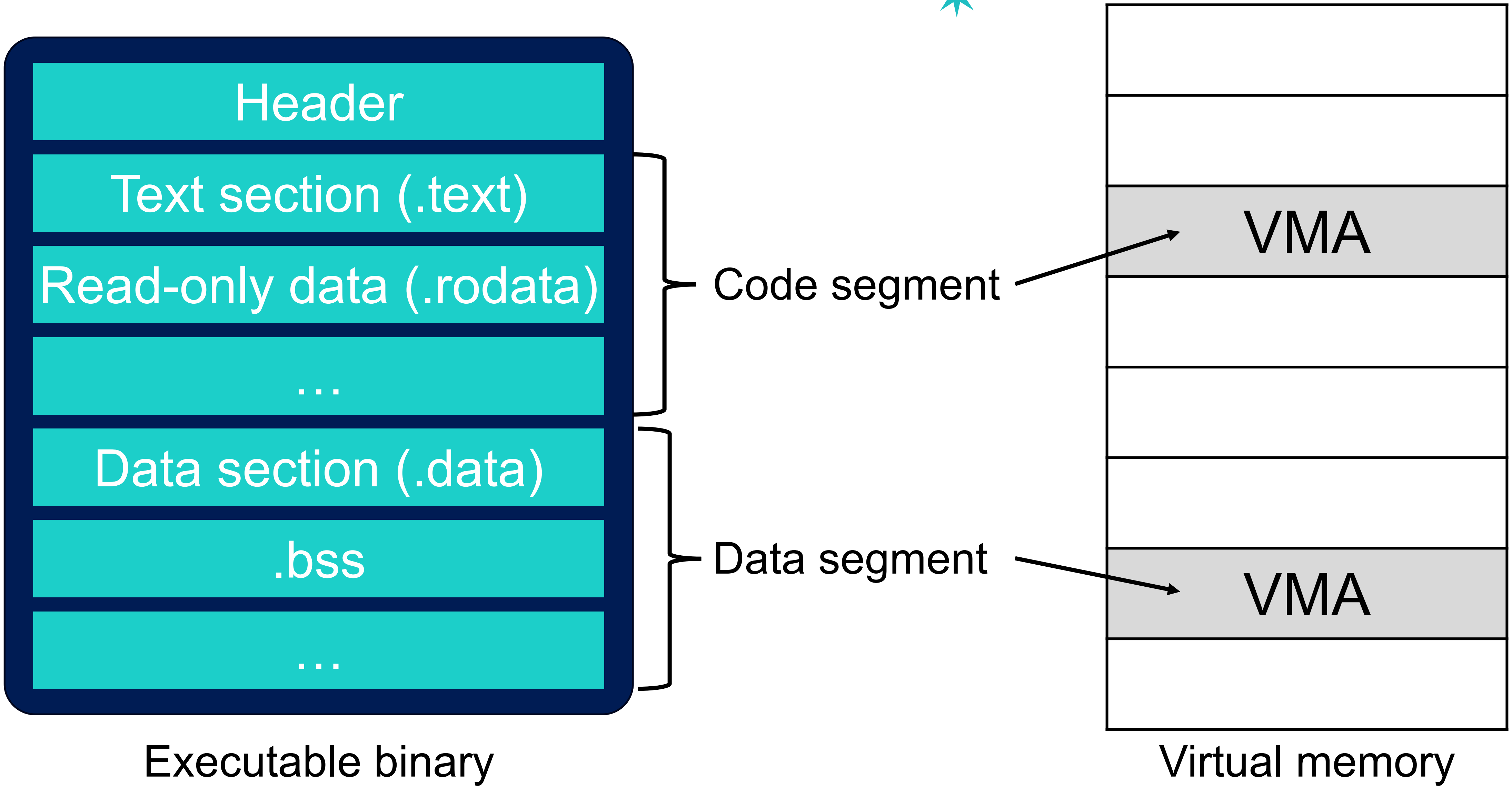

Source
code #1
Source
code #2
…
Source
code #N
Binary code #1
(Object file)
Binary code #2
(Object file)
…
Binary code #N
(Object file)
Executable binary code
(= Executable)
Linking

Executable Binary (=Executable, Binary)



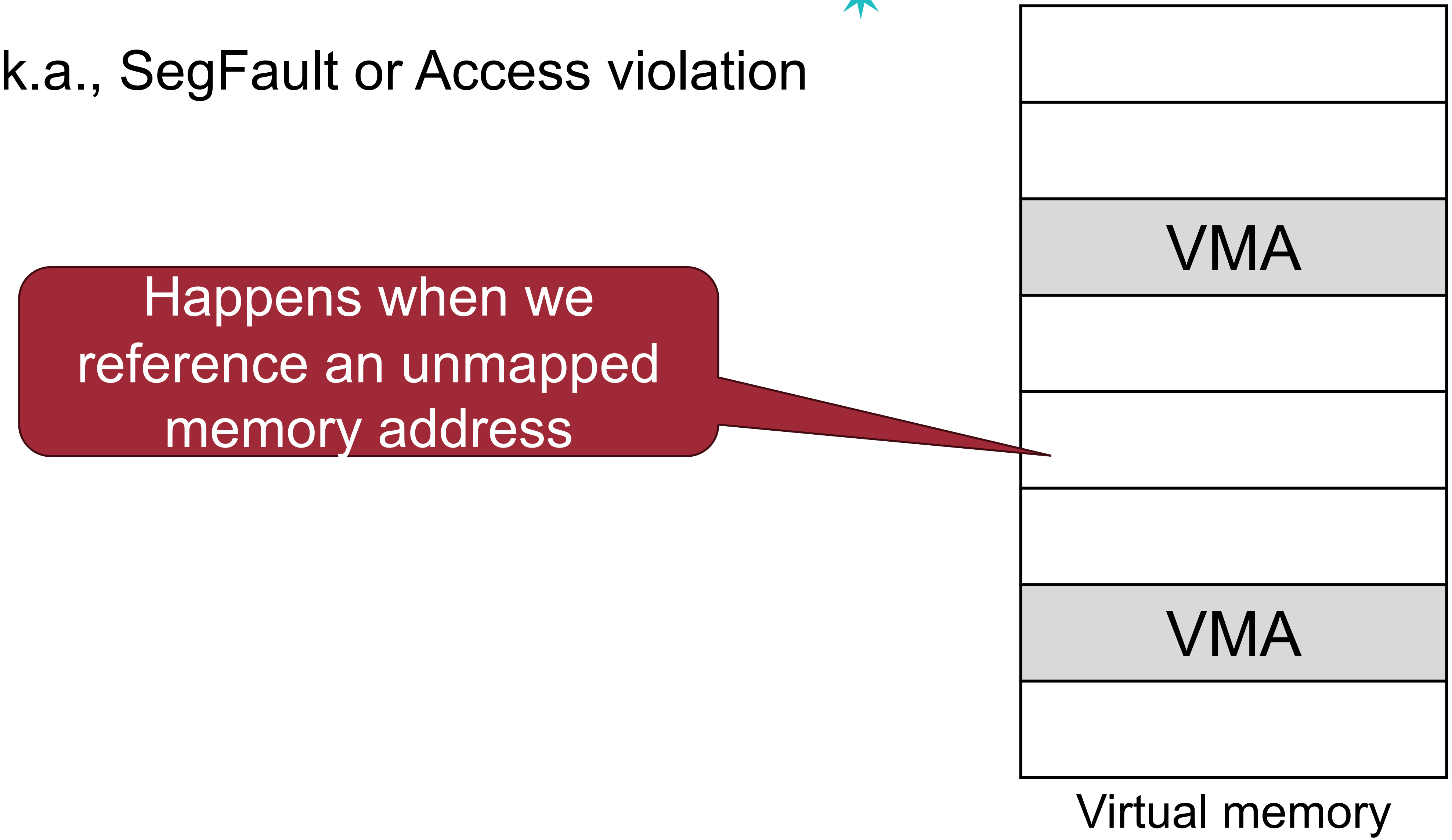
Executable binary

Executable Binary (=Executable, Binary)



Segmentation Fault

- a.k.a., SegFault or Access violation



x86 (IA-32) Architecture

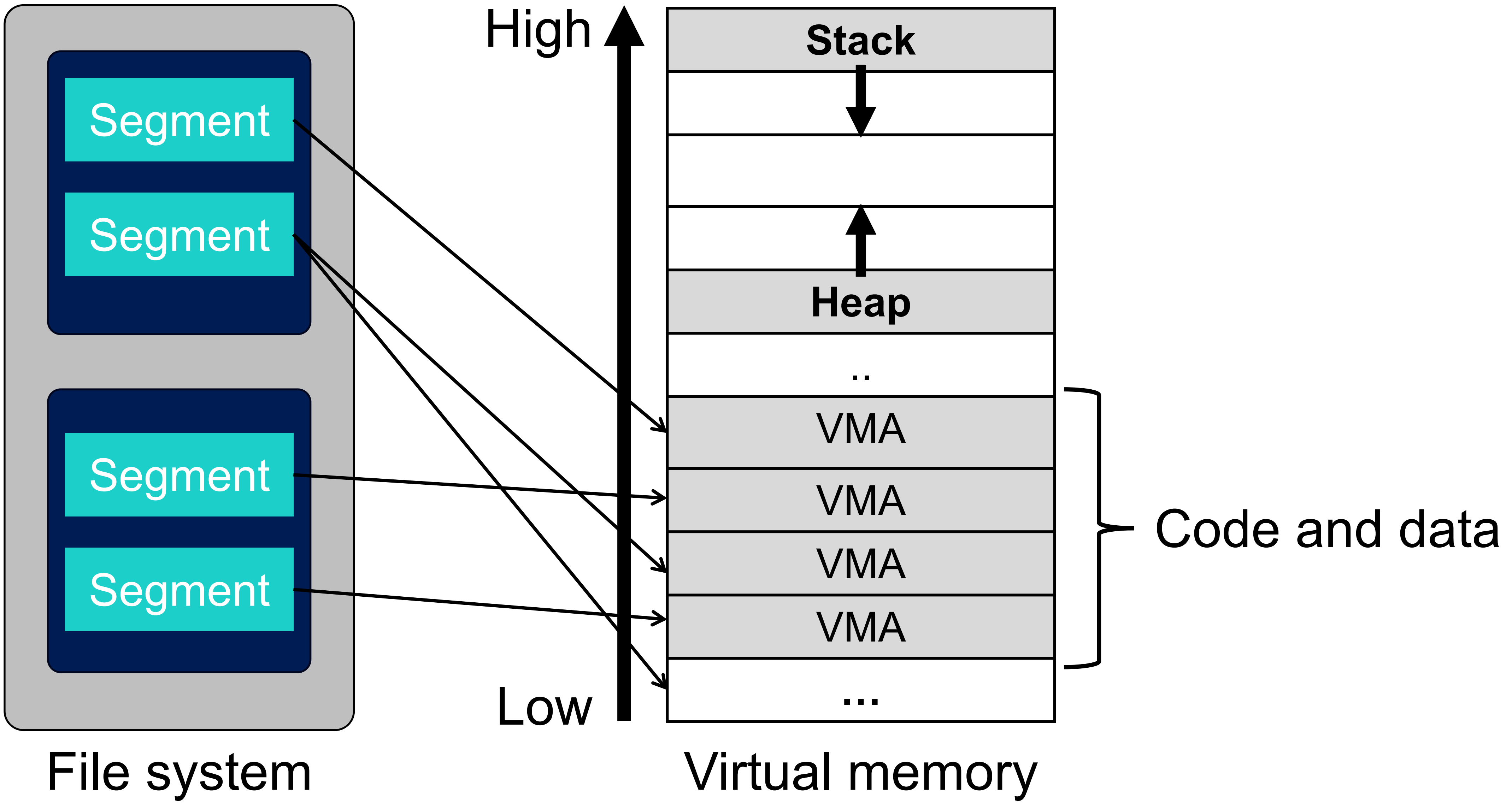
x86 Instruction Set Architecture

- Developed by Intel in 1985
- CISC (Complex Instruction Set Computer) architecture
- **32-bit** address space
- One of the most common architecture

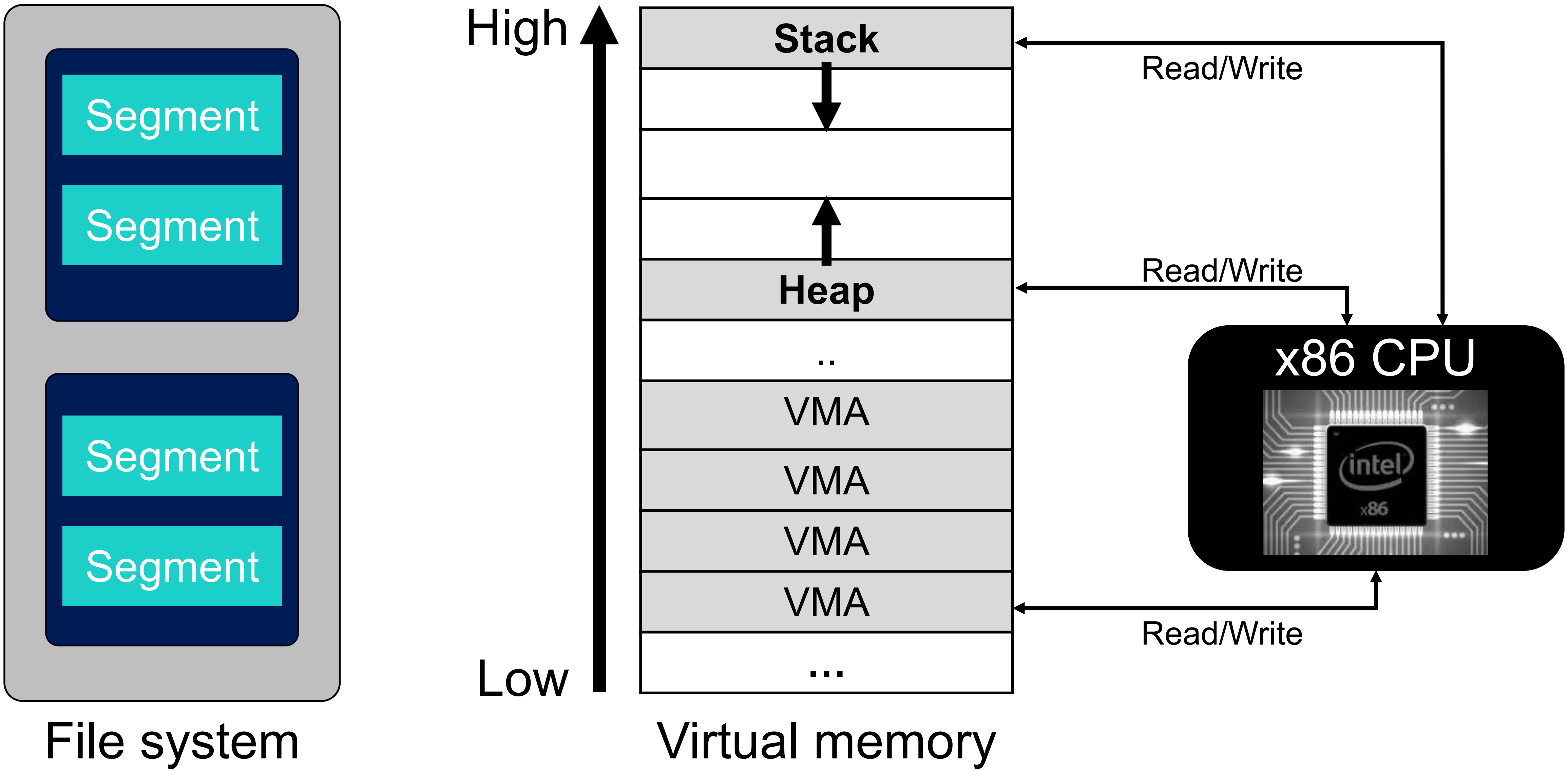
History of x86 ISA

- (**8086**) 16-bit address space (in 1978)
- (**x86 or IA-32**) 32-bit address space (in 1985)
- (**x86-64 or x64 or AMD64**) 64-bit address space (in 2003)

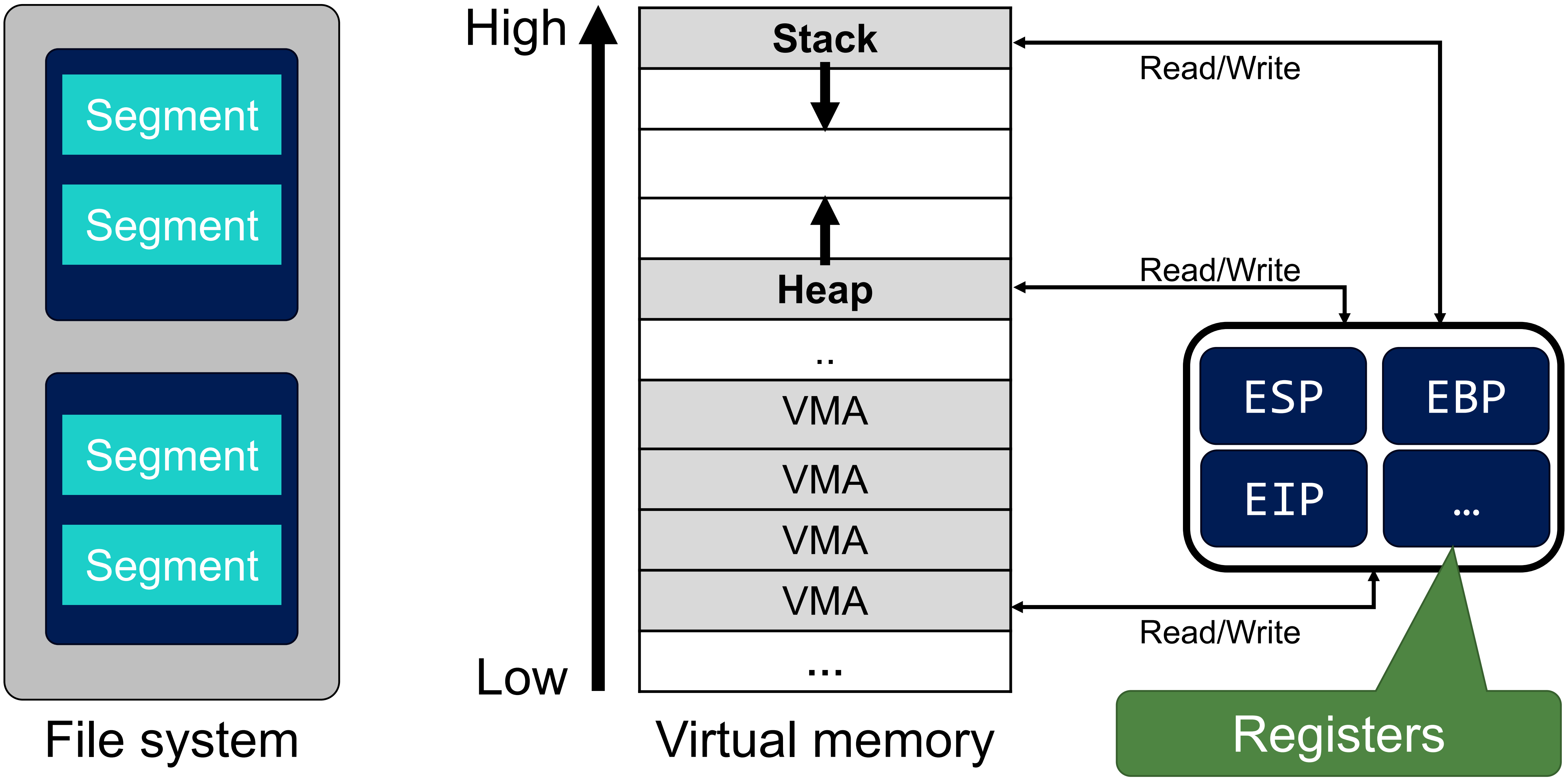
Memory Layout and CPU Registers



Memory Layout and CPU Registers



Memory Layout and CPU Registers



Registers in x86

- Program counter (instruction pointer)
  - **`EIP:`** points to the instruction to execute

- Stack pointers
  - **`ESP:`** points to the top of the stack
  - **`EBP:`** points to the base of the current stack frame

- Status register (FLAGS register)
  - **`EFLAGS:`** contains the current condition flags

- Other general purpose registers
  - **`EAX`**, **`EBX`**, **`ECX`**, **`EDX`**, **`ESI`**, **`EDI`**

All of them have a size of a ***double word*** (=32 bit)

Size of Registers

- x86 registers are 32-bit
- A word is the natural unit of data used by a processor.
  - Typically, a word size is 32 bits on a 32-bit machine
- However, Intel says a word is 16 bits on x86 (32-bit machine)

History of Intel/AMD Processors

- 1978: 8086
- 1982: 80286
- 1985: 80386
- 1989: 80486
- ...
- 2003: Opteron
- 2005: Prescott
- 2006: Core 2
- 2008: Core i7
- …

16-bit processor,
Registers (`SP, BP, IP, ...`)

32-bit processor,
Registers (ESP, EBP, EIP, ...)

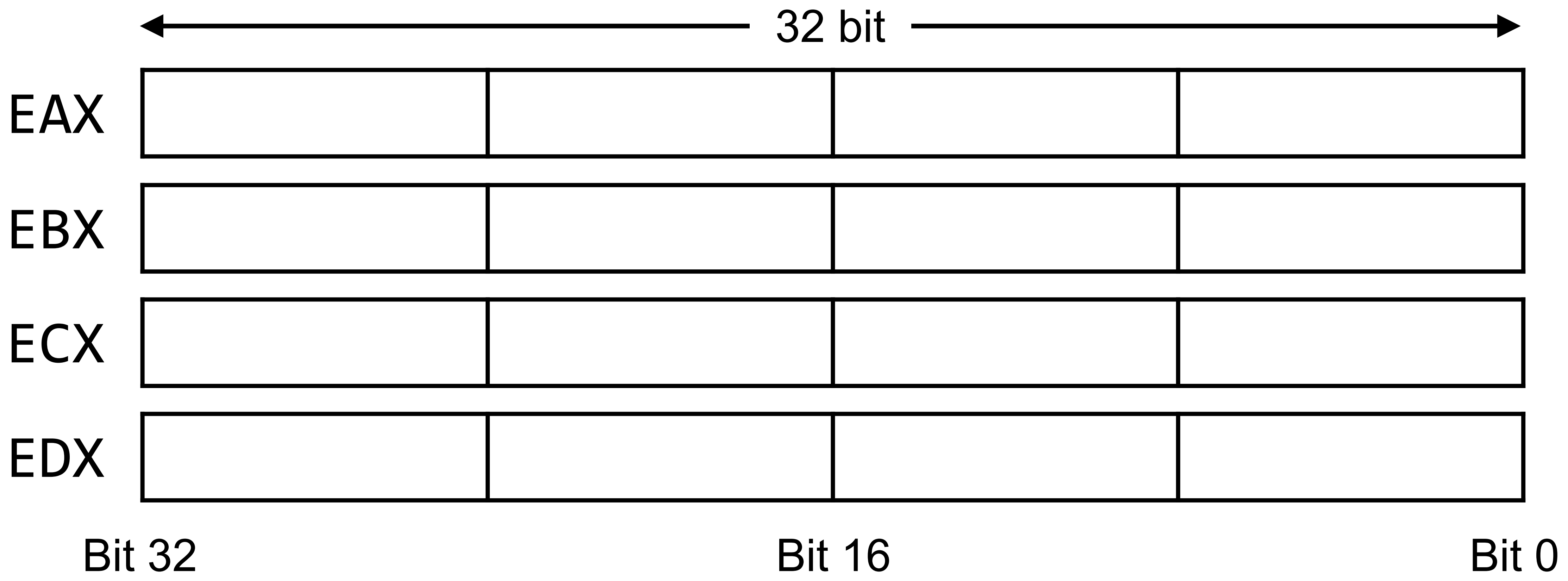
64-bit processor,
Registers (RSP, RBP, RIP, ...)

x86 Convention

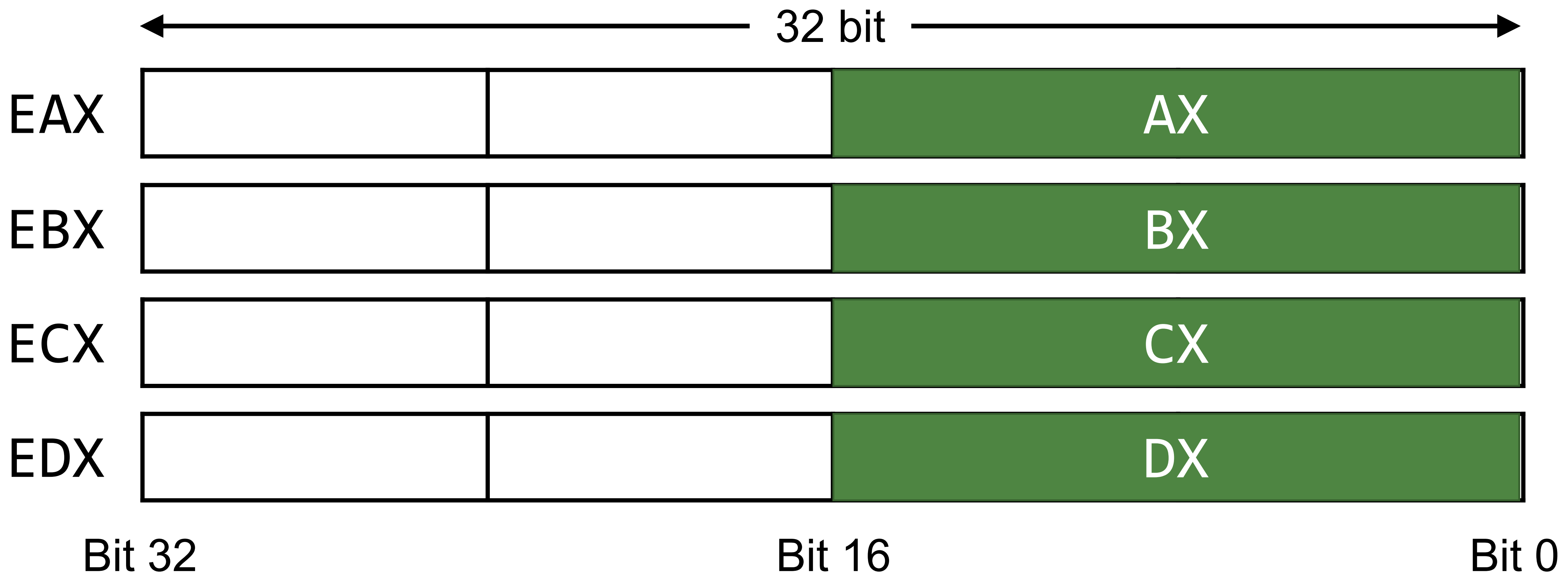
- Word = 16 bits
- Double Word (DWORD) = 32 bits

- Linear address space = 0 ~ 2^{32} bits

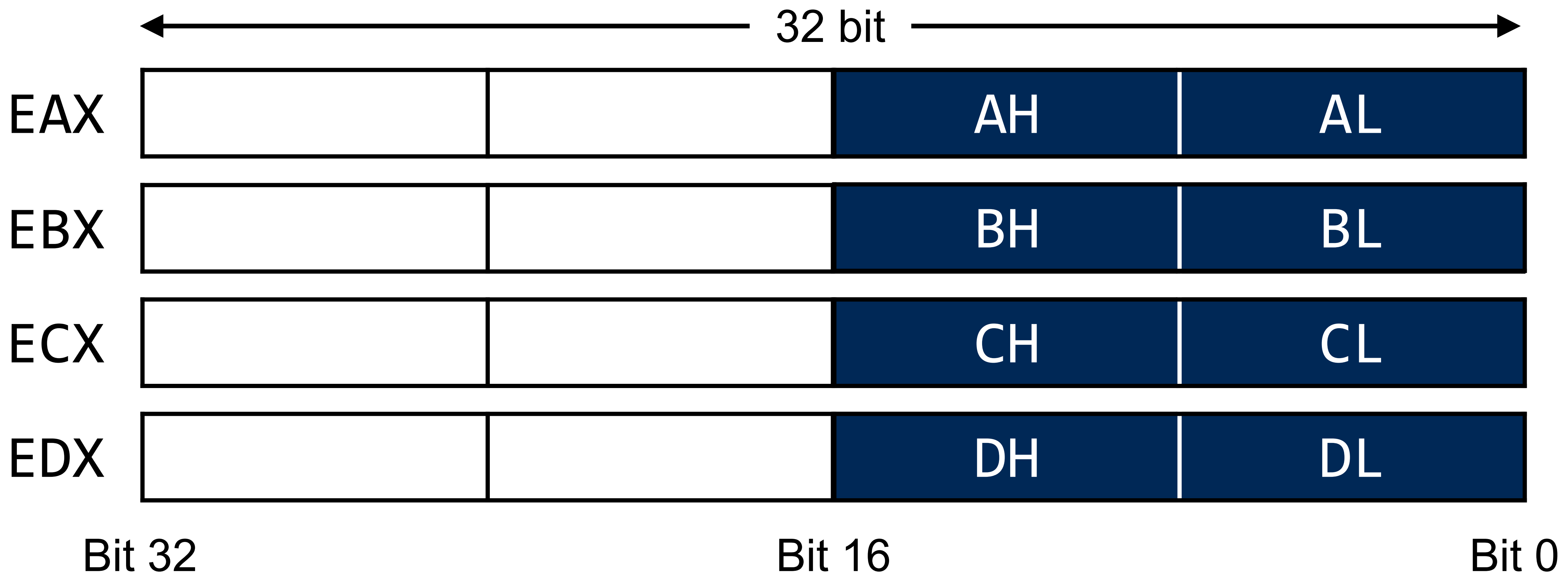
x86 Register Access



x86 Register Access



x86 Register Access



x86 Register Access

32 bit

EAX	11110000	00000000	00000000	00000111

AX	00000000	00000111
AH	00000000	
AL		00000111

x86 Memory Access = Byte Addressing

x86 Assembly Basics

Basic Formats

- Three formats of Instructions
  - 2 operands
  - 1 operands
  - 0 operands

Basic Format #1: Instructions with 2 Operands


```
mov eax, ebx
```

Opcode

Operand 1

Operand 2

Basic Format #2: Instructions with 1 Operand


```
inc eax
```

Opcode

Operand

Basic Format #3: Instructions with 0 Operand


```
ret
```

Opcode

Intel vs AT&T Format

- There are two ways to represent x86 assembly code

AT&T

```
mov %eax, %ebx
```

Intel

```
mov ebx, eax
```

We will use the Intel syntax

Recap: Compilation Process

Source code

Intermediate code

Assembly code

Binary code

test.ll

```
define dso_local i32 @test(i32) #0
{
  %2 = alloca i32, align 4
  store i32 %0, i32* %2, align 4
  ret i32 32
}
```

Intel syntax

```
$llc --x86-asm-syntax=intel test.ll
```

test.s

```
test:                    # @test
# %bb.0:
push ebp
mov ebp, esp
mov eax, dword ptr [ebp + 8]
mov eax, 32
pop ebp
ret
```

The last human-readable format

Recap: GNU objdump

Source code

↓

Intermediate code

↓

Assembly code

↓ ↑

Binary code

```
00000000 <test>:
 0: 55                 push ebp
 1: 89 e5              mov  ebp,esp
 3: 8b 45 08           mov  eax,DWORD PTR [ebp+0x8]
 6: b8 20 00 00 00     mov  eax,0x20
 b: 5d                 pop  ebp
 c: c3                 ret
```

```
$ objdump -M intel -d test.o
```

Intel syntax

test.o

```
01000100101001010010101001010 0
10101001011110100100010010101 0
10111000101010001011100101001 1
```

Opcode Decides Semantics

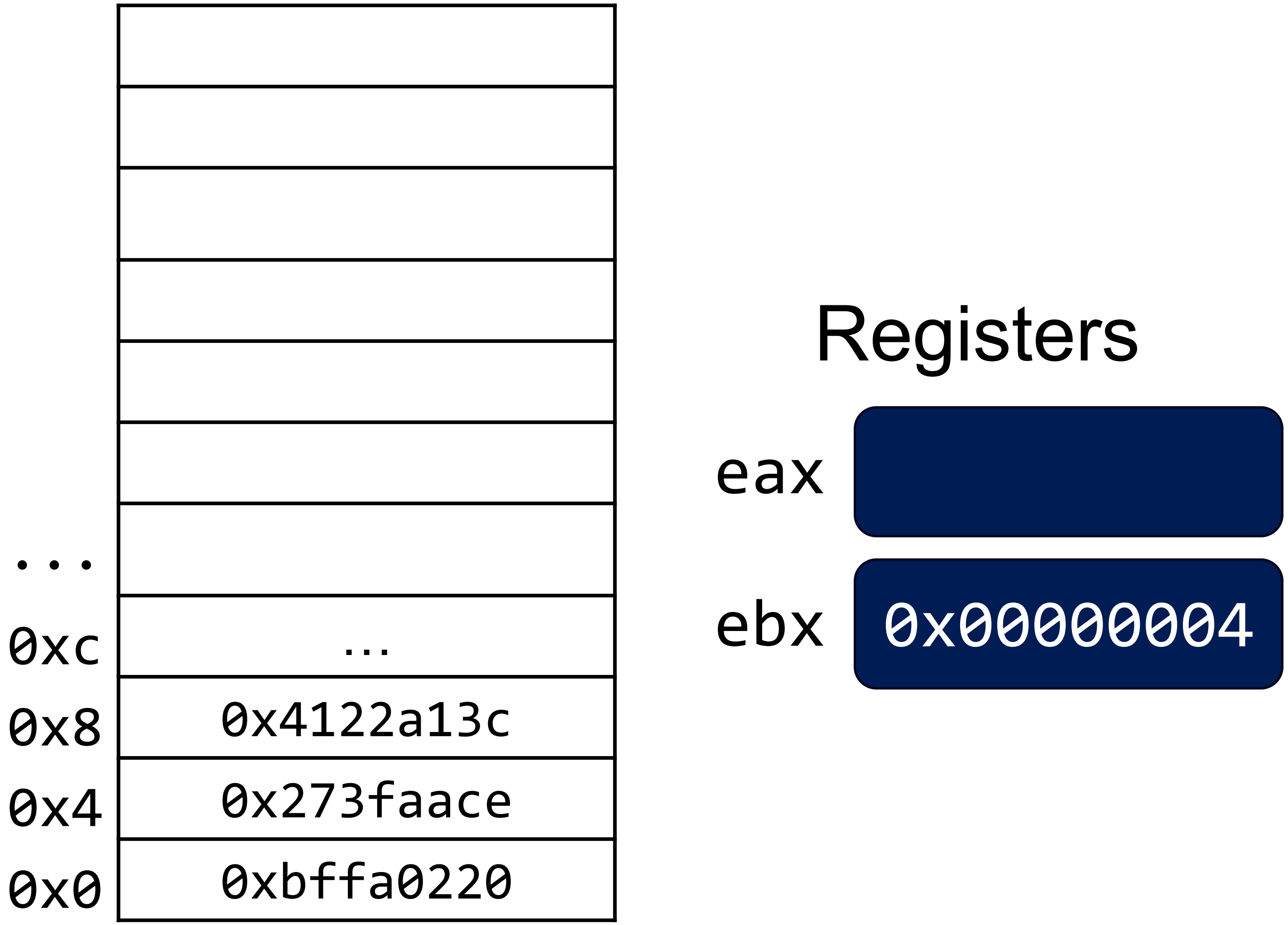
`mov eax, ebx`	eax ← ebx
`sub esp, 0x8`	esp ← esp − 0x8
`inc eax`	eax ← eax + 1

Operand Types

Memory pointed by ebx

```
mov eax, [ebx]
```

Register

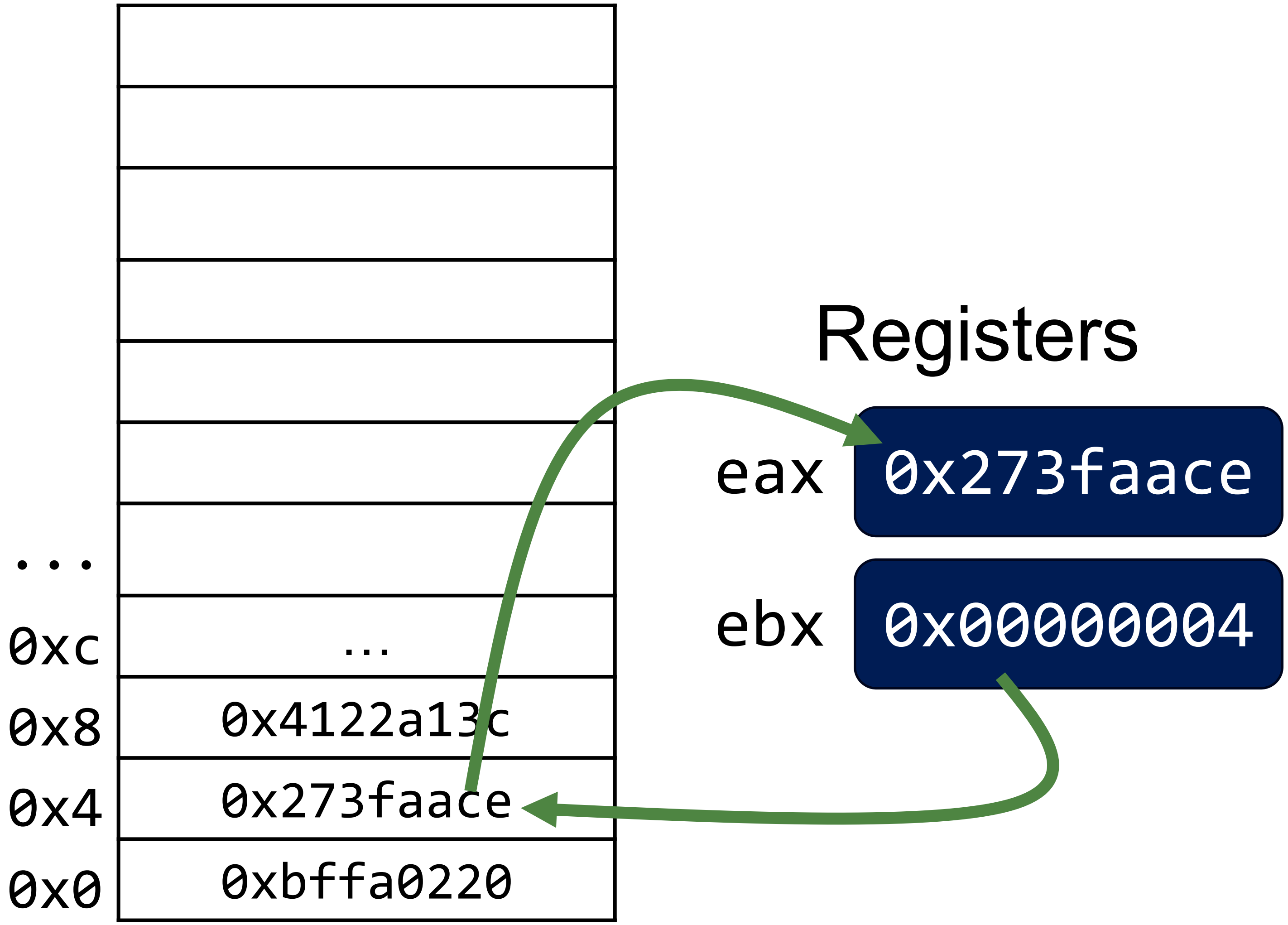


Operand Types

Memory pointed by ebx

```
mov eax, [ebx]
```

Register



Operand Types

```
sub esp, 0x8
```

Constant integer

esp 0x0000000a

esp 0x00000002

Operand Types

```
mov cl, BYTE ptr [eax]
```

Pointer directive

Pointer Directive

```
mov [esi], al     ; ok
mov [esi], 1      ; error
```

Error: ambiguous operand size for ‘mov’

Because it could be any of the followings

- `mov BYTE PTR [esi], 1`
- `mov WORD PTR [esi], 1`
- `mov DWORD PTR [esi], 1`
- `mov QWORD PTR [esi], 1`

Therefore, we need pointer directive ☺

Moving Data Around (mov)

- `mov eax, ebx`
- `mov al, bl`

Register to Register

- `mov [eax], ebx`

Register to Memory

- `mov eax, [ebx]`
- `mov eax, [ebx + edx * 4]`
- `mov al, BYTE PTR [esi]`

Memory to Register

- `mov eax, 42`
- `mov BYTE PTR [eax], 42`

Constant to Memory/Register

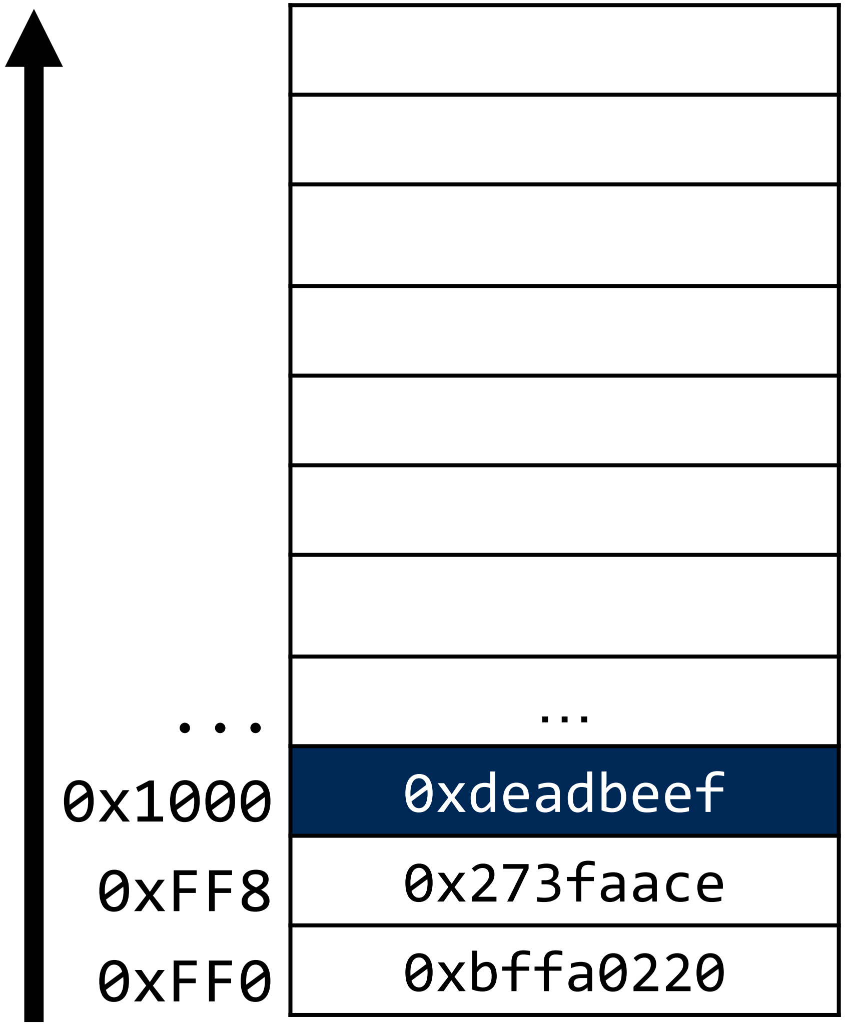
Example: Storing a DWORD in Memory

```
mov [eax], 0xdeadbeef      ; eax = 0x1000
```

...	...
0x1000	0xdfaa2312
0xFF8	0x273faace
0xFF0	0xbffa0220

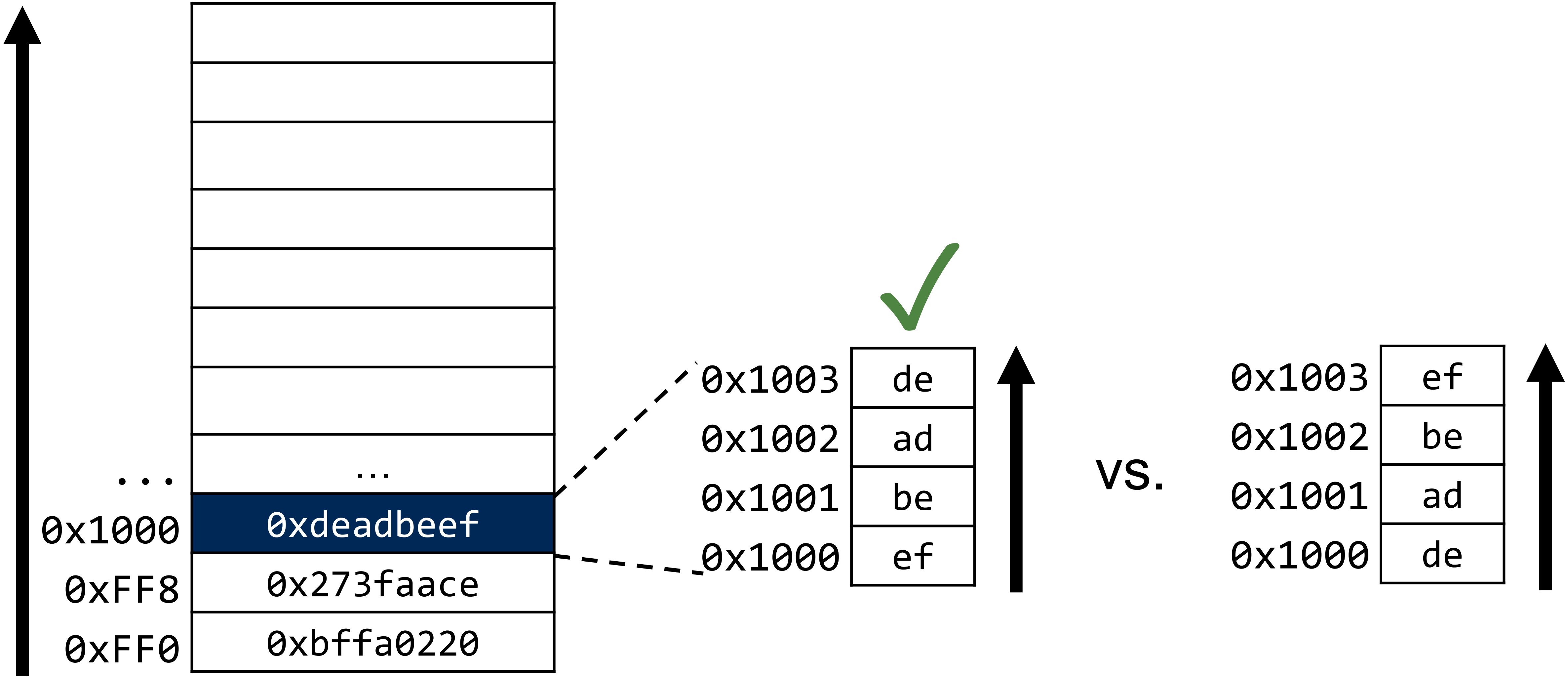
Example: Storing a DWORD in Memory

```
mov [eax], 0xdeadbeef      ; eax = 0x1000
```



Example: Storing a DWORD in Memory

```
mov [eax], 0xdeadbeef     ; eax = 0x1000
```



Endianness

- The order in which a sequence of bytes are stored in memory

- Big Endian = The MSB goes to the lowest address
- Little Endian = The LSB goes to the lowest address

x86 uses Little Endian

Exercise

Pointer directive

```
mov cl, BYTE ptr [eax]
```

Address	Memory
...	
0xc	...
0x8	0x4122a13c
0x4	0x273faace
0x0	0xbffa0220

Registers

eax	0x00000009
ecx	0x13ef0c2e

What value will be after the instruction?

Addressing Modes

Specify how a memory operand is interpreted to derive an effective address

- **`register`**
  - ✓`mov eax, [eax]`
- **`register + register`**
  - ✓`mov eax, [eax + ebx]`
- **`displacement`**
  - ✓`mov eax, [0x1000]`
- **`register + register × scale + displacement`**
  - ✓`mov eax, [eax + ebx * 4 + 0x1000]`

Addressing Modes

Specify how a memory operand is interpreted to derive an effective address

$$\left[\begin{Bmatrix} \texttt{eax} \\ \texttt{ebx} \\ \texttt{ecx} \\ \texttt{edx} \\ \texttt{esp} \\ \texttt{ebp} \\ \texttt{esi} \\ \texttt{edi} \end{Bmatrix} + \begin{Bmatrix} \texttt{eax} \\ \texttt{ebx} \\ \texttt{ecx} \\ \texttt{edx} \\ \texttt{ebp} \\ \texttt{esi} \\ \texttt{edi} \end{Bmatrix} \times \begin{Bmatrix} 1 \\ 2 \\ 4 \\ 8 \end{Bmatrix} + \begin{matrix} \texttt{displacement} \\ \text{(32-bit integer)} \end{matrix} \right]$$

Loading Address (lea)

```
lea eax, [ebx]
lea eax, [ebp-0x8]
```

Memory address to Register

What is the Difference?

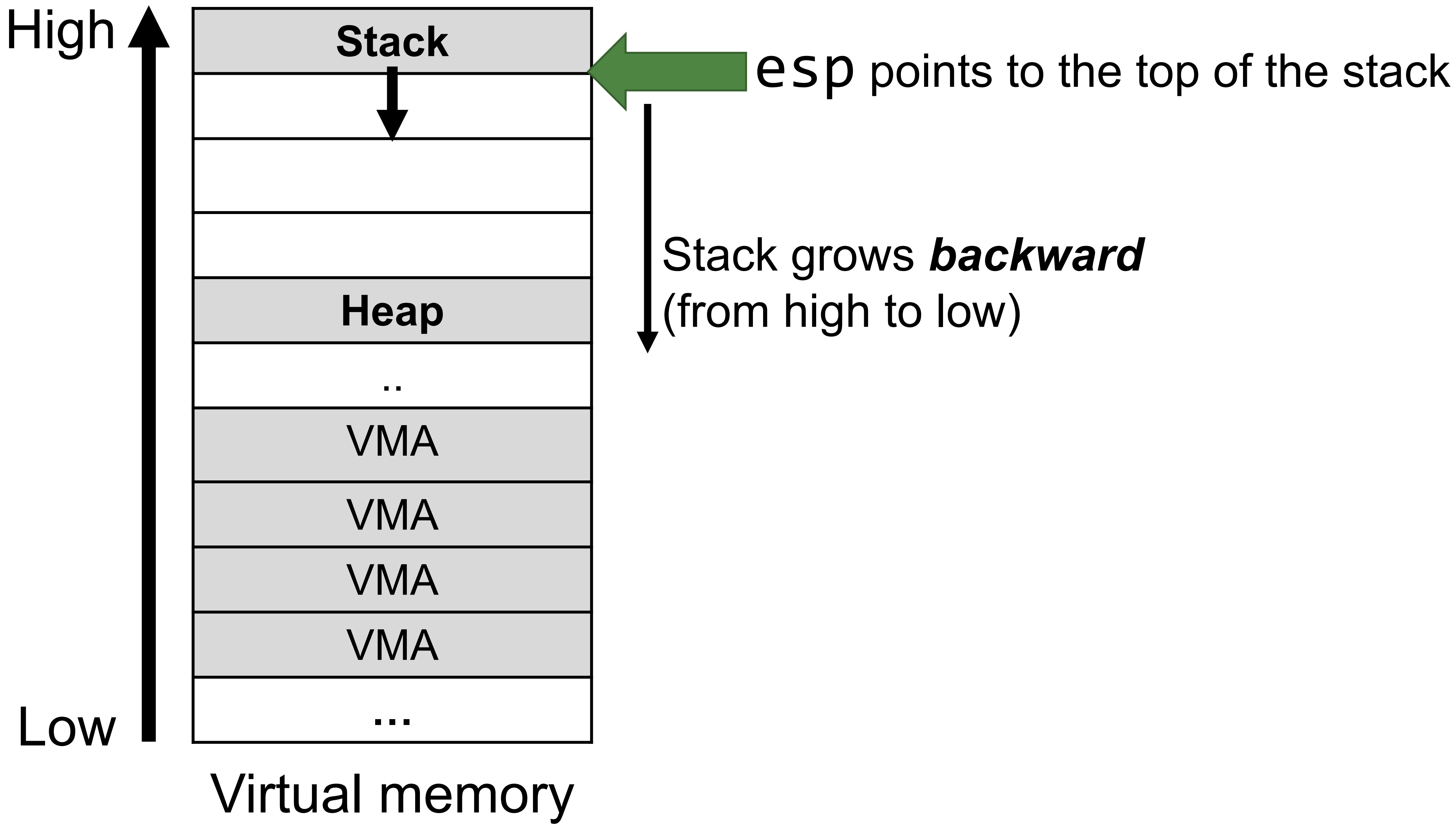

```
mov eax, [ebp + 0x10]
vs.
lea eax, [ebp + 0x10]
```

```
eax ← *(ebp + 0x10)
vs.
eax ← (ebp + 0x10)
```

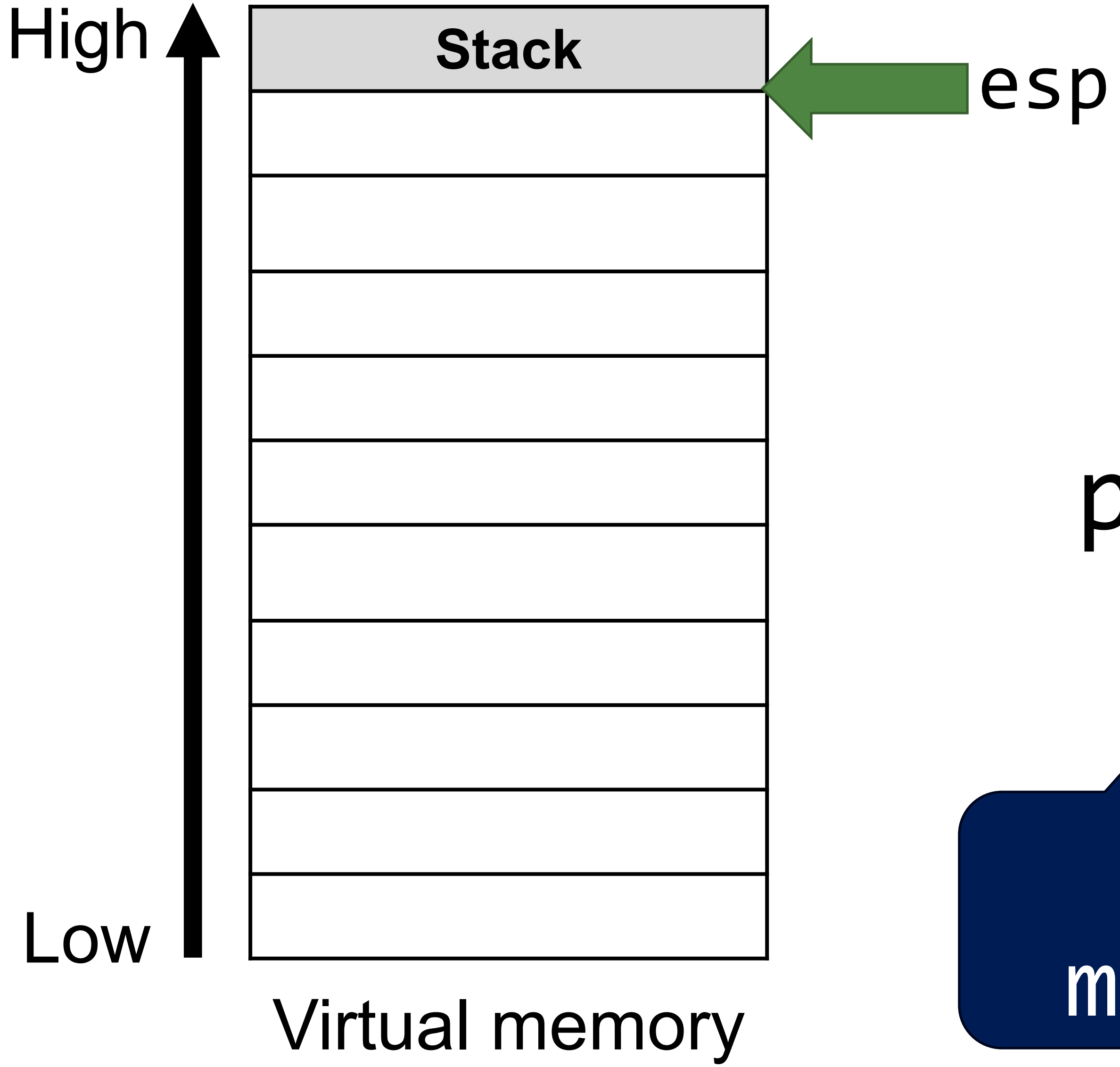
Stack Memory

- Stack stores data in a LIFO (Last-In-First-Out) fashion. When a function is invoked, a new ***stack frame*** is allocated at the top of the stack memory

Stack Operations



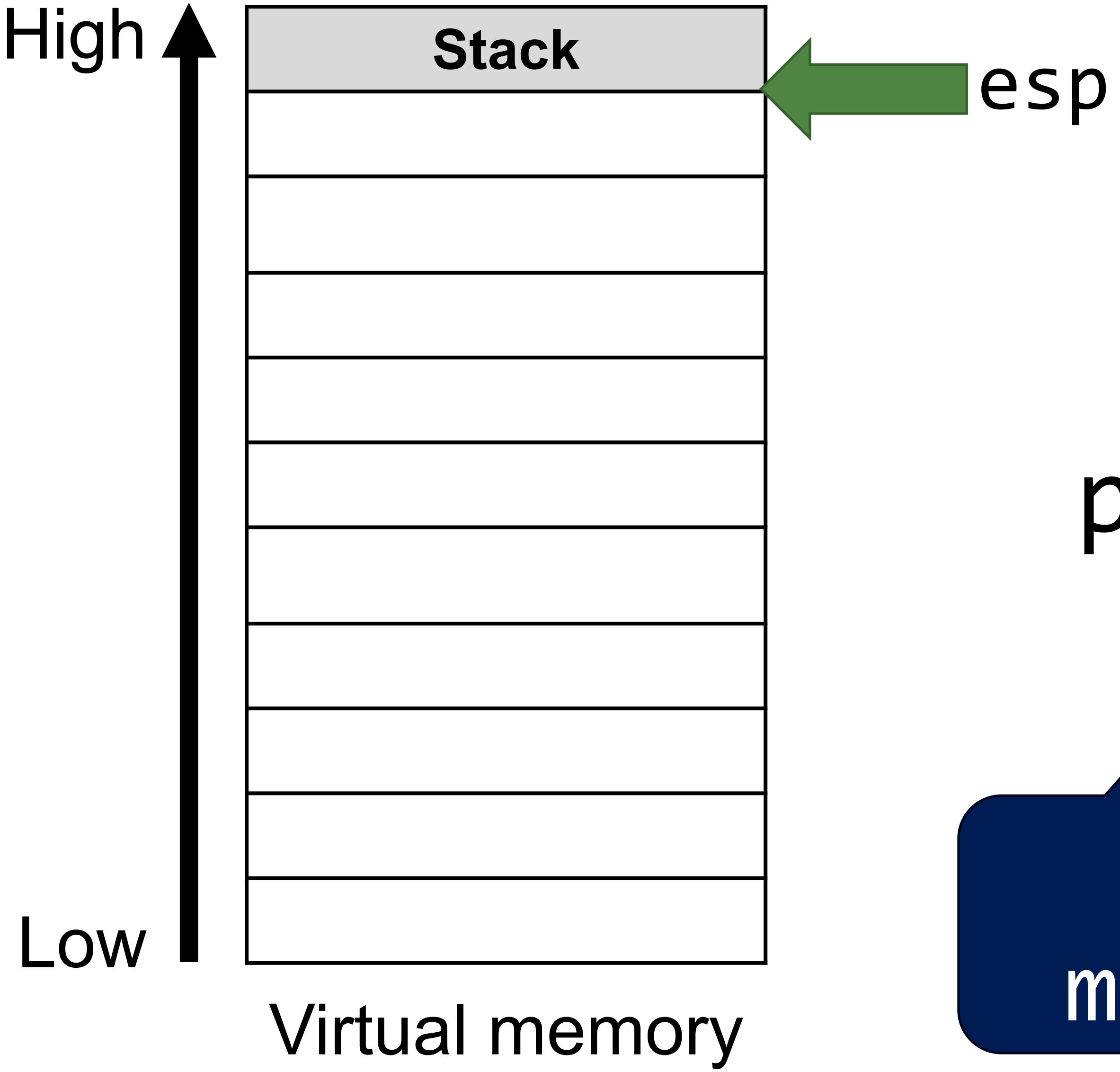
Stack Operations – Push and Pop




```
push eax
```

```
sub esp, 4
mov [esp], x
```

Stack Operations – Push and Pop



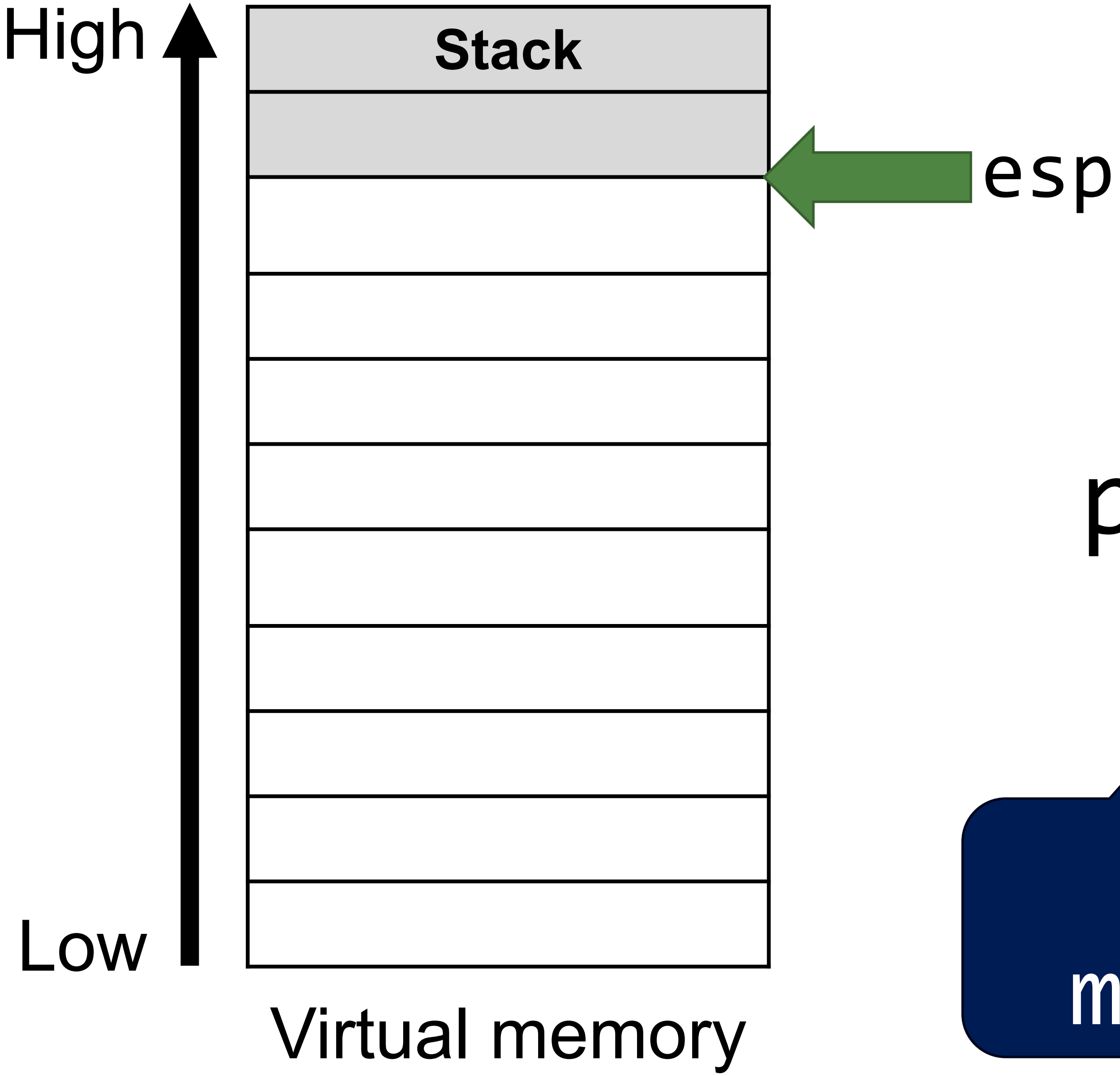

```
push eax
```

Registers

eax `0x00000009`

```
sub esp, 4
mov [esp], x
```

Stack Operations – Push and Pop



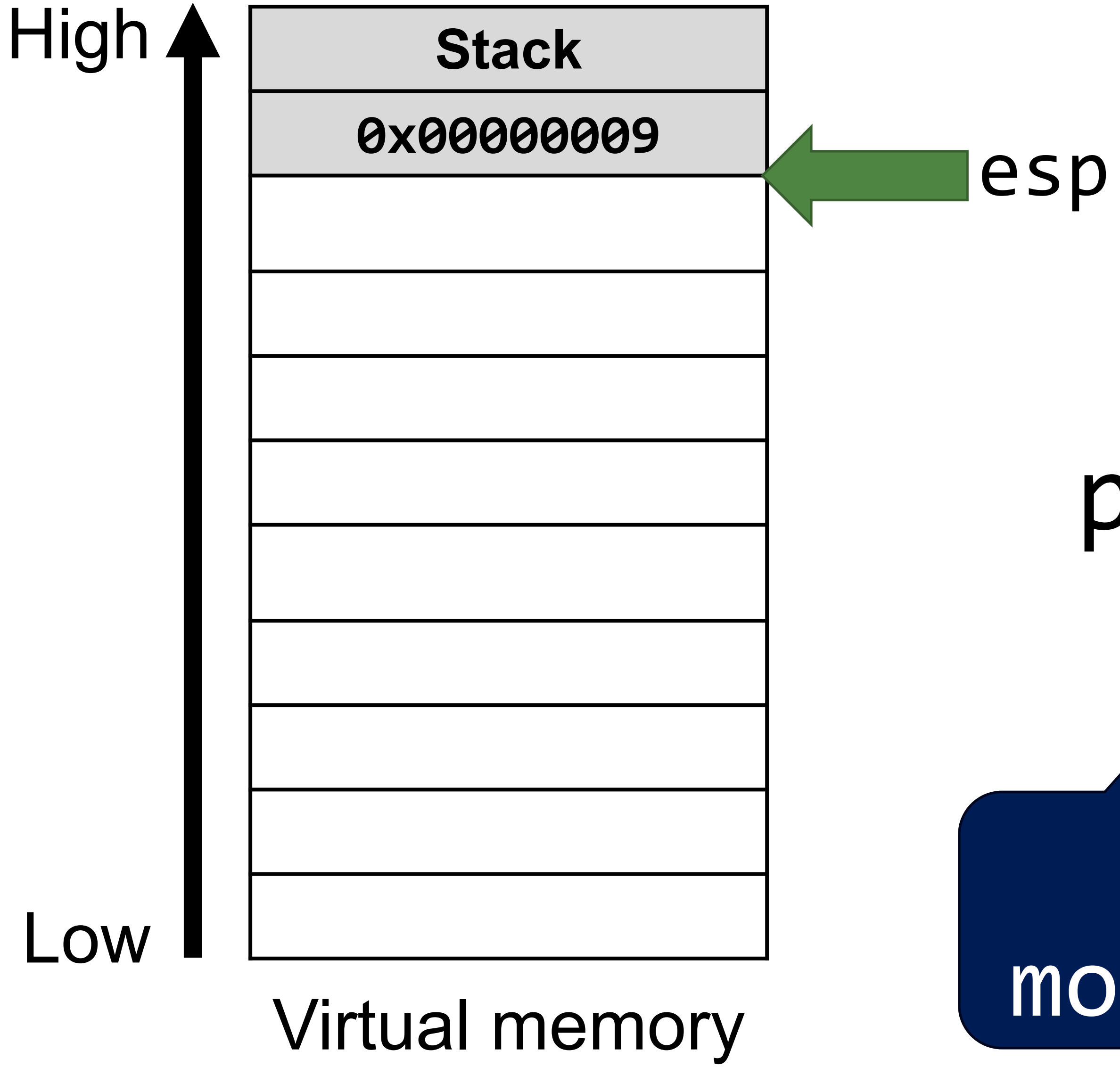

```
push eax
```

Registers

eax `0x00000009`

```
sub esp, 4
mov [esp], x
```

Stack Operations – Push and Pop




```
push eax
```

```
sub esp, 4
mov [esp], eax
```

Registers

eax `0x00000009`

Stack Operations (push)

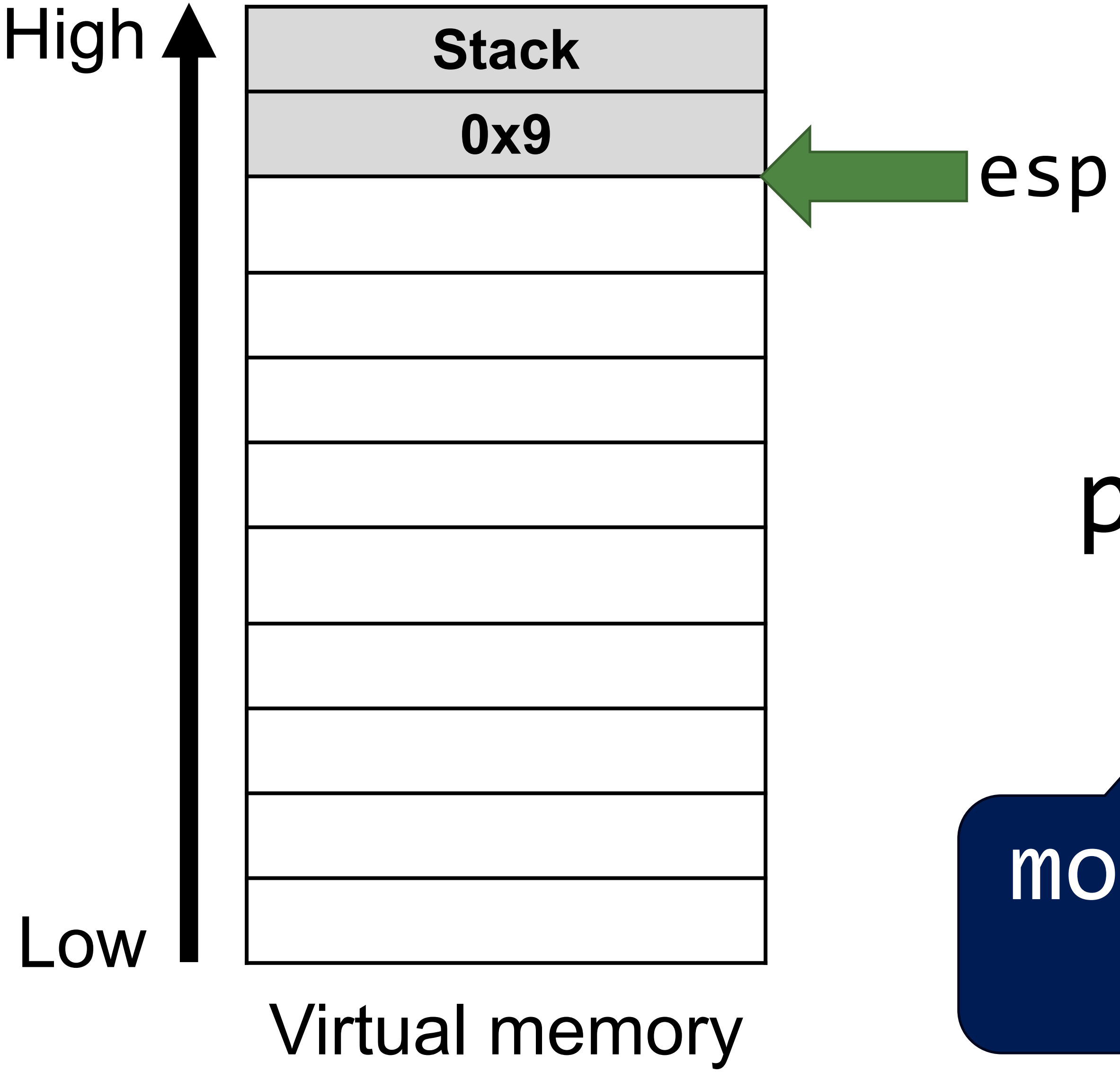
`push eax`	Push register on the stack
`push 0x42`	Push constant on the stack
`push [eax]`	Push a value at the memory address on the stack

```
push x
```

=

```
sub esp, 4
mov [esp], x
```

Stack Operations – Push and Pop



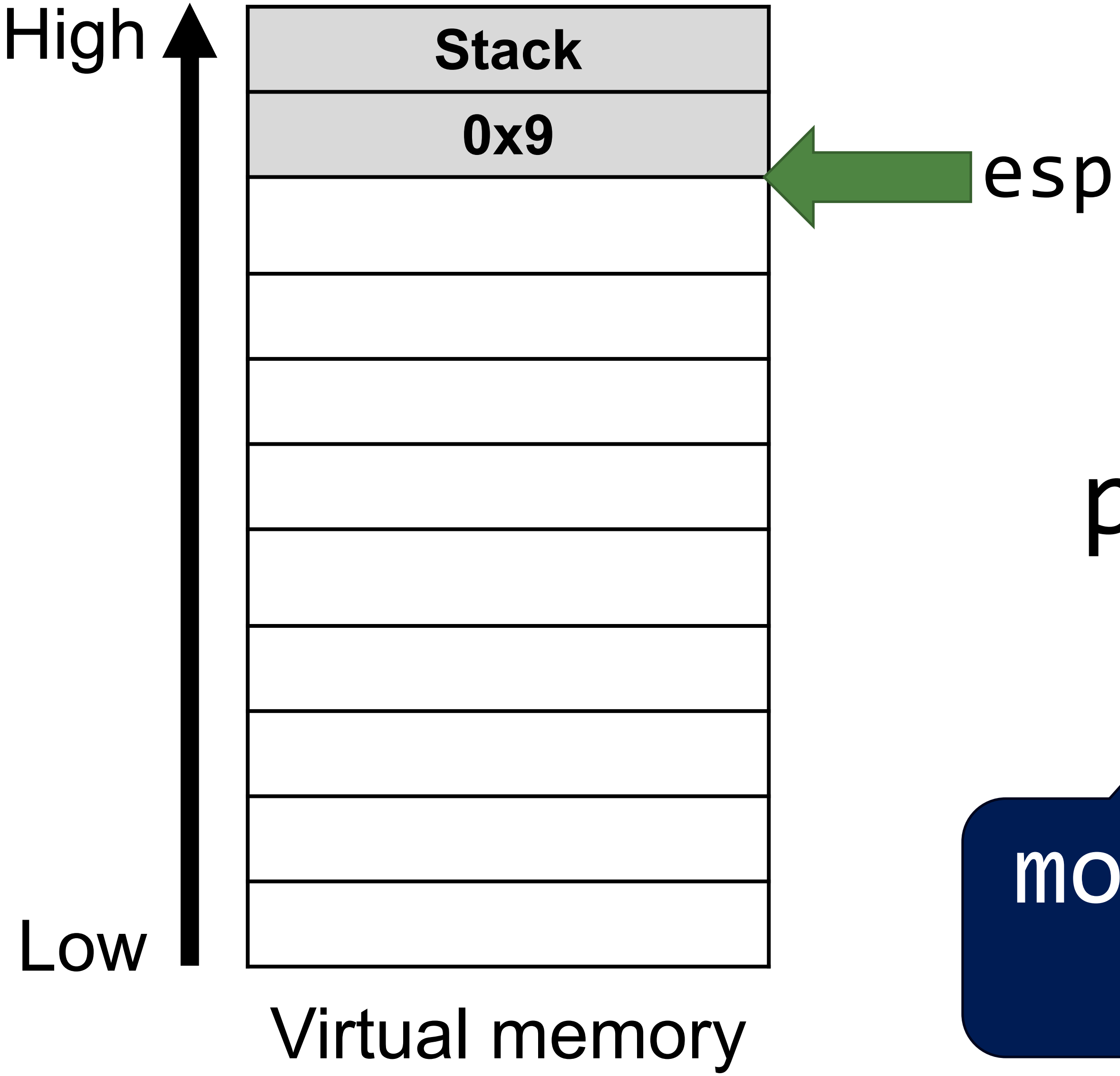

```
pop ecx
```

```
mov ecx, [esp]
add esp, 4
```

Registers

ecx `0x13ef0ca1`

Stack Operations – Push and Pop



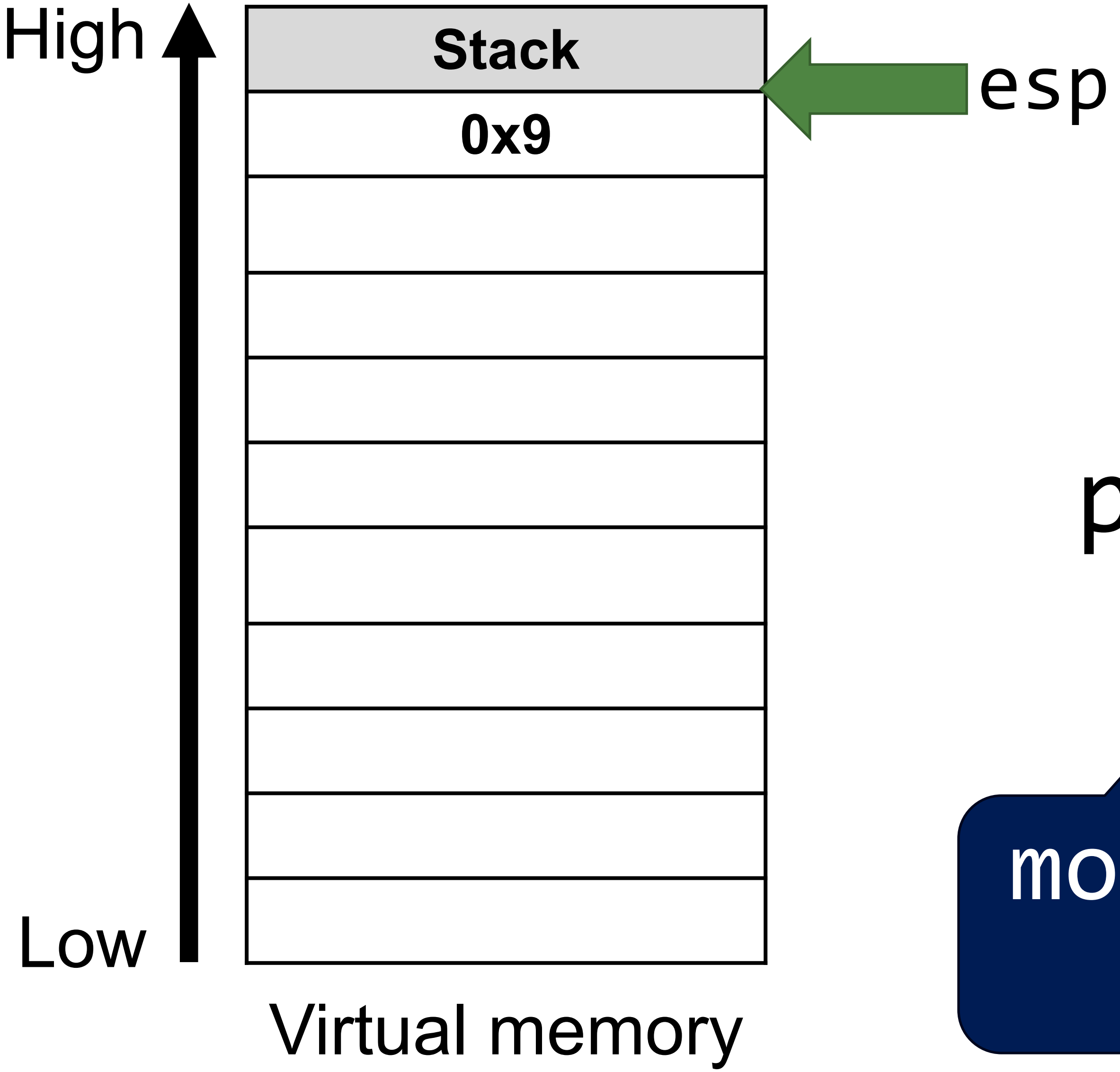
pop ecx

```
mov ecx, [esp]
add esp, 4
```

Registers

ecx 0x00000009

Stack Operations – Push and Pop



pop ecx

```
mov ecx, [esp]
add esp, 4
```

Registers

Stack Operations (pop)

`pop eax`	Pop the top element of the stack into register
`pop [eax]`	Pop the top element of the stack into the memory address

```
pop x
```

=

```
mov x, [esp]
add esp, 4
```

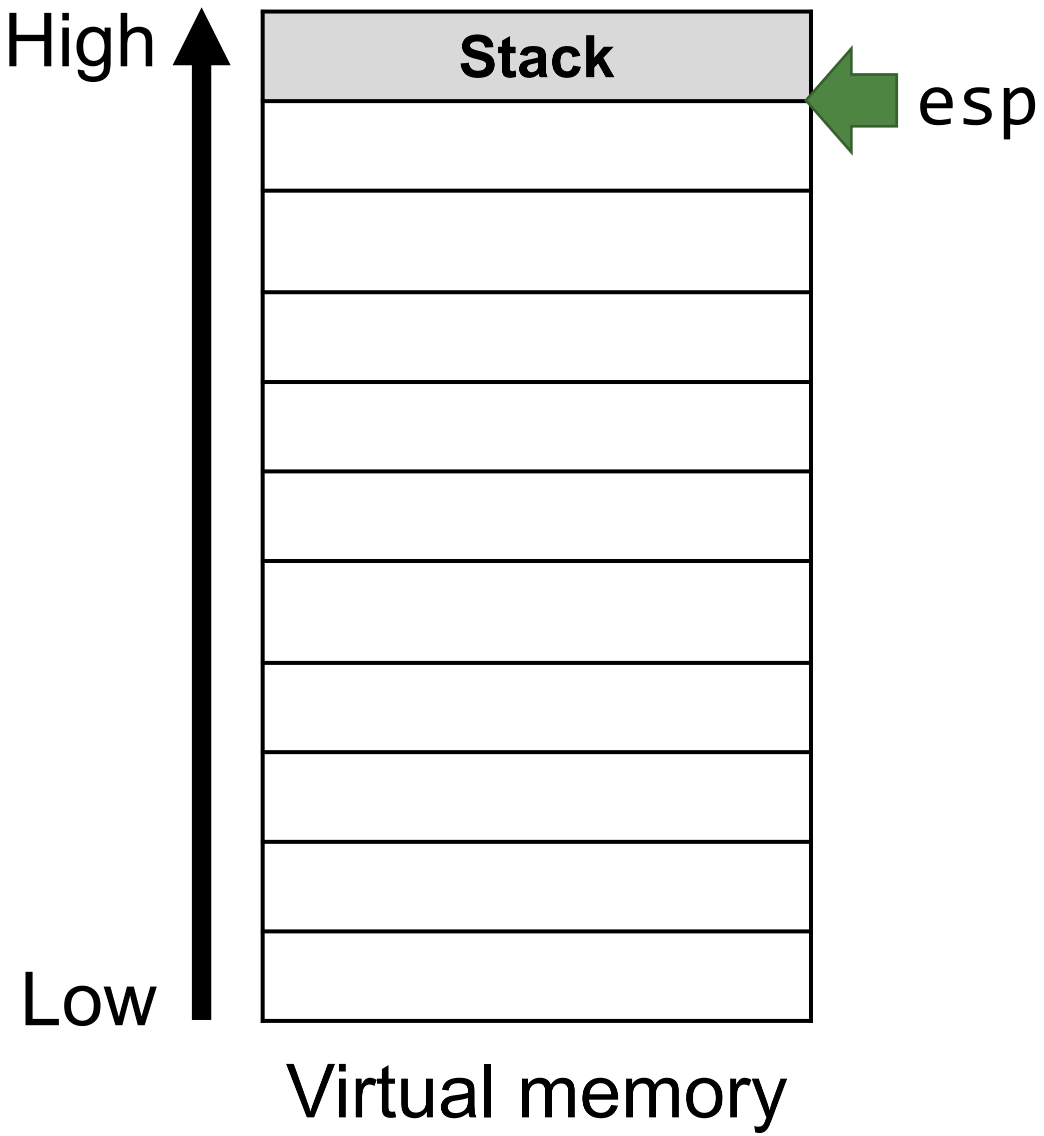
Stack Operations (leave)

```
leave = mov esp, ebp
        pop ebp
```

Function Call (call)

```
    …
    call foo
nextret:
    nop
    nop
    ...
foo:
    nop
    nop
```

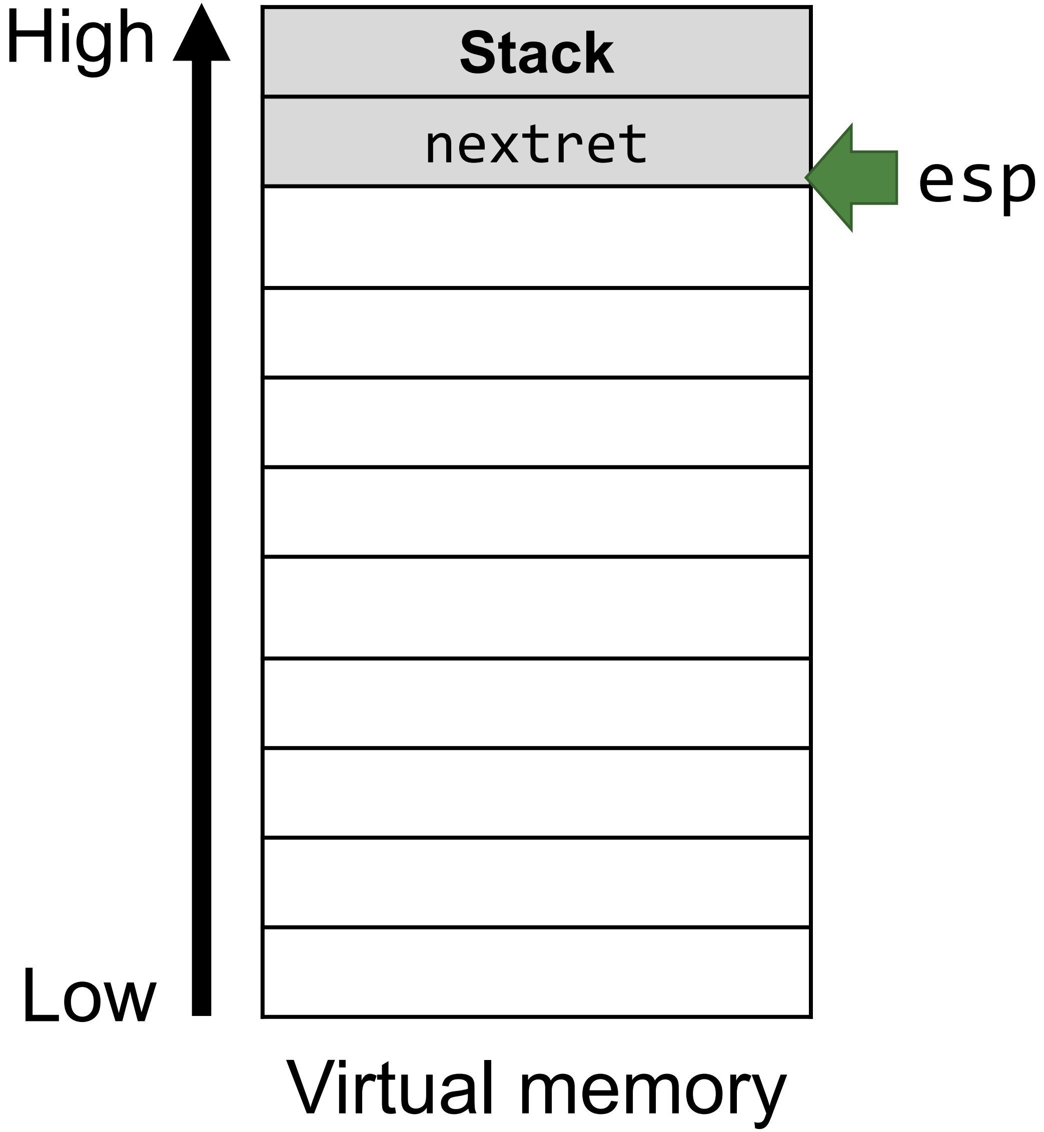
```
push nextret
jmp foo
```



Function Call (call)

```
    …
    call foo
nextret:
    nop
    nop
    ...
foo:
    nop
    nop
```

```
push nextret
jmp foo
```

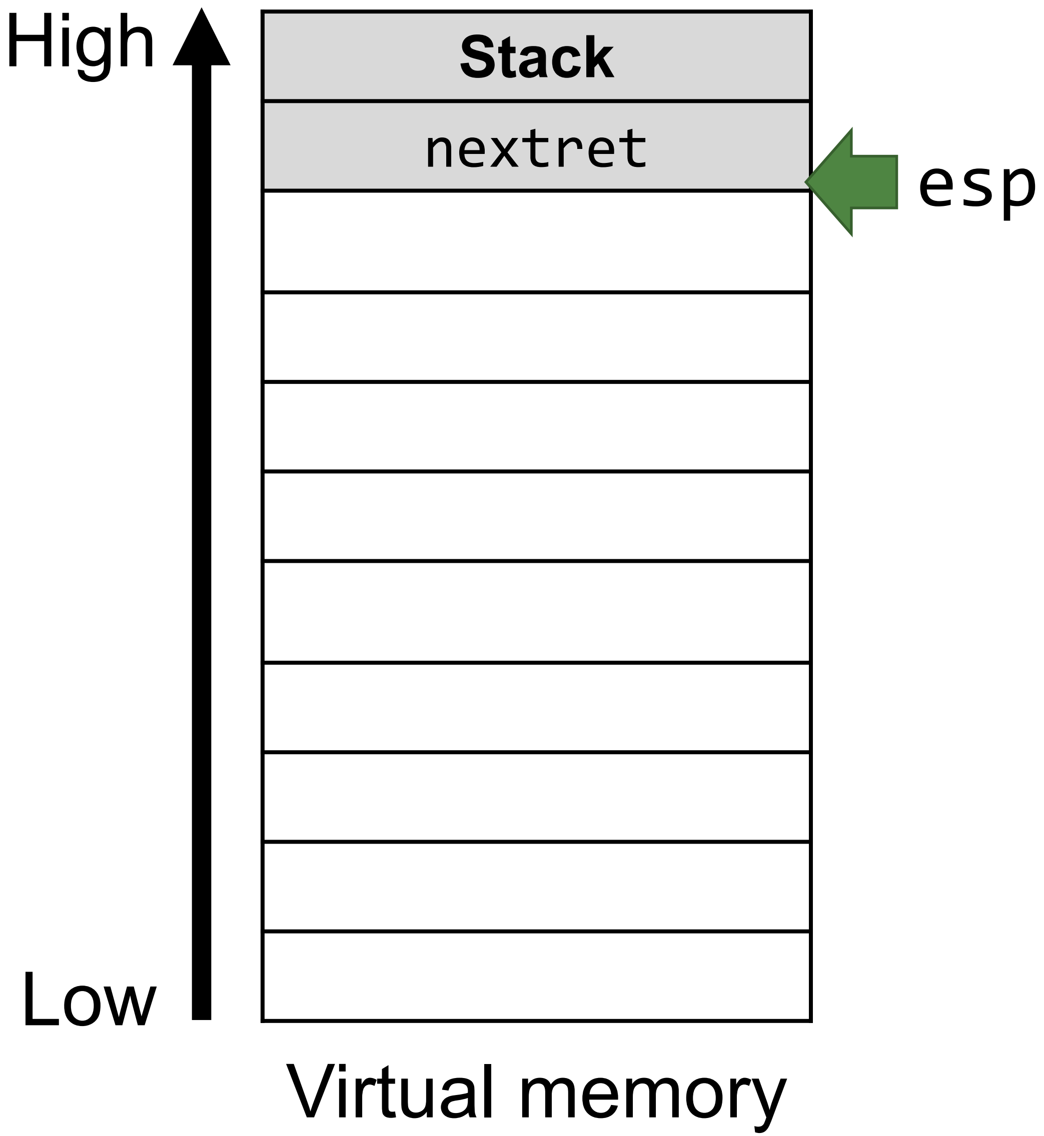


Function Call (call)

```
    …
    call foo
nextret:
    nop
    nop
    ...
foo:
    nop
    nop
```

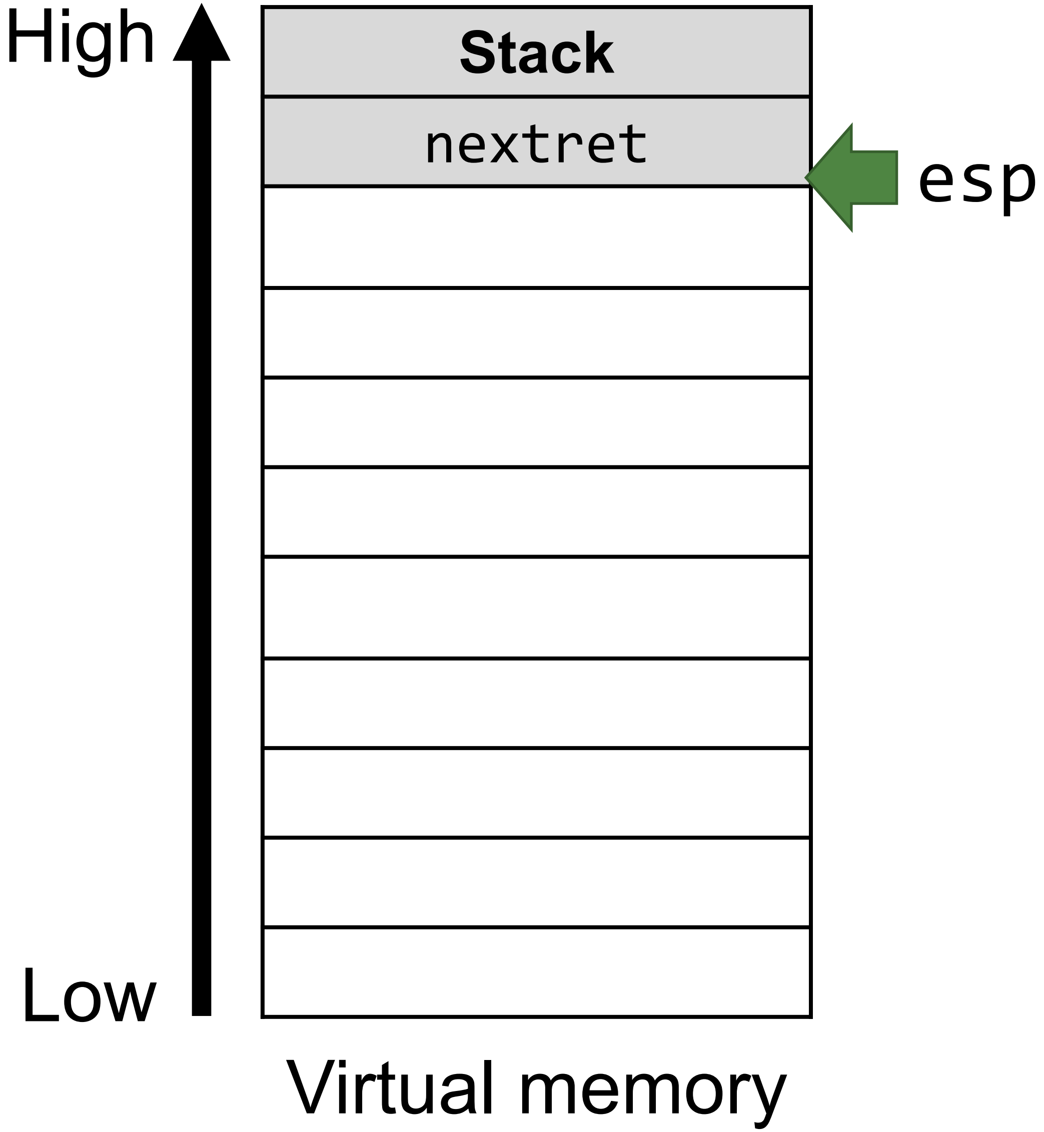
```
push nextret
jmp foo
```

Control flow



Function Return (ret)

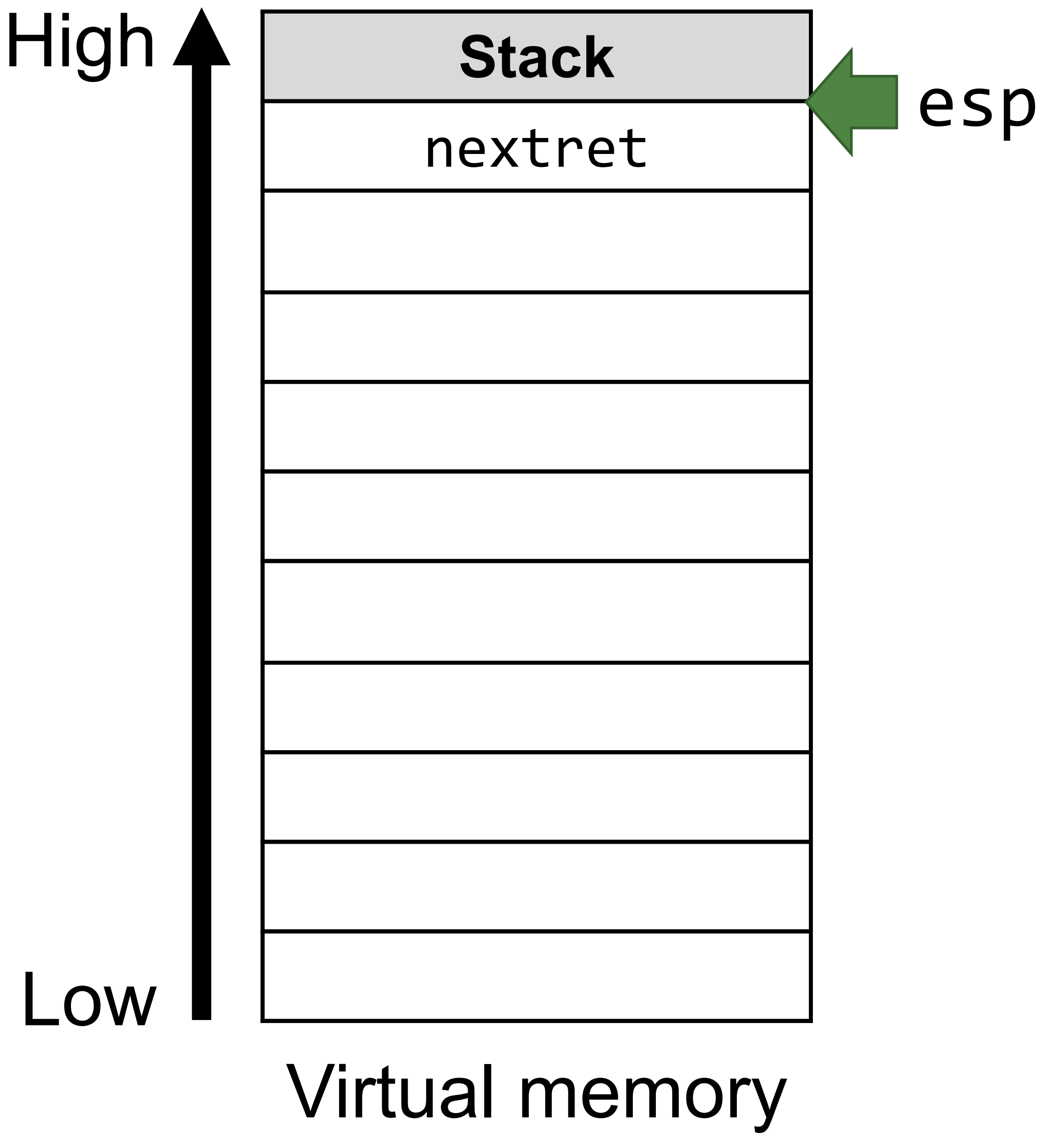
`ret = pop eip`



Function Return (ret)

ret = pop eip

eip nextret



Arithmetic and Logical Operations

- `add eax, [ebx]`
- `sub esp, 0x40`
- `inc ecx`
- `dec edx`
- `and [eax + ecx], ebx`
- `xor edx, ebx`
- `shl eax, 1`
- ...

Control Flows


```
if ( x ) {
  /* A */
}
else {
  /* B */
}
```

```
while (x) {
}
```

```
for (i = 0; i < n ; i++) {
}
```

How to represent in assembly?

Control Flows in Assembly (1)

- There are only "if" and "goto" (no "else")

```
if ( x ) {
  /* A */
}
else {
  /* B */
}
```

How assembly looks like…

```
  if (!x) goto F;
  /* A */
  goto DONE;
F:
  /* B */
DONE:
```

Control Flows in Assembly (2)

- There are only "if" and "goto" (no "else")

```
while (x) {
    /* body */
}
```

How assembly looks like…

```
WHILE:
   if (!x) goto DONE;
   /* body */
   goto WHILE;
DONE:
```

Control Flows in Assembly (3)

- There are only "if" and "goto" (no "else")

```c
for (i = 0; i < n ; i++) {
  /* body */
}
```

How assembly looks like…

```
   i = 0;
LOOP:
   if (i >= n) goto DONE;
   /* body */
   i++;
   goto LOOP;
DONE:
```

Control Flows in Assembly (Example)


```
if (!x) goto F;
```

➡

```
cmp x, 0
je F
```

Test if x is zero

If x=zero then goto F

Control Flows in Assembly (Example)

```
if (!x) goto F;
```

→

```
cmp x, 0
je F
```

```
if (i >=n ) goto F;
```

→

```
cmp i, n
jge F
```

Test if `i >= n`

If `i >= n` then goto F

Control Flows in Assembly (Example)

Where do we store the result of comparison (cmp)?

```
if (!x) goto F;
```

→

```
cmp x, 0
je F
```

```
if (i >=n ) goto F;
```

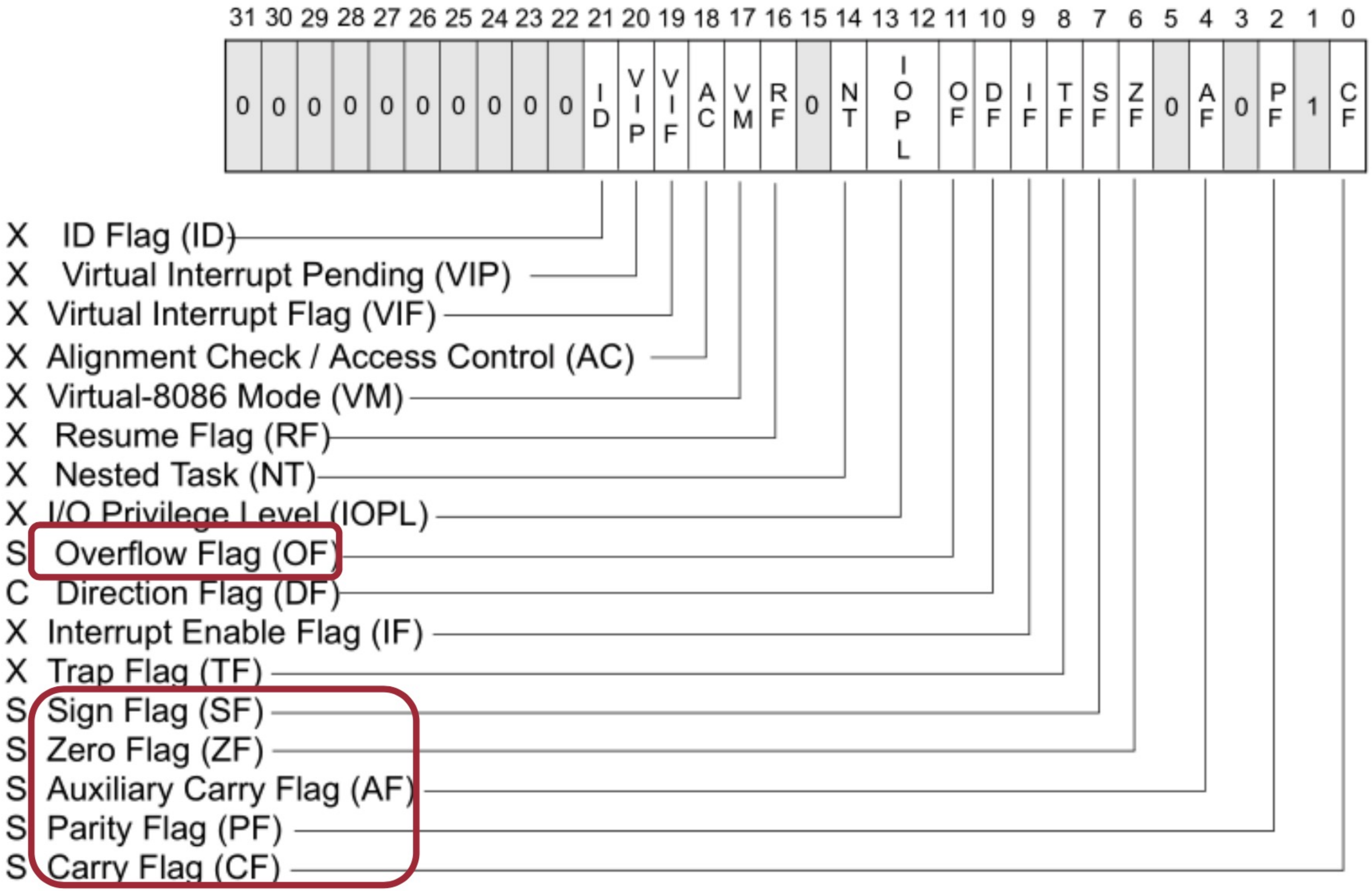
→

```
cmp i, n
jge F
```

EFLAGS: Storing the Processor State

- EFLAGS is a status register used in x86, which is essentially a collection of status flag bits.

- There are approximately 20 different flag bits used in x86, but we are mainly interested in 6 condition flags:
  - OF: Overflow flag
  - SF: Sign flag
  - ZF: Zero flag
  - AF: Auxiliary carry flag
  - PF: Parity flag
  - CF: Carry flag



S Indicates a Status Flag
C Indicates a Control Flag
X Indicates a System Flag

Reserved bit positions. DO NOT USE.
Always set to values previously read.

`cmp` Only Affects EFLAGS

- `cmp` is the same as `sub`, except that it only affects EFLAGS, but not the destination operand. For example, `cmp eax, ebx` will not change the `eax` register

Branch Instructions

Assume that a comparison instruction (`cmp`) precedes the branch instruction

Branch Instruction	Condition	Description
`ja`	`CF = 0 and ZF = 0`	Jump if above
`jb`	`CF = 1`	Jump if below
`je`	`ZF = 1`	Jump if equal
`jl`	`SF ≠ F`	Jump if less
`jle`	`ZF = 1 or SF ≠ F`	Jump if less or equal
`jna`	`CF = 1 or ZF = 1`	Jump if not above
`jnb`	`CF = 0`	Jump if not below
`jz`	`ZF = 1`	Jump if zero
… (many more)		

Summary So Far

- We learned how to move around data
  - `mov, lea, push, pop, etc.`
- We learned how to perform arithmetic and logical operations
  - `add, sub, and, or, etc.`
- We also learned how to control program flows
  - `cmp, jmp, ja, jz, etc.`

Already Turing Complete!

x86 Execution Model

Our Example


```
int purple(int a1, int a2) {
    return 2 + a1 - a2;
}

int blue(int a1) {
    return 1 + purple(a1, b);
}

int red(int a1) {
    return blue(a1 - 42);
}
```

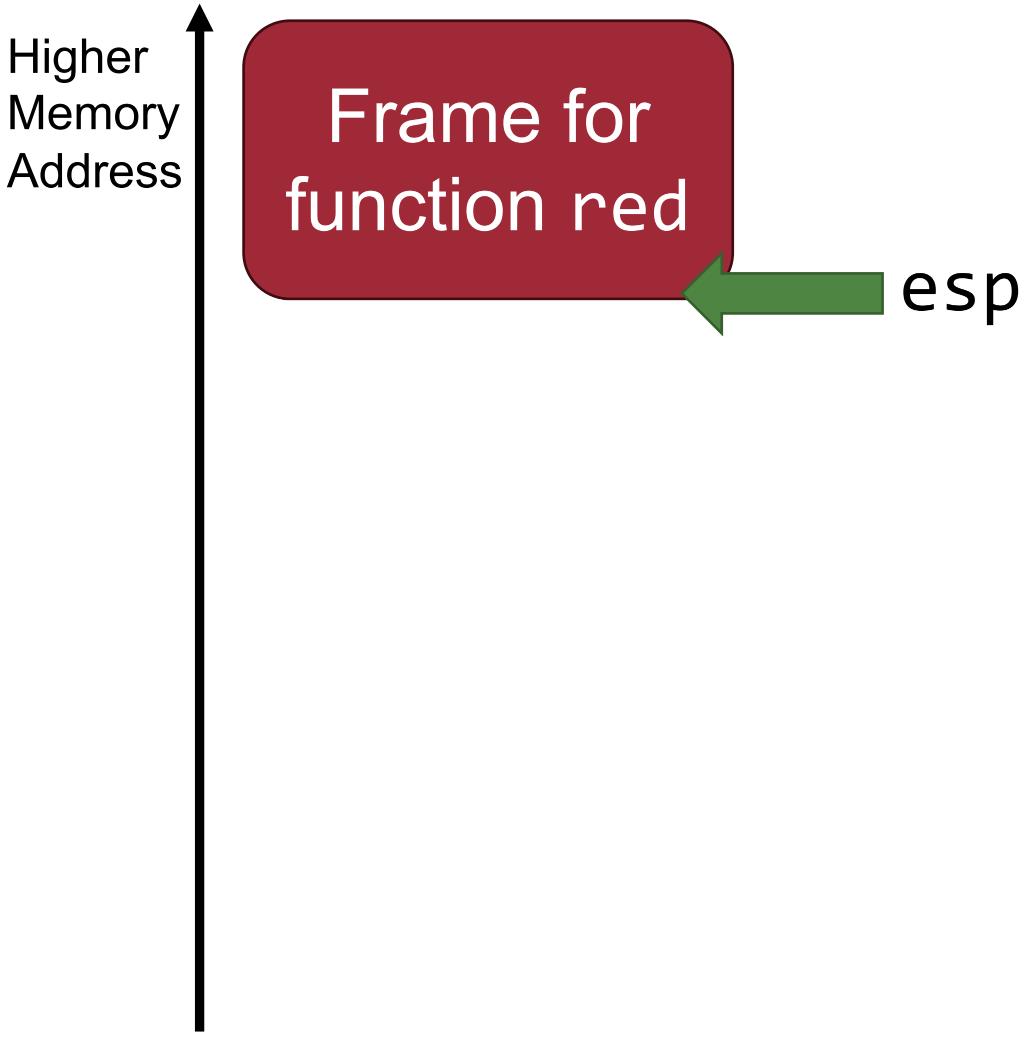
Our Example – Stack

```
int purple(int a1, int a2) {
    return 2 + a1 - a2;
}

int blue(int a1) {
    return 1 + purple(a1, b);
}

int red(int a1) {
    return blue(a1 - 42);
}
```

Start

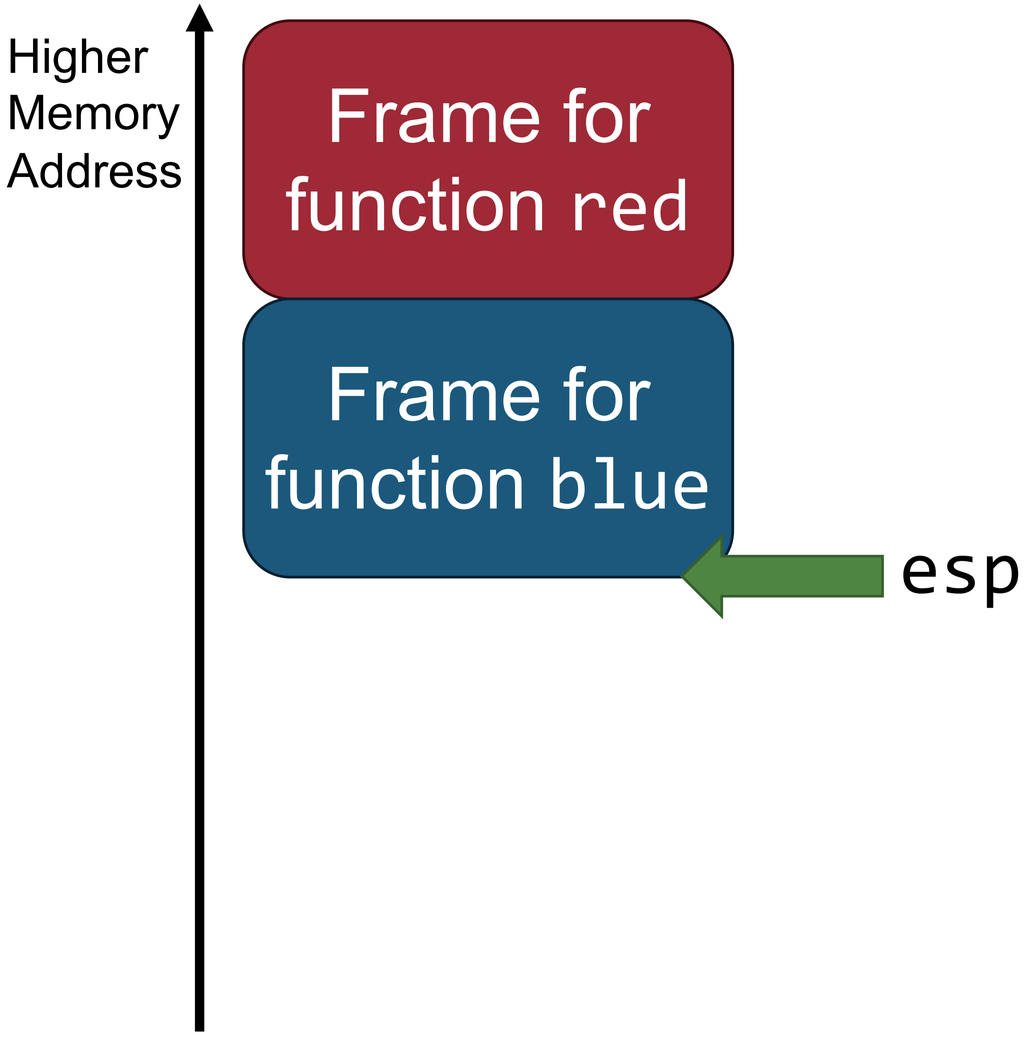


Our Example – Stack

```
int purple(int a1, int a2) {
    return 2 + a1 - a2;
}

int blue(int a1) {
    return 1 + purple(a1, b);
}

int red(int a1) {
    return blue(a1 - 42);
}
```

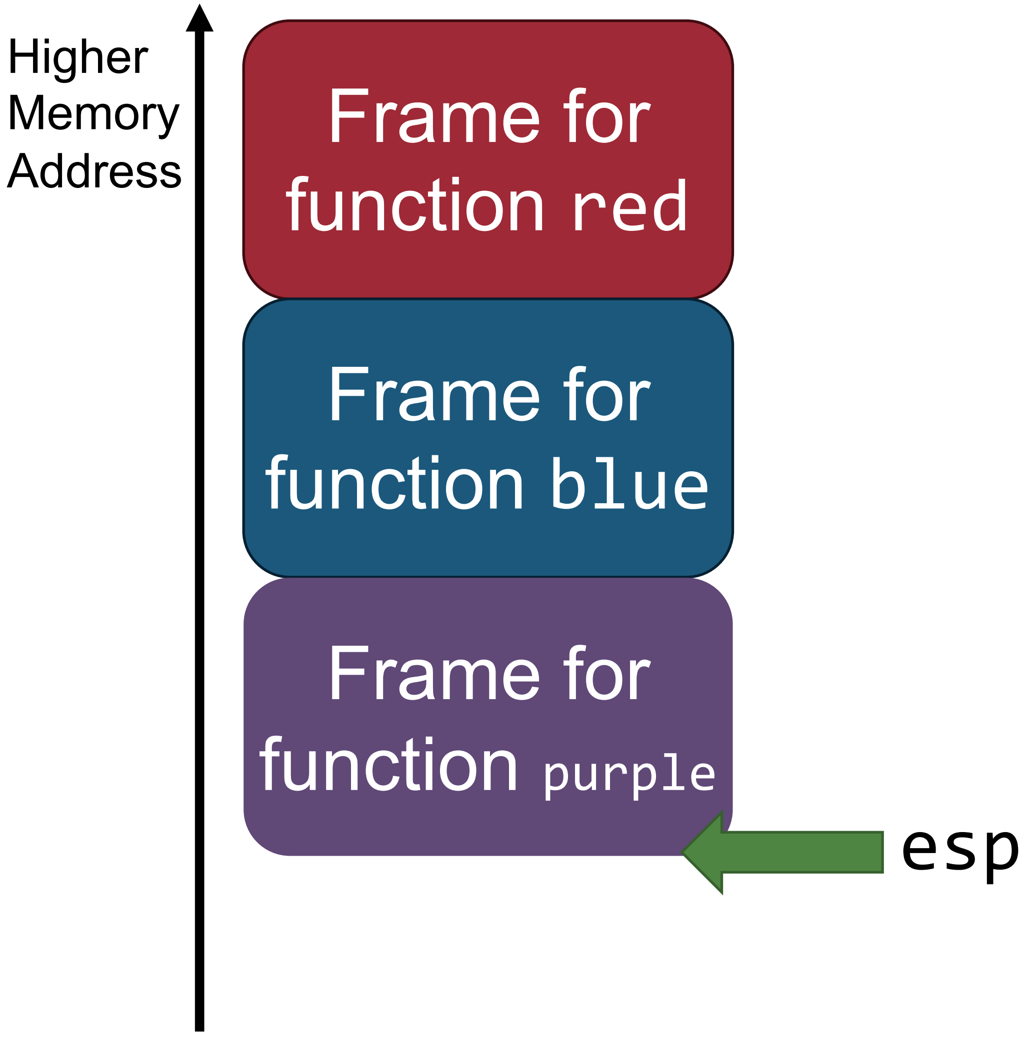


Our Example – Stack

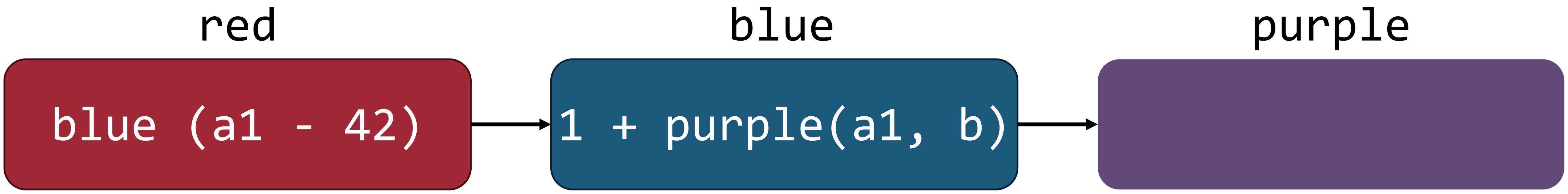
```
int purple(int a1, int a2) {
    return 2 + a1 - a2;
}

int blue(int a1) {
    return 1 + purple(a1, b);
}

int red(int a1) {
    return blue(a1 - 42);
}
```



Questions



- How do we pass function parameters?
- When a function returns, how do we restore the register values of the caller
- Where do we store local variables?

We can easily get the answer by compiling the example program and disassembling the resulting binary

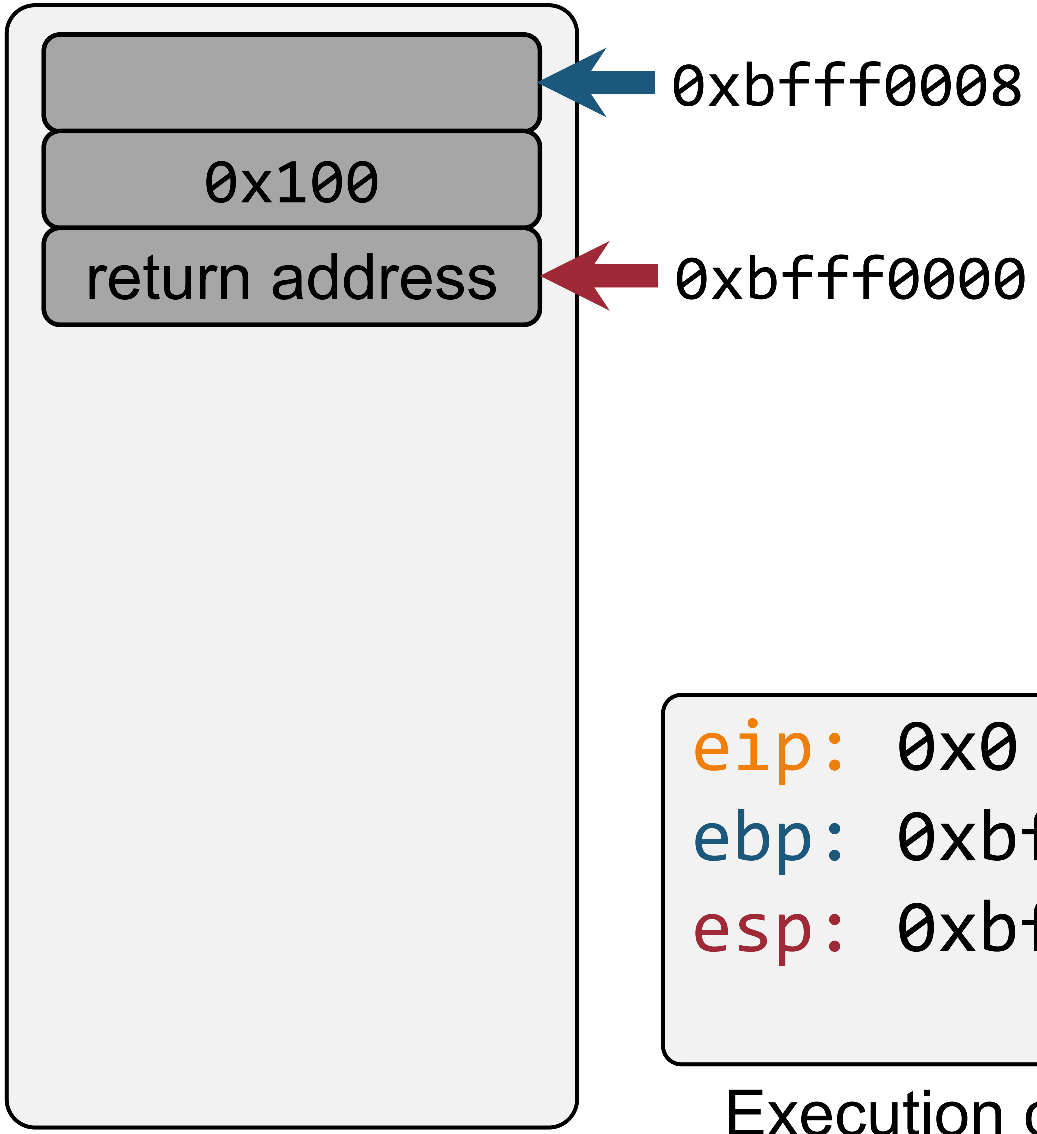
Disassembled Code (x86)

```
<red>:
   0:   push    ebp
   1:   mov     ebp,esp
   3:   sub     esp,0x28
   6:   mov     DWORD PTR [ebp-0xc],0x0
   d:   mov     eax,DWORD PTR [ebp+0x8]
  10:   sub     eax,0x2a
  13:   mov     DWORD PTR [esp],eax
  16:   call    Blue
  1b:   mov     edx,DWORD PTR [ebp-0xc]
  1e:   add     eax,edx
  20:   leave
  21:   ret

<blue>:
  22:   push    ebp
  23:   mov     ebp,esp
  25:   sub     esp,0x28
  28:   mov     DWORD PTR [ebp-0xc],0x1
  2f:   mov     eax, DWORD PTR [ebp-0xc]
  32:   mov     DWORD PTR [esp+0x4], eax
  36:   mov     eax, DWORD PTR [ebp+0x8]
  39:   mov     DWORD PTR [esp], eax
  3c:   call    purple
  41:   mov     edx, DWORD PTR [ebp-0xc]
  44:   add     eax, edx
  46:   leave
  47:   ret

<purple>:
  48:   push    ebp
  49:   mov     ebp,esp
  4b:   sub     esp,0x10
  4e:   mov     DWORD PTR [ebp -0x4],0x2
  55:   mov     eax,DWORD PTR [ebp+0x8]
  58:   mov     eax,DWORD PTR [ebp-0x4]
  5b:   add     eax,edx
  5d:   sub     eax,DWORD PTR [ebp+0xc]
  60:   leave
  61:   ret
```

Execution Example



Virtual memory

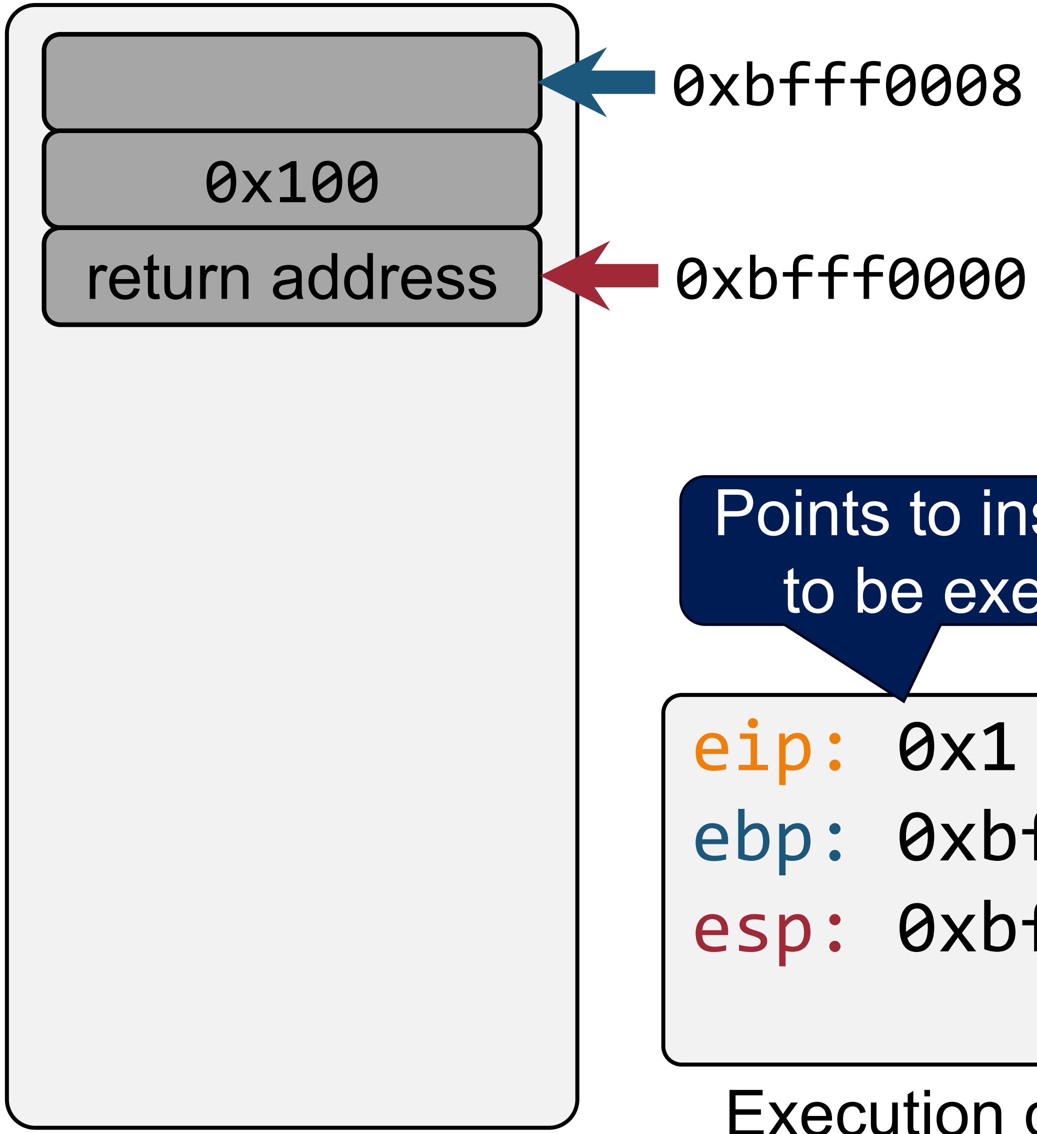
```
eip: 0x0
ebp: 0xbfff0008
esp: 0xbfff0000
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



Virtual memory

Points to instruction to be executed

```
eip: 0x1
ebp: 0xbfff0008
esp: 0xbfff0000
```

Execution context

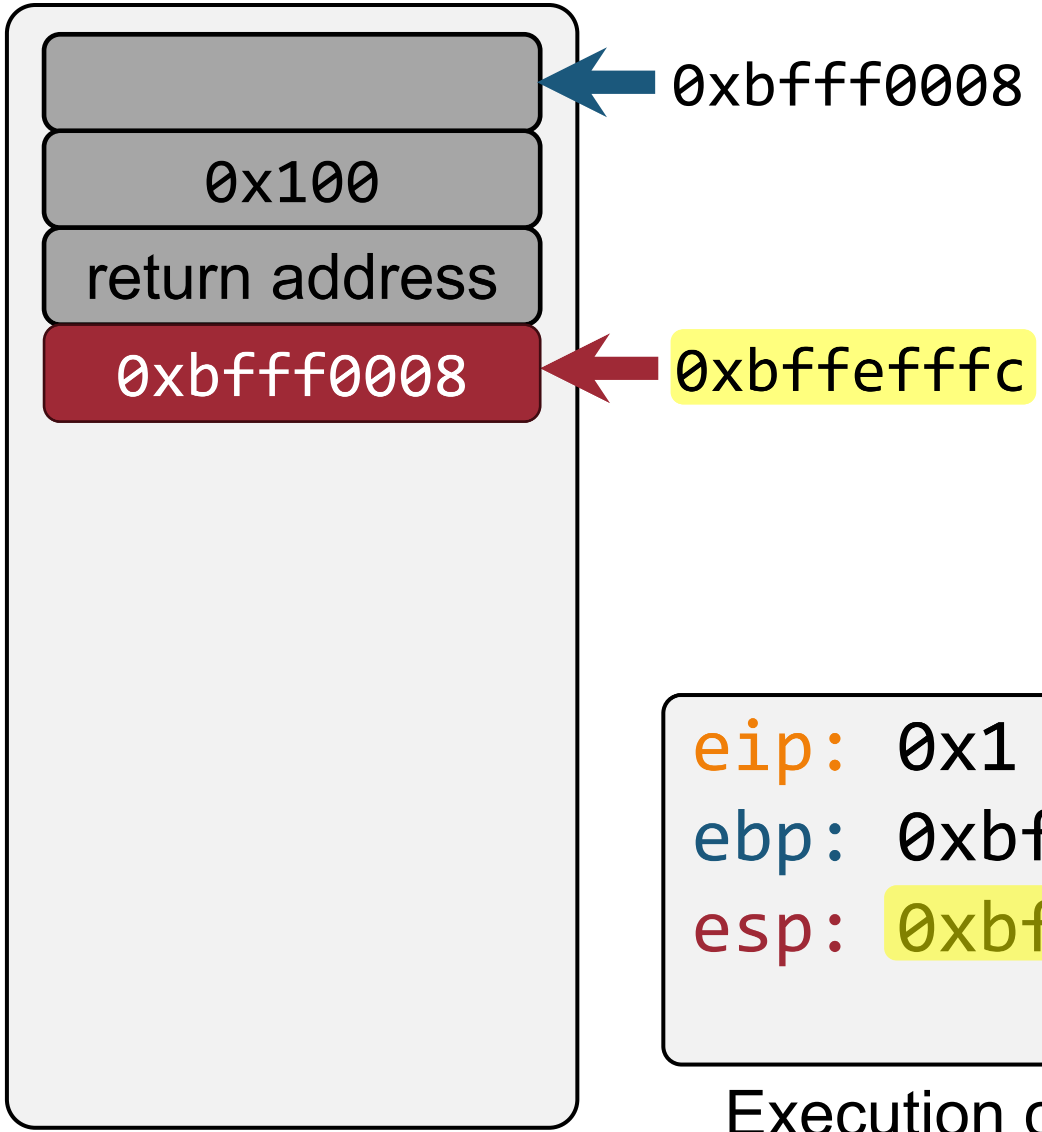
Currently executed instruction

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example

Save the base address of the previous function's stack frame



Virtual memory

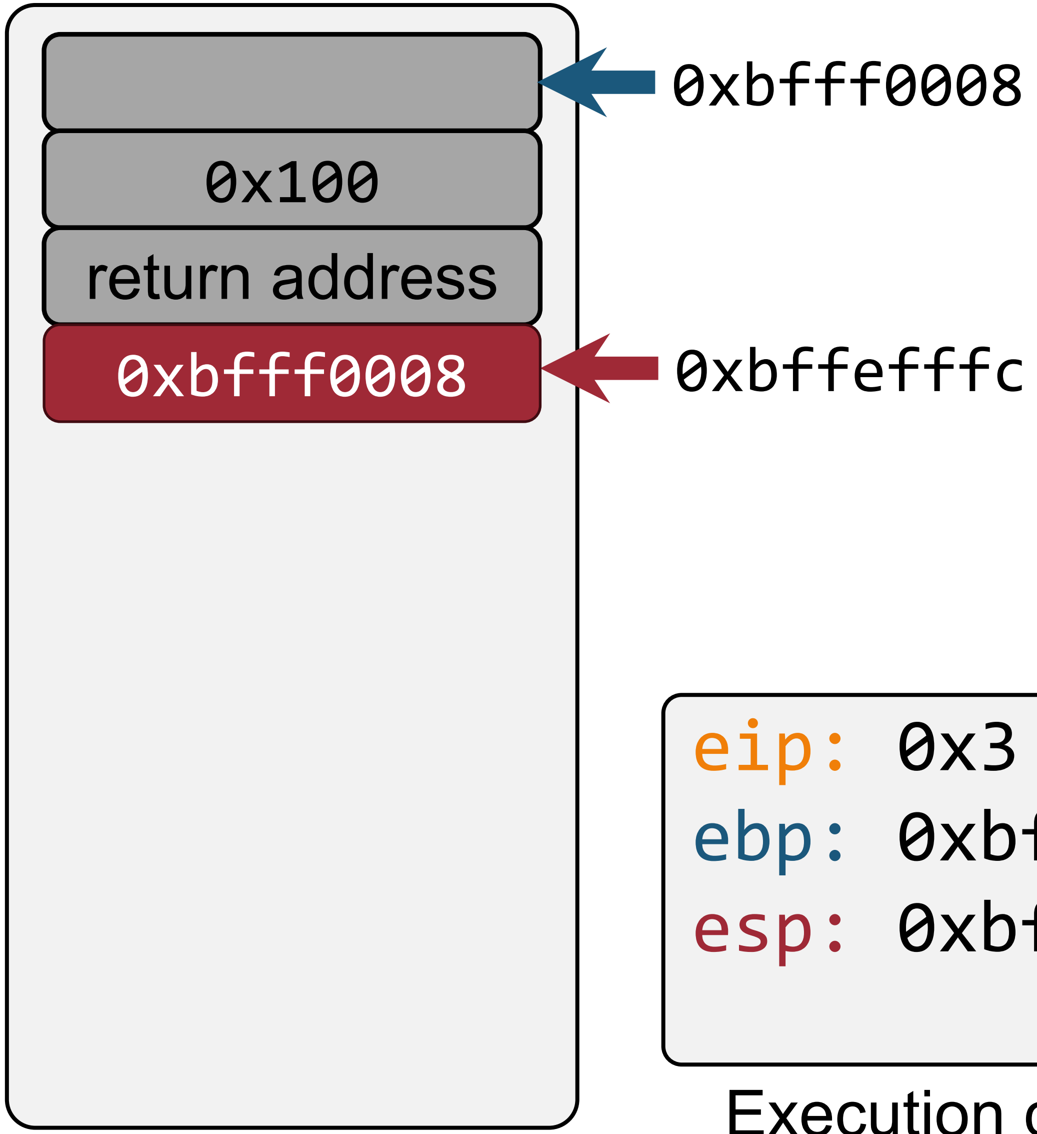
```
eip: 0x1
ebp: 0xbfff0008
esp: 0xbffefffc
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:  leave
  47:  ret
```

Execution Example



Virtual memory

```
eip: 0x3
ebp: 0xbfff0008
esp: 0xbffefffc
```

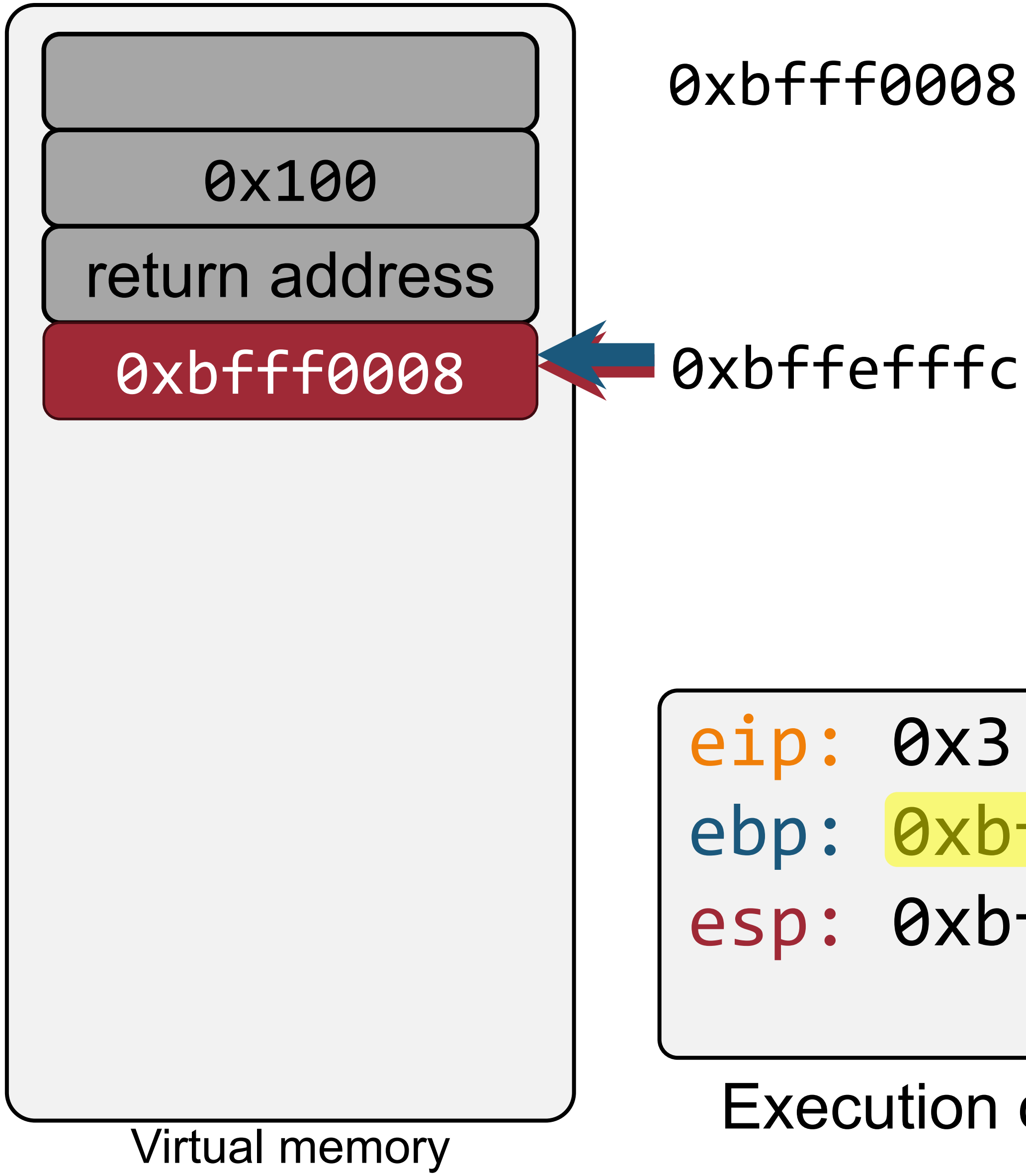
Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example

Now, ebp points to the base of the current stack frame



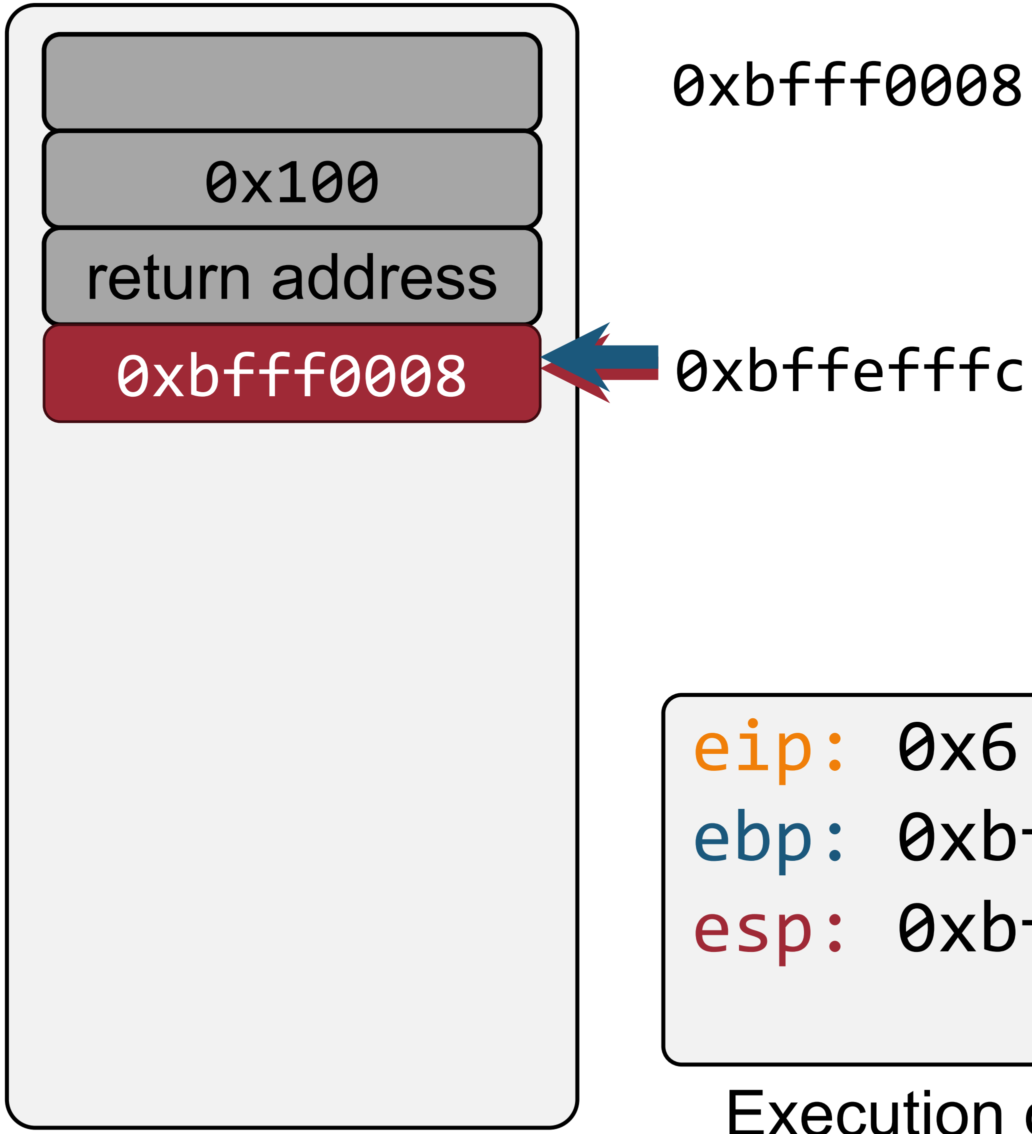
eip: 0x3
ebp: 0xbffefffc
esp: 0xbffefffc

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



Virtual memory

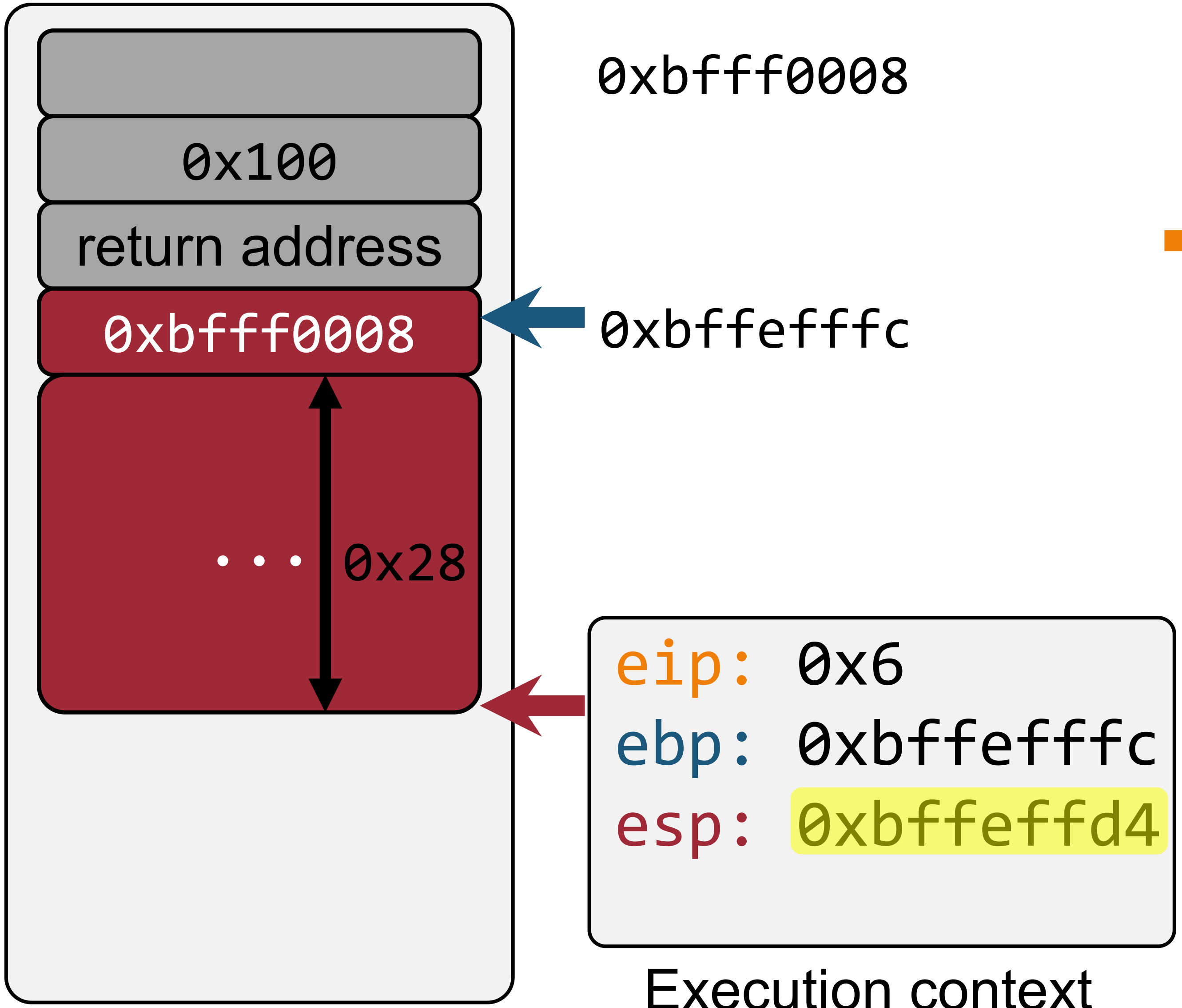
```
eip: 0x6
ebp: 0xbffefffc
esp: 0xbffefffc
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



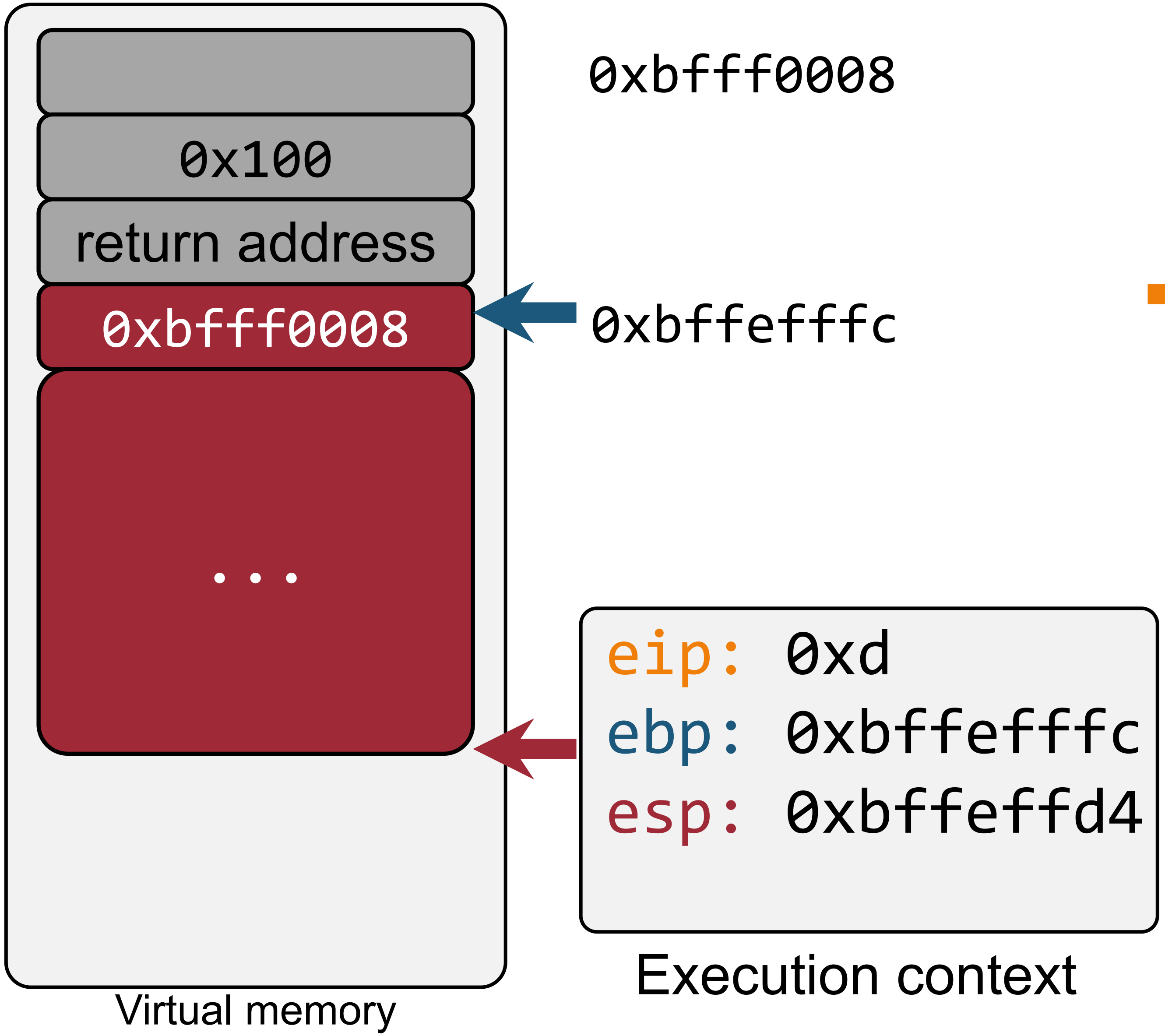
Virtual memory

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

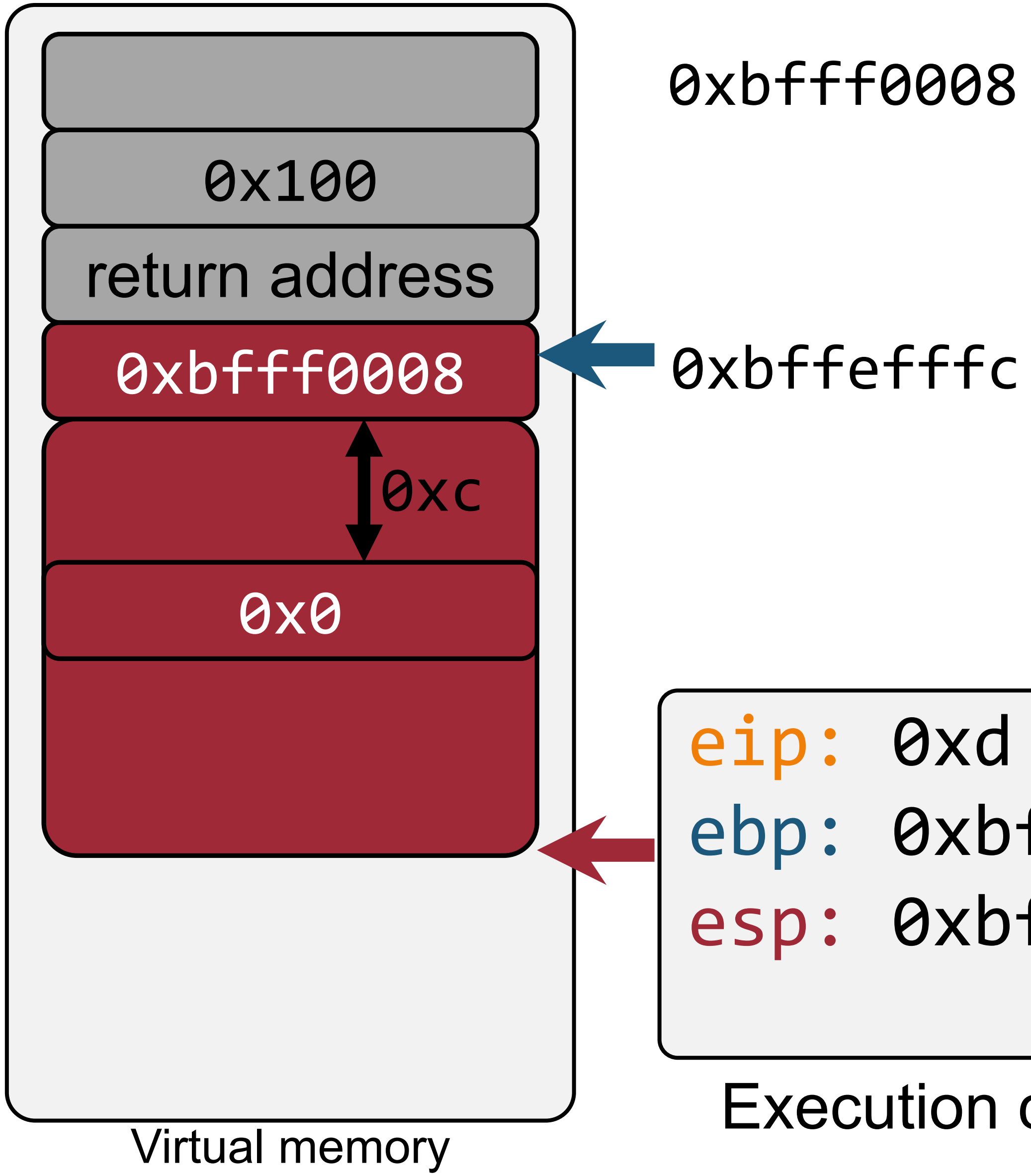
Execution Example




```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



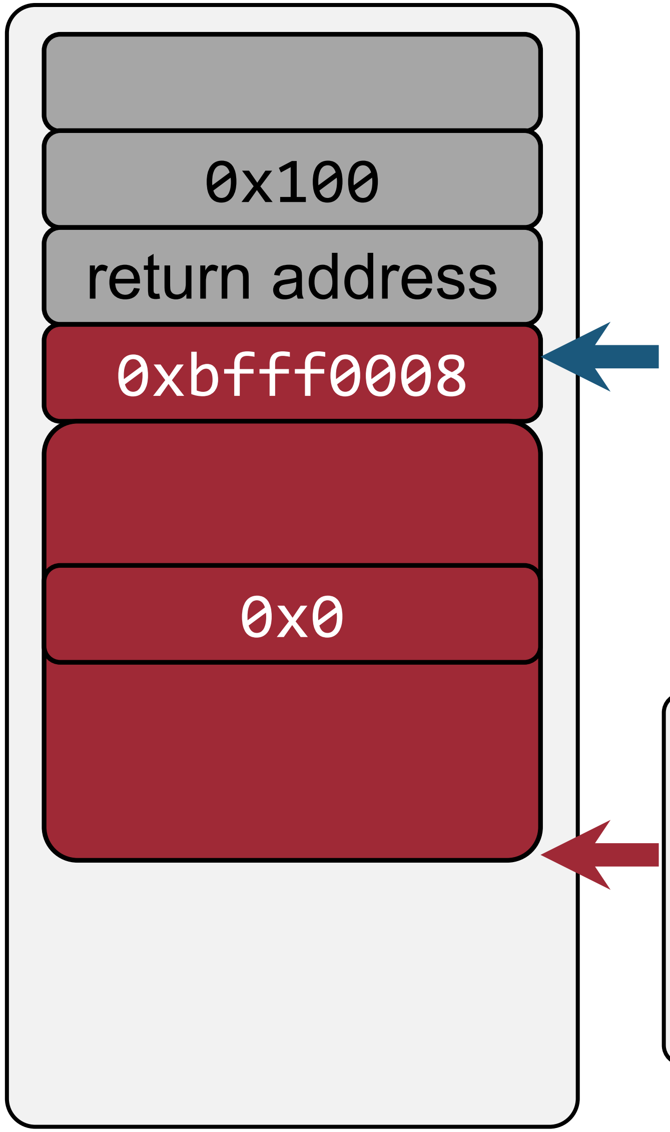

```
eip: 0xd
ebp: 0xbffefffc
esp: 0xbffeffd4
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



Virtual memory

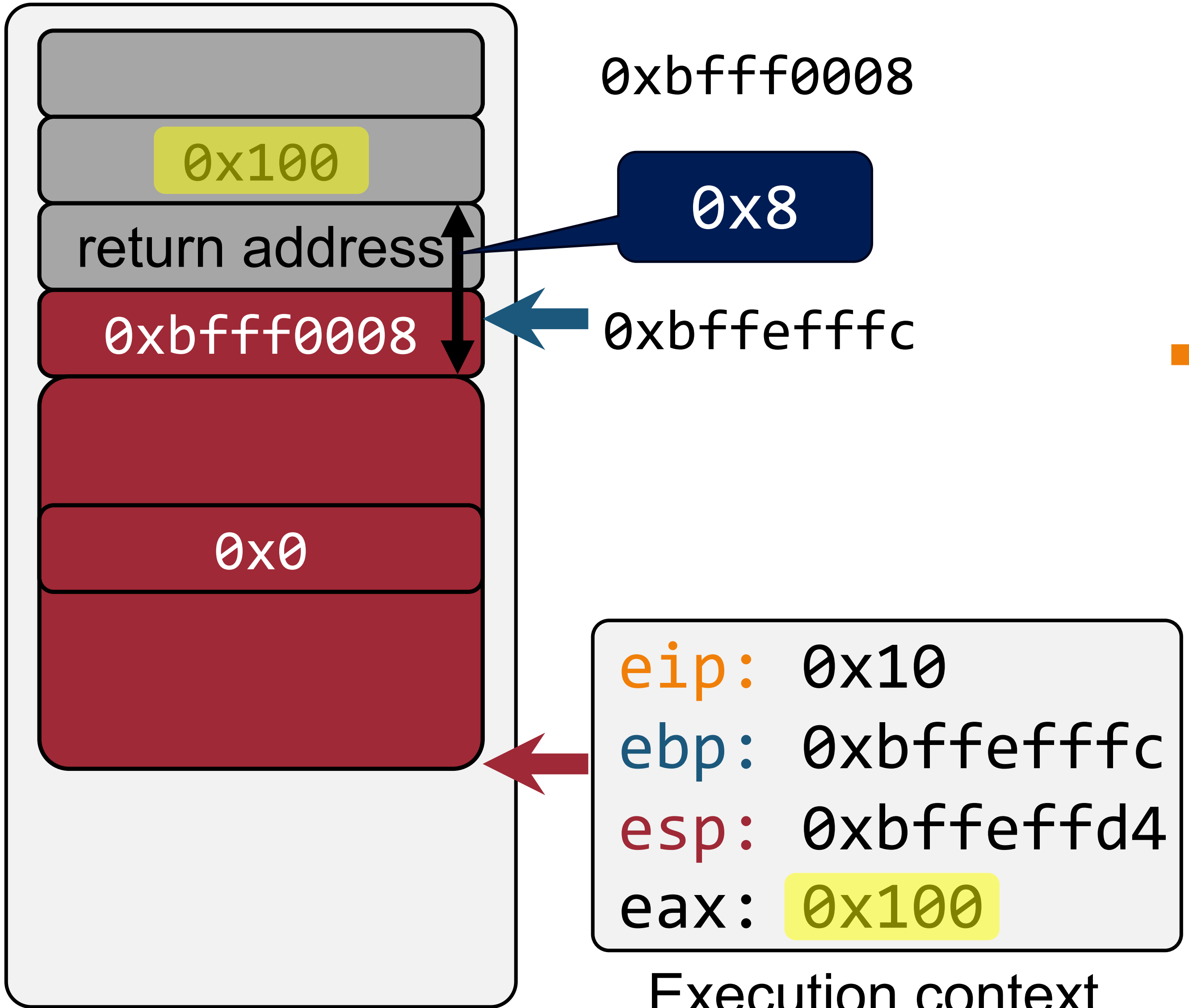
```
eip: 0x10
ebp: 0xbffefffc
esp: 0xbffeffd4
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:  leave
  47:  ret
```

Execution Example



Virtual memory

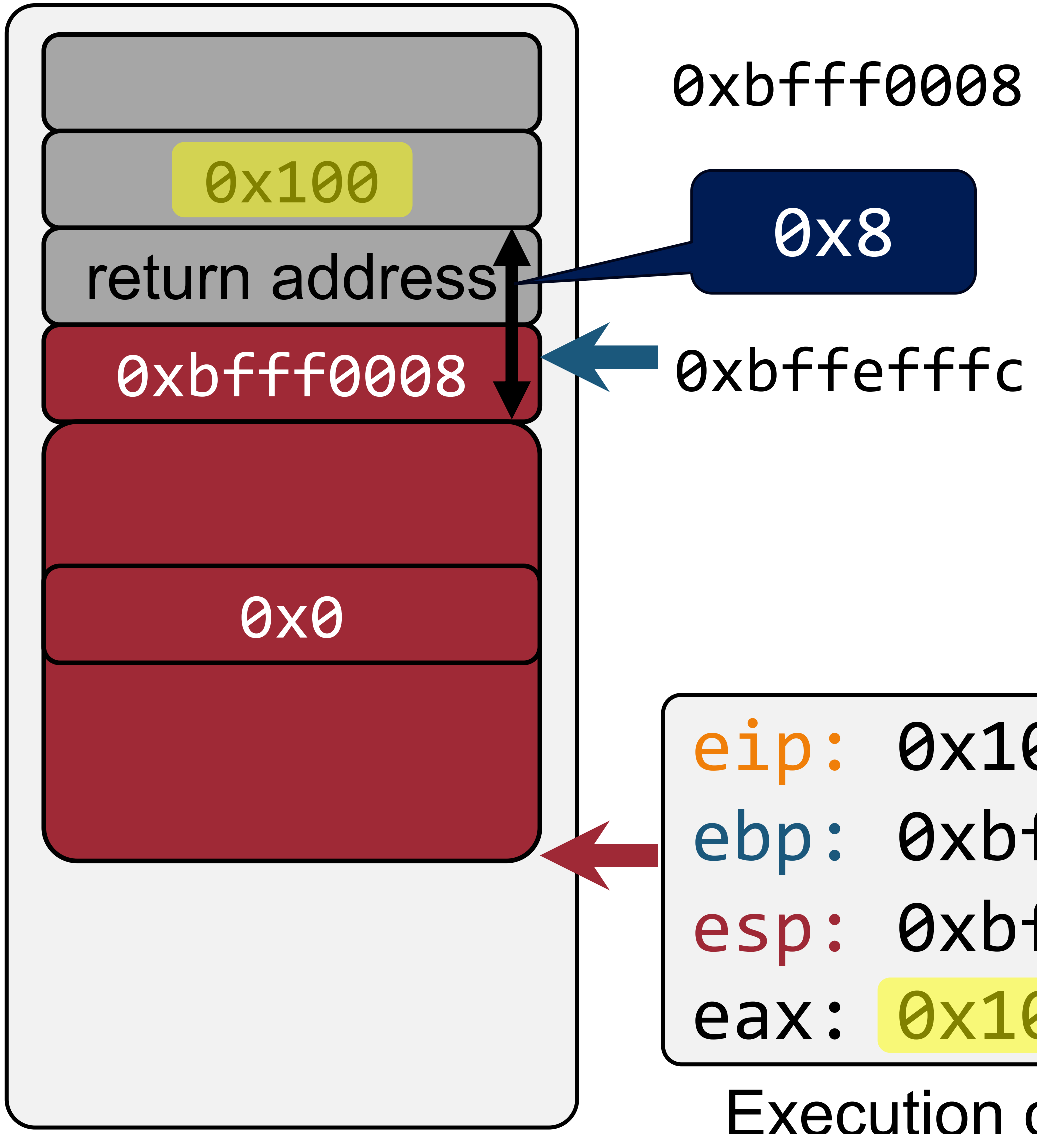
Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example

```
int red(int a1) {
    return blue(a1 - 42);
}
```



Virtual memory

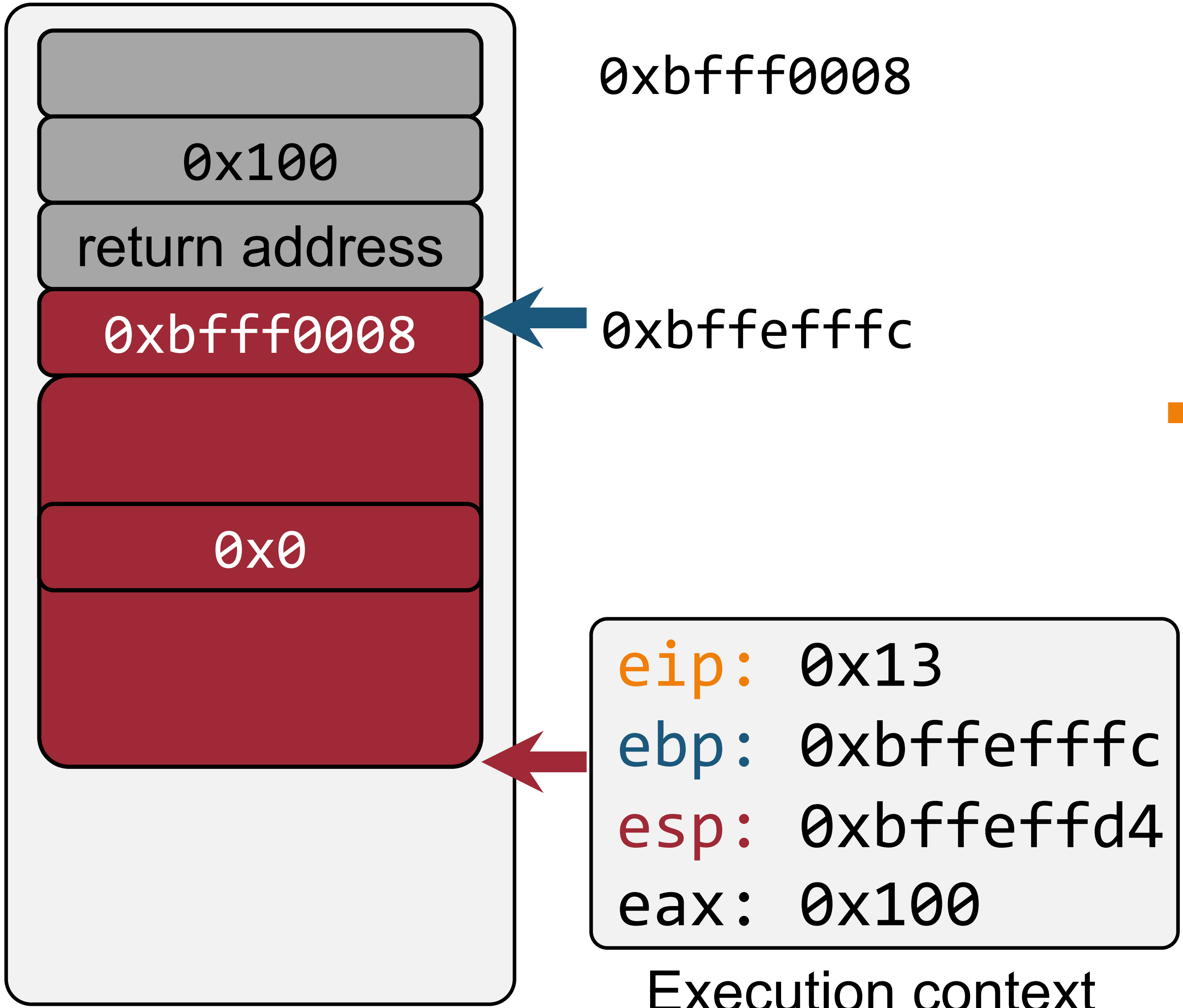
```
eip: 0x10
ebp: 0xbffefffc
esp: 0xbffeffd4
eax: 0x100
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



Virtual memory

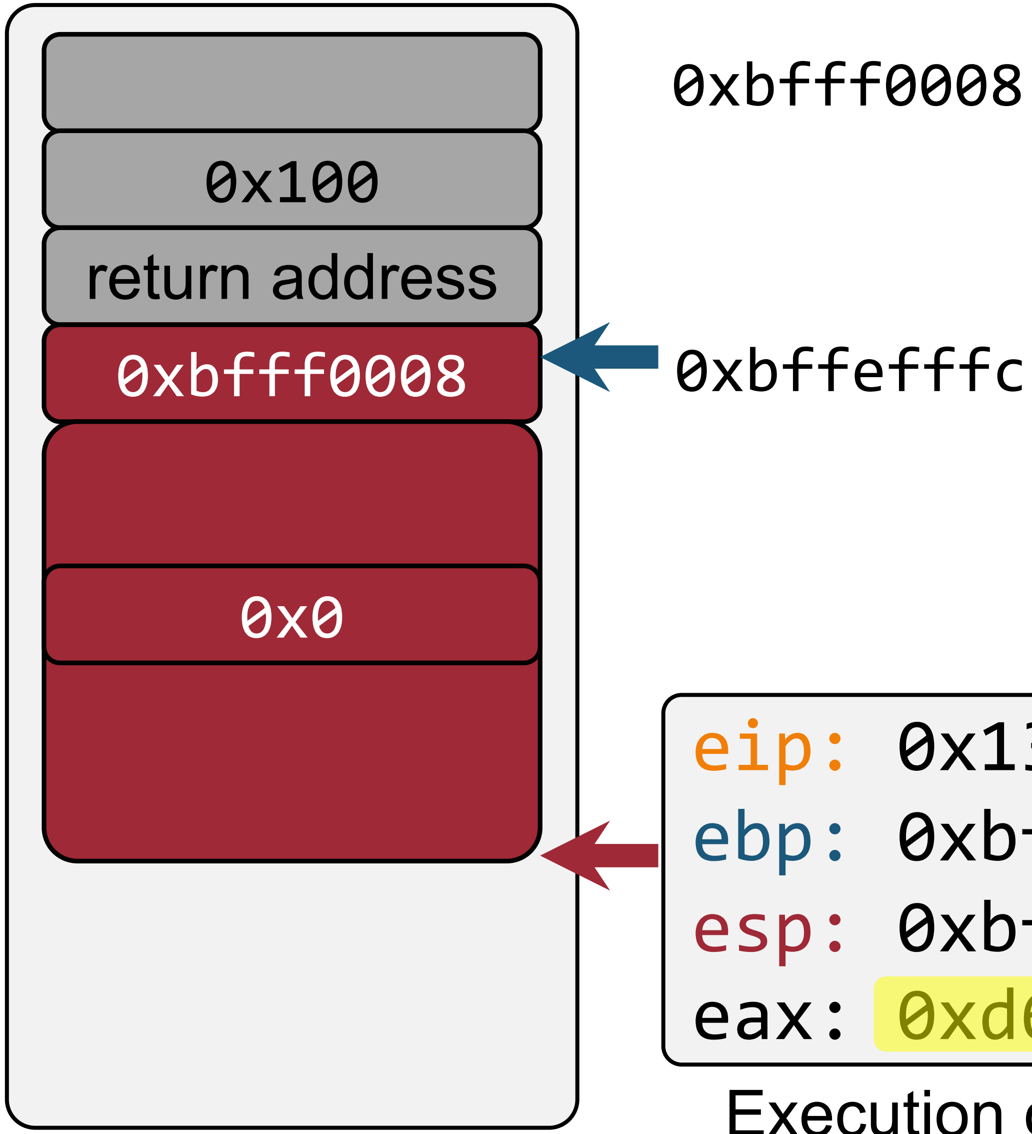
Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example

```c
int red(int a1) {
    return blue(a1 - 42);
}
```



Virtual memory

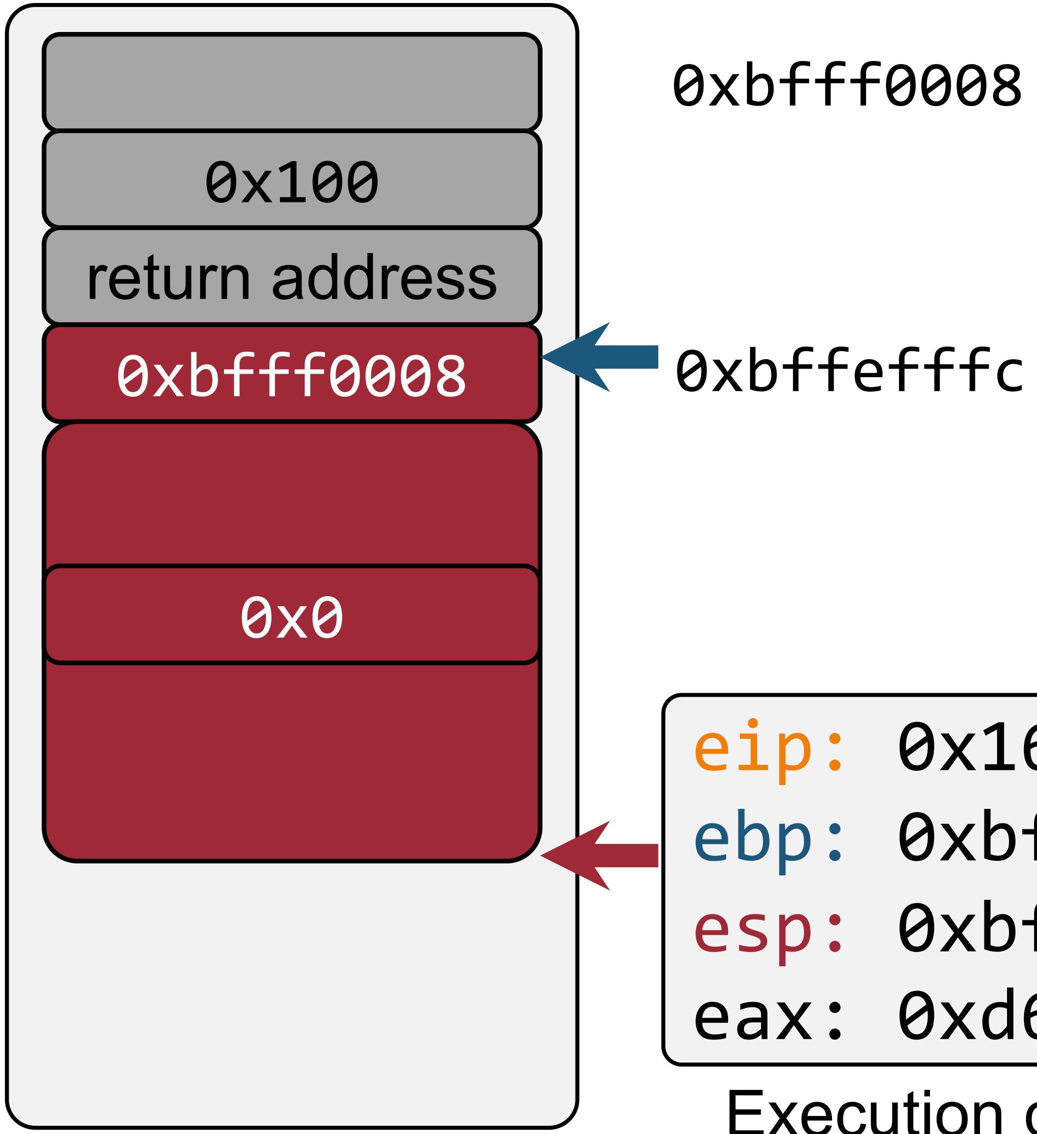
```
eip: 0x13
ebp: 0xbffefffc
esp: 0xbffeffd4
eax: 0xd6
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



Virtual memory

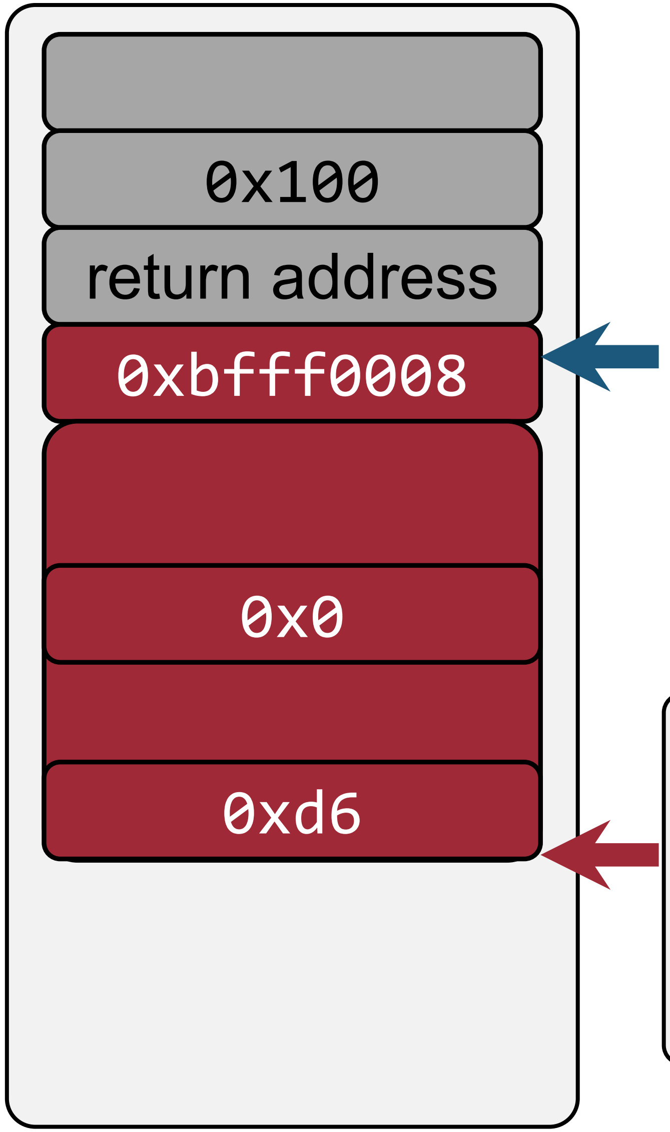
eip: 0x16
ebp: 0xbffefffc
esp: 0xbffeffd4
eax: 0xd6

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



Virtual memory

0xbfff0008

0xbffefffc

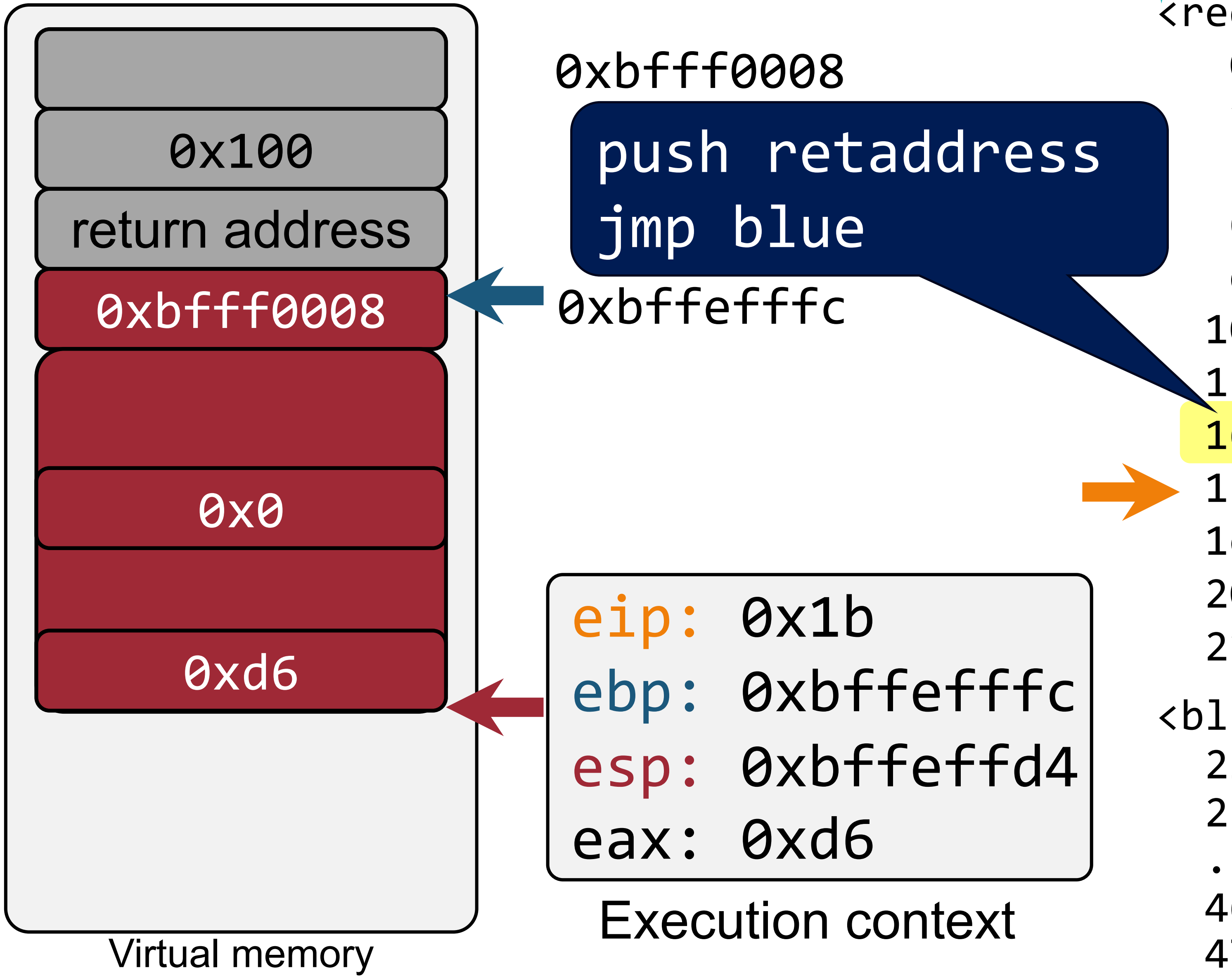
```
eip: 0x16
ebp: 0xbffefffc
esp: 0xbffeffd4
eax: 0xd6
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



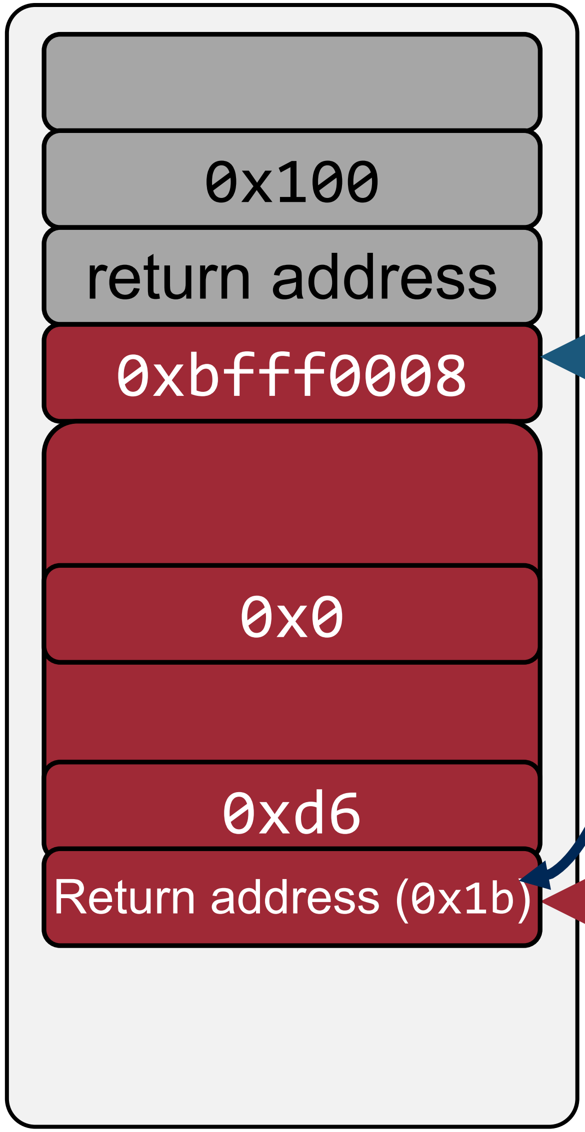

```
eip: 0x1b
ebp: 0xbffefffc
esp: 0xbffeffd4
eax: 0xd6
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



Virtual memory

0xbfff0008

push retaddress
jmp blue

0xbffefffc

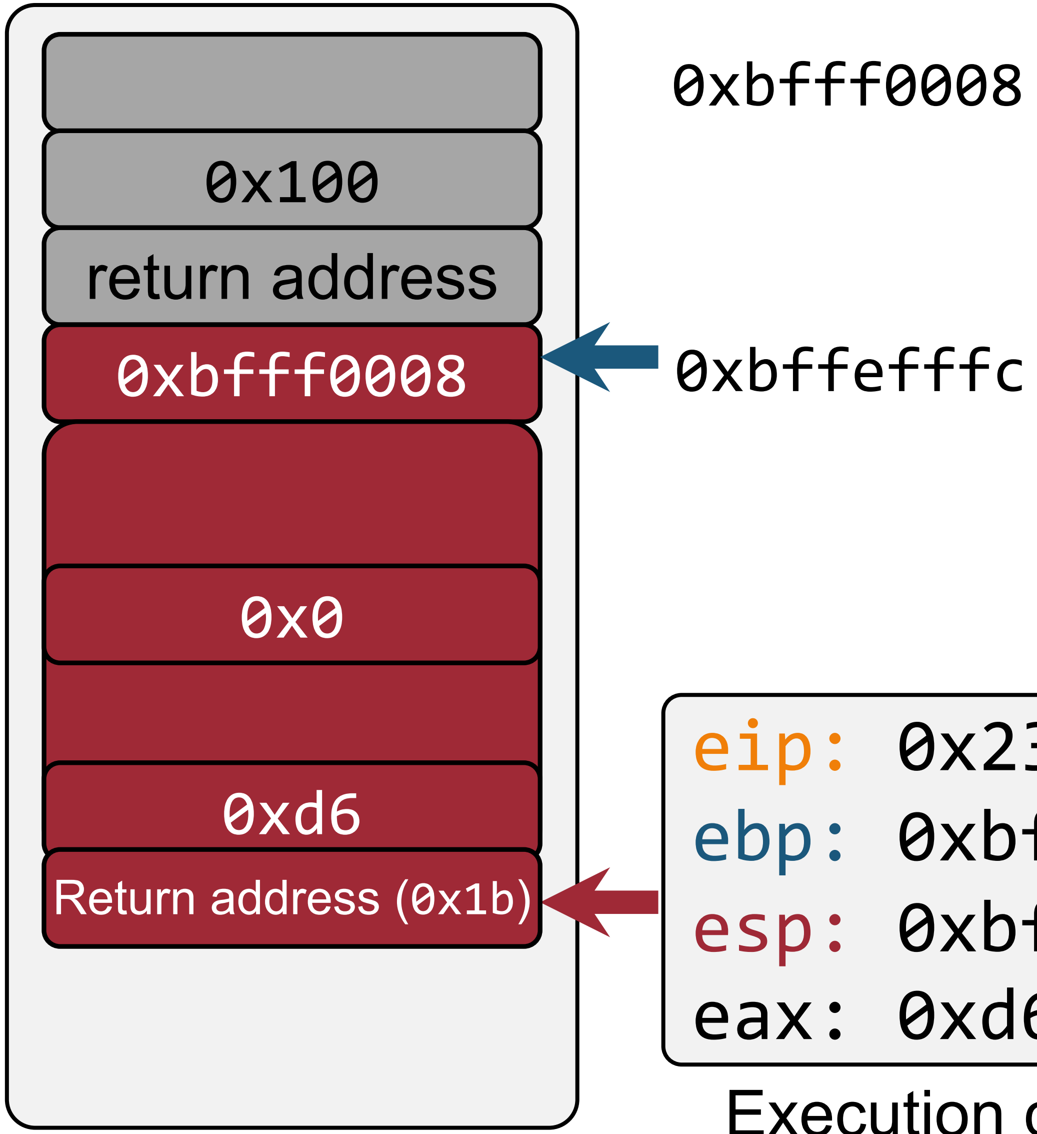
```
eip: 0x22
ebp: 0xbffefffc
esp: 0xbffeffd0
eax: 0xd6
```

Execution context

```
<red>:
  0:  push   ebp
  1:  mov    ebp,esp
  3:  sub    esp,0x28
  6:  mov    DWORD PTR [ebp-0xc],0x0
  d:  mov    eax,DWORD PTR [ebp+0x8]
 10:  sub    eax,0x2a
 13:  mov    DWORD PTR [esp],eax
 16:  call   blue
 1b:  mov    edx,DWORD PTR [ebp-0xc]
 1e:  add    eax,edx
 20:  leave
 21:  ret

<blue>:
 22:  push    ebp
 23:  mov     ebp,esp
 ...
 46:   leave
 47:   ret
```

Execution Example



Virtual memory

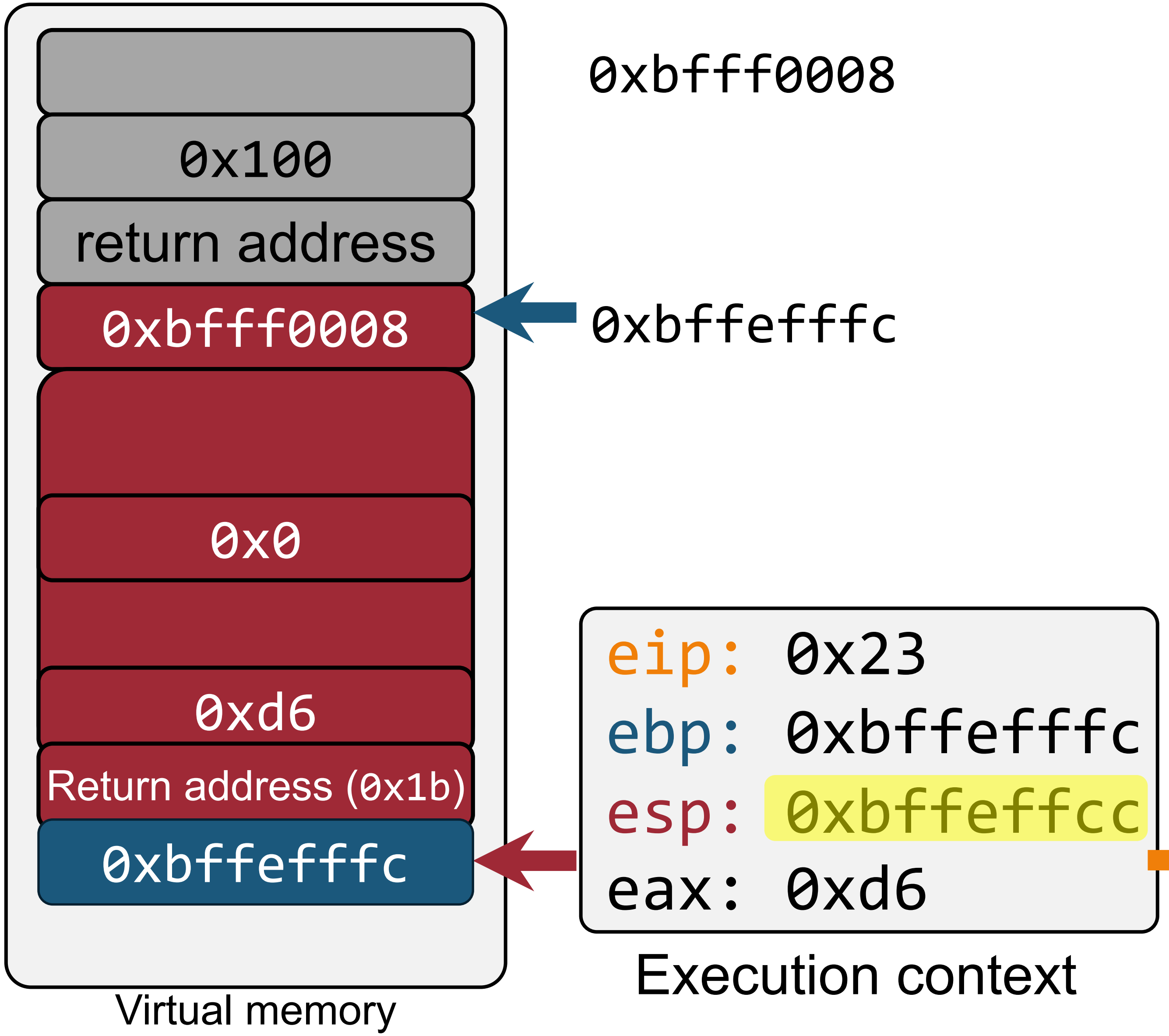
```
eip: 0x23
ebp: 0xbffefffc
esp: 0xbffeffd0
eax: 0xd6
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

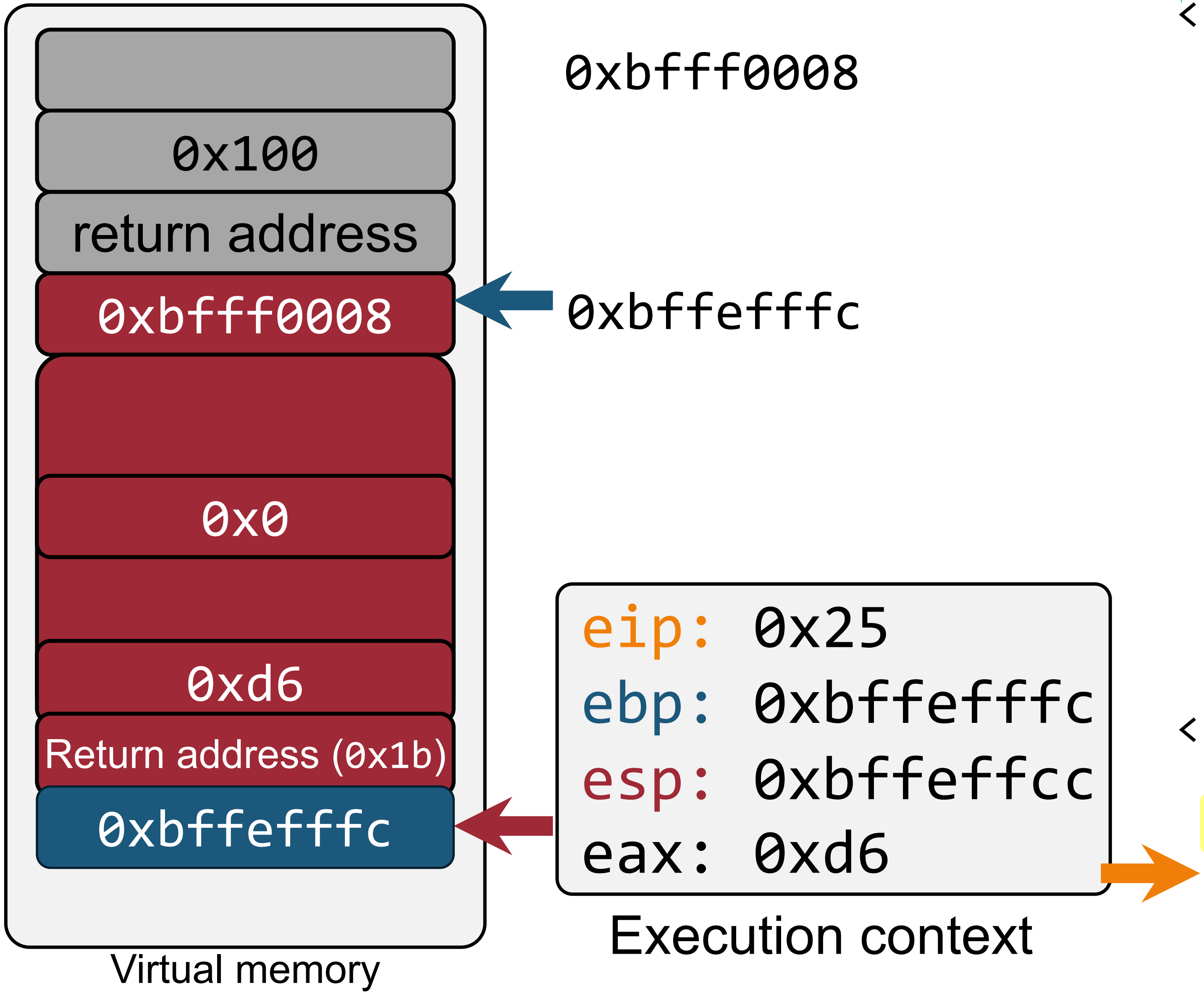
Execution Example




```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

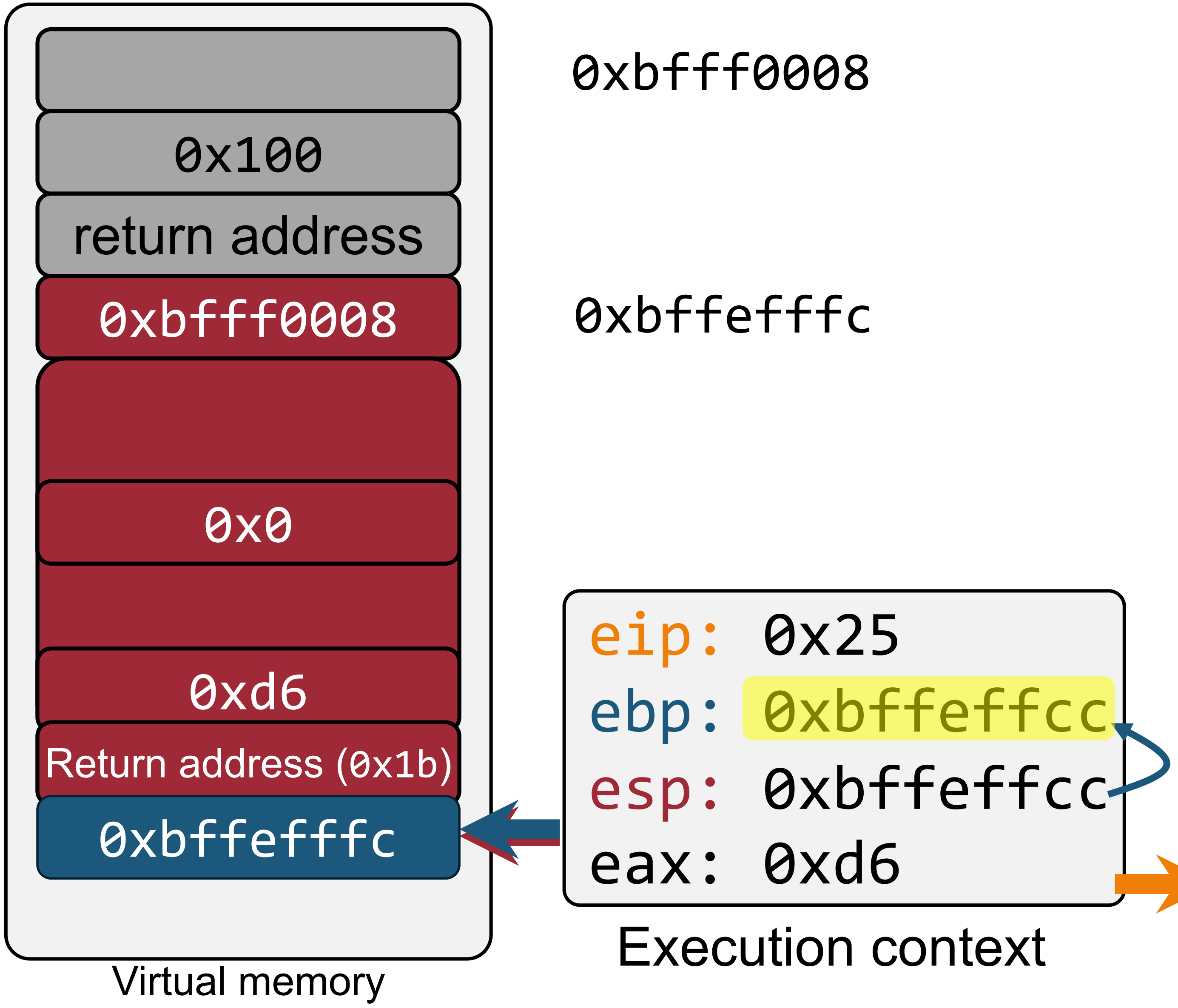
Execution Example




```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

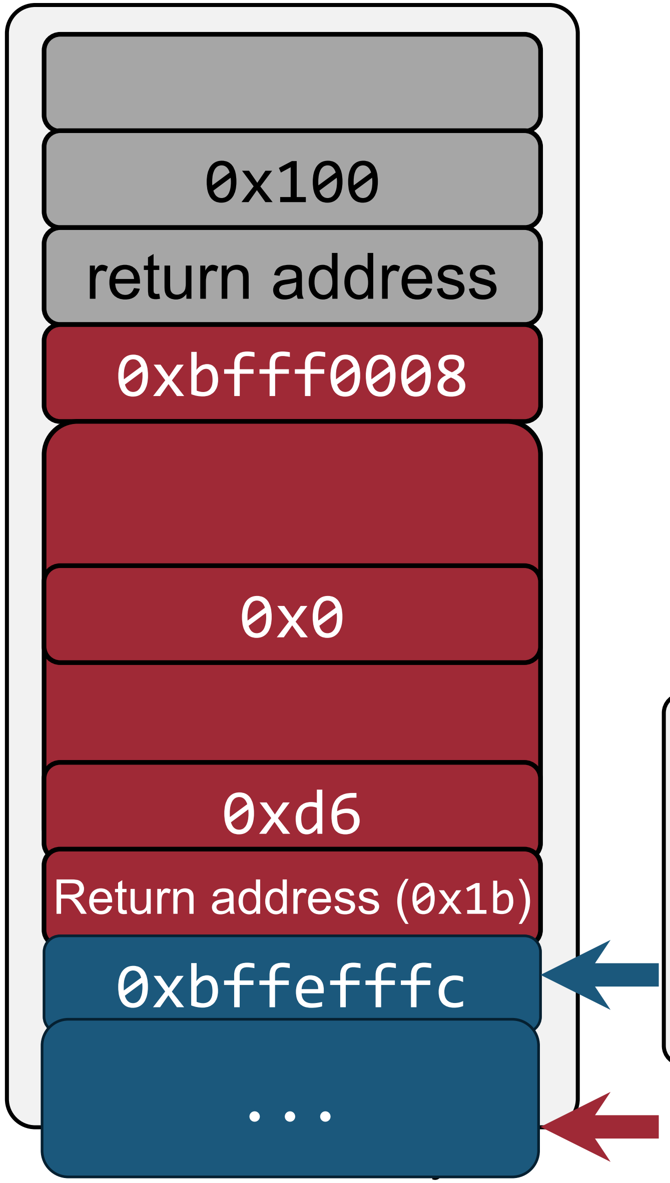
Execution Example




```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

Execution Example



0xbfff0008

0xbffefffc

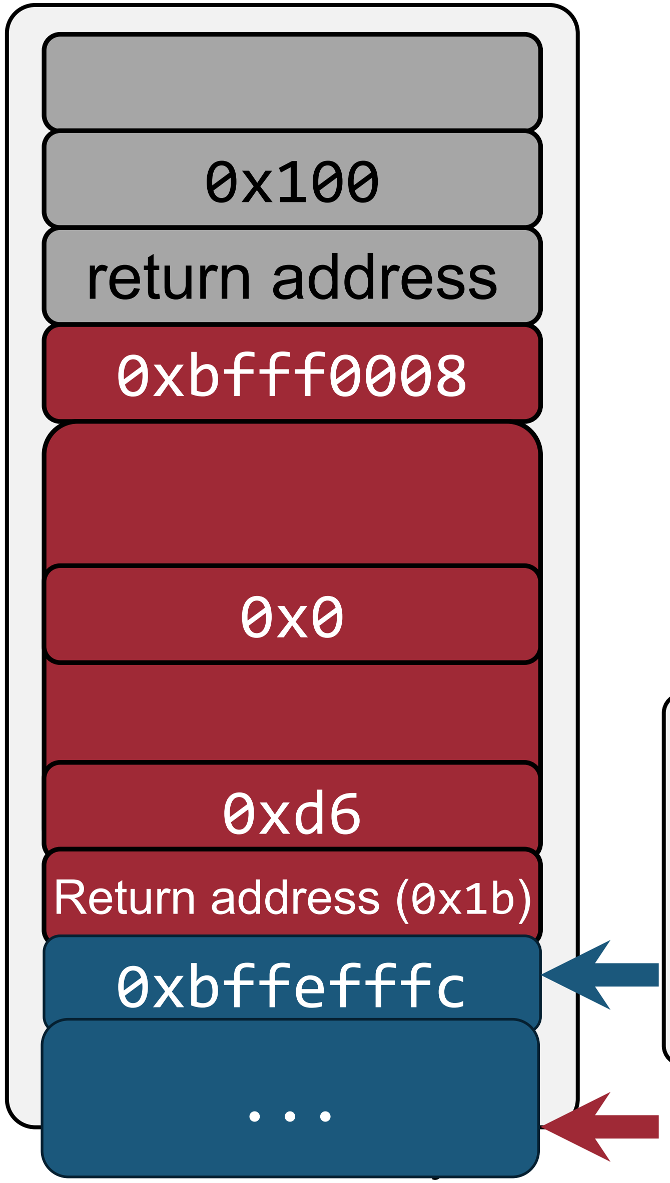
```
eip: 0x46
ebp: 0xbffeffcc
esp: 0xbffeffac
```

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...                      Skip!
  46:   leave
  47:   ret
```

Execution Example



0xbfff0008

0xbffefffc

```
eip: 0x47
ebp: 0xbffeffcc
esp: 0xbffeffac
```

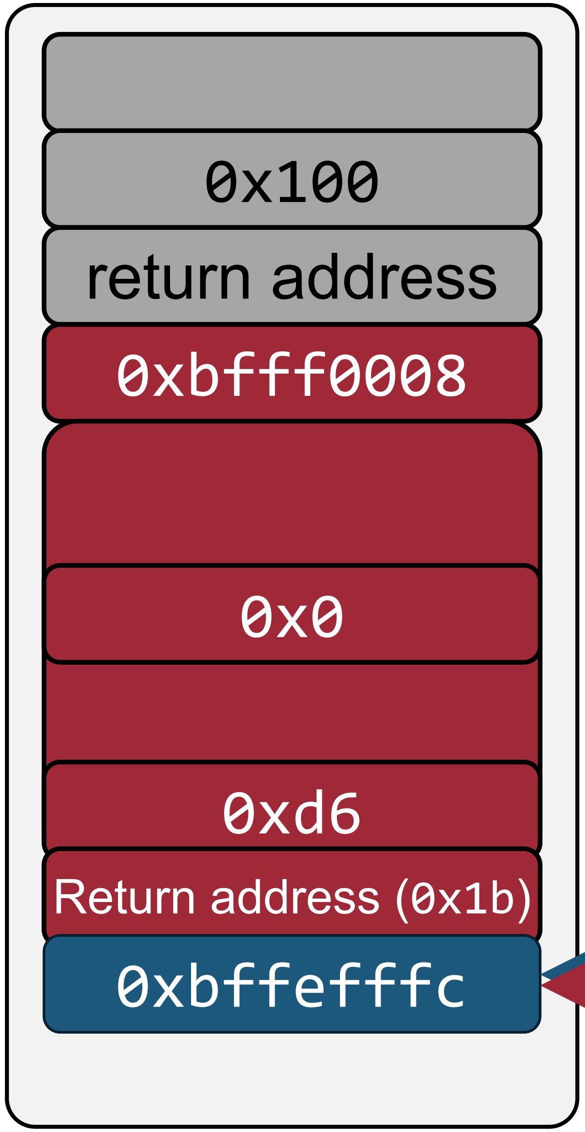
Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

mov esp, ebp
pop ebp

Execution Example



Virtual memory

0xbfff0008

0xbffefffc

```
eip: 0x47
ebp: 0xbffeffcc
esp: 0xbffeffcc
```

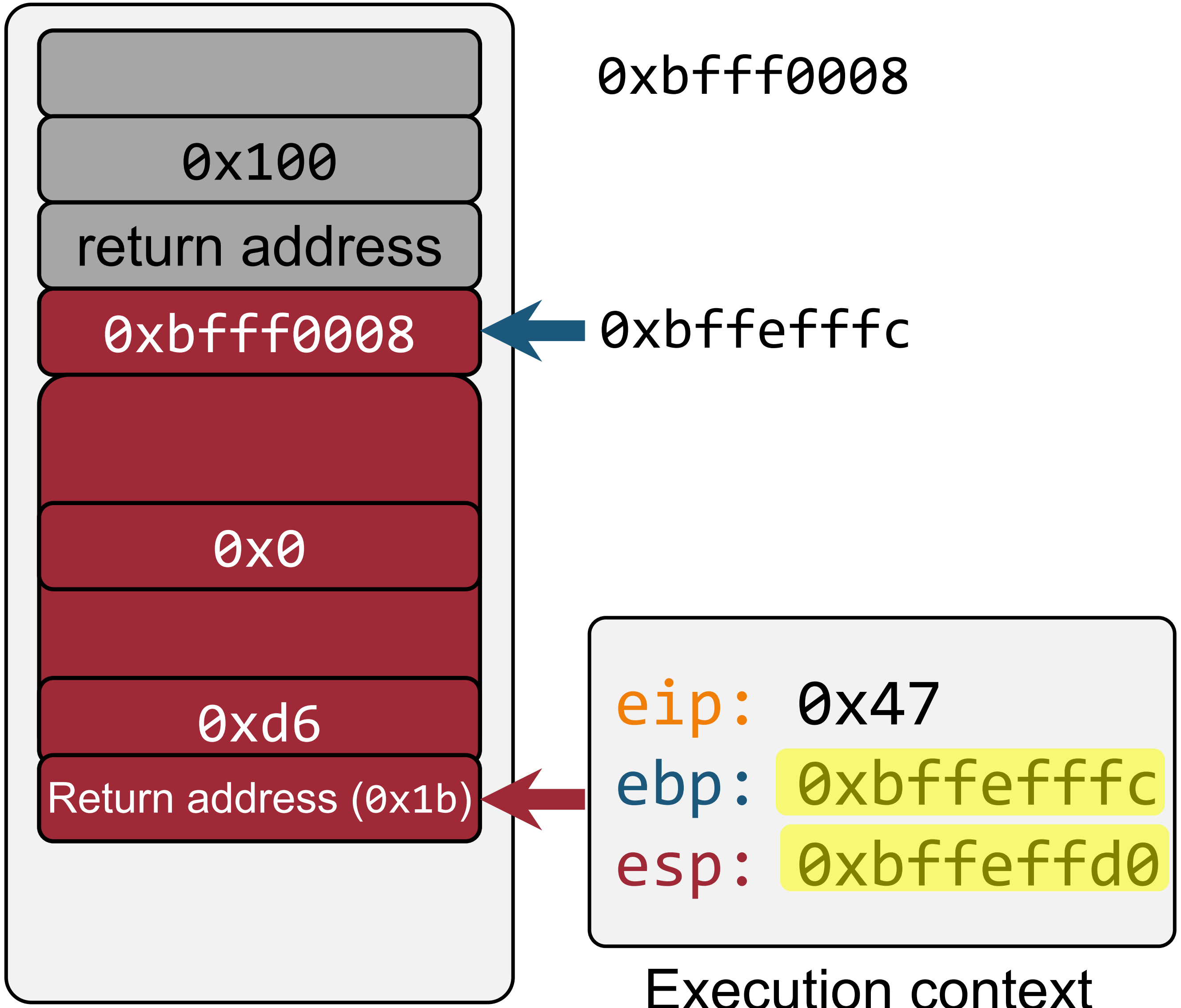
Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

mov esp, ebp
pop ebp

Execution Example



Virtual memory

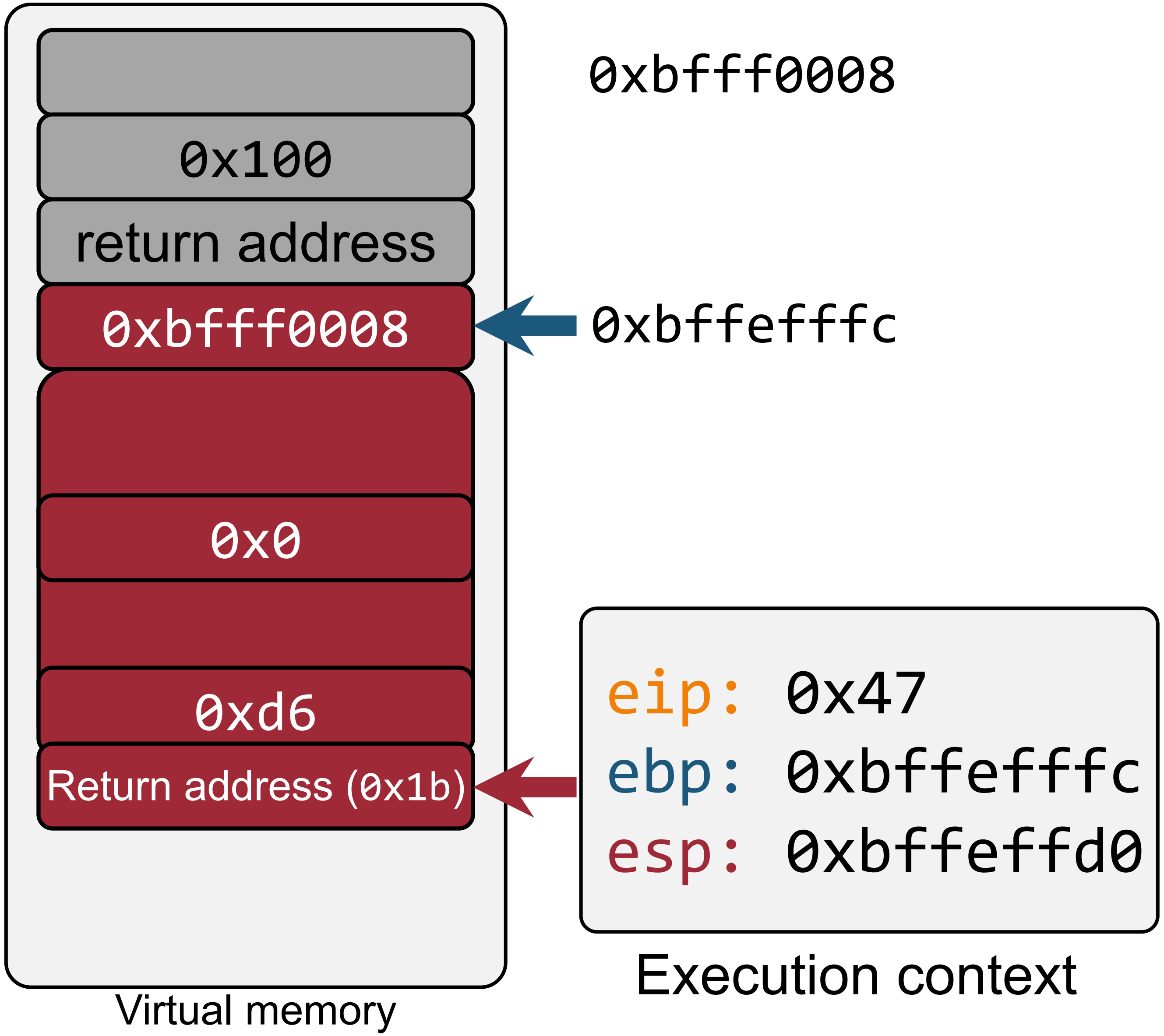
Execution context

```
<red>:
   0:   push   ebp
   1:   mov    ebp,esp
   3:   sub    esp,0x28
   6:   mov    DWORD PTR [ebp-0xc],0x0
   d:   mov    eax,DWORD PTR [ebp+0x8]
  10:   sub    eax,0x2a
  13:   mov    DWORD PTR [esp],eax
  16:   call   blue
  1b:   mov    edx,DWORD PTR [ebp-0xc]
  1e:   add    eax,edx
  20:   leave
  21:   ret

<blue>:
  22:   push    ebp
  23:   mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

mov esp, ebp
pop ebp

Execution Example

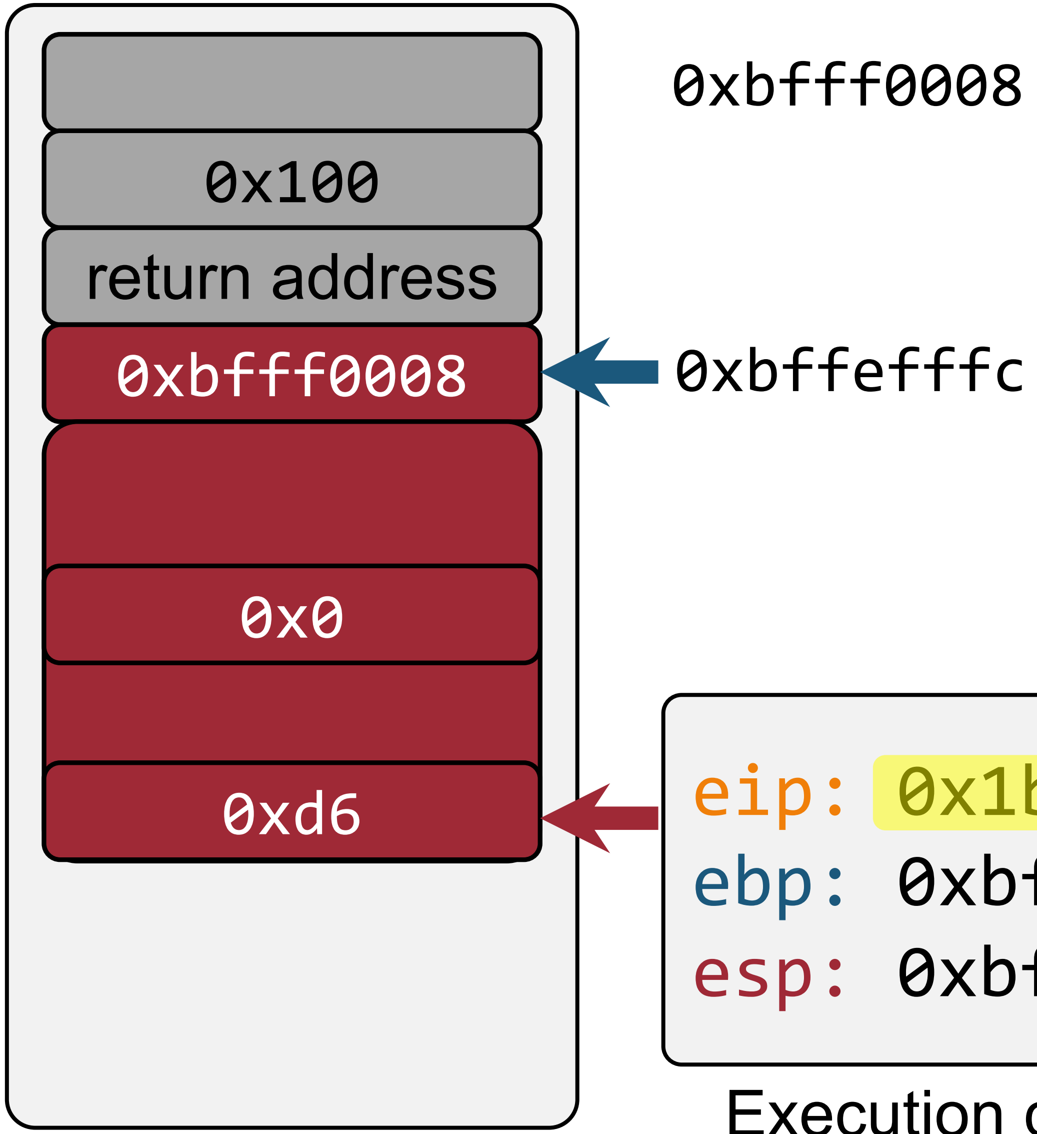



```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

pop eip

Execution Example



Virtual memory

eip: 0x1b
ebp: 0xbffefffc
esp: 0xbffeffd0

Execution context

```
<red>:
   0:  push   ebp
   1:  mov    ebp,esp
   3:  sub    esp,0x28
   6:  mov    DWORD PTR [ebp-0xc],0x0
   d:  mov    eax,DWORD PTR [ebp+0x8]
  10:  sub    eax,0x2a
  13:  mov    DWORD PTR [esp],eax
  16:  call   blue
  1b:  mov    edx,DWORD PTR [ebp-0xc]
  1e:  add    eax,edx
  20:  leave
  21:  ret

<blue>:
  22:  push    ebp
  23:  mov     ebp,esp
  ...
  46:   leave
  47:   ret
```

pop eip

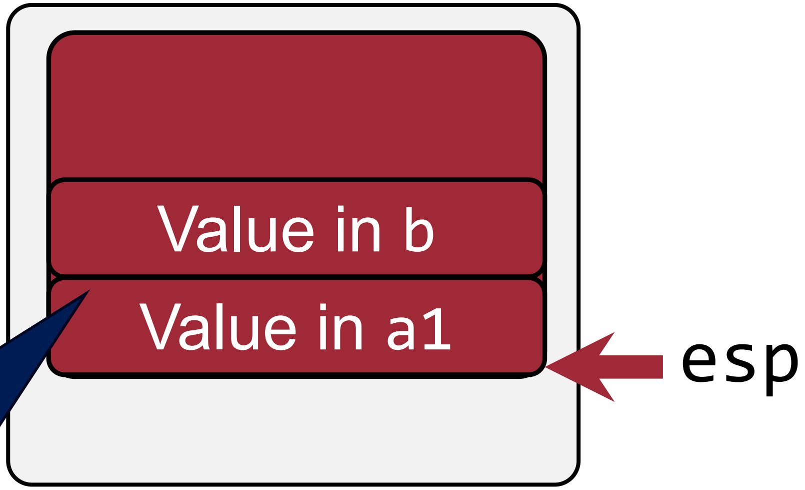
Calling Convention

```
<blue>:
  22:   push    ebp
  23:   mov     ebp,esp
  25:   sub     esp,0x28
  28:   mov     DWORD PTR [ebp-0xc],0x1
  2f:   mov     eax, DWORD PTR [ebp-0xc]
  32:   mov     DWORD PTR [esp+0x4], eax
  36:   mov     eax, DWORD PTR [ebp+0x8]
  39:   mov     DWORD PTR [esp], eax
  3c:   call    purple
  41:   mov     edx, DWORD PTR [ebp-0xc]
  44:   add     eax, edx
  46:   leave
  47:   ret
```

b

a1

```c
int blue(int a1) {
    return 1 + purple(a1, b);
}
```



Virtual memory

Passing parameter values in a reverse order

Calling Convention


```
<blue>:
  22:   push    ebp
  23:   mov     ebp,esp
  25:   sub     esp,0x28
  28:   mov     DWORD PTR [ebp-0xc],0x1
  2f:   mov     eax, DWORD PTR [ebp-0xc]
  32:   mov     DWORD PTR [esp+0x4], eax
  36:   mov     eax, DWORD PTR [ebp+0x8]
  39:   mov     DWORD PTR [esp], eax
  3c:   call    purple
  41:   mov     edx, DWORD PTR [ebp-0xc]
  44:   add     eax, edx
  46:   leave
  47:   ret
```

b

a1

```c
int blue(int a1) {
    return 1 + purple(a1, b);
}
```

Storing a return value in eax

Key Concepts

- Compilation pipeline
- x86 architecture
- Assembly
- Disassembly

Question?