

Table 11. Alternate function

		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
Po	ort	sys	TIM1/2	TIM3/4/5	TIM8/9/ 10/11/ CEC	I2C1/2/3 /4/CEC	SPI1/2/3/ 4	SPI2/3/4/ SAI1	SPI2/3/ USART1/ 2/3/UART 5/SPDIFR X	SAI/ USART6/ UART4/5/ SPDIFRX	CAN1/2 TIM12/13/ 14/ QUADSPI	SAI2/ QUADSPI/ OTG2_HS/ OTG1_FS	OTG1_FS	FMC/ SDIO/ OTG2_FS	DCMI	-	sys
	PA0	-	TIM2_CH1/ TIM2_ETR	TIM5_CH1	TIM8_ETR	-	-	-	USART2_ CTS	UART4_ TX	-	-	-	-	-	-	EVENT OUT
	PA1	-	TIM2_CH2	TIM5_CH2	-	-	-	-	USART2_ RTS	UART4_ RX	QUADSPI_ BK1_IO3	SAI2_ MCLK_B	-	-	-	-	EVENT OUT
	PA2	-	TIM2_CH3	TIM5_CH3	TIM9_CH1	-	-	-	USART2_ TX	SAI2_ SCK_B	-	-	-	-	-	-	EVENT OUT
	PA3	-	TIM2_CH4	TIM5_CH4	TIM9_CH2	-	-	SAI1_ FS_A	USART2_ RX	-	-	OTG_HS_ ULPI_D0	-	-	-	-	EVENT OUT
	PA4	-	-	-	-	-	SPI1_NSS/I 2S1_WS	SPI3_NSS / I2S3_WS	USART2_ CK	-	-	-	-	OTG_HS_ SOF	DCMI_ HSYNC	-	EVENT OUT
	PA5	-	TIM2_CH1/ TIM2_ETR	-	TIM8_ CH1N	-	SPI1_SCK/I 2S1_CK	-	-	-	-	OTG_HS_ ULPI_CK	-	-	-	-	EVENT OUT
	PA6	-	TIM1_ BKIN	TIM3_CH1	TIM8_ BKIN	-	SPI1_MISO	I2S2_ MCK	-	-	TIM13_CH1	-	-	-	DCMI_ PIXCLK	-	EVENT OUT
Port A	PA7	-	TIM1_ CH1N	TIM3_CH2	TIM8_ CH1N	-	SPI1_MOSI / I2S1_SD	-	-	-	TIM14_CH1	-	-	FMC_ SDNWE	-	-	EVENT OUT
	PA8	MCO1	TIM1_CH1	-	-	I2C3_ SCL	-	-	USART1_ CK	-	-	OTG_FS_ SOF	-	-	-	-	EVENT OUT
	PA9	-	TIM1_CH2	-	-	I2C3_ SMBA	SPI2_SCK /I2S2_CK	SAI1_ SD_B	USART1_ TX	-	-	-	-	-	DCMI_D0	-	EVENT OUT
	PA10	-	TIM1_CH3	-	-	-	-	-	USART1_ RX	-	-	OTG_FS_ ID	-	-	DCMI_D1	-	EVENT OUT
	PA11	-	TIM1_CH4	-	-	-	-	-	USART1_ CTS	-	CAN1_RX	OTG_FS_ DM	-	-	-	-	EVENT OUT
	PA12	-	TIM1_ETR	-	-	-	-	-	USART1_ RTS	SAI2_ FS_B	CAN1_TX	OTG_FS_ DP	-	-	-	-	EVENT OUT
	PA13	JTMS- SWDIO	-	-	-	-	-	-	-	-	-	·	-	-	-	-	EVENT OUT
	PA14	JTCK- SWCLK	-	-	-	-	-	-	-	-	-	·	-	-	-	-	EVENT OUT
	PA15	JTDI	TIM2_CH1/ TIM2_ETR	-	-	HDMI_ CEC	SPI1_NSS/ I2S1_WS	SPI3_ NSS/ I2S3_WS	-	UART4_RT S	-	-	-	-	-	-	EVENT OUT

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		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
Po	ort	sys	TIM1/2	TIM3/4/5	TIM8/9/ 10/11/ CEC	I2C1/2/3 /4/CEC	SPI1/2/3/ 4	SPI2/3/4/ SAI1	SPI2/3/ USART1/ 2/3/UART 5/SPDIFR X	SAI/ USART6/ UART4/5/ SPDIFRX	CAN1/2 TIM12/13/ 14/ QUADSPI	SAI2/ QUADSPI/ OTG2_HS/ OTG1_FS	OTG1_FS	FMC/ SDIO/ OTG2_FS	DCMI	-	sys
	PB0	-	TIM1_CH2N	TIM3_CH3	TIM8_ CH2N	-	-	-	SPI3_MOS I/ I2S3_SD	UART4_ CTS	-	OTG_HS_ ULPI_D1	-	SDIO_D1	-	-	EVENT OUT
	PB1	-	TIM1_CH3N	TIM3_CH4	TIM8_ CH3N	-	-	-	-	-	-	OTG_HS_ ULPI_D2	-	SDIO_D2	-	-	EVENT OUT
	PB2	-	TIM2_CH4	-	-	-	-	SAI1_ SD_A	SPI3_MOS I/ I2S3_SD	-	QUADSPI_ CLK	OTG_HS_ ULPI_D4	-	SDIO_CK	-	-	EVENT OUT
	PB3	JTDO/ TRACES WO	TIM2_CH2	-	-	I2C2_ SDA	SPI1_SCK /I2S1_CK	SPI3_SCK / I2S3_CK	-	-	-	-	-	-	-	-	EVENT OUT
	PB4	NJTRST	-	TIM3_CH1	-	I2C3_ SDA	SPI1_MISO	SPI3_ MISO	SPI2_NSS/ I2S2_WS	-	-	-	-	-	-	-	EVENT OUT
	PB5	-	-	TIM3_CH2	-	I2C1_ SMBA	SPI1_MOSI /I2S1_SD	SPI3_ MOSI/ I2S3_SD	-	-	CAN2_RX	OTG_HS_ ULPI_D7	-	FMC_ SDCKE1	DCMI_ D10	-	EVENT OUT
	PB6	-	-	TIM4_CH1	HDMI_ CEC	I2C1_ SCL	-	-	USART1_ TX	-	CAN2_TX	QUADSPI_ BK1_NCS	-	FMC_ SDNE1	DCMI_D5	-	EVENT OUT
Port B	PB7	-	-	TIM4_CH2	-	I2C1_ SDA	-	-	USART1_ RX	SPDIF_ RX0	-	-	-	FMC_NL	DCMI_ VSYNC	-	EVENT OUT
	PB8	-	TIM2_CH1/ TIM2_ETR	TIM4_CH3	TIM10_ CH1	I2C1_ SCL	-	-	-	-	CAN1_RX	-	-	SDIO_D4	DCMI_D6	-	EVENT OUT
	PB9	-	TIM2_ CH2	TIM4_CH4	TIM11_ CH1	I2C1_ SDA	SPI2_NSS/ I2S2_WS	SAI1_ FS_B	-	-	CAN1_TX	-	-	SDIO_D5	DCMI_D7	-	EVENT OUT
	PB10	-	TIM2_CH3	-	-	I2C2_ SCL	SPI2_SCK/ I2S2_CK	SAI1_ SCK_A	USART3_ TX	-	-	OTG_HS_ ULPI_D3	-	-	-	-	EVENT OUT
	PB11	-	TIM2_CH4	-	-	I2C2_ SDA	-	-	USART3_ RX	SAI2_ SD_A	-	-	-	-	-	-	EVENT OUT
	PB12	-	TIM1_BKIN	-	-	I2C2_ SMBA	SPI2_NSS/ I2S2_WS	SAI1_ SCK_B	USART3_ CK	-	CAN2_RX	OTG_HS_ ULPI_D5	-	OTG_ HS_ID	-	-	EVENT OUT
	PB13	-	TIM1_CH1N	-	-	-	SPI2_SCK/ I2S2_CK	-	USART3_ CTS	-	CAN2_TX	OTG_HS_ ULPI_D6	-	-	-	-	EVENT OUT
	PB14	-	TIM1_CH2N	-	TIM8_ CH2N	-	SPI2_MISO	-	USART3_ RTS	-	TIM12_CH1	-	-	OTG_ HS_DM	-	-	EVENT OUT
	PB15	RTC_ REFIN	TIM1_CH3N	-	TIM8_ CH3N	-	SPI2_MOSI /I2S2_SD	-	-	-	TIM12_CH2	-	-	OTG_ HS_DP	-	-	EVENT OUT





Table 11. Alternate function (continued)

		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
Po	ort	sys	TIM1/2	TIM3/4/5	TIM8/9/ 10/11/ CEC	I2C1/2/3 /4/CEC	SPI1/2/3/ 4	SPI2/3/4/ SAI1	SPI2/3/ USART1/ 2/3/UART 5/SPDIFR X	SAI/ USART6/ UART4/5/ SPDIFRX	CAN1/2 TIM12/13/ 14/ QUADSPI	SAI2/ QUADSPI/ OTG2_HS/ OTG1_FS	OTG1_FS	FMC/ SDIO/ OTG2_FS	DCMI	-	SYS
	PC0	-	-	-	-	-	-	SAI1_ MCLK_B	-	-	-	OTG_HS_ ULPI_STP	ī	FMC_ SDNWE	-	-	EVENT OUT
	PC1	-	-	-	-	-	SPI3_MOSI /I2S3_SD	SAI1_ SD_A	SPI2_MOS I /I2S2_SD	-	-	-	-	-	-	-	EVENT OUT
	PC2	-	-	-	-	-	SPI2_MISO	-	-	-	-	OTG_HS_ ULPI_DIR	-	FMC_ SDNE0	-	-	EVENT OUT
	PC3	-	-	-	-	-	SPI2_MOSI / I2S2_SD	-	-	-	-	OTG_HS_ ULPI_NXT	-	FMC_ SDCKE0	-	-	EVENT OUT
	PC4	-	-	-	-	-	I2S1_MCK	-	-	SPDIF_ RX2	-	-	-	FMC_ SDNE0	-	-	EVENT OUT
	PC5	-	-	-	-	-	-	-	USART3_ RX	SPDIF_ RX3	-	-	-	FMC_ SDCKE0	-	-	EVENT OUT
	PC6	-	-	TIM3_CH1	TIM8_CH1	FMPI2C1 _SCL	I2S2_MCK	-	-	USART6_T	-	-	-	SDIO_D6	DCMI_D0	-	EVENT OUT
Port C	PC7	-	-	TIM3_CH2	TIM8_CH2	FMPI2C1 _SDA	SPI2_SCK/ I2S2_CK	I2S3_MCK	SPDIF_ RX1	USART6_R X	-	-	-	SDIO_D7	DCMI_D1	-	EVENT OUT
	PC8	TRACE D0	-	TIM3_CH3	TIM8_CH3	-	-	-	UART5_ RTS	USART6_C K	-	-	-	SDIO_D0	DCMI_D2	-	EVENT OUT
	PC9	MCO2	-	TIM3_CH4	TIM8_CH4	I2C3_ SDA	I2S_CKIN	-	UART5_ CTS	-	QUADSPI_ BK1_IO0	-	-	SDIO_D1	DCMI_D3	-	EVENT OUT
	PC10	-	-	-	-	-	-	SPI3_SCK / I2S3_CK	USART3_ TX	UART4_TX	QUADSPI_ BK1_IO1	-	-	SDIO_D2	DCMI_D8	-	EVENT OUT
	PC11	-	-	-	-	-	-	SPI3_ MISO	USART3_ RX	UART4_RX	QUADSPI_ BK2_NCS	-	-	SDIO_D3	DCMI_D4	-	EVENT OUT
	PC12	-	-	-	-	I2C2_ SDA	-	SPI3_ MOSI/ I2S3_SD	USART3_ CK	UART5_TX	-	-	-	SDIO_CK	DCMI_D9	-	EVENT OUT
	PC13	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT
	PC14	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT
	PC15	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT

						1	able 11.	Alterna	te funct	ion (con	tinued)						
		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
Po	ort	sys	TIM1/2	TIM3/4/5	TIM8/9/ 10/11/ CEC	I2C1/2/3 /4/CEC	SPI1/2/3/ 4	SPI2/3/4/ SAI1	SPI2/3/ USART1/ 2/3/UART 5/SPDIFR X	SAI/ USART6/ UART4/5/ SPDIFRX	CAN1/2 TIM12/13/ 14/ QUADSPI	SAI2/ QUADSPI/ OTG2_HS/ OTG1_FS	OTG1_FS	FMC/ SDIO/ OTG2_FS	DCMI	-	sys
	PD0	-	-	-	-	-	SPI4_MISO	SPI3_ MOSI/ I2S3_SD	-	-	CAN1_RX	-	-	FMC_D2	-	-	EVENT OUT
	PD1	-	-	-	-	-	-	-	SPI2_NSS/ I2S2_WS	-	CAN1_TX	-	-	FMC_D3	-	-	EVENT OUT
	PD2	-	-	TIM3_ETR	-	-	-	-	-	UART5_RX	-	-	-	SDIO_CMD	DCMI_ D11	-	EVENT OUT
	PD3	TRACE D1	1	-	-	-	SPI2_SCK/ I2S2_CK	-	USART2_ CTS	-	QUADSPI_ CLK	-	-	FMC_CLK	DCMI_ D5	-	EVENT OUT
	PD4	-	ı	-	-	-	-	-	USART2_ RTS	-	-	-	-	FMC_NOE	-	-	EVENT OUT
	PD5	-	1	-	-	-	-	-	USART2_ TX	-	-	-	-	FMC_NWE	-	-	EVENT OUT
	PD6	-	-	-	-	-	SPI3_ MOSI/ I2S3_SD	SAI1_ SD_A	USART2_ RX	-	-	-	-	FMC_ NWAIT	DCMI_ D10	-	EVENT OUT
Port D	PD7	-	-	-	-	-	-	-	USART2_ CK	SPDIF_ RX0	-	-	-	FMC_NE1	-	-	EVENT OUT
	PD8	-	-	-	-	-	-	-	USART3_ TX	SPDIF_ RX1	-	-	-	FMC_D13	-	-	EVENT OUT
	PD9	-	-	-	-	-	-	-	USART3_ RX	-	-	-	-	FMC_D14	-	-	EVENT OUT
	PD10	-	-	-	-	-	-	-	USART3_ CK	-	-	-	-	FMC_D15	-	-	EVENT OUT
	PD11	-	-	-	-	FMPI2C1 _SMBA	-	-	USART3_ CTS	-	QUADSPI_ BK1_IO0	SAI2_SD_A	-	FMC_A16	-	-	EVENT OUT
	PD12	-	-	TIM4_CH1	-	FMPI2C1 _SCL	-	-	USART3_ RTS	-	QUADSPI_ BK1_IO1	SAI2_FS_A	-	FMC_A17	-	-	EVENT OUT
	PD13	-	-	TIM4_CH2	-	FMPI2C1 _SDA	-	-	-	-	QUADSPI_ BK1_IO3	SAI2_SCK_A	-	FMC_A18	-	-	EVENT OUT
	PD14	-	-	TIM4_CH3	-	FMPI2C1 _SCL	-	-	-	SAI2_ SCK_A	-	-	-	FMC_D0	-	-	EVENT OUT
	PD15	-	-	TIM4_CH4	-	FMPI2C1 _SDA	-	-	-	-	-	-	-	FMC_D1	-	-	EVENT OUT





Table 11. Alternate function (continued)

		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
Po	ort	sys	TIM1/2	TIM3/4/5	TIM8/9/ 10/11/ CEC	I2C1/2/3 /4/CEC	SPI1/2/3/ 4	SPI2/3/4/ SAI1	SPI2/3/ USART1/ 2/3/UART 5/SPDIFR X	SAI/ USART6/ UART4/5/ SPDIFRX	CAN1/2 TIM12/13/ 14/ QUADSPI	SAI2/ QUADSPI/ OTG2_HS/ OTG1_FS	OTG1_FS	FMC/ SDIO/ OTG2_FS	DCMI	-	sys
	PE0	-	-	TIM4_ETR	-	-	-	-	-	-	-	SAI2_ MCLK_A	-	FMC_ NBL0	DCMI_D2	-	EVENT OUT
	PE1	-	-	-	-	-	-	-	-	-	-	-	-	FMC_ NBL1	DCMI_D3	-	EVENT OUT
	PE2	TRACE CLK	-	-	-	-	SPI4_SCK	SAI1_ MCLK_A	-	-	QUADSPI_ BK1_IO2	-	-	FMC_A23	-	-	EVENT OUT
	PE3	TRACE D0	-	-	-	-	-	SAI1_ SD_B	-	-	-	-	-	FMC_A19	-	-	EVENT OUT
	PE4	TRACE D1	-	-	-	-	SPI4_NSS	SAI1_ FS_A	-	-	-	-	-	FMC_A20	DCMI_D4	-	EVENT OUT
	PE5	TRACE D2	-	-	TIM9_CH1	-	SPI4_MISO	SAI1_ SCK_A	-	-	-	-	-	FMC_A21	DCMI_D6	-	EVENT OUT
	PE6	TRACE D3	-	-	TIM9_CH2	-	SPI4_MOSI	SAI1_ SD_A	-	-	-	-	-	FMC_A22	DCMI_D7	-	EVENT OUT
	PE7	-	TIM1_ETR	-	-	-	-	-	-	UART5_RX	-	QUADSPI_ BK2_IO0	-	FMC_D4	-	-	EVENT OUT
Port E	PE8	-	TIM1_CH1N	-	-	-	-	-	-	UART5_TX	-	QUADSPI_ BK2_IO1	-	FMC_D5	-	-	EVENT OUT
	PE9	-	TIM1_CH1	-	-	-	-	-	-	-	-	QUADSPI_ BK2_IO2	-	FMC_D6	-	-	EVENT OUT
	PE10	-	TIM1_CH2N	-	-	-	-	-	-	-	-	QUADSPI_ BK2_IO3	-	FMC_D7	-	-	EVENT OUT
	PE11	-	TIM1_CH2	-	-	-	SPI4_NSS	-	-	-	-	SAI2_ SD_B	-	FMC_D8	-	-	EVENT OUT
	PE12	-	TIM1_CH3N	-	-	-	SPI4_SCK	-	-	-	-	SAI2_ SCK_B	-	FMC_D9	-	-	EVENT OUT
	PE13	-	TIM1_CH3	-	-	-	SPI4_MISO	-	-	-	-	SAI2_ FS_B	-	FMC_D10	-	-	EVENT OUT
	PE14	-	TIM1_CH4	-	-	-	SPI4_MOSI	-	-	-	-	SAI2_ MCLK_B	-	FMC_D11	-	-	EVENT OUT
	PE15	-	TIM1_BKIN	-	-	-	-	-	-	-	-	-	-	FMC_D12	-	-	EVENT OUT

Pinout and pin description

Table 11. Alternate function (continued)

		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
Po	ort	sys	TIM1/2	TIM3/4/5	TIM8/9/ 10/11/ CEC	I2C1/2/3 /4/CEC	SPI1/2/3/ 4	SPI2/3/4/ SAI1	SPI2/3/ USART1/ 2/3/UART 5/SPDIFR X	SAI/ USART6/ UART4/5/ SPDIFRX	CAN1/2 TIM12/13/ 14/ QUADSPI	SAI2/ QUADSPI/ OTG2_HS/ OTG1_FS	OTG1_FS	FMC/ SDIO/ OTG2_FS	DCMI	-	sys
	PF0	-	-	-	-	I2C2_ SDA	-	-	-	-	-	-	-	FMC_A0	-	-	EVENT OUT
	PF1	-	-	-	-	I2C2_ SCL	-	-	-	-	-	-	-	FMC_A1	-	-	EVENT OUT
	PF2	-	-	-	-	I2C2_ SMBA	-	-	-	-	-	-	-	FMC_A2	-	-	EVENT OUT
	PF3	-	-	-	-	-	-	-	-	-	-	-	-	FMC_A3	-	-	EVENT OUT
	PF4	-	-	-	-	-	-	-	-	-	-	-	-	FMC_A4	-	-	EVENT OUT
	PF5	-	-	-	-	-	-	-	-	-	-	-	-	FMC_A5	-	-	EVENT OUT
	PF6	-	-	-	TIM10_ CH1	-	-	SAI1_ SD_B	-	-	QUADSPI_ BK1_IO3	-	-	-	-	-	EVENT OUT
	PF7	-	-	-	TIM11_ CH1	-	-	SAI1_ MCLK_B	-	-	QUADSPI_ BK1_IO2	-	-	-	-	-	EVENT OUT
Port F	PF8	-	-	-	-	-	-	SAI1_ SCK_B	-	-	TIM13_CH1	QUADSPI_ BK1_IO0	-	-	-	-	EVENT OUT
	PF9	-	-	-	-	-	-	SAI1_ FS_B	-	-	TIM14_CH1	QUADSPI_ BK1_IO1	-	-	-	-	EVENT OUT
	PF10	-	-	-	-	-	-	-	-	-	-	-	-	-	DCMI_ D11	-	EVENT OUT
	PF11	-	-	-	-	-	-	-	-	-	-	SAI2_SD_B	-	FMC_ SDNRAS	DCMI_ D12	-	EVENT OUT
	PF12	-	-	-	-	-	-	-	-	-	-	-	-	FMC_A6	-	-	EVENT OUT
	PF13	-	-	-	-	FMPI2C1 _SMBA	-	-	-	-	-	-	-	FMC_A7	-	-	EVENT OUT
	PF14	-	-	-	-	FMPI2C1 _SCL	-	-	-	-	-	-	-	FMC_A8	-	-	EVENT OUT
	PF15	-	-	-	-	FMPI2C1 _SDA	-	-	-	-	-	-	-	FMC_A9	-	-	EVENT OUT





Table 11. Alternate function (continued)

		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
Po	ort	sys	TIM1/2	TIM3/4/5	TIM8/9/ 10/11/ CEC	I2C1/2/3 /4/CEC	SPI1/2/3/ 4	SPI2/3/4/ SAI1	SPI2/3/ USART1/ 2/3/UART 5/SPDIFR X	SAI/ USART6/ UART4/5/ SPDIFRX	CAN1/2 TIM12/13/ 14/ QUADSPI	SAI2/ QUADSPI/ OTG2_HS/ OTG1_FS	OTG1_FS	FMC/ SDIO/ OTG2_FS	DCMI	-	sys
	PG0	-	-	-	-	-	-	-	-	-	-	-	-	FMC_A10	-	-	EVENT OUT
	PG1	-	-	-	-	-	-	-	-	-	-	-	-	FMC_A11	-	-	EVENT OUT
	PG2	-	-	-	-	-	-	-	-	-	-	-	-	FMC_A12	-	-	EVENT OUT
	PG3	-	-	-	-	-	-	-	-	-	-	-	-	FMC_A13	-	-	EVENT OUT
	PG4	-	-	-	-	-	-	-	-	-	-	-	-	FMC_A14/ FMC_BA0	-	-	EVENT OUT
	PG5	-	-	-	-	-	-	-	-	-	-	-		FMC_A15/ FMC_BA1	-	-	EVENT OUT
	PG6	-	-	-	-	-	-	-	-	-	-	QUADSPI_ BK1_NCS		-	DCMI_ D12	-	EVENT OUT
	PG7	-	-	-	-	-	-	-	-	USART6_C K	-	-		FMC_INT	DCMI_ D13	-	EVENT OUT
Port G	PG8	-	-	-	-	-	-	-	SPDIFRX_ IN2	USART6_R TS	-	-	-	FMC_ SDCLK	-	-	EVENT OUT
	PG9	-	-	-	-	-	-	-	SPDIFRX_ IN3	USART6_R X	QUADSPI_ BK2_IO2	SAI2_FS_B	-	FMC_NE2/ FMC_NCE3	DCMI_ VSYNC ⁽¹⁾	-	EVENT OUT
	PG10	-	-	-	-	-	-	-	-	-	-	SAI2_SD_B		FMC_NE3	DCMI_D2	-	EVENT OUT
	PG11	-	-	-	-	-	-	SPI4_ SCK	SPDIFRX_ IN0	-	-	-	-	-	DCMI_D3	-	EVENT OUT
	PG12	-	-	-	-	-	-	SPI4_ MISO	SPDIFRX_ IN1	USART6_R TS	-	-	-	FMC_NE4	-	-	EVENT OUT
	PG13	TRACE D2	-	-	-	-	-	SPI4_ MOSI	-	USART6_C TS	-	-	-	FMC_A24	-	-	EVENT OUT
	PG14	TRACE D3	-	-	-	-	-	SPI4_ NSS	-	USART6_T X	QUADSPI_ BK2_IO3	-	-	FMC_A25	-	-	EVENT OUT
	PG15	-	-	-	-	-	-	-	-	USART6_C TS	-	-	-	FMC_ SDNCAS	DCMI_ D13	-	EVENT OUT

Table 11. Alternate function (continued)

		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
Port		sys	TIM1/2	TIM3/4/5	TIM8/9/ 10/11/ CEC	I2C1/2/3 /4/CEC	SPI1/2/3/ 4	SPI2/3/4/ SAI1	SPI2/3/ USART1/ 2/3/UART 5/SPDIFR X	SAI/ USART6/ UART4/5/ SPDIFRX	CAN1/2 TIM12/13/ 14/ QUADSPI	SAI2/ QUADSPI/ OTG2_HS/ OTG1_FS	OTG1_FS	FMC/ SDIO/ OTG2_FS	DCMI	-	SYS
D-#II	PH0	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT
Port H	PH1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENT OUT

^{1.} The DCMI_VSYNC alternate function on PG9 is only available on silicon revision 3.

