

Assignment -5

D Given en Cox = 100 u A /2 W = 1 V+21V To 22001A B= en Cox W = 100 u A/v2

> BEDGE : Drain Condrid : Gade

Now lets assume A(+) and B(-). For finding right feedback, lets slightly increment the eensing voltage by AN. at VD. Bf-the feed back is -ve, this Ip has to re-established.

When No -> Vo + DN, Va -> Va + ADN, when Vat, Vast and so does Ida. Hence Ida > Io, there residual auxount has to be supplied by the parasthetic Capacitance, there by reducing its voltage, which makes Not. Hence, for given eight Configuration, The feedback is -Ve i.e. A(+ve); B(-ve)

Then You = 4V Ris

VDS = 4V --- (Virtual short)



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And Ids = B (Vas-V+)2

>> Vas= Vr+ \[2] \text{18} = 1+ \[\frac{2\text{2000}}{1004} = 3V

... VGS=3V and VDS=4V

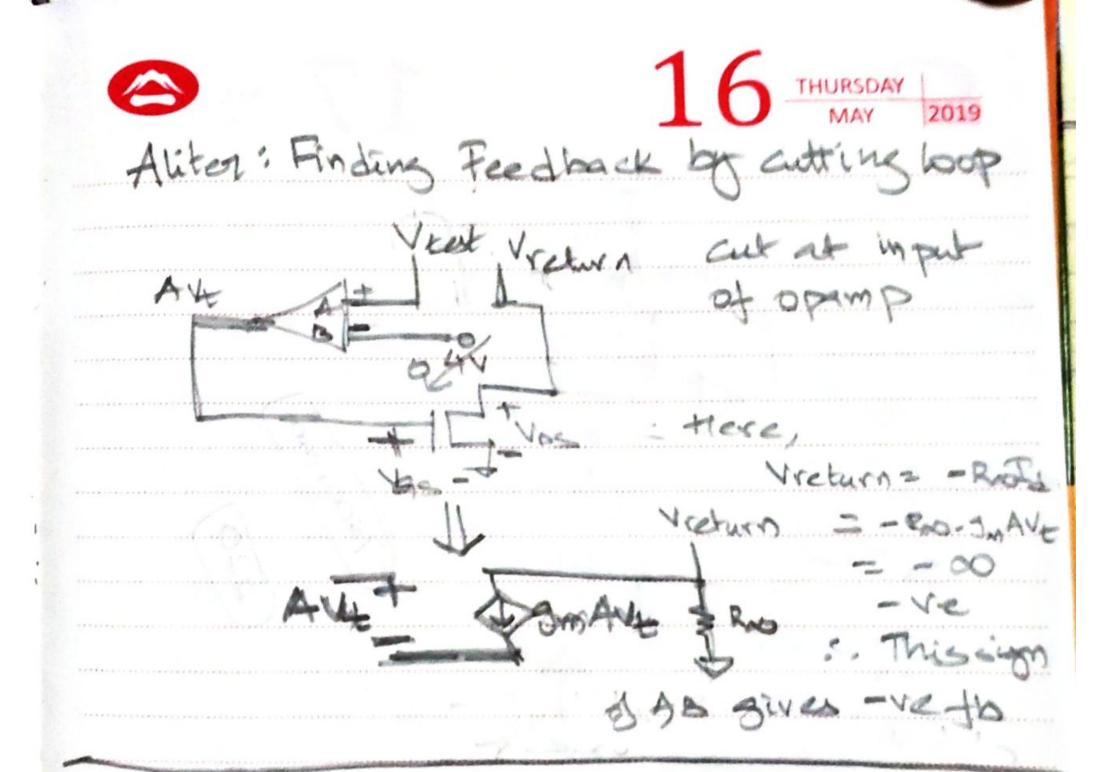
Now for using it as common source amplified the output can be extracted from drain, as opening has high input impedance, all current thems through the MOS. But for imput, a series high resistance is needed to be connected with opening prevent input current flow through apamp, not officing high input impedance as clown

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Let assume A(+) and B(-). A small AN perturbal at Np. Then Ns will increase by AAN, When Ns T, Vas I and so does Isat. How To >Ida, Bo the great Carrent flows farough the captures by even increasing the Np, hence is sowing as the feedback. For we feedback, we have to switch the tammals he A(-) and B(+).

Here, assuming Virtual shoot $V_0 = 5V$, $V_0 = 3V$ $V_0 = 0$, Stoica can also be seen as $V_0 = 0$, $V_0 =$

.. Vas=3v and vas=5v

For coopline the AC supply and boad at input we can couple directly as we can still have the high impedence. But



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output, the award from op-amp interference with the output's current. To limit the current from op-amp/we can add a high resis tance Composed to output as shoon.

Solution of the output of the first terms of the output of the

Aliter: For finding—the J-b, we can cut
woop at input of op-amp

Hence,

The Hence,

> Ids = John



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FIOK52

Bense: Source

Control: Gate

increment at sensing woltage, we obscure vs1, by by-ADV. Here

Vag decreases, and hence I'ds decreases. To maintain Is, the parasthetic capates source discharges, de creasing the forcement. ... A is -ve, B is the

By assuming virtual short,

NS=1V NDS=(GV-10K×2004+)-1V

And Vas= V++ 12745 =1+2=3V

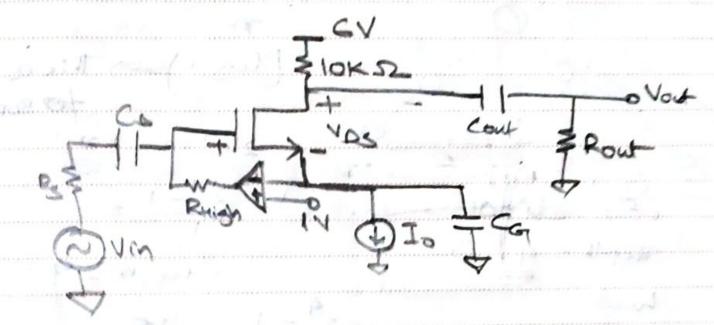
-. Vos=3V and Vas=3V

there the imput impedence is not high as the op-amps output terminal can load the input and hence we connect high series rusistance.



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The output is fine to connect as, the current is flowing completely between MOS and the output resistor.



Autor: For tinding feed back, we can also cut the loop and apply test voltage. Suppose we cut it at input of Mos, we get

VS=9m(Vb-Va).PL

No Solve Vs=3mRooVe

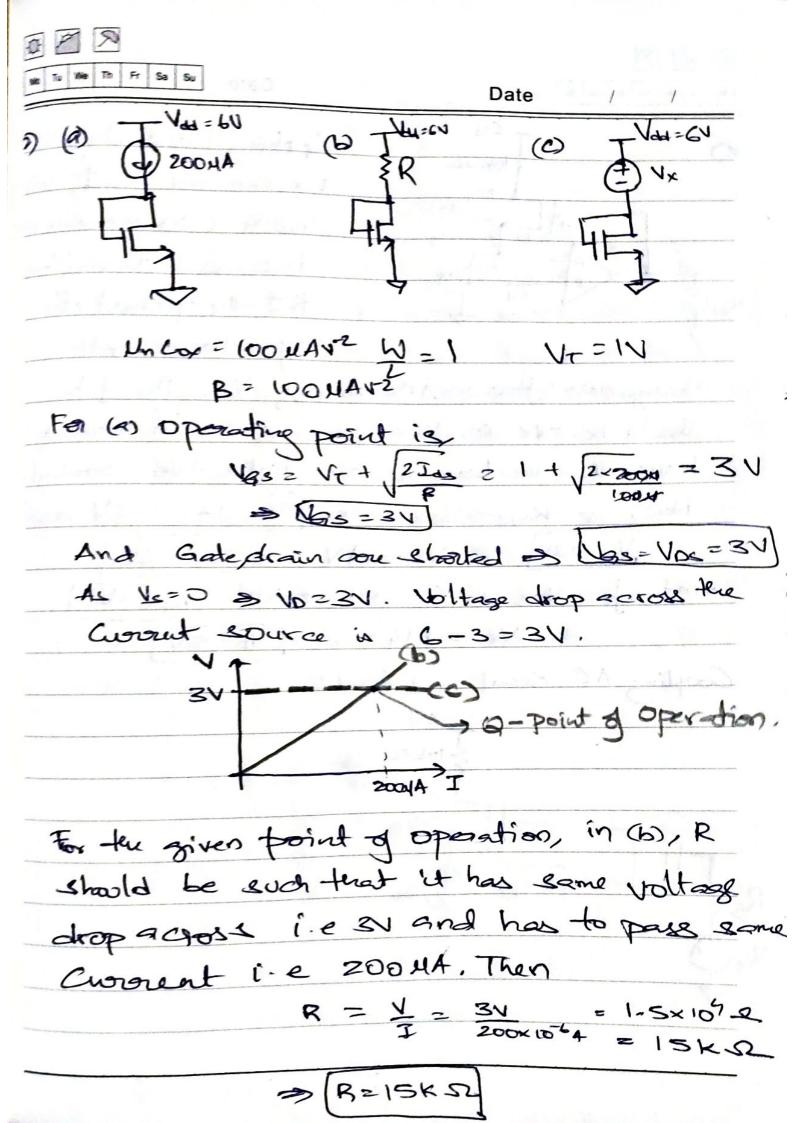
Vs=3mRooVe

1+3mRo

2 Ve

No Solve Vs

No Sol





Similarly, for co-the drop aros the botton has to be 3V and should mandain 200 UA. As a voltage aboonce remains some top all consente use can lake, (Vx = 3V)

(ii) Lyply change: BYOD In given. Car-a: The Ise has driven by account source and change in supply boltage is taken none by the account againso. > DIde=0 Case - b: Here Ide = & (Vas - VT) = E (Vu - Idek - V+)

Shar = BCAN-JUE A. (Shir - Wills). (Shir - Wills)

= 100×166 × 2 (1 - 15×103 2Tac)

>> 28aa = 2×100×106 = 50×106

| + 100×166 × 2×13×168

BHROIXOE = ENTE = ELIA (= DIVO

Case-c: there Vas=Vos= lu-Vx; Bu=B(ba- 4) R (Vas- 42) [3 Mas] = 100x166 x 2 x 3(M2- 42) >> 21/25 >Vas

> (DIds = 200×152 × A V)



(iii) Effect of change in VT.

Case-a: The coarent Ide is driven by external crowner source and change VT Yesults in change in Vas and Vos

but I de gremains sance.

Case-b: Fds = B (Uss-47)2

= B (VOD-TOSR = - VT)2

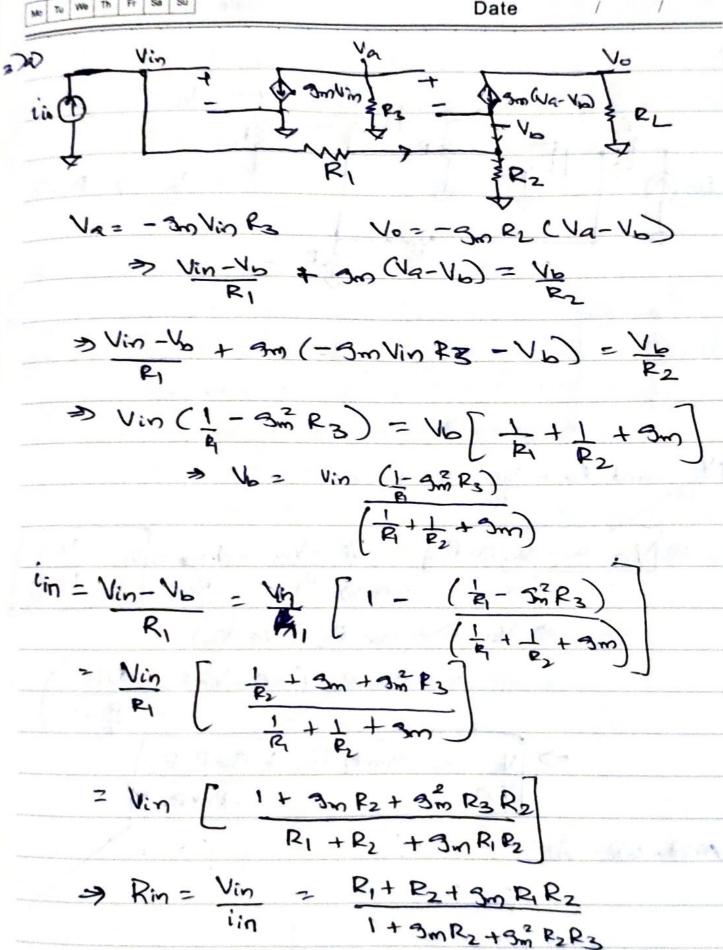
=> 2] = B (Vad -] = RS-VT) (-1) = 100×10⁻⁶ (2) (-1) = -200×10⁻⁶

> STal= -200×10-6 x DVT

Cax-c: Ids=B(4s-4)2=B(Vd-Vy-V7)2 -> 3/4= = B(V4-1/x-V+) .- 3/4 = -B(V64-1/x-V+)

> = - 100×10 × (2) 2-200×10 = > DZ48 = -200×106 AVT







Now,
$$V_0 = -g_m R_L (V_R - V_D)$$
 $\Rightarrow V_0 = -g_m R_L (-g_n) V_{in} R_3 - V_{in} (\frac{1}{R_1} - \frac{1}{3^n} R_3)$
 $= g_m R_L V_{in} \left[g_m R_3 + \left(\frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{3^n} - \frac{1}{3^n} R_3 + \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{3^n} - \frac{1}{3^n} R_3 + \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{3^n} - \frac{1}{3^n} R_3 + \frac{1}{R_2} + \frac{1}{R_2} + \frac{1}{3^n} R_3 + \frac{1}{R_2} + \frac{1}{R_2} + \frac{1}{3^n} R_2 + \frac{1}{R_2} + \frac{1}{3^n} R_2 + \frac{1}{3$



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 $RL \Rightarrow V_{R} = -3m(Vin-V_{B})R_{L} --- (1)$ $RL \Rightarrow V_{B} - V_{0} = 3mV_{R}R_{2} --- (2)$ $RL \Rightarrow V_{B} - V_{0} = 3mV_{R}R_{2} --- (2)$ $RL \Rightarrow V_{B} - V_{0} = V_{0} + V_{0} - V_{0}$ $R_{1} = R_{2}$ $R_{2} = V_{0} \left[\frac{1}{R} + \frac{1}{R_{2}} + 3m\right] --- (3)$

From (1) and (2),

V_D-V_O = 3P₂ (-3m (Vin-V_O)) P_L

= -3² P₂ P_L Vin + 3² P₂ P_L V_D

V_D(1-3² P₂P_L) = V_O - 3² P₂ P_L Vin

V_D = V_O - 3² P₂ P_L Vin

1-3² P₂ P_L Vin

1-3² P₂ P_L — Alg in (3)

 $393 \text{nVin} + \frac{V_0}{R_2} = \frac{1 - 30^2 R_2 R_1 V \text{in}}{1 - 30^2 R_2 R_2} \left[\frac{R_1 + R_2 + 30 R_1 R_2}{R_1 R_2} \right]$

> Vin [3m+ 3m R2RL [21+02+3mR102]]

= Vo [1 [R1+R2+3mRR2] - 1]
[1-32R2RL [R1 R2] - R2]



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Fon Vout, Ve Bt Vetat

Date Ve-Vb = Irat - 3m/a DVE-VD = PITEST - 30 VBRLR2 > Vb = Vt - Pz Itat from previous relation of Vb, we set VE - P2 TE [R1+ R2+ 3mRR2] = VE 1-3m2 P2 [R1 + R2 + 3mRR2] = P2 DVE-P2It = (1-3m PLP2) P1 >> VE RI+R2+3MP1P2+3mo PiP1P2-PT => VE R/2 (1+3mR, +3m PLR) = \$2 IL R1 + P2 + 3m AR2 3) Rout= Yt 2 R1+P2+ 8m R1 R2 It 1+3mR1+32RR1 When smoo 32 [(P1+P2)/3m+ P1P2/3m)] 3% [1/37 + P/20] = 0 > [Pout = 0]